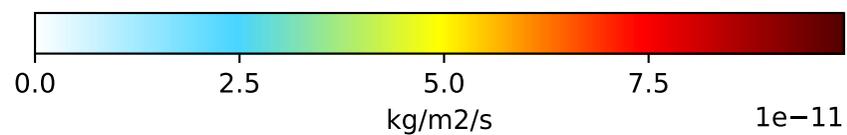
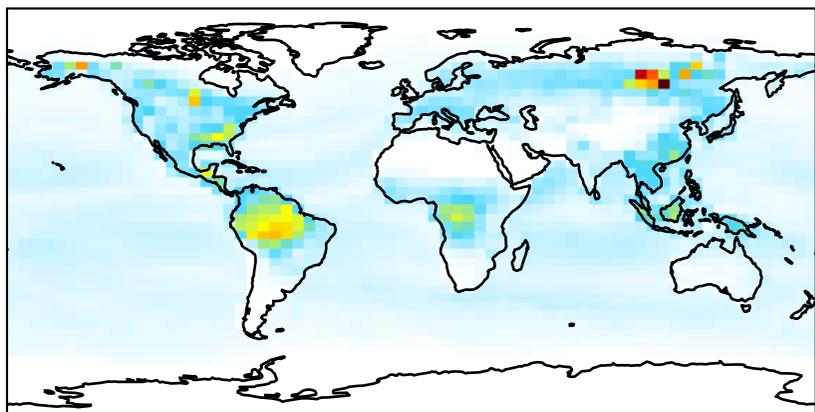
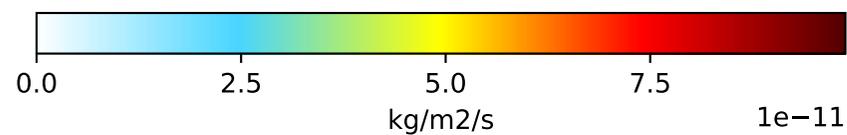
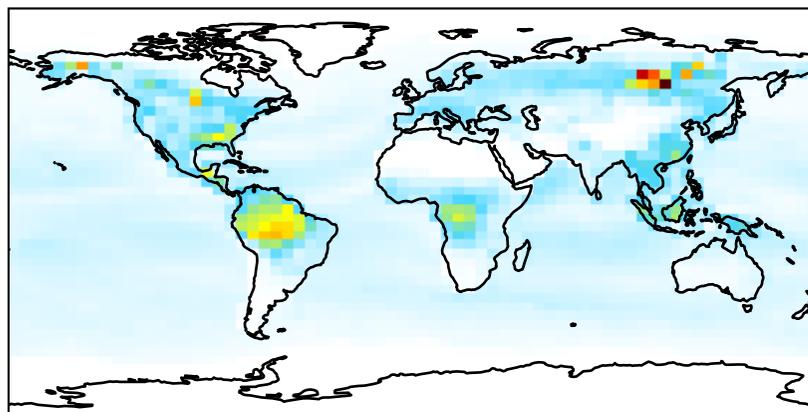


# EmisACET\_Total (Jul2019)

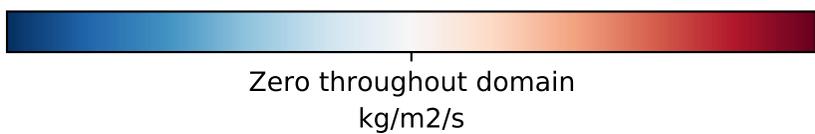
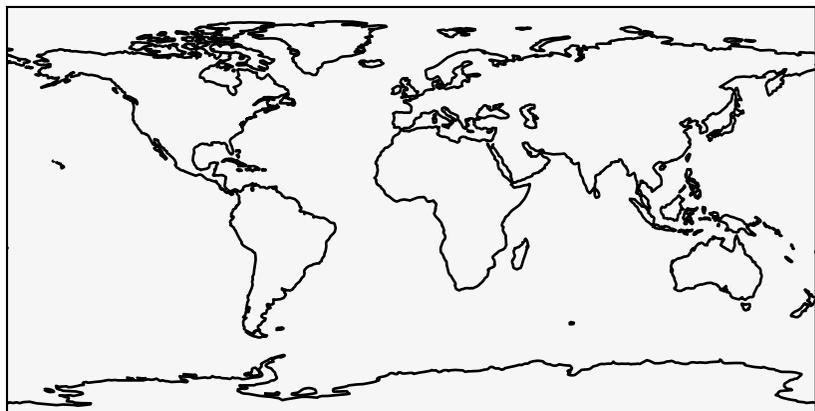
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



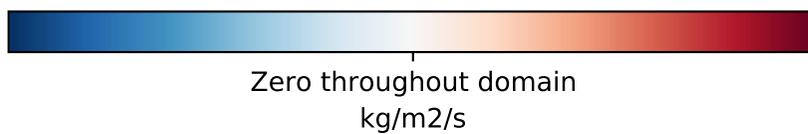
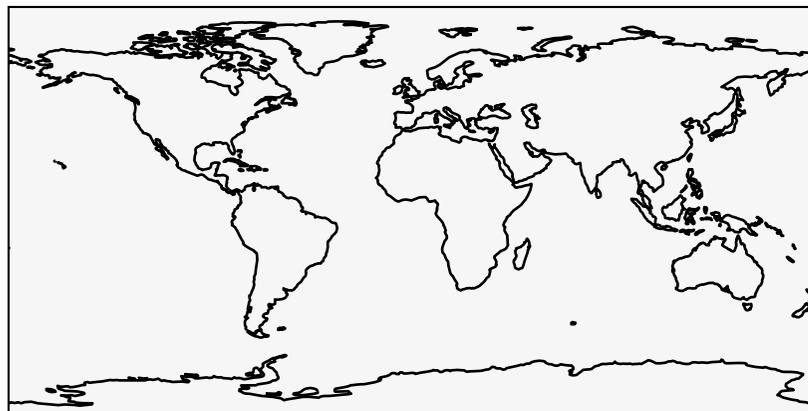
GCC\_13.0.0 (Dev)  
4.0x5.0



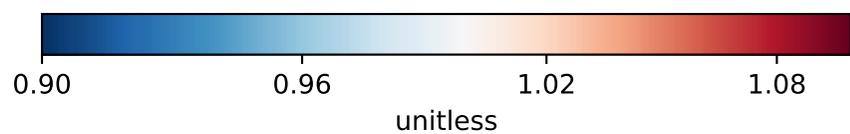
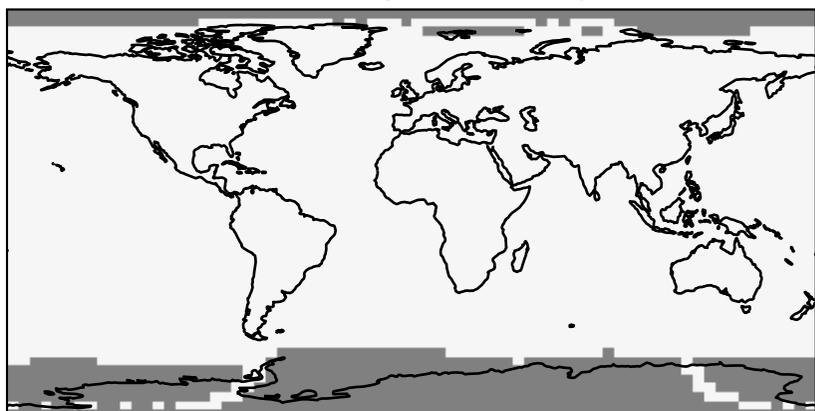
Difference  
Dev - Ref, Dynamic Range



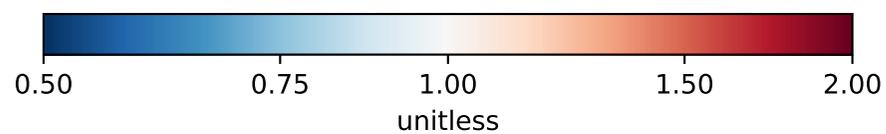
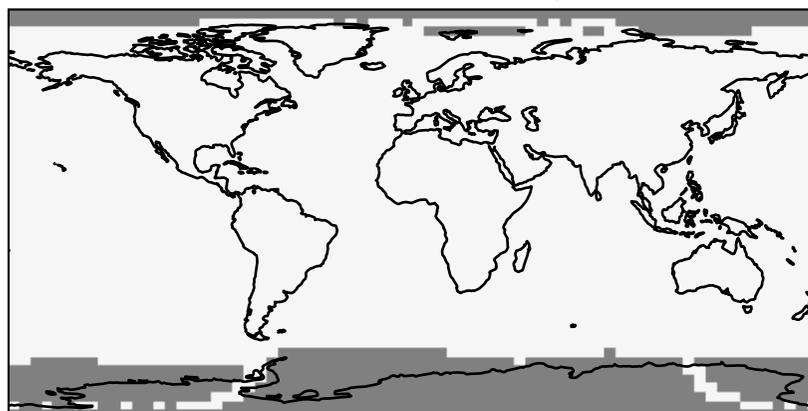
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

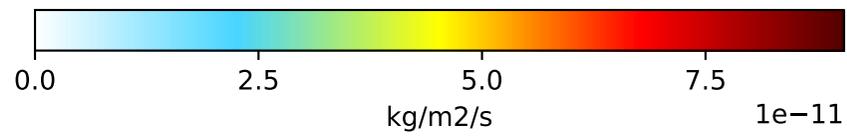
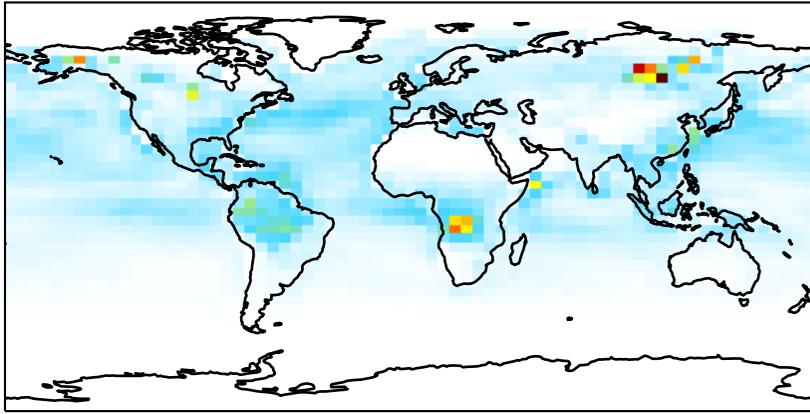


Ratio  
Dev/Ref, Fixed Range

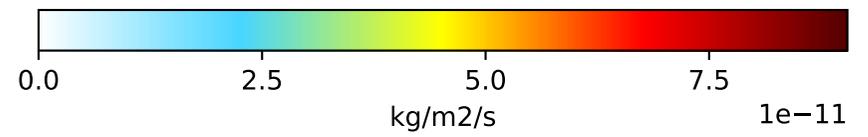
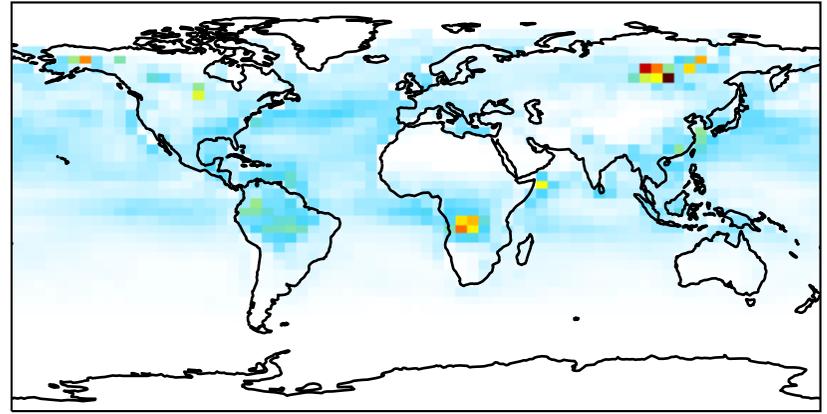


# EmisALD2\_Total (Jul2019)

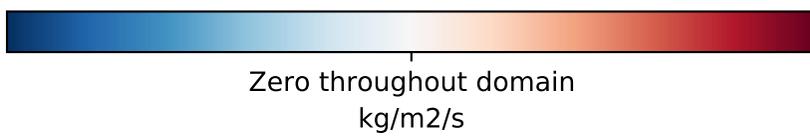
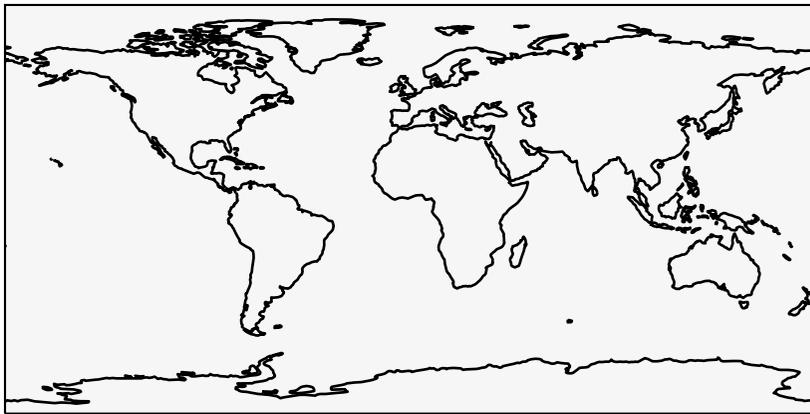
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



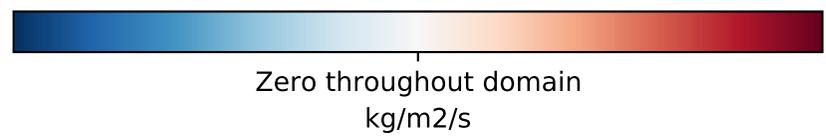
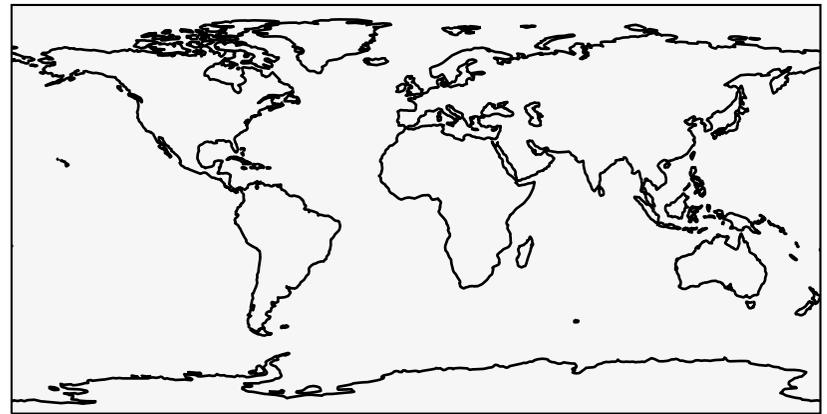
GCC\_13.0.0 (Dev)  
4.0x5.0



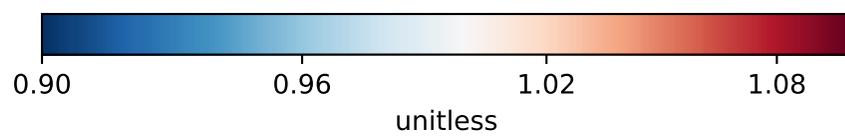
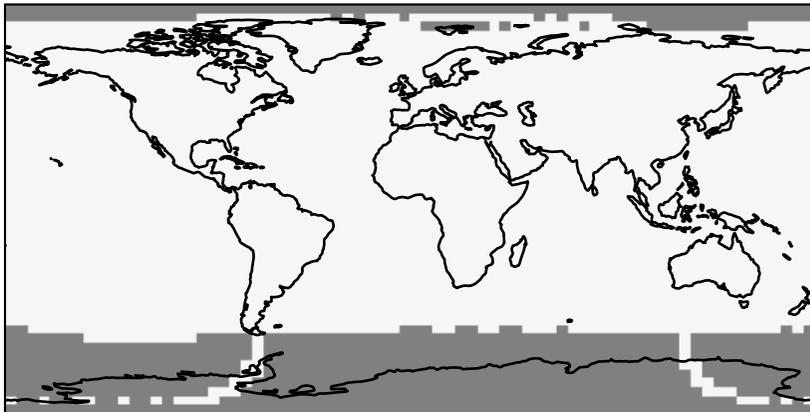
Difference  
Dev - Ref, Dynamic Range



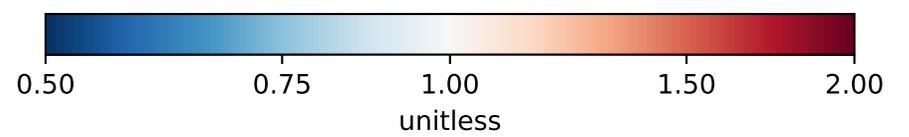
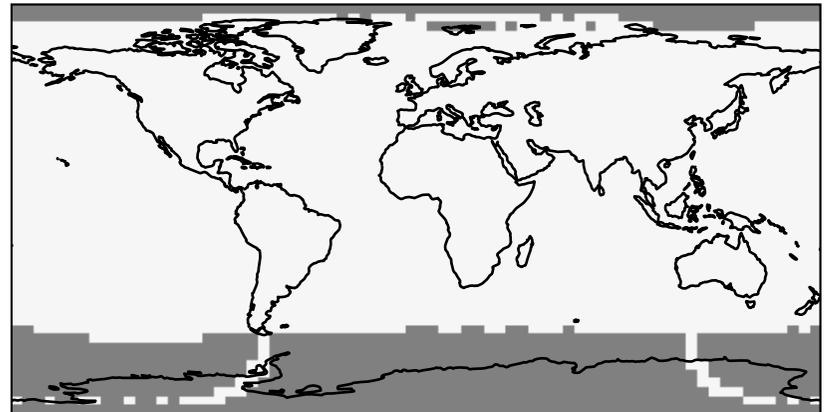
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

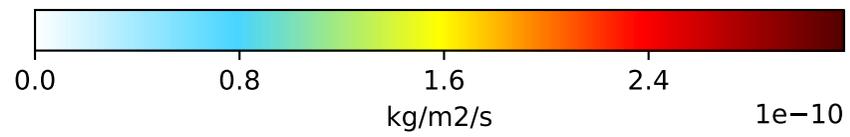
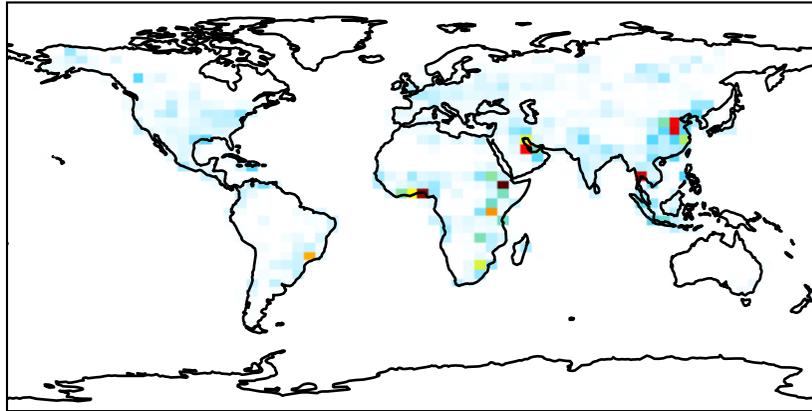


Ratio  
Dev/Ref, Fixed Range

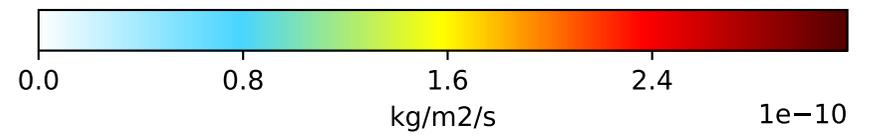
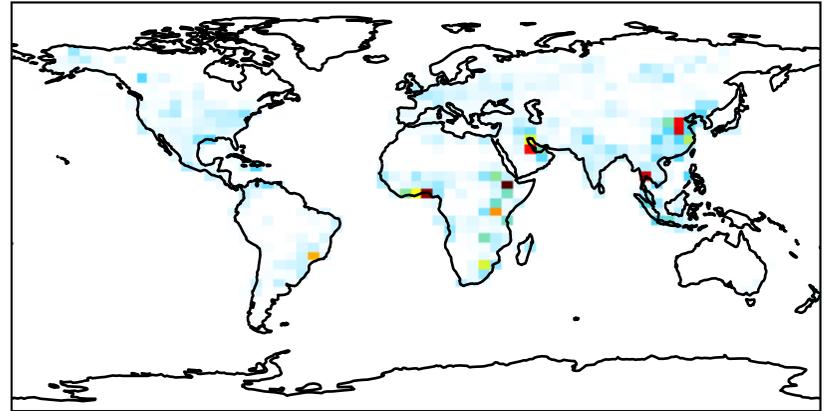


# EmisALK4\_Total (Jul2019)

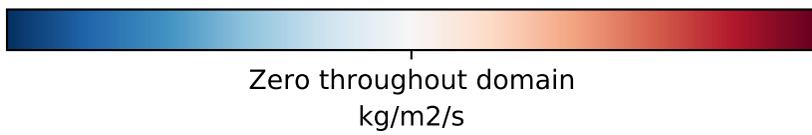
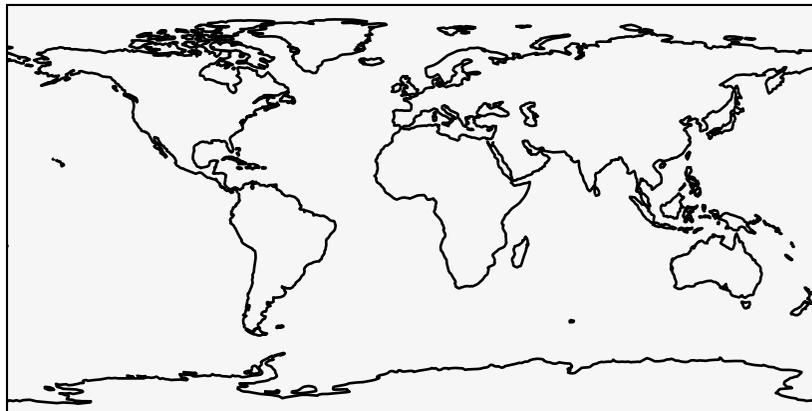
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



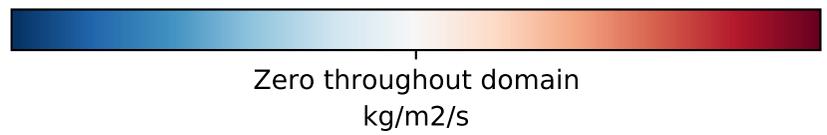
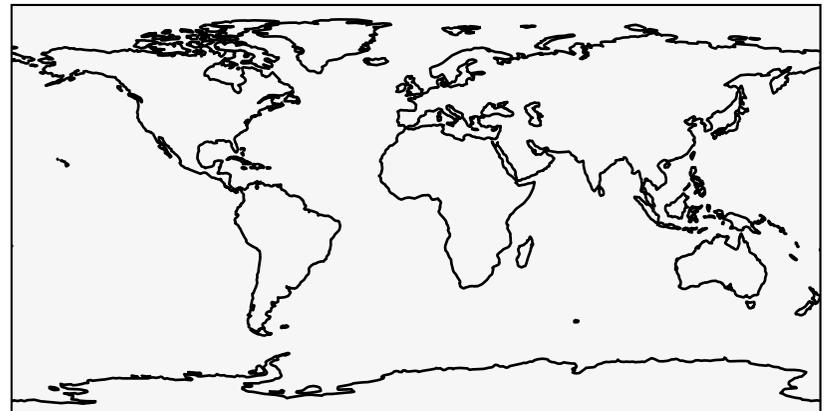
GCC\_13.0.0 (Dev)  
4.0x5.0



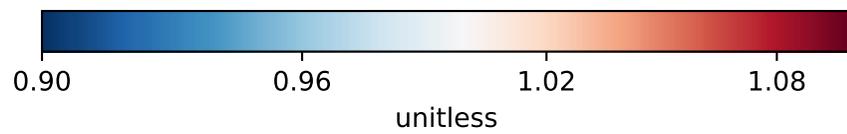
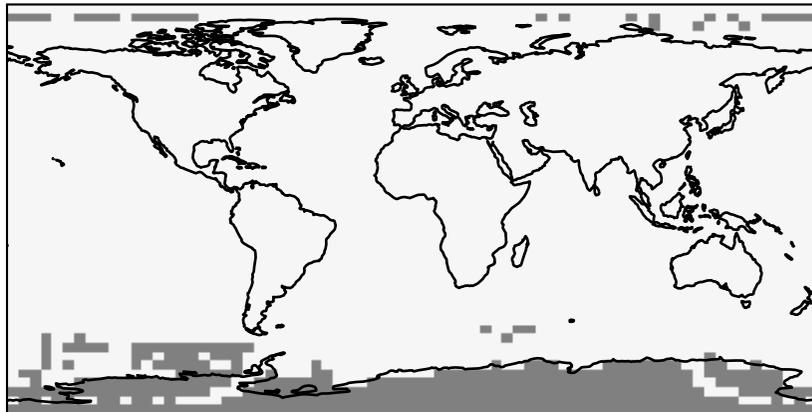
Difference  
Dev - Ref, Dynamic Range



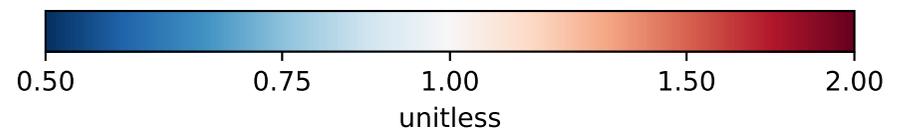
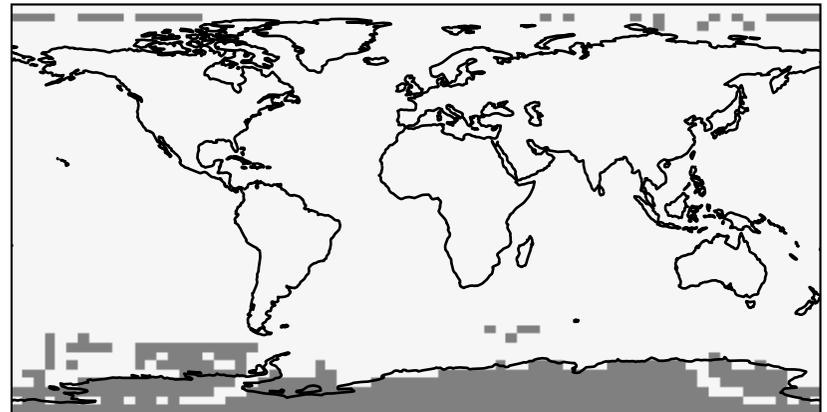
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

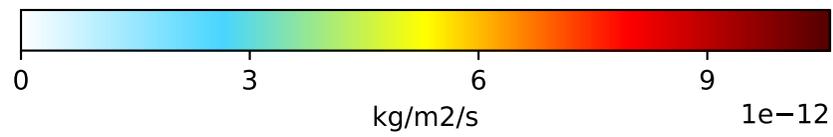
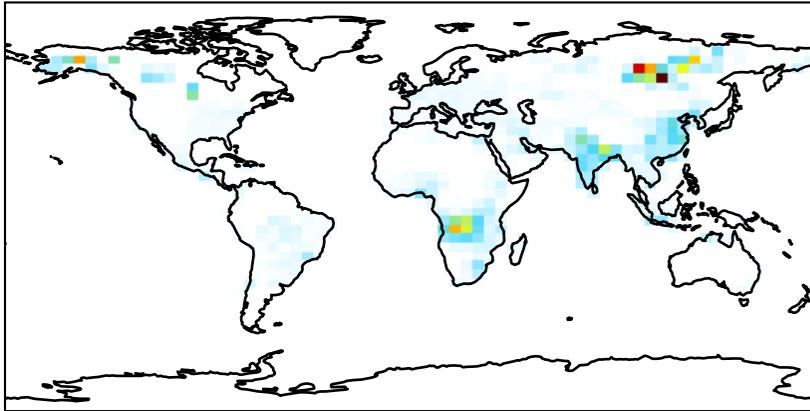


Ratio  
Dev/Ref, Fixed Range

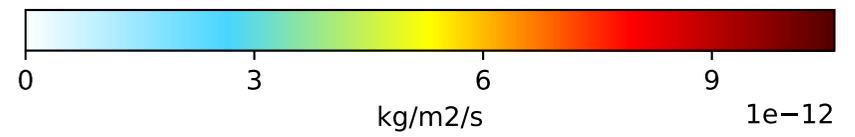
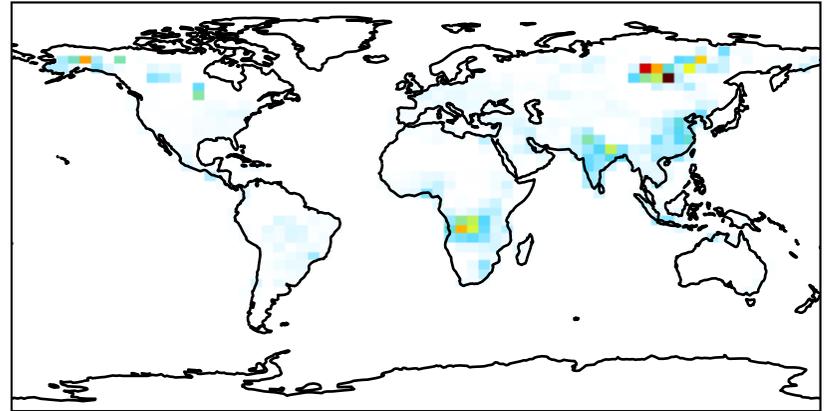


# EmisBCPI\_Total (Jul2019)

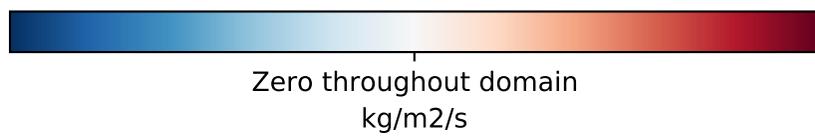
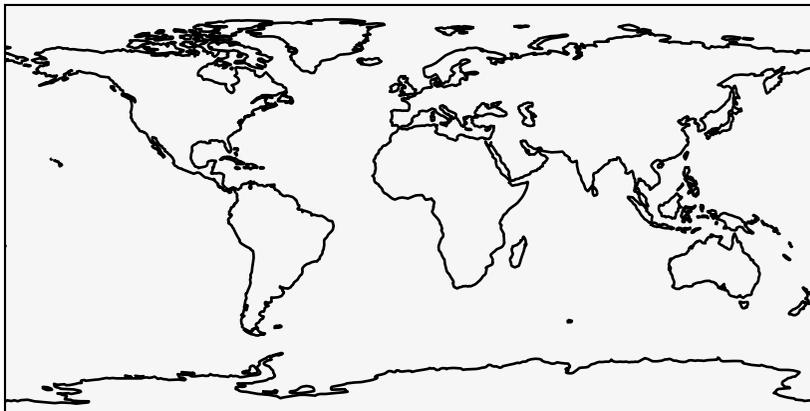
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



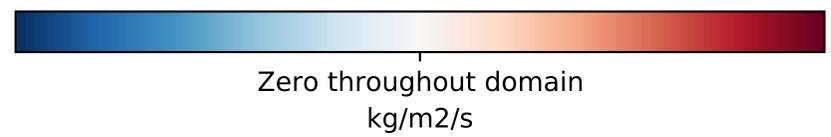
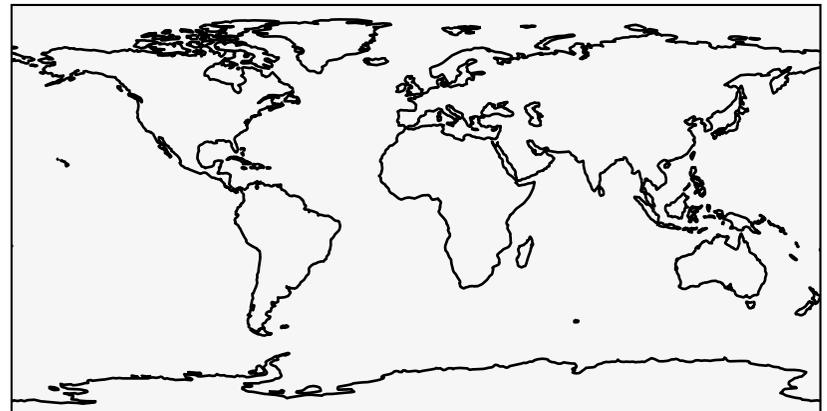
GCC\_13.0.0 (Dev)  
4.0x5.0



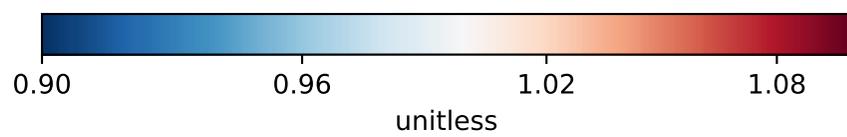
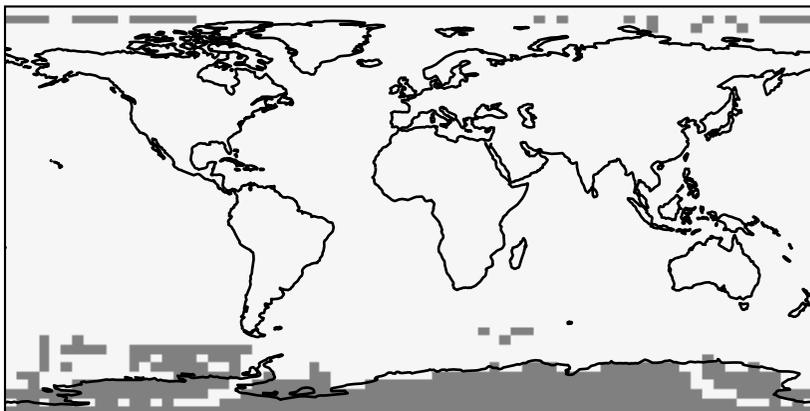
Difference  
Dev - Ref, Dynamic Range



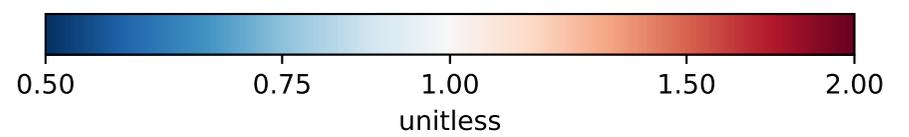
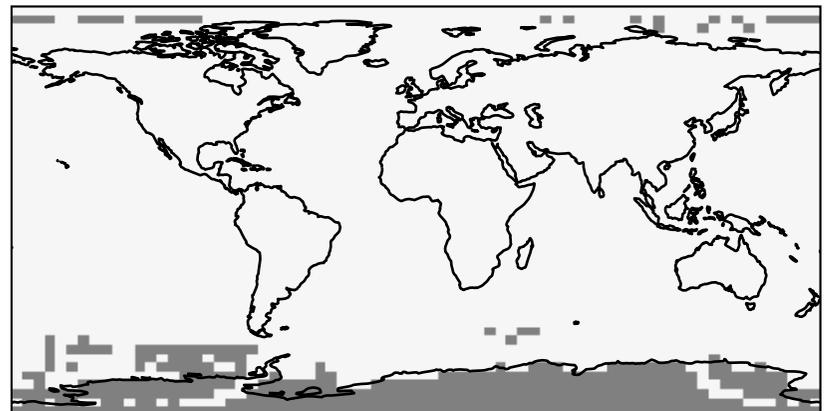
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

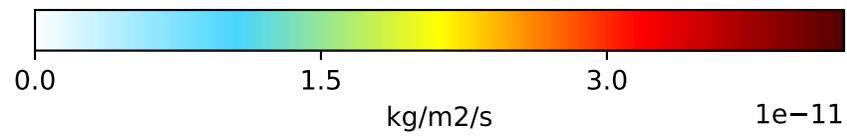
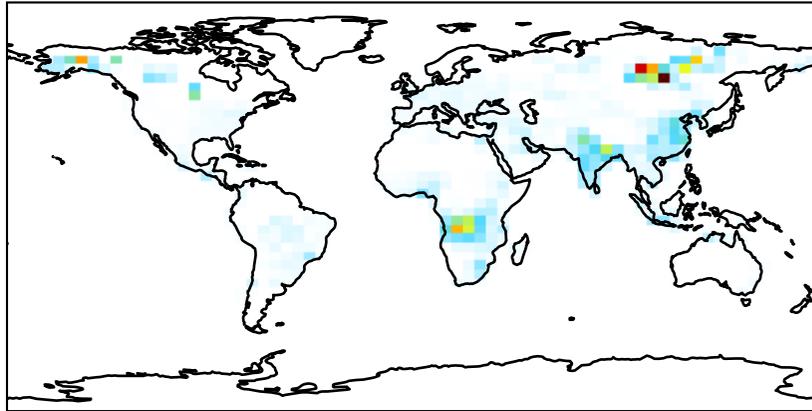


Ratio  
Dev/Ref, Fixed Range

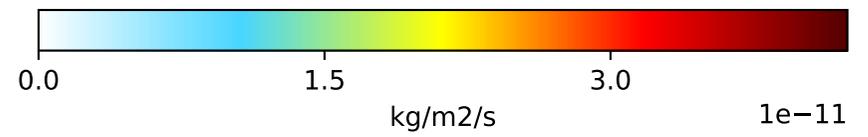
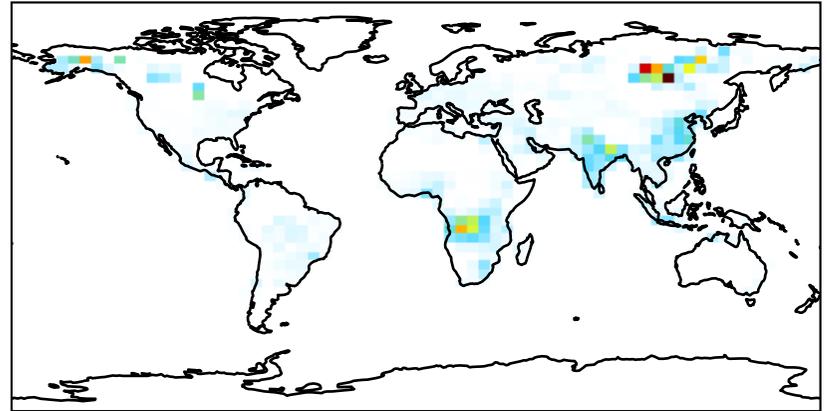


# EmisBCPO\_Total (Jul2019)

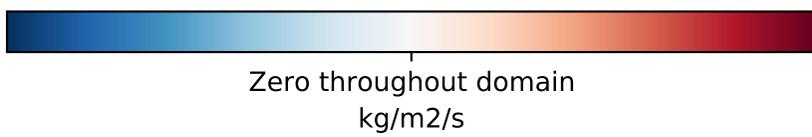
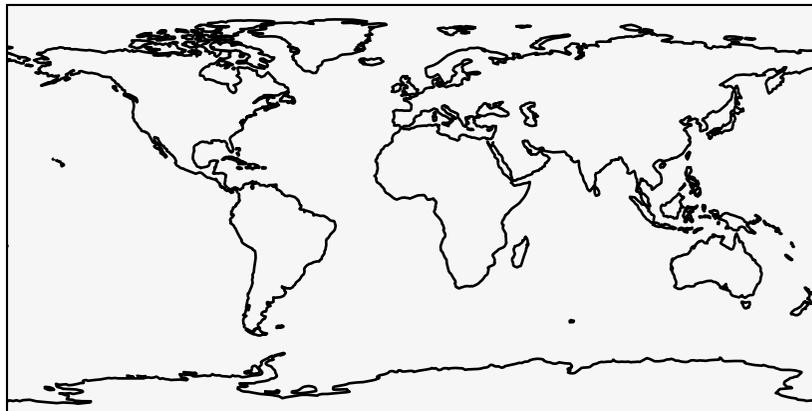
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



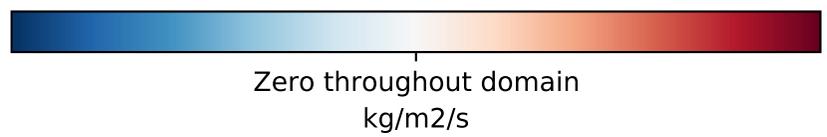
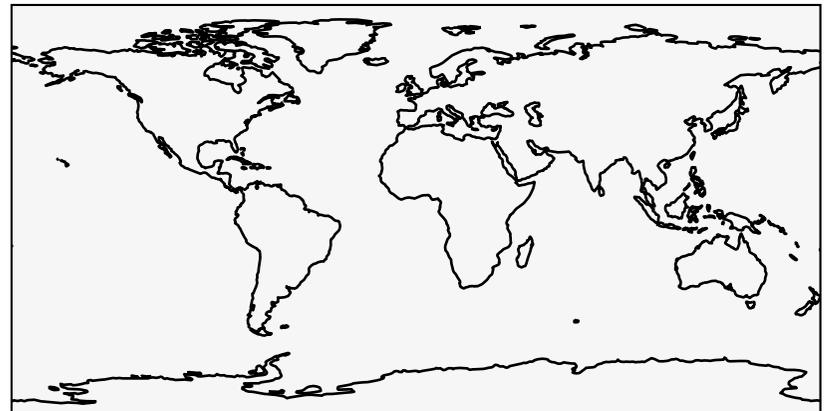
GCC\_13.0.0 (Dev)  
4.0x5.0



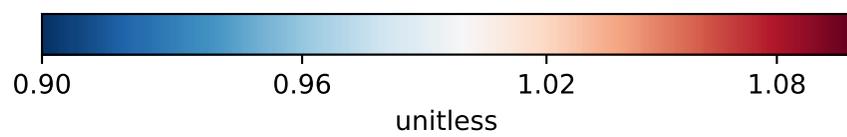
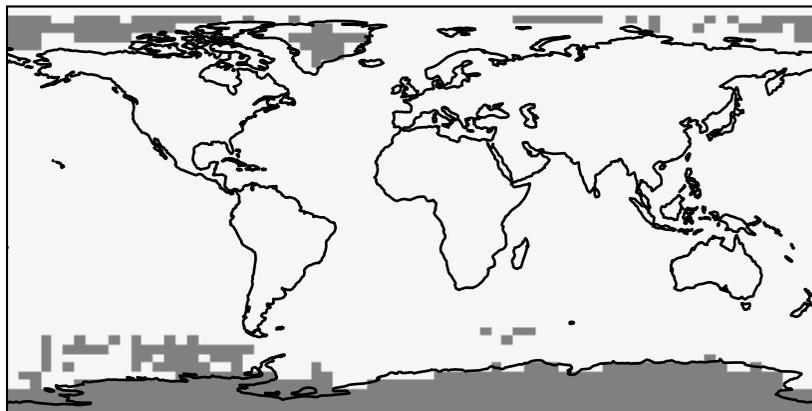
Difference  
Dev - Ref, Dynamic Range



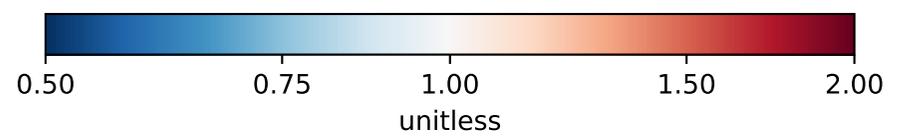
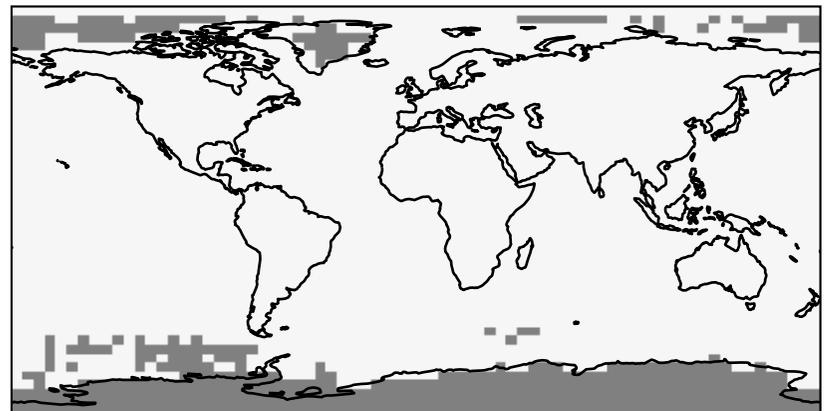
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

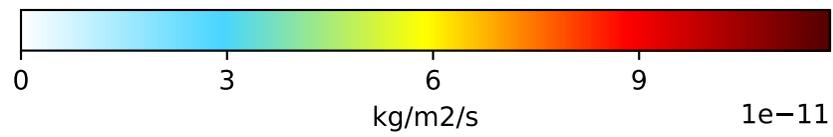
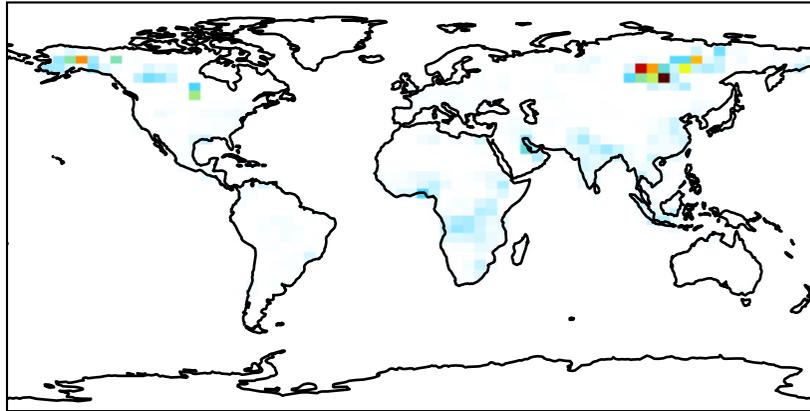


Ratio  
Dev/Ref, Fixed Range

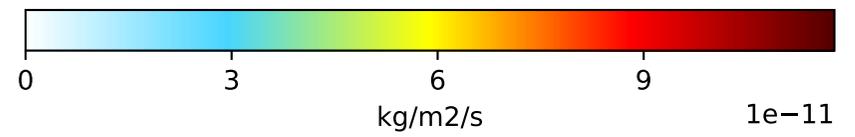
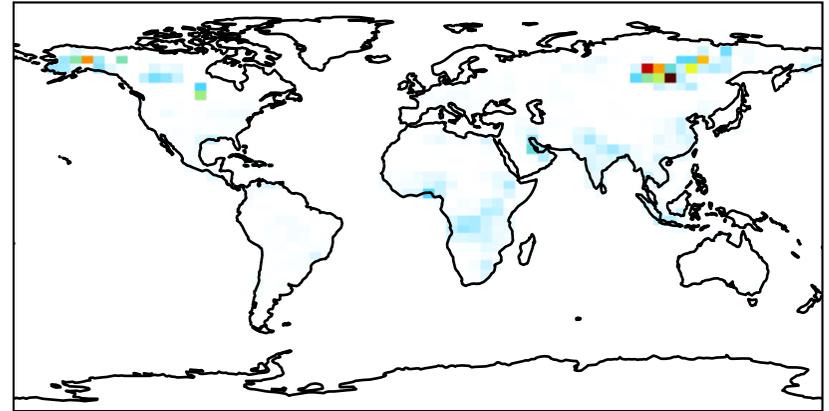


# EmisBENZ\_Total (Jul2019)

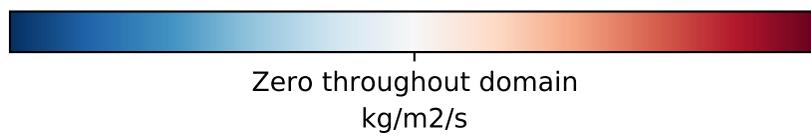
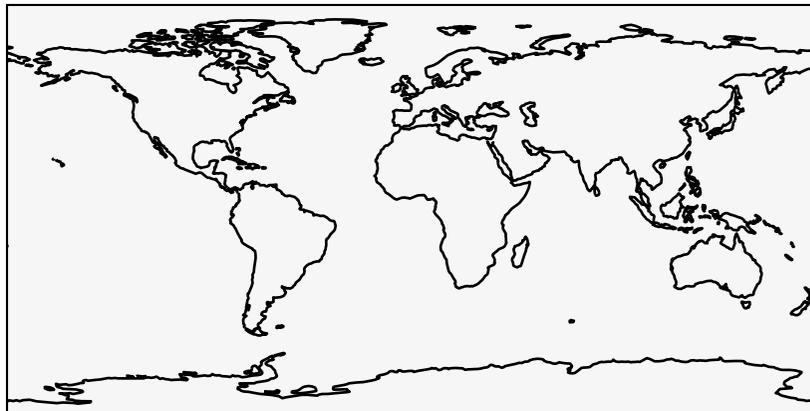
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



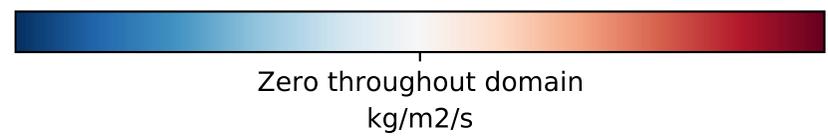
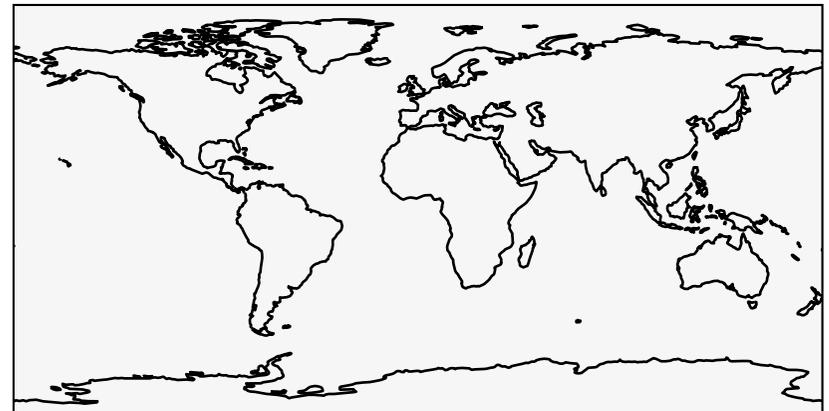
GCC\_13.0.0 (Dev)  
4.0x5.0



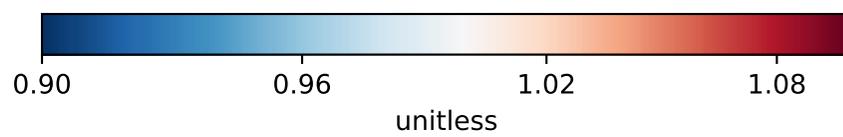
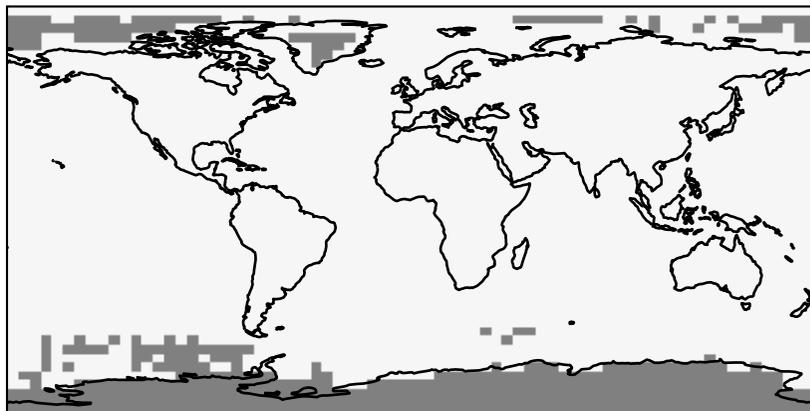
Difference  
Dev - Ref, Dynamic Range



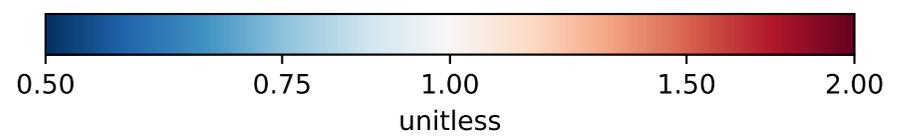
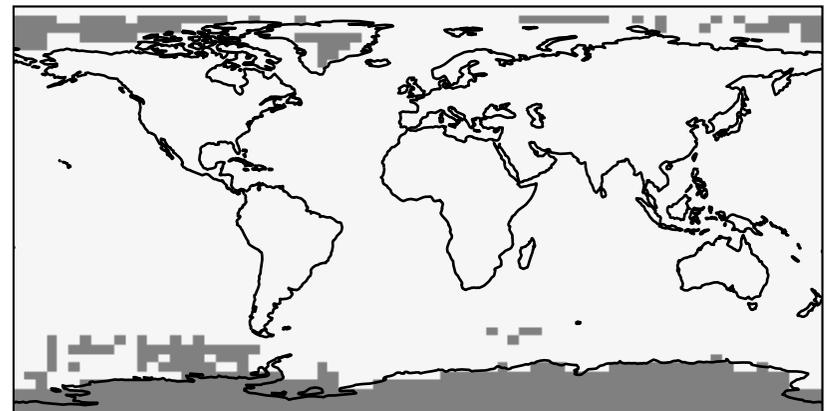
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

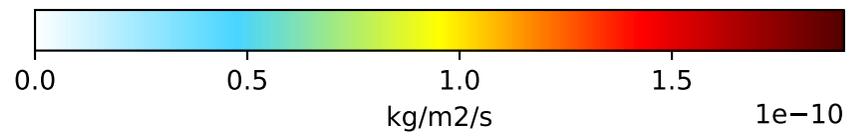
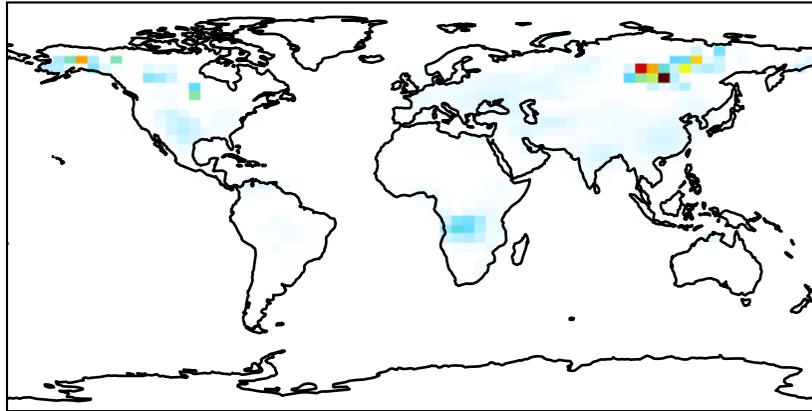


Ratio  
Dev/Ref, Fixed Range

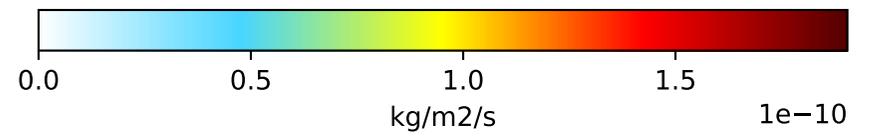
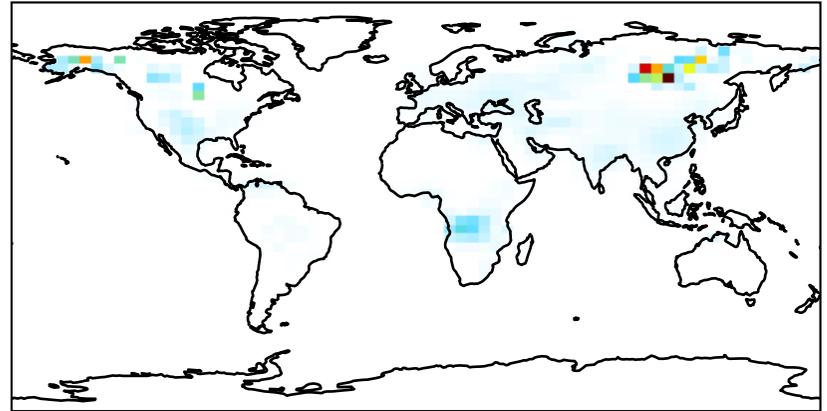


# EmisC2H6\_Total (Jul2019)

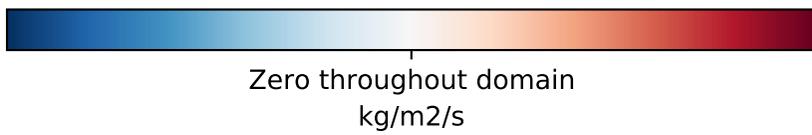
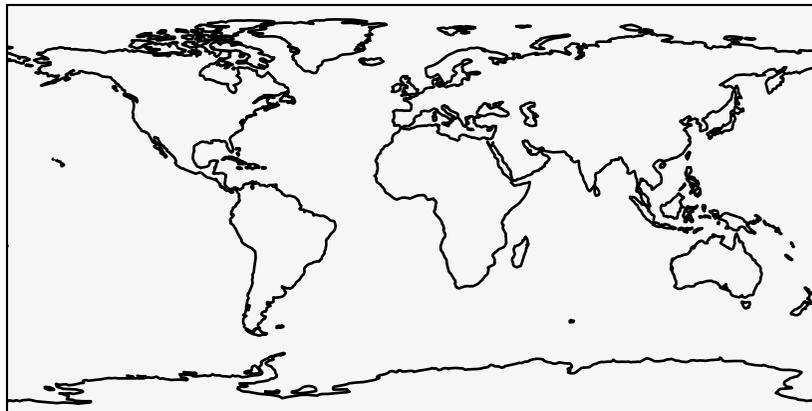
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



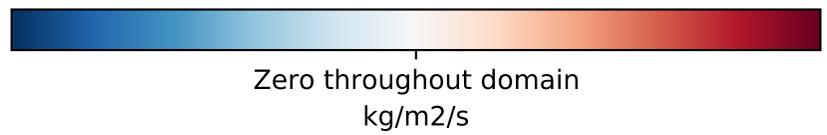
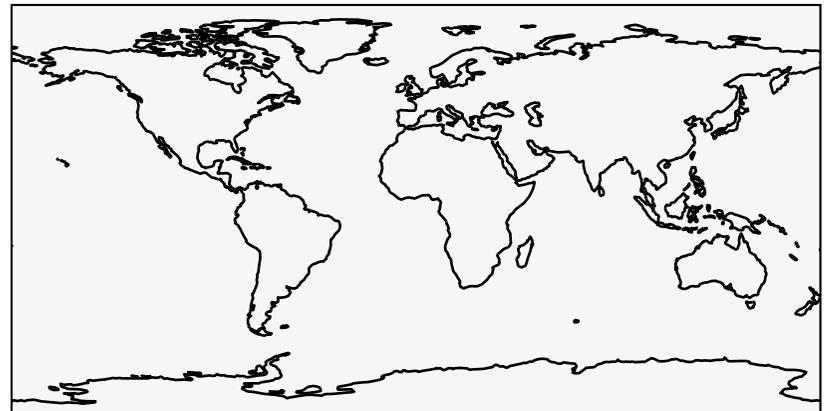
GCC\_13.0.0 (Dev)  
4.0x5.0



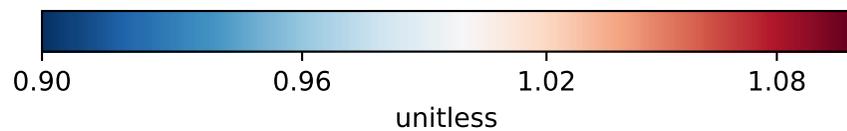
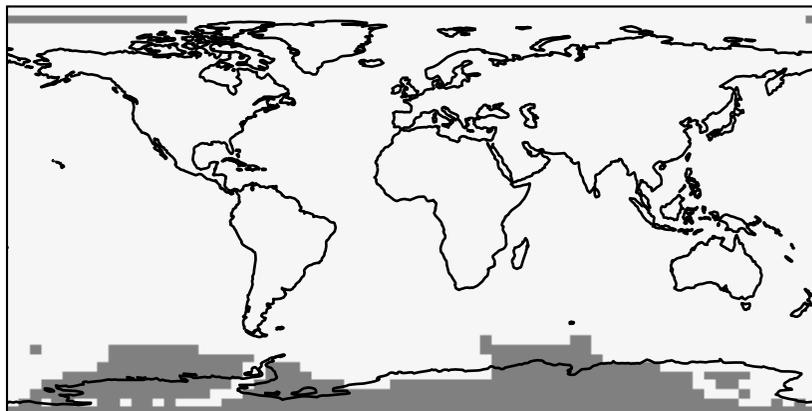
Difference  
Dev - Ref, Dynamic Range



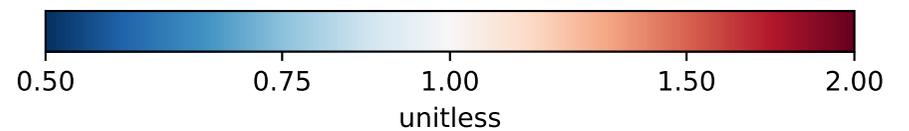
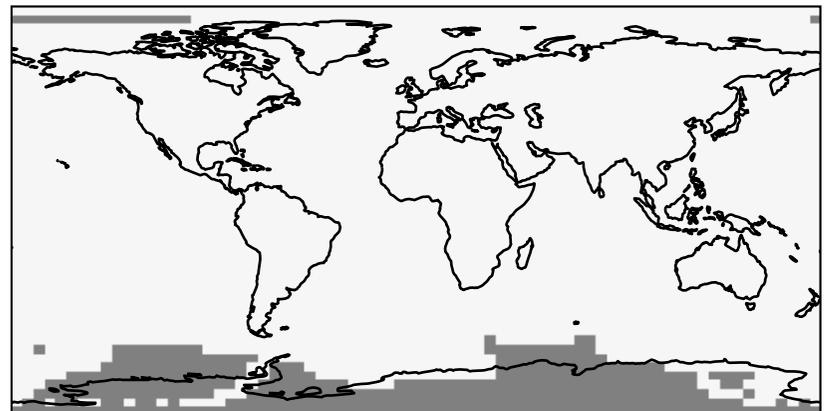
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

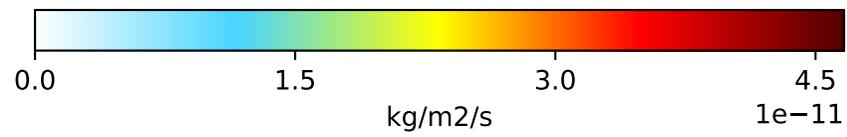
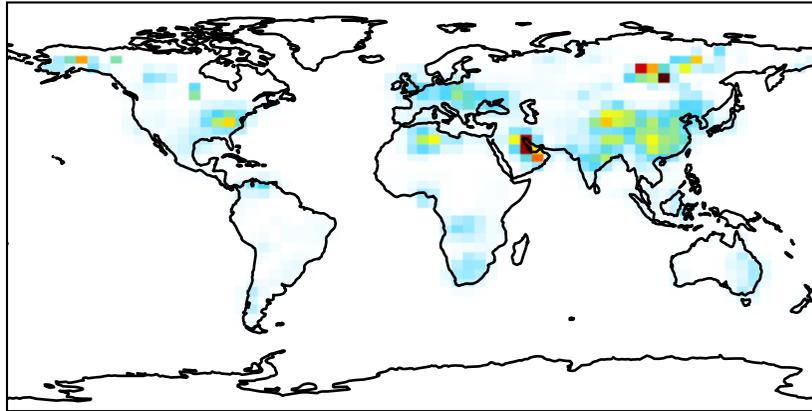


Ratio  
Dev/Ref, Fixed Range

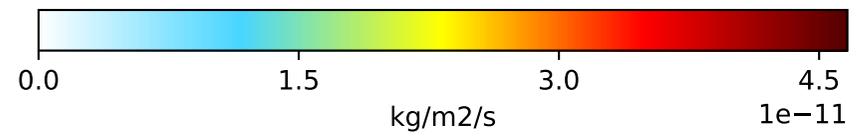
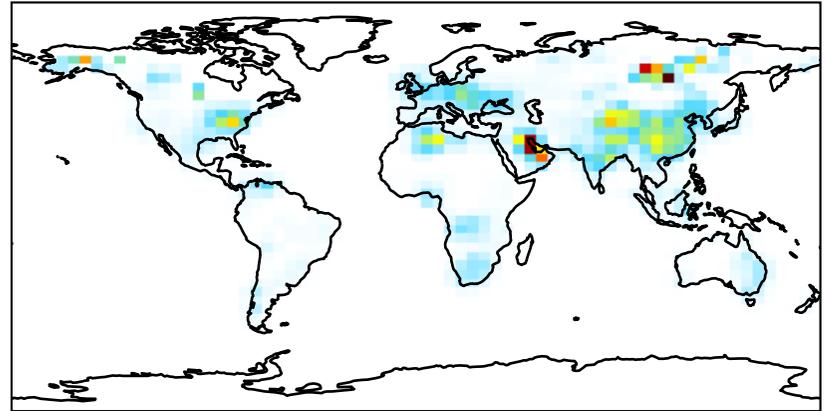


# EmisC3H8\_Total (Jul2019)

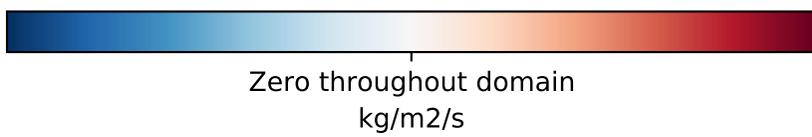
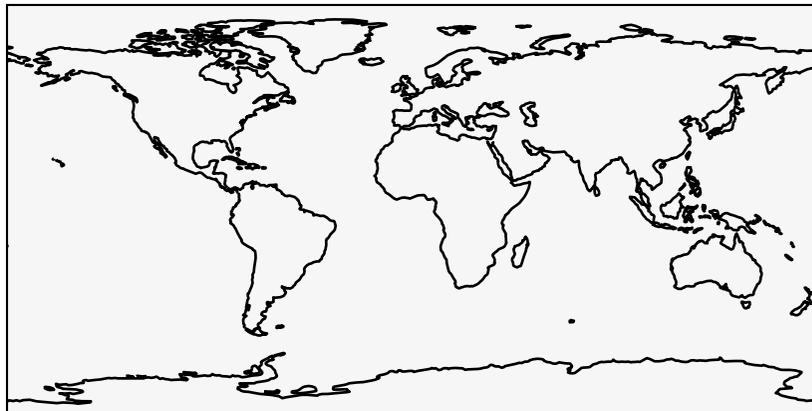
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



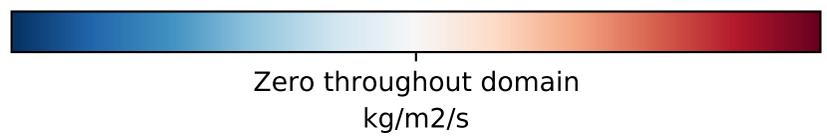
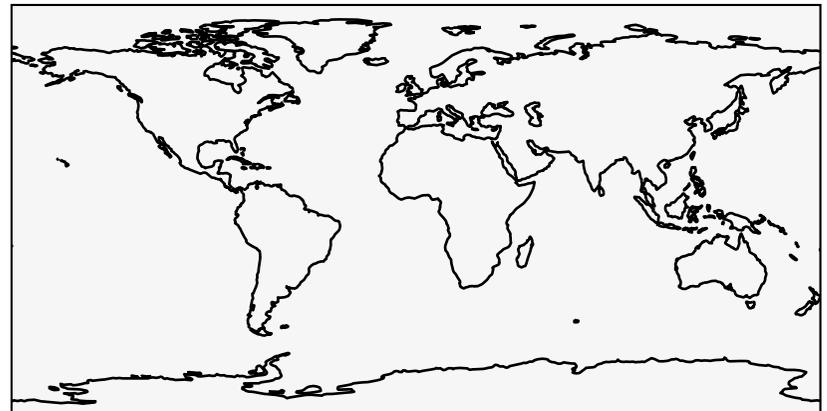
GCC\_13.0.0 (Dev)  
4.0x5.0



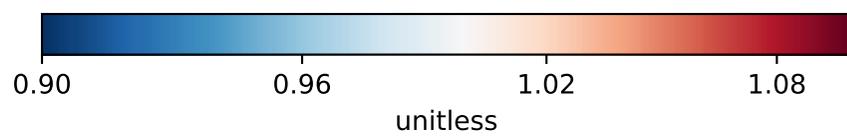
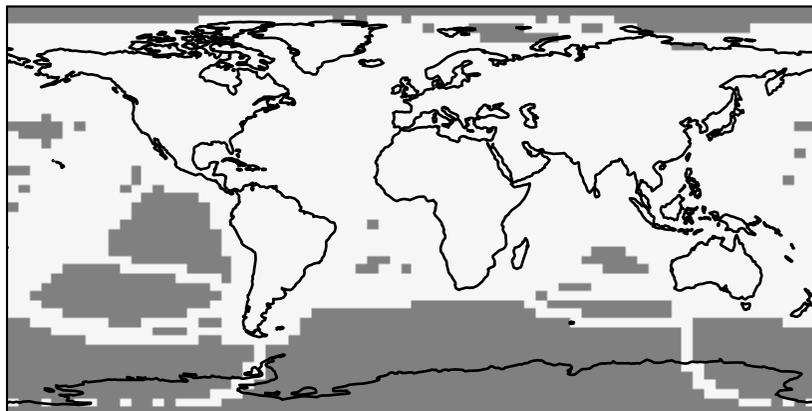
Difference  
Dev - Ref, Dynamic Range



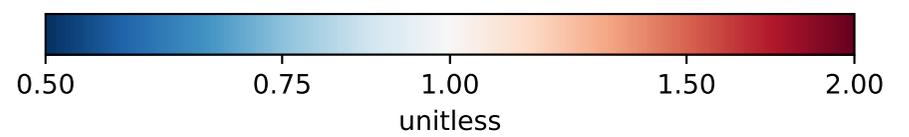
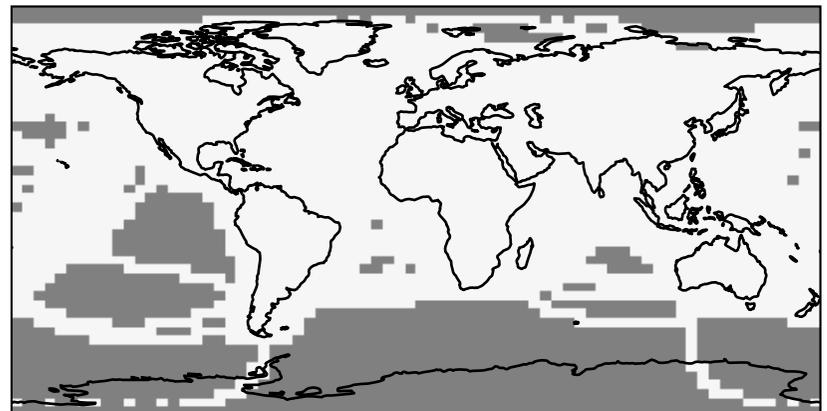
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

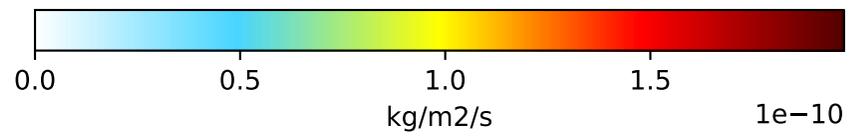
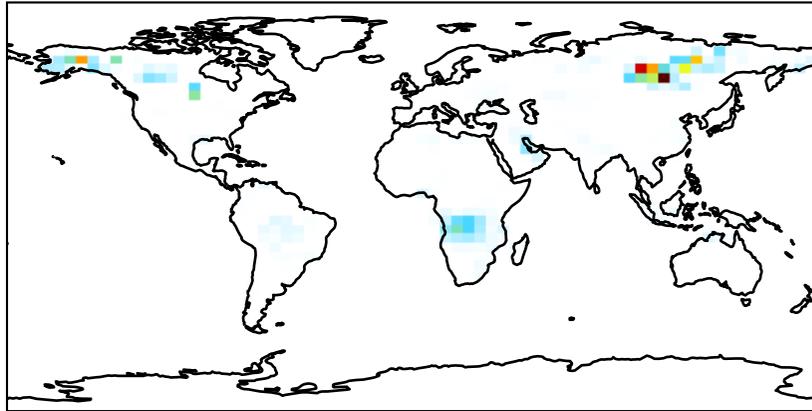


Ratio  
Dev/Ref, Fixed Range

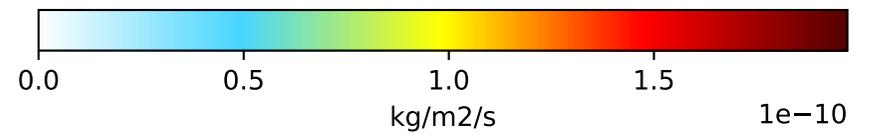
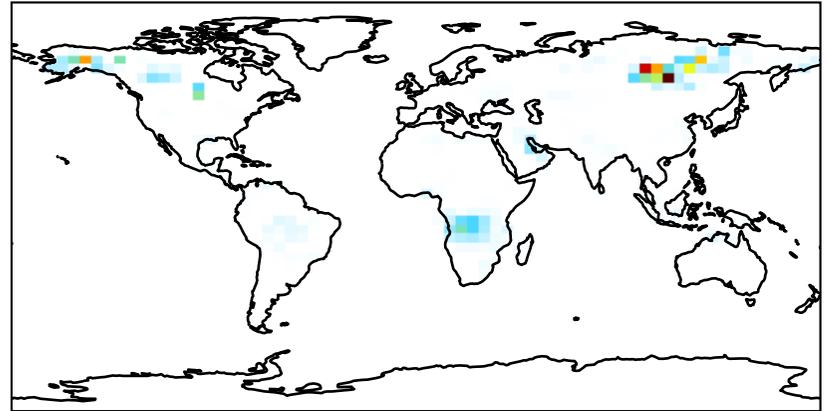


# EmisCH2O\_Total (Jul2019)

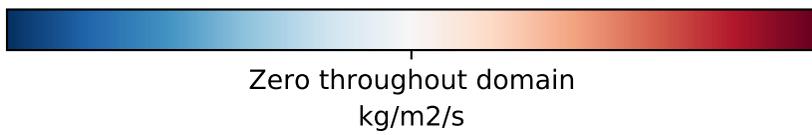
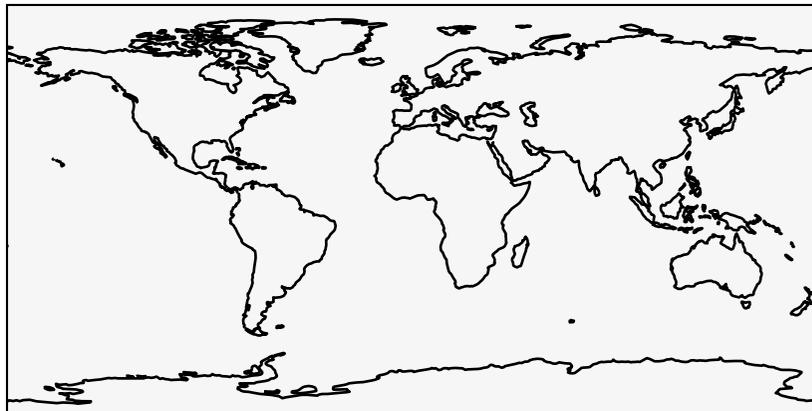
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



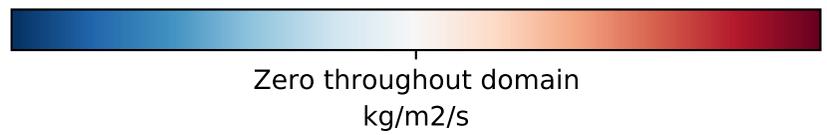
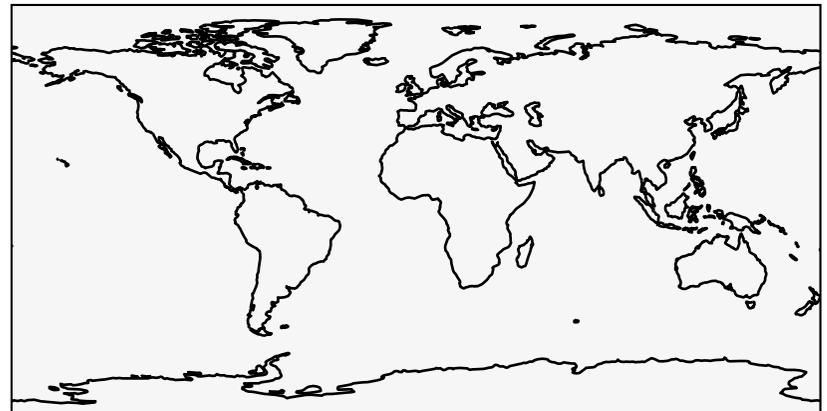
GCC\_13.0.0 (Dev)  
4.0x5.0



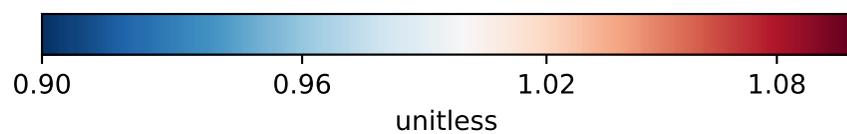
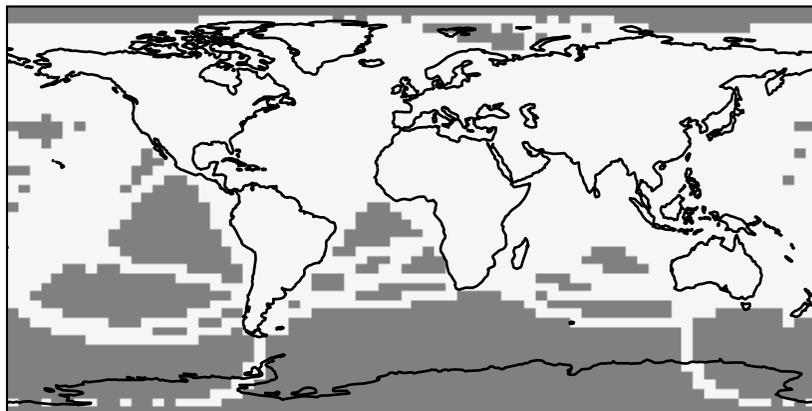
Difference  
Dev - Ref, Dynamic Range



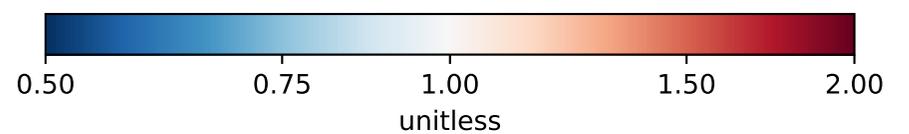
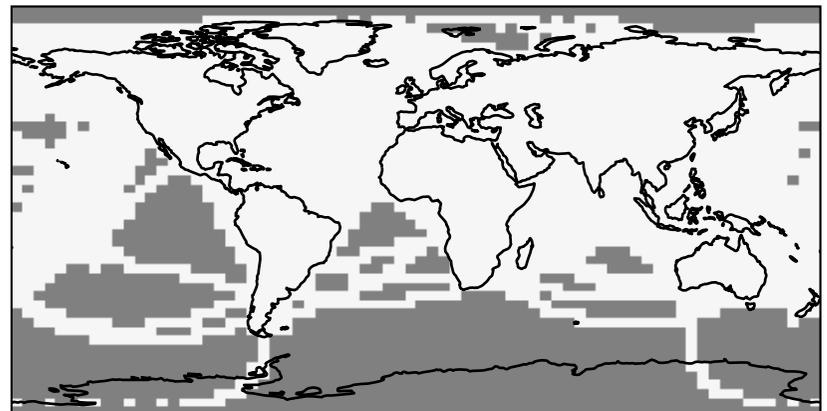
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

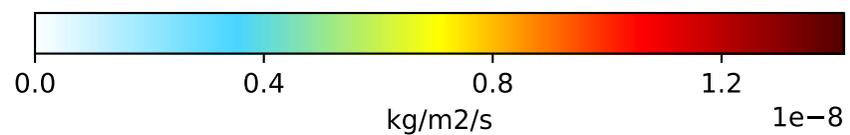
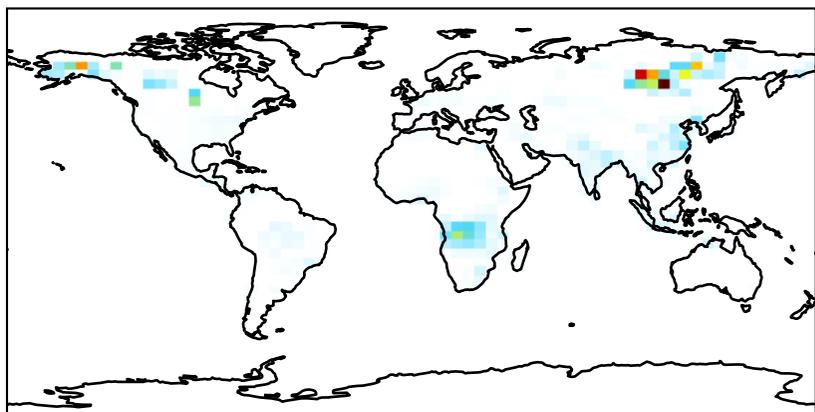


Ratio  
Dev/Ref, Fixed Range

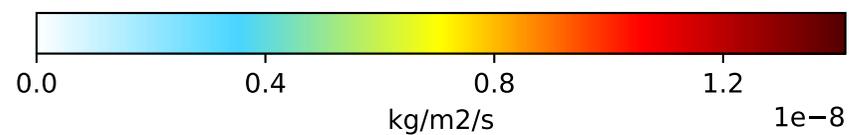
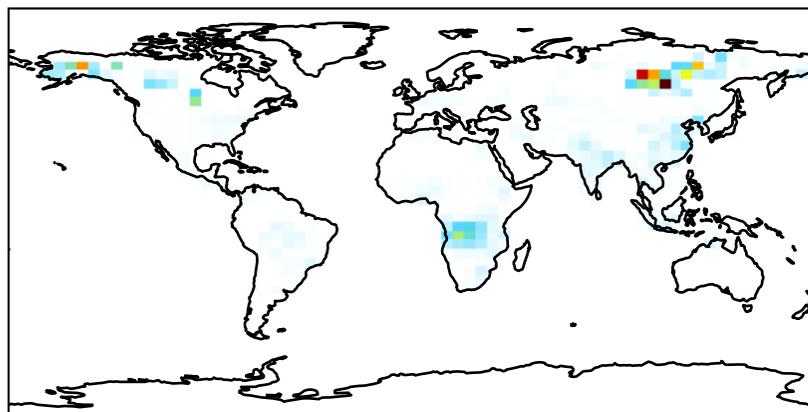


# EmisCO\_Total (Jul2019)

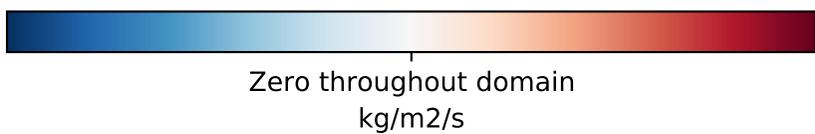
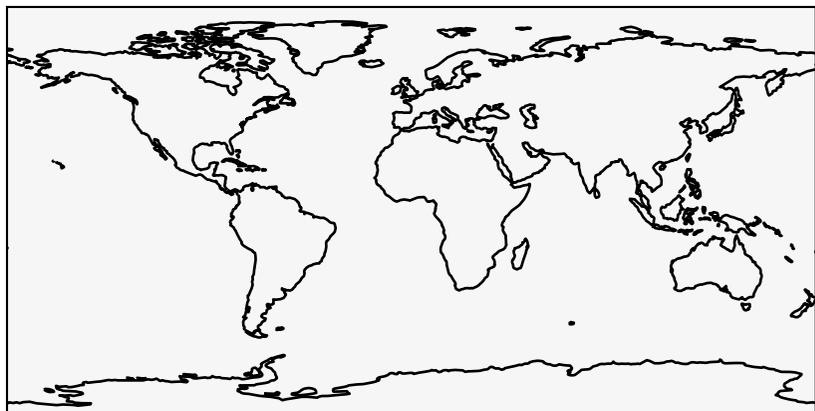
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



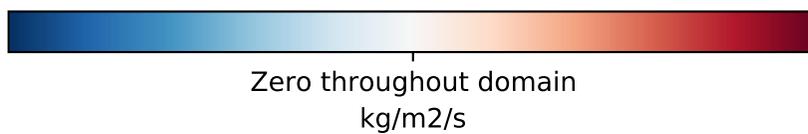
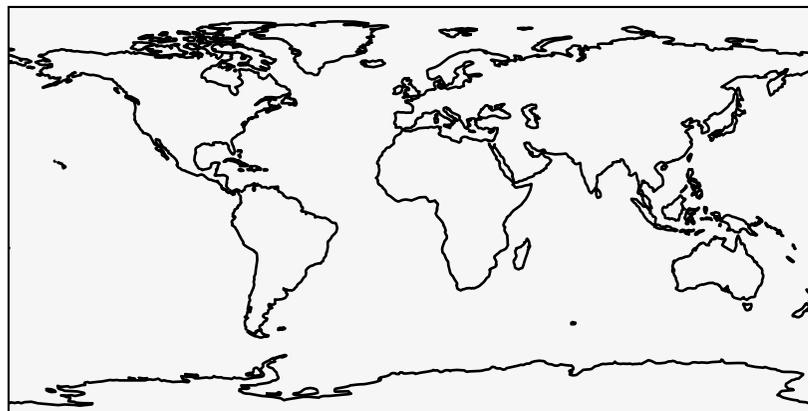
GCC\_13.0.0 (Dev)  
4.0x5.0



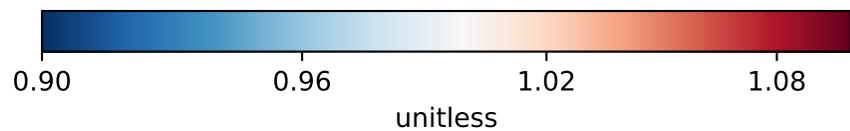
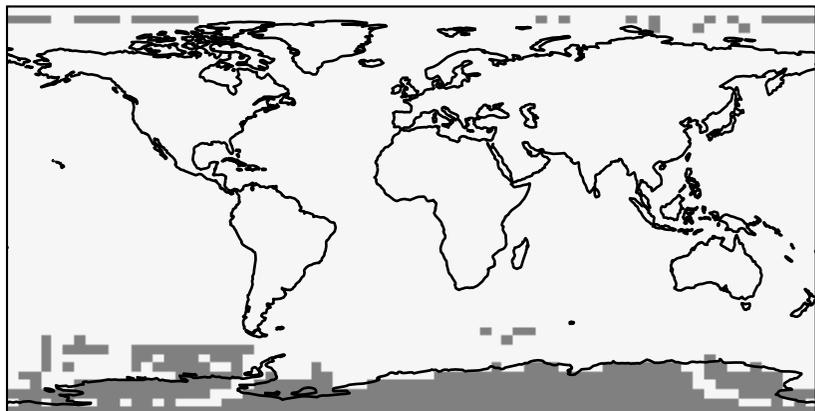
Difference  
Dev - Ref, Dynamic Range



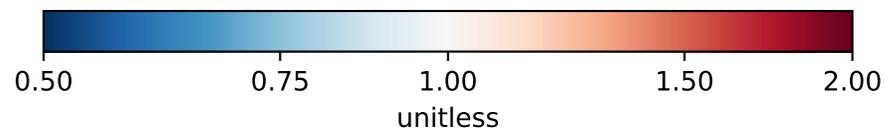
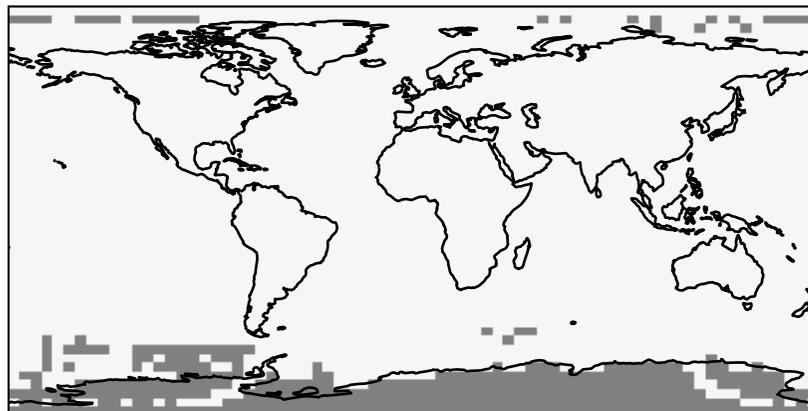
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

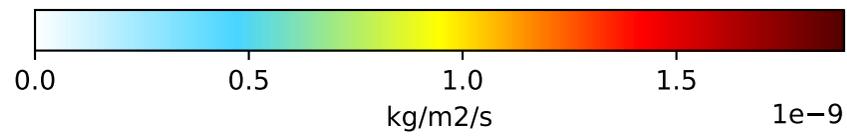
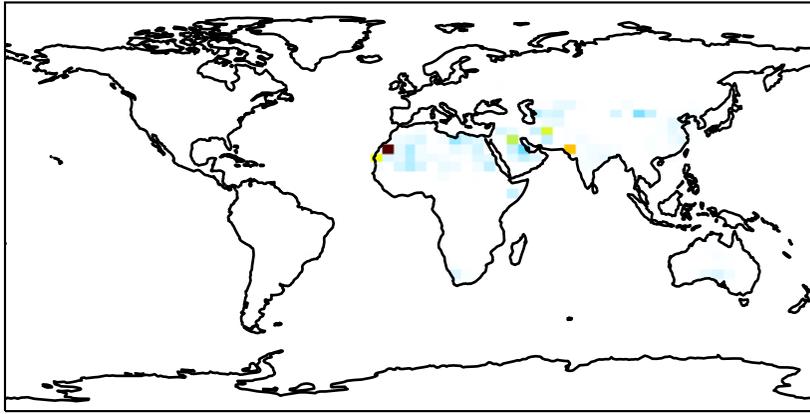


Ratio  
Dev/Ref, Fixed Range

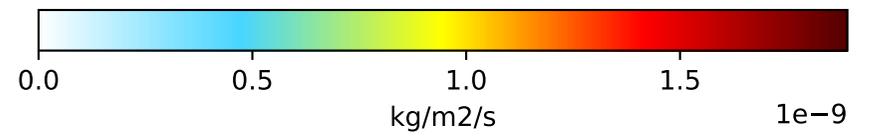
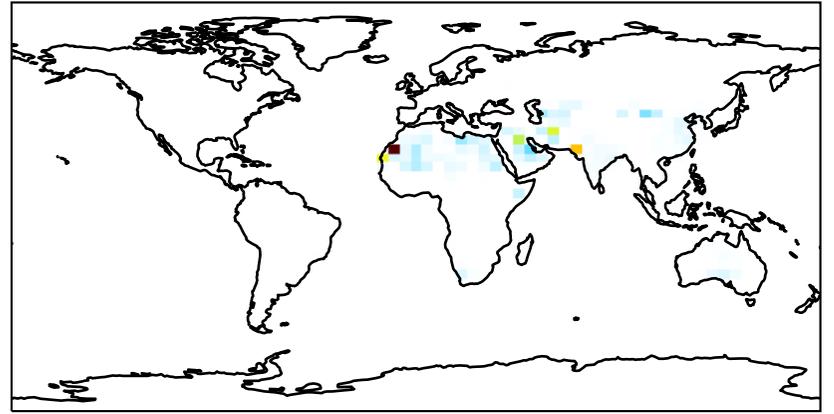


# EmisDST1\_Total (Jul2019)

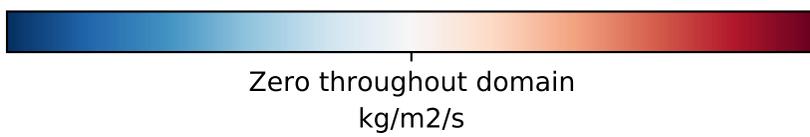
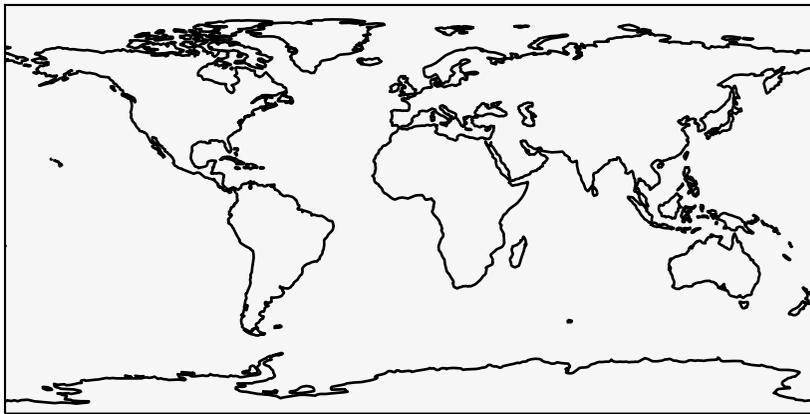
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



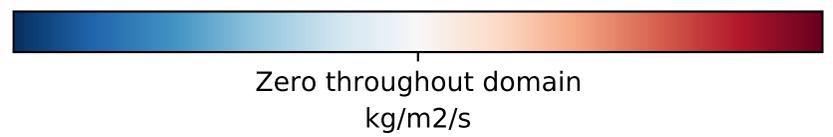
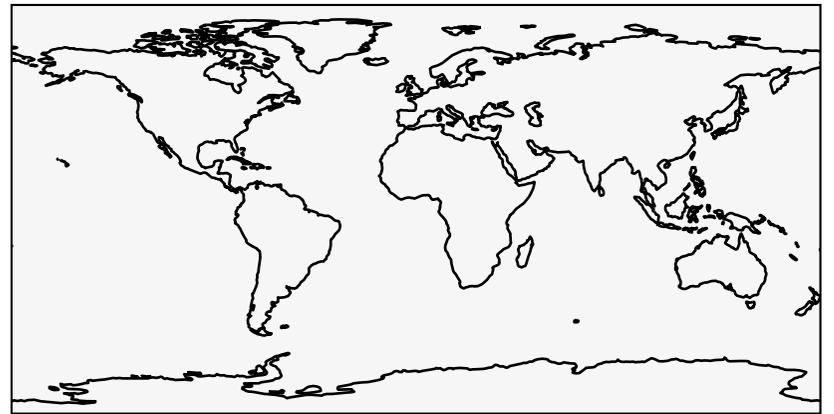
GCC\_13.0.0 (Dev)  
4.0x5.0



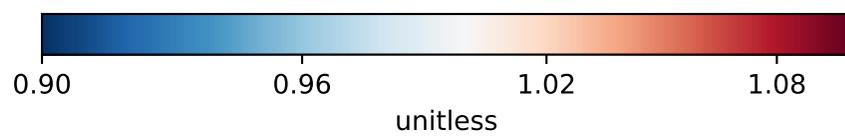
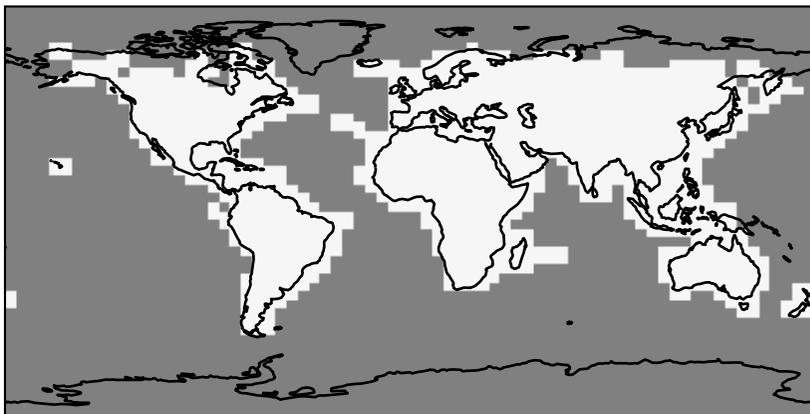
Difference  
Dev - Ref, Dynamic Range



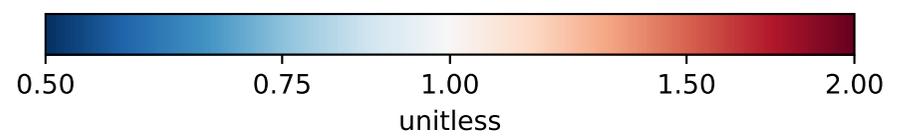
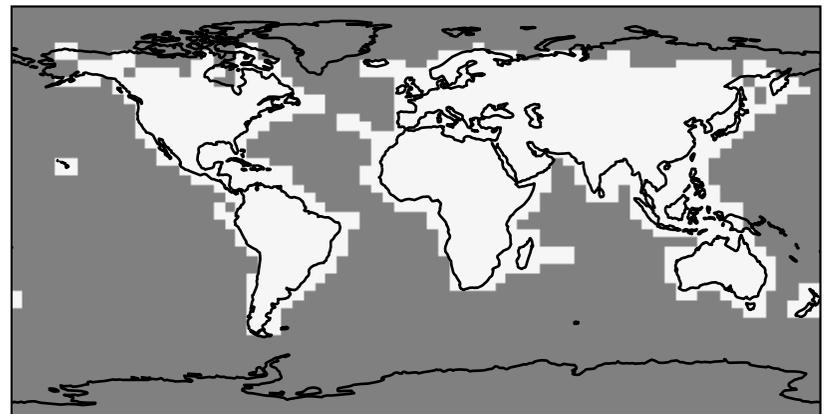
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

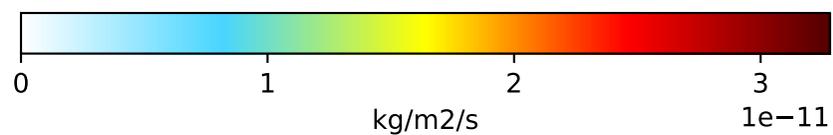
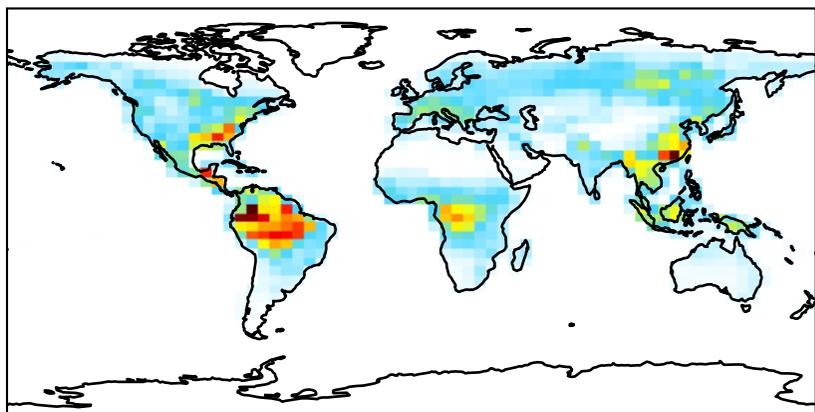


Ratio  
Dev/Ref, Fixed Range

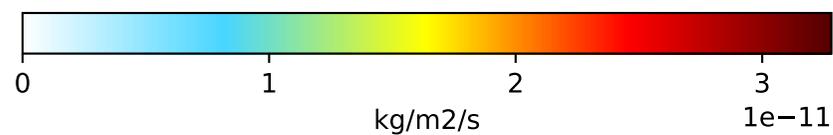
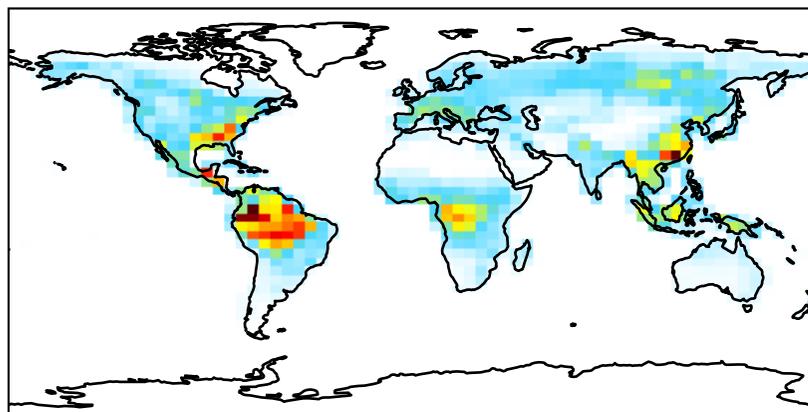


# EmisEOH\_Total (Jul2019)

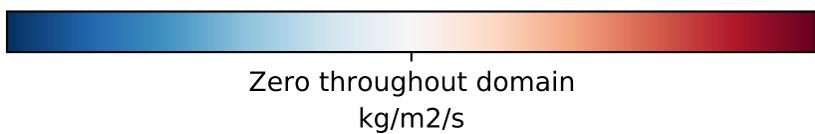
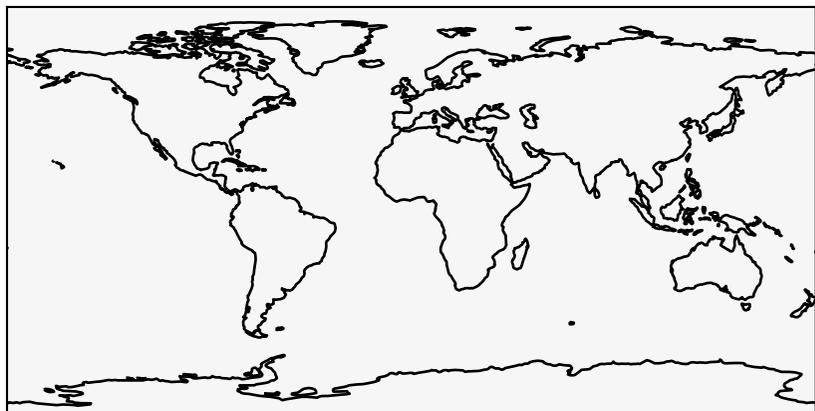
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



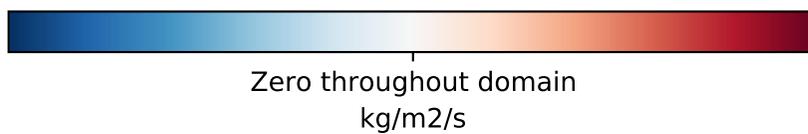
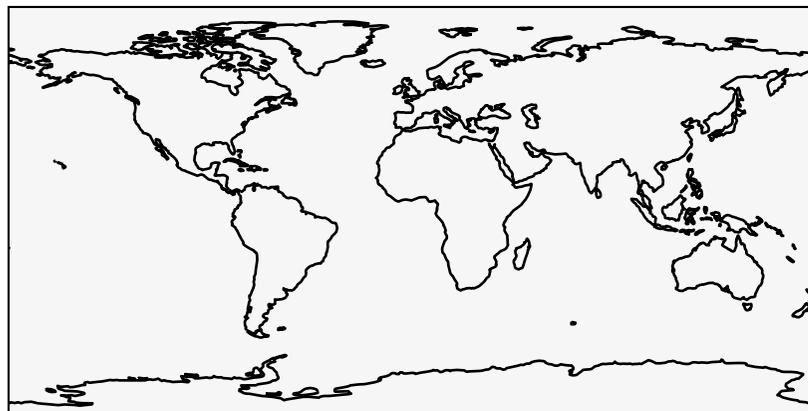
GCC\_13.0.0 (Dev)  
4.0x5.0



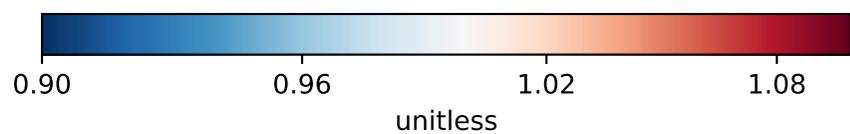
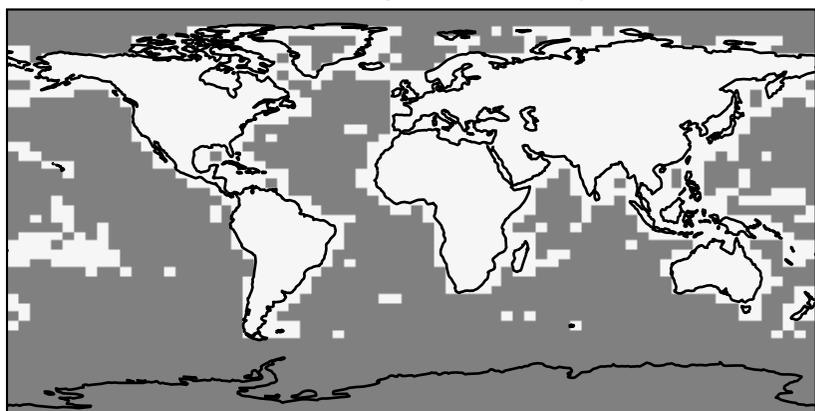
Difference  
Dev - Ref, Dynamic Range



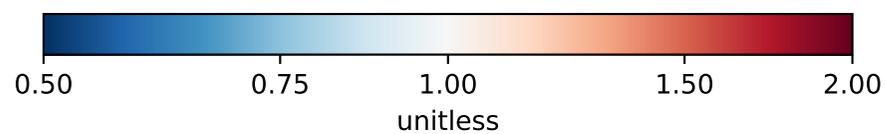
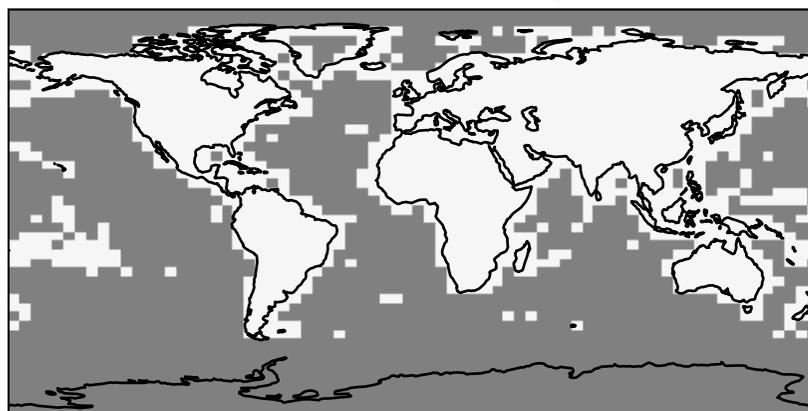
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

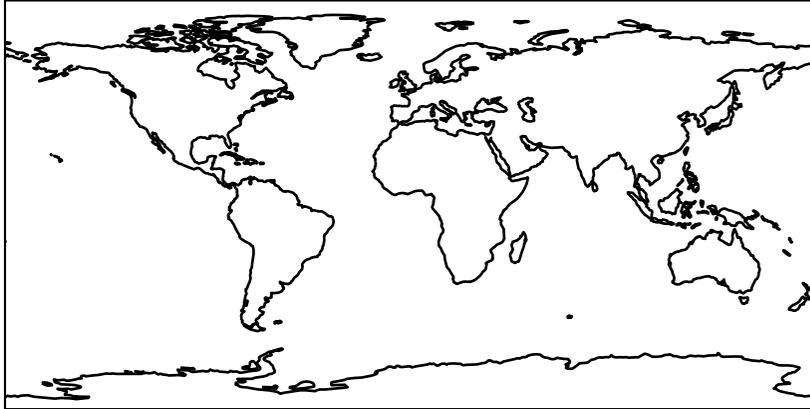


Ratio  
Dev/Ref, Fixed Range



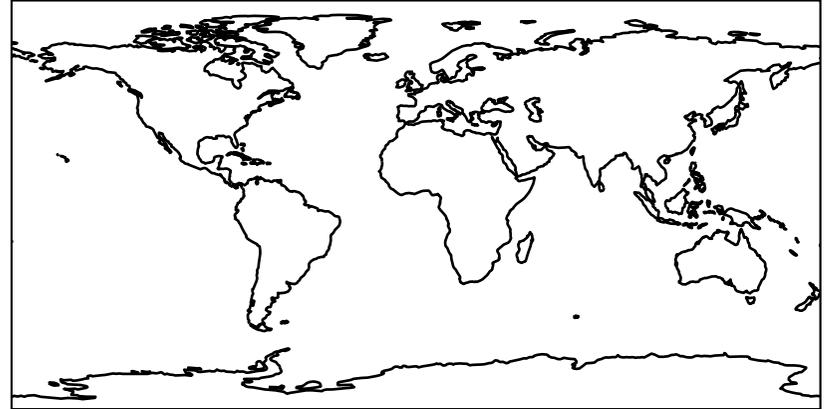
# EmisGLYC\_Total (Jul2019)

GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



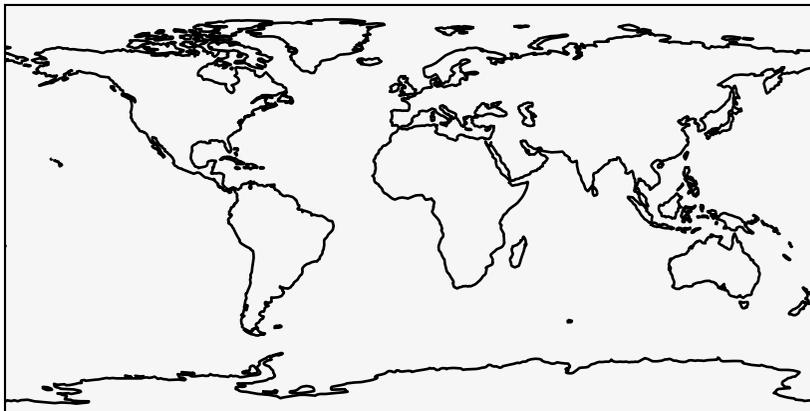
Zero throughout domain  
kg/m2/s

GCC\_13.0.0 (Dev)  
4.0x5.0



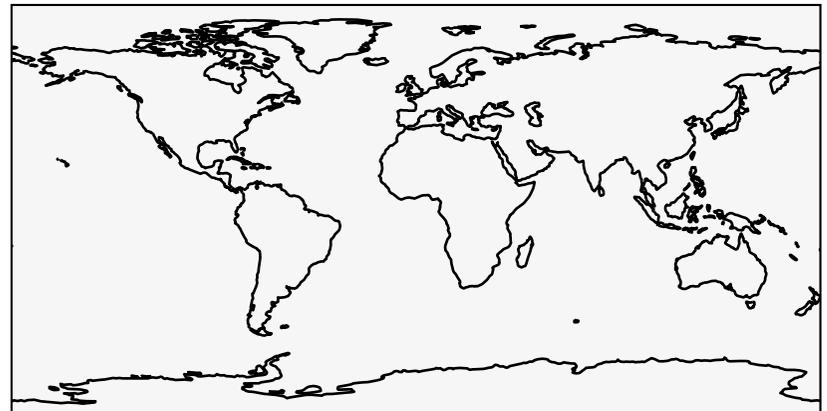
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Dynamic Range



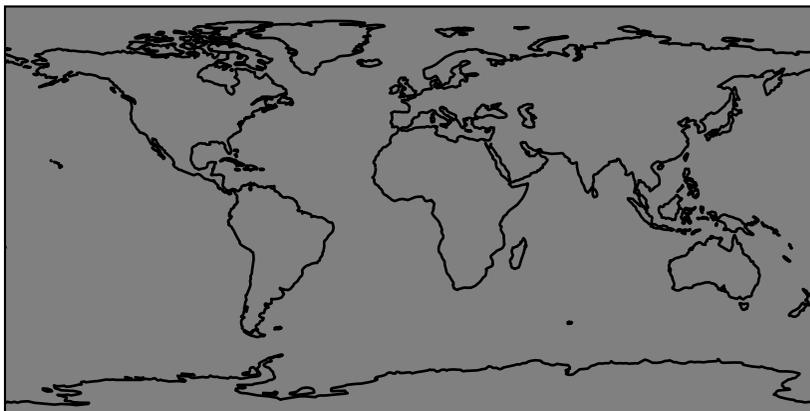
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



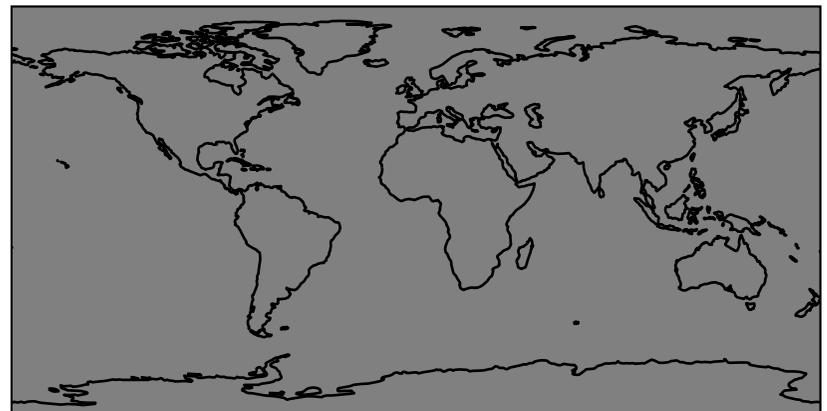
Zero throughout domain  
kg/m2/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

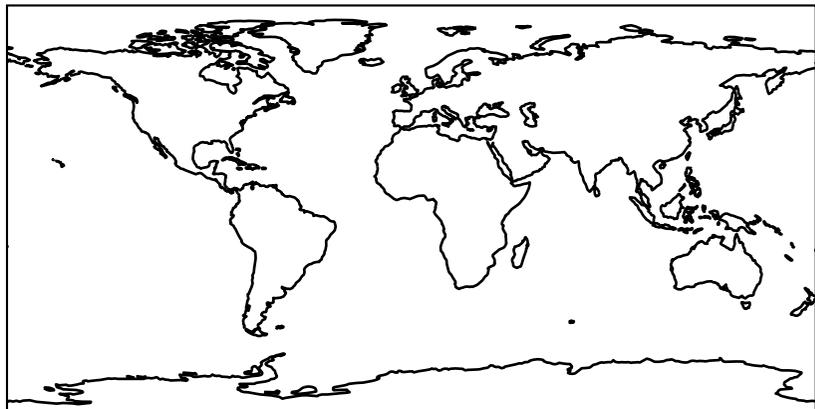
Ratio  
Dev/Ref, Fixed Range



Undefined throughout domain  
unitless

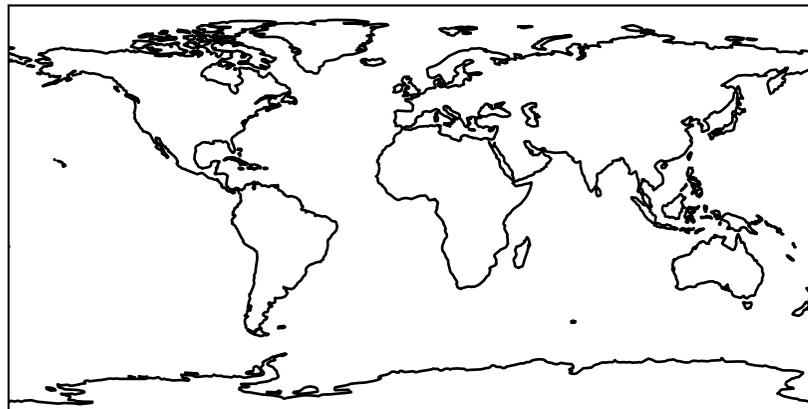
# EmisHAC\_Total (Jul2019)

GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



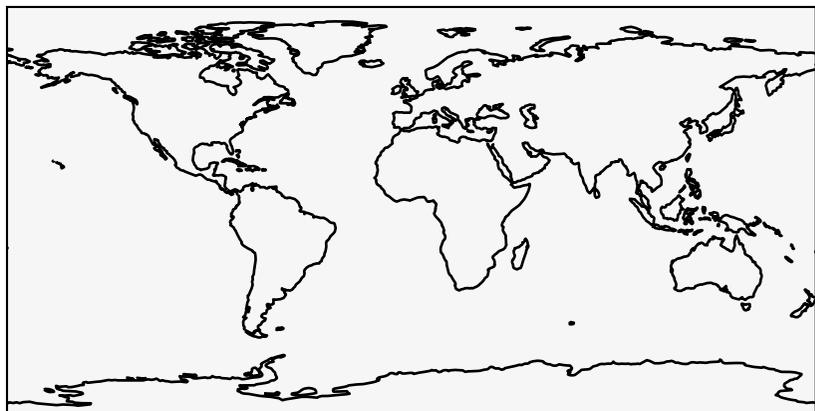
Zero throughout domain  
kg/m2/s

GCC\_13.0.0 (Dev)  
4.0x5.0



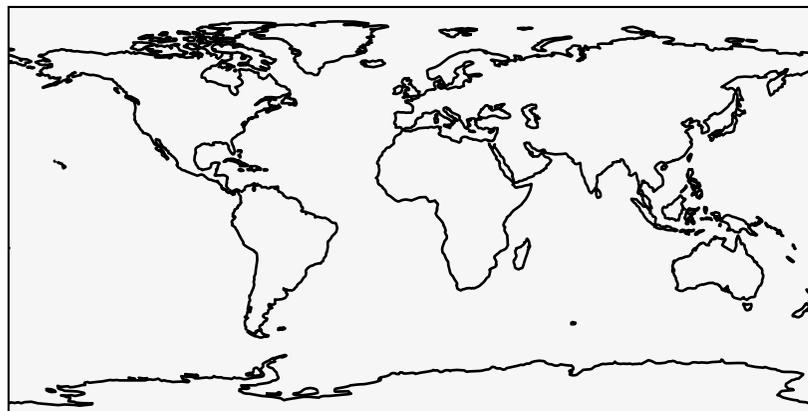
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Dynamic Range



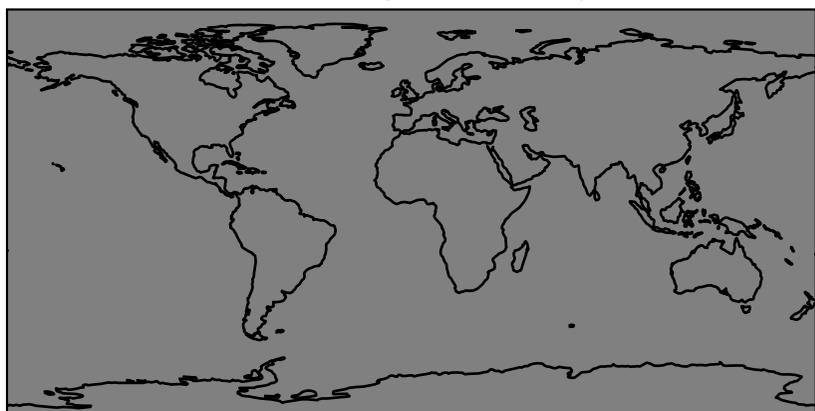
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



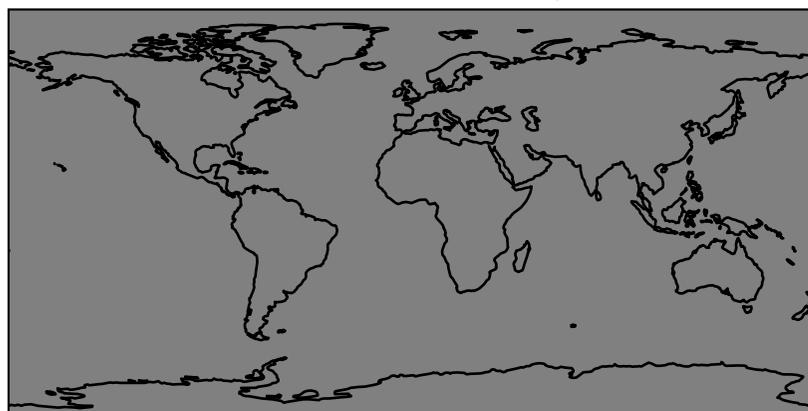
Zero throughout domain  
kg/m2/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

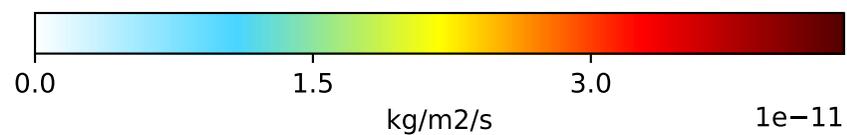
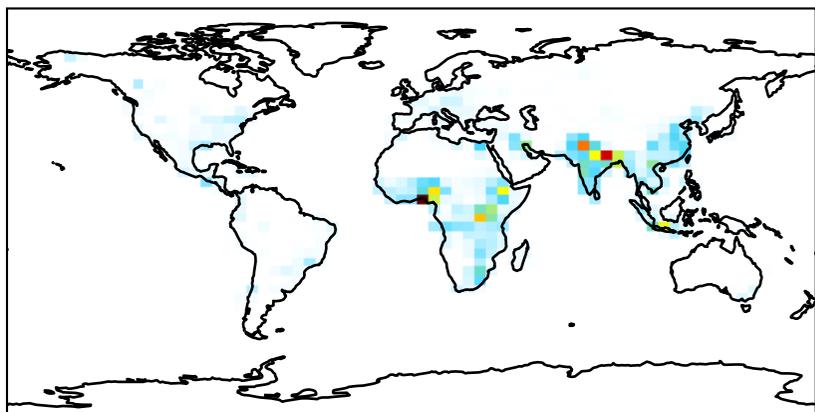
Ratio  
Dev/Ref, Fixed Range



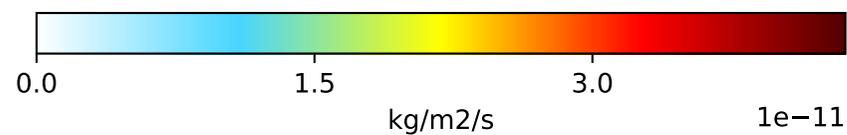
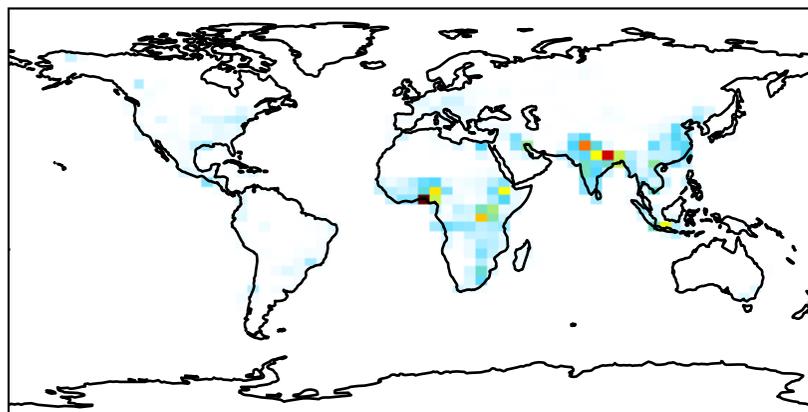
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unitless

# EmisHCOOH\_Total (Jul2019)

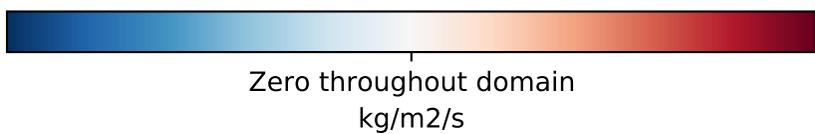
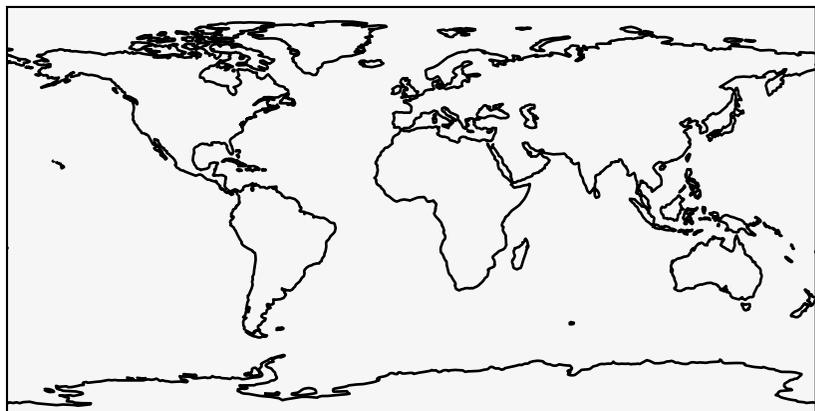
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



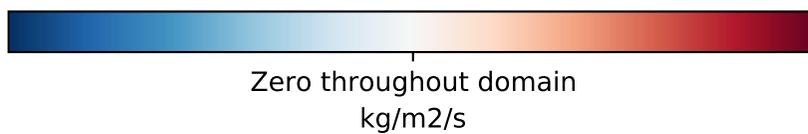
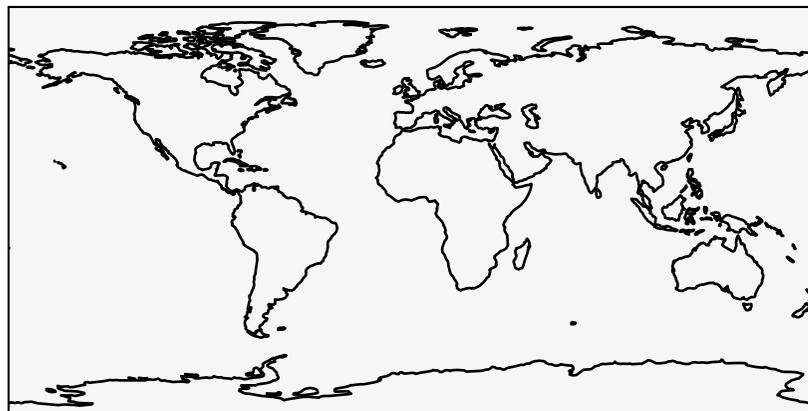
GCC\_13.0.0 (Dev)  
4.0x5.0



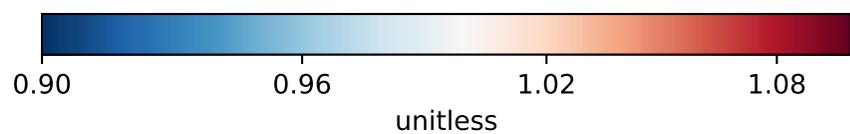
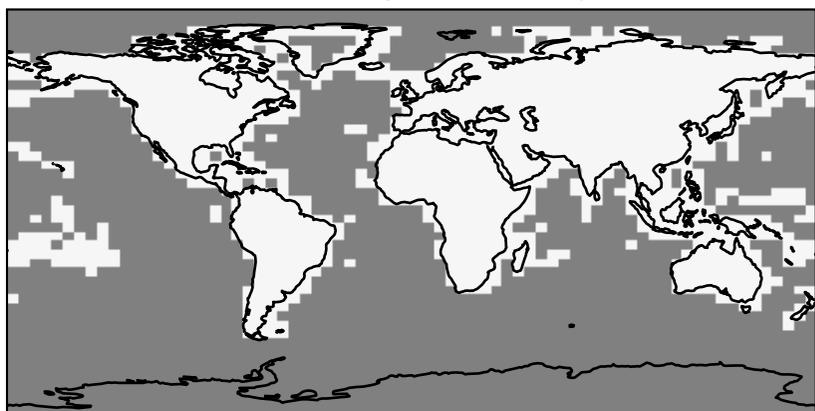
Difference  
Dev - Ref, Dynamic Range



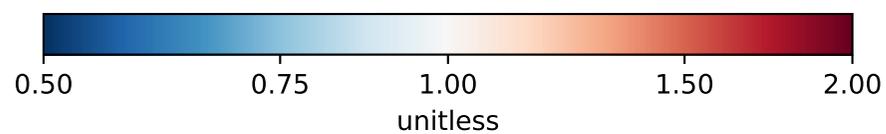
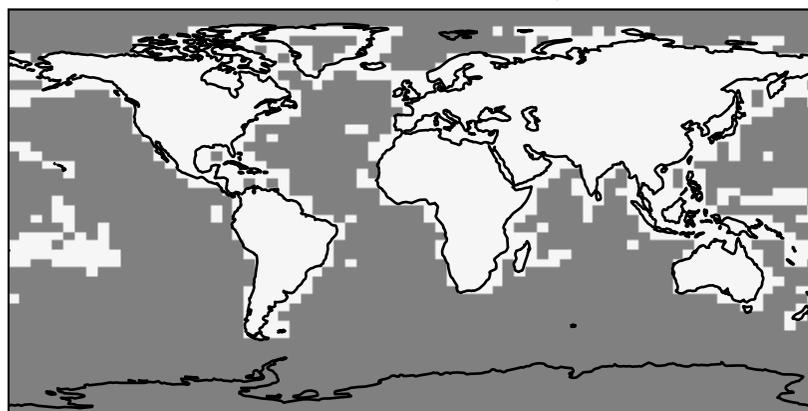
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

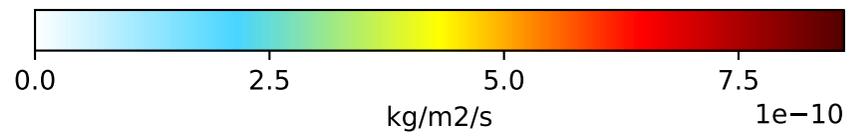
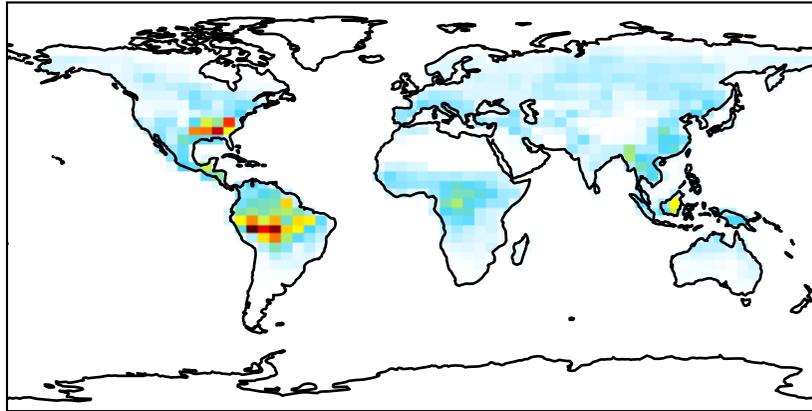


Ratio  
Dev/Ref, Fixed Range

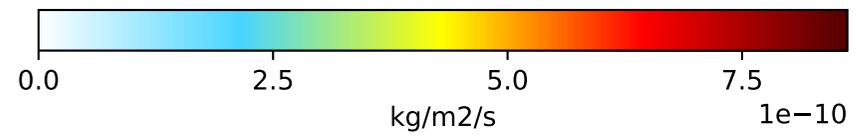
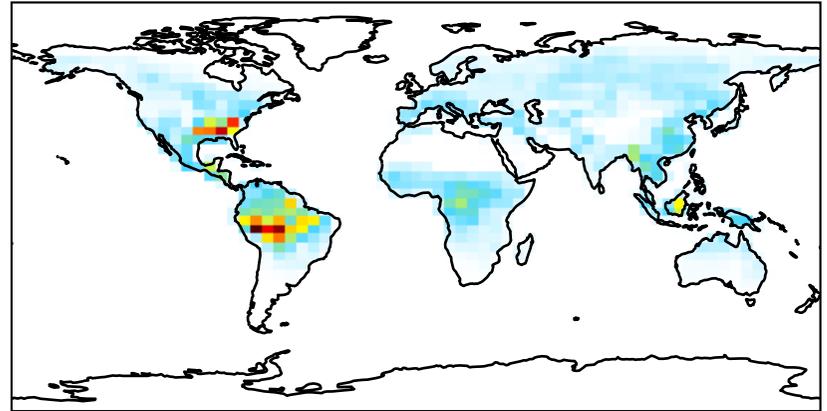


# EmisSOP\_Total (Jul2019)

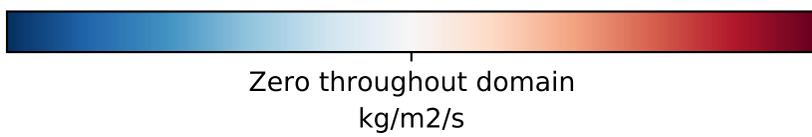
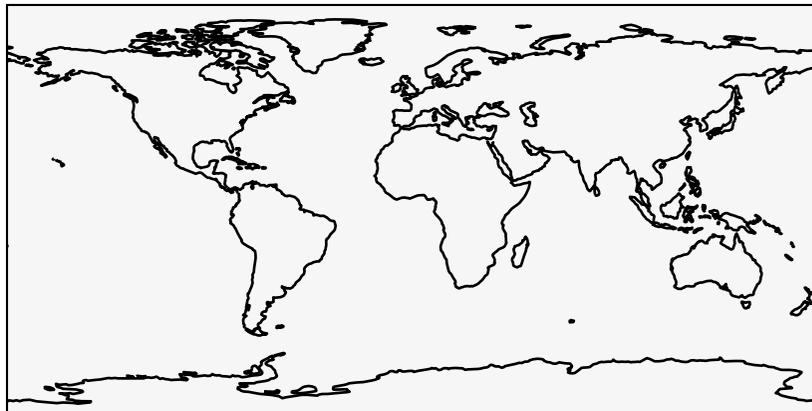
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



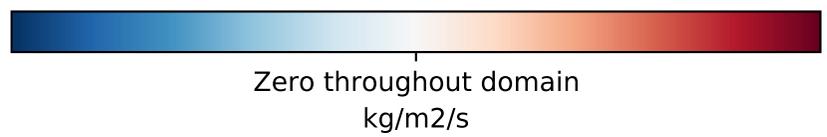
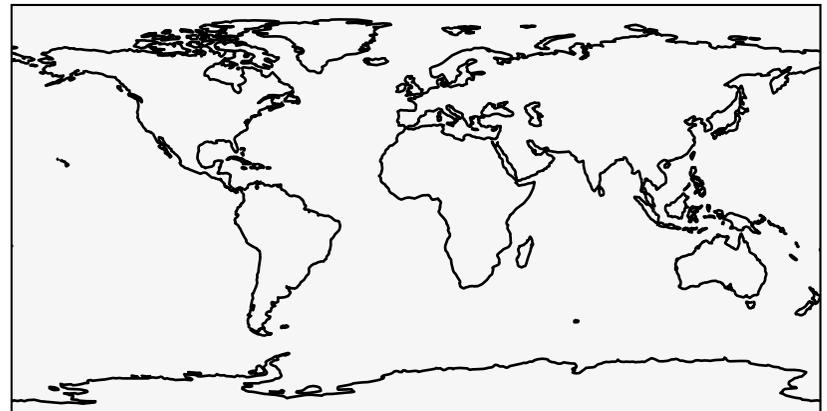
GCC\_13.0.0 (Dev)  
4.0x5.0



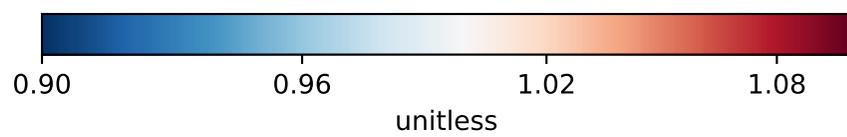
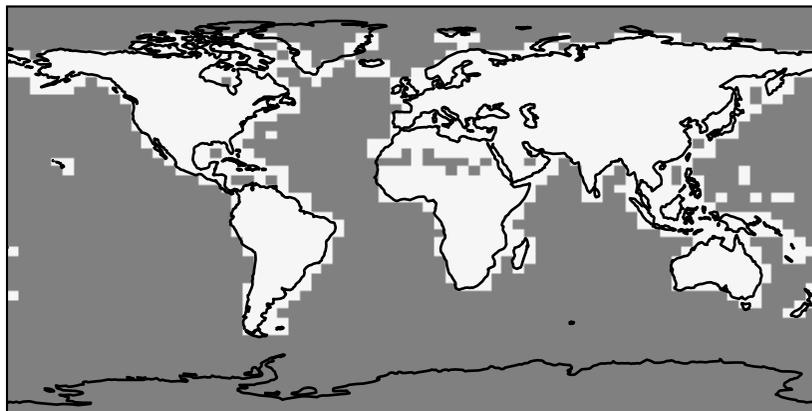
Difference  
Dev - Ref, Dynamic Range



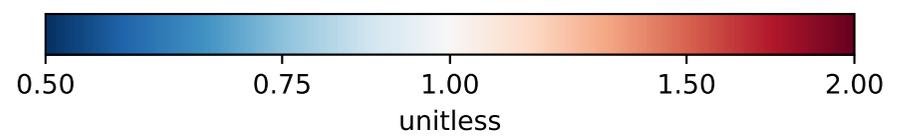
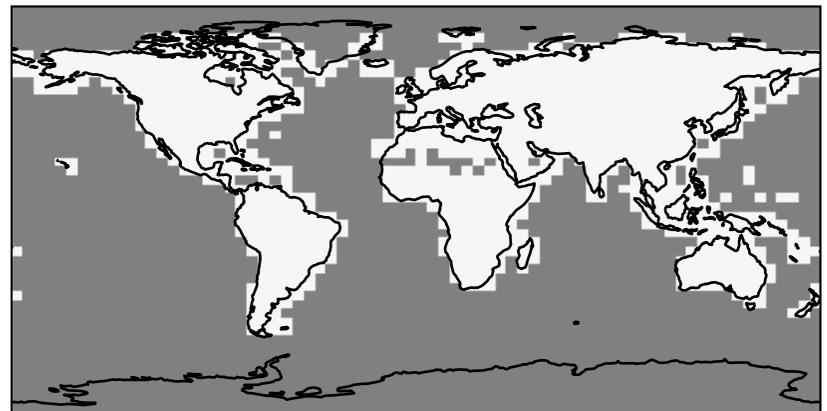
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

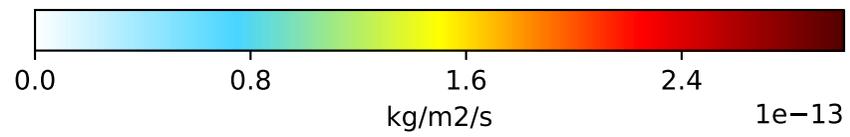
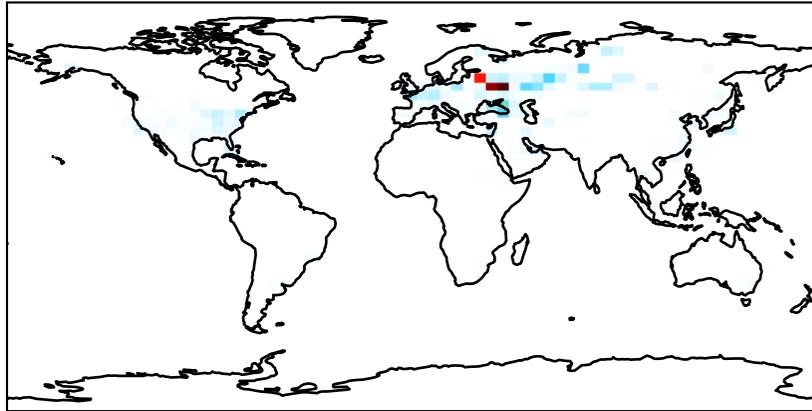


Ratio  
Dev/Ref, Fixed Range

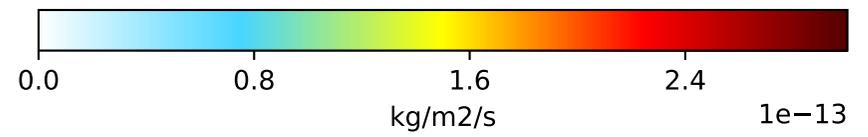
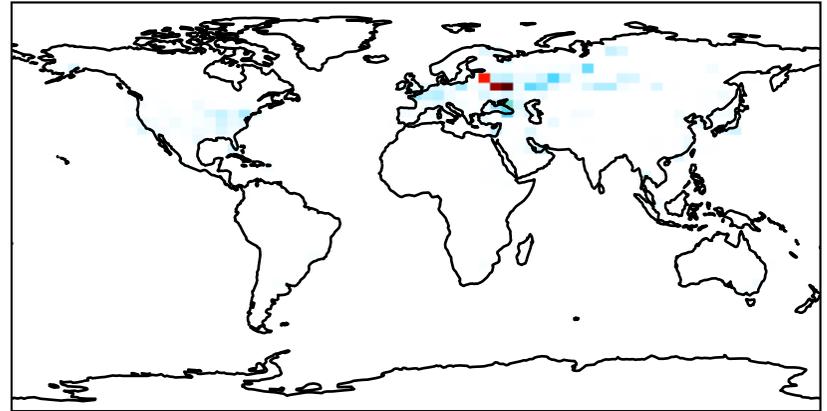


# EmisMACR\_Total (Jul2019)

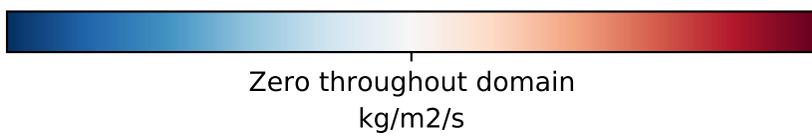
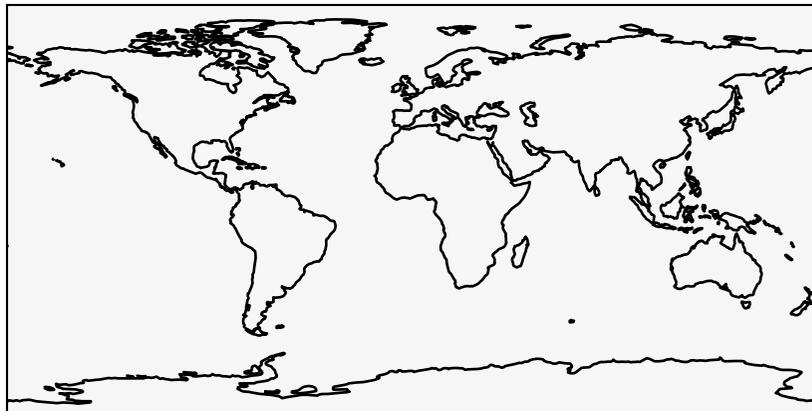
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



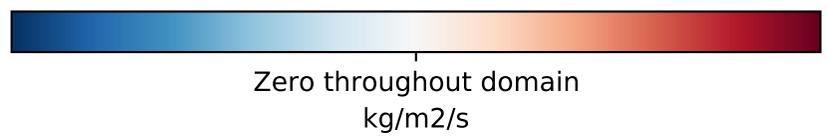
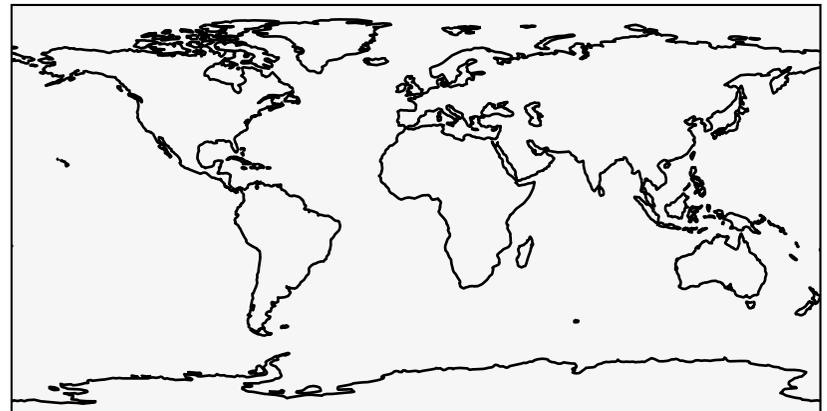
GCC\_13.0.0 (Dev)  
4.0x5.0



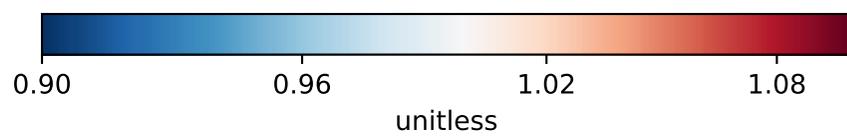
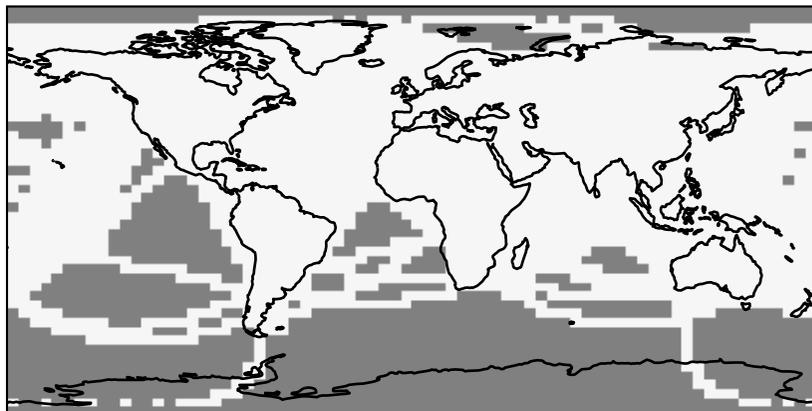
Difference  
Dev - Ref, Dynamic Range



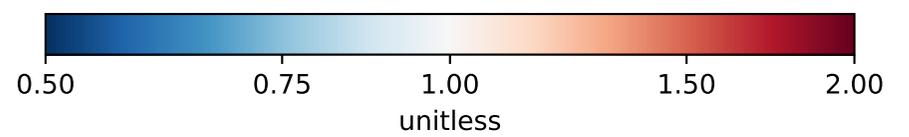
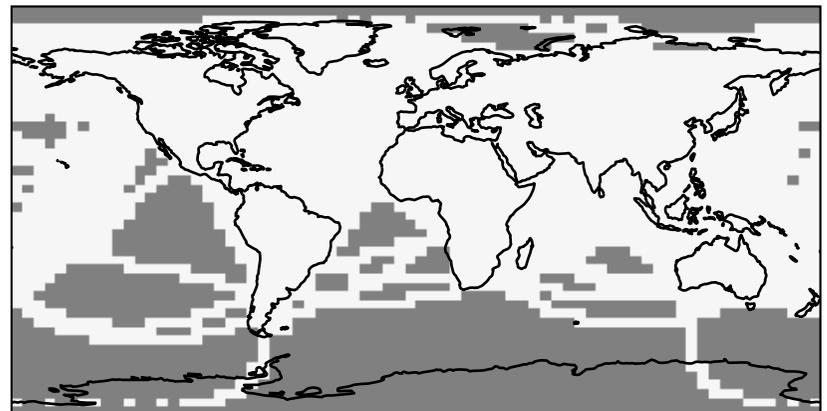
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

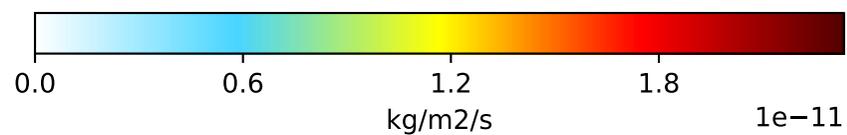
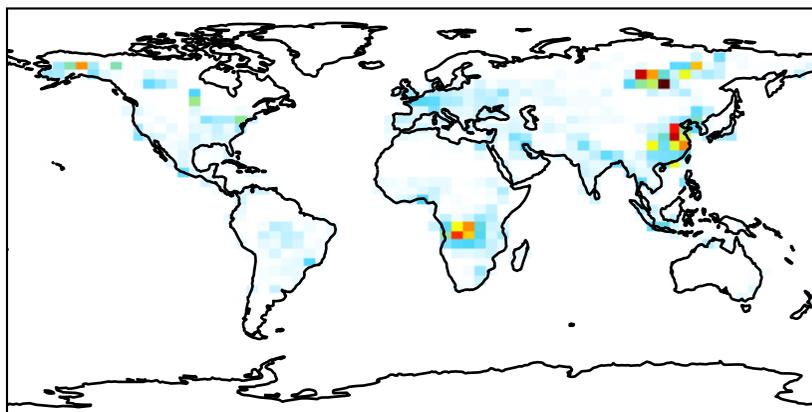


Ratio  
Dev/Ref, Fixed Range

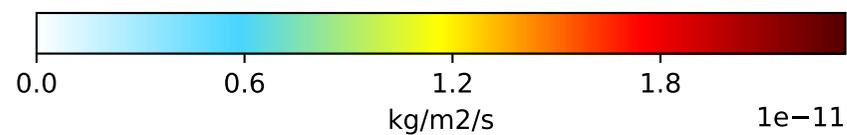
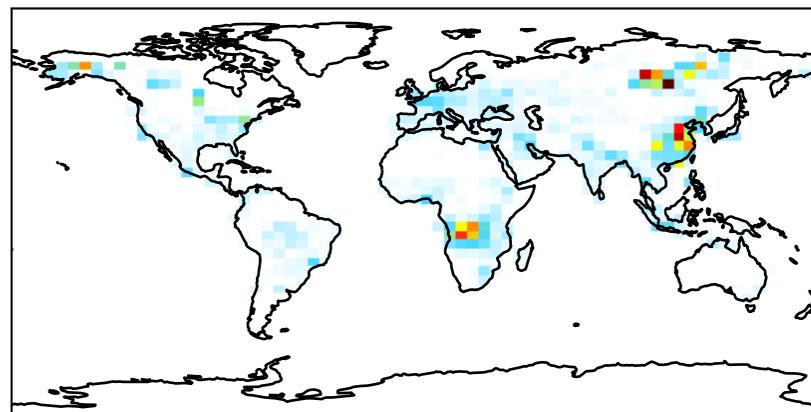


# EmisMEK\_Total (Jul2019)

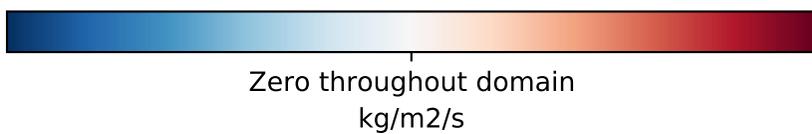
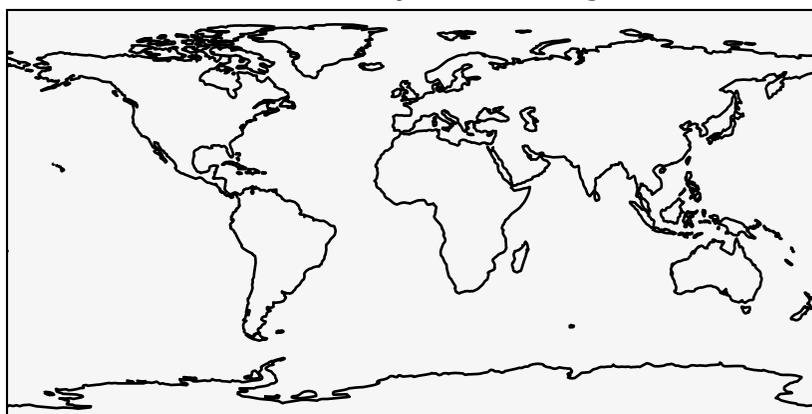
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



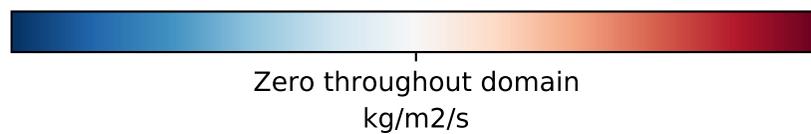
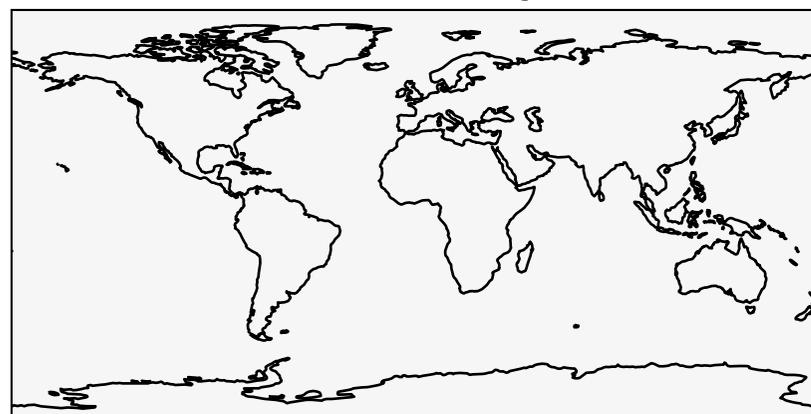
GCC\_13.0.0 (Dev)  
4.0x5.0



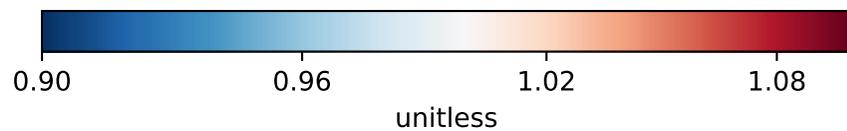
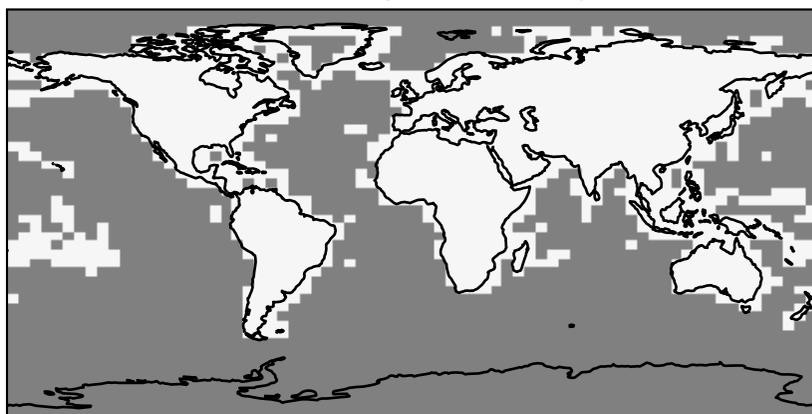
Difference  
Dev - Ref, Dynamic Range



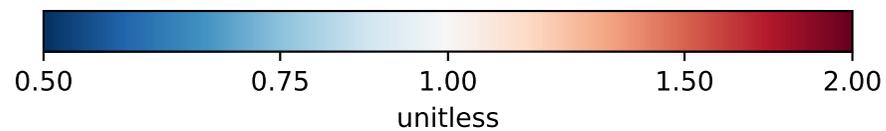
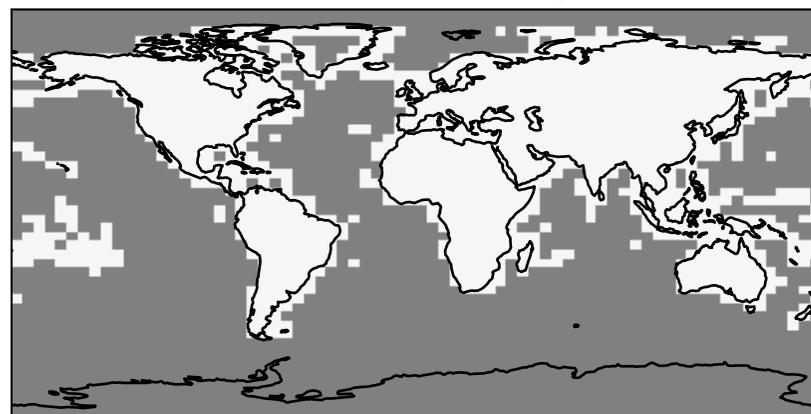
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

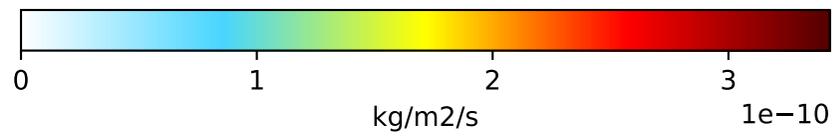
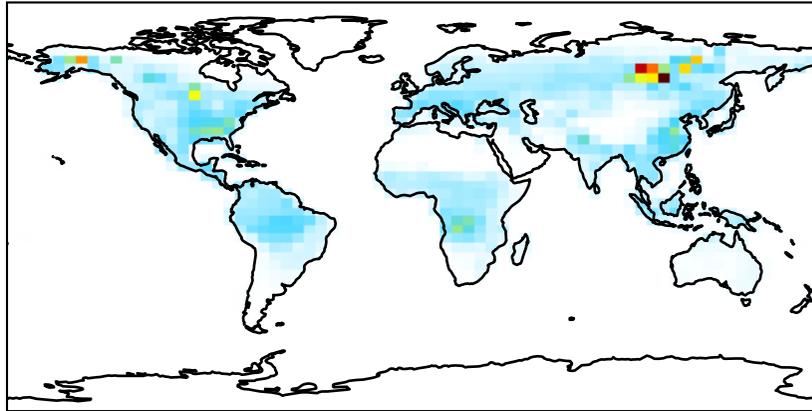


Ratio  
Dev/Ref, Fixed Range

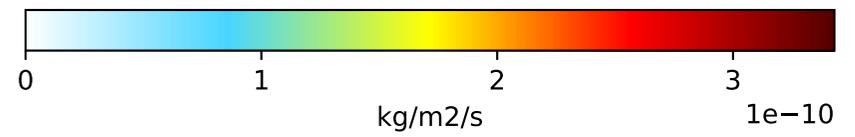
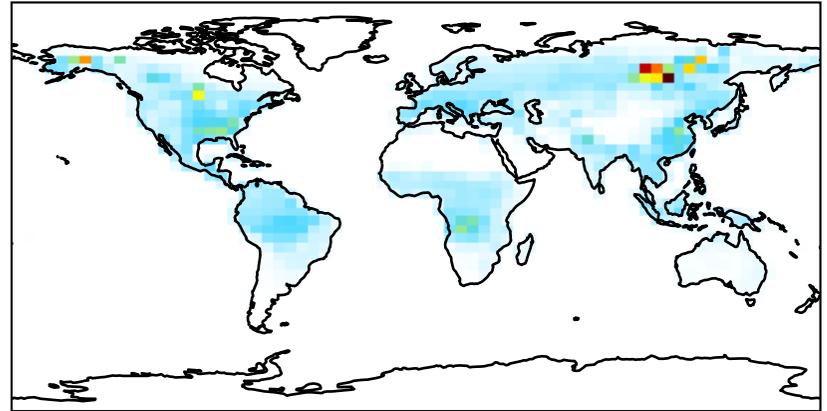


# EmisMOH\_Total (Jul2019)

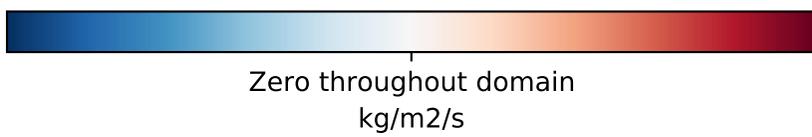
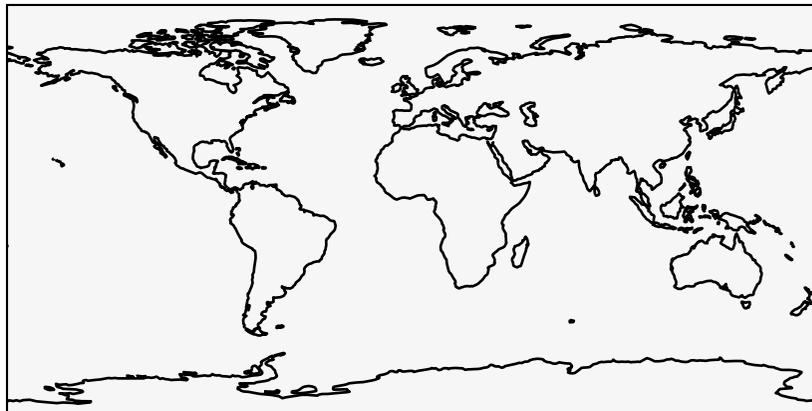
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



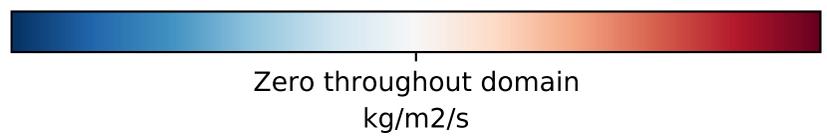
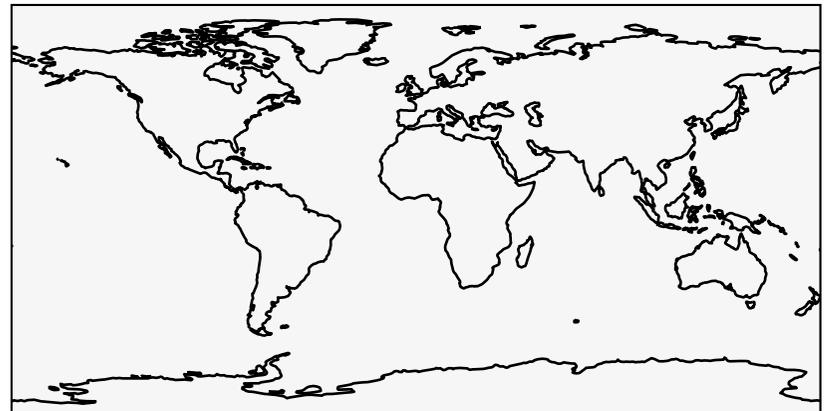
GCC\_13.0.0 (Dev)  
4.0x5.0



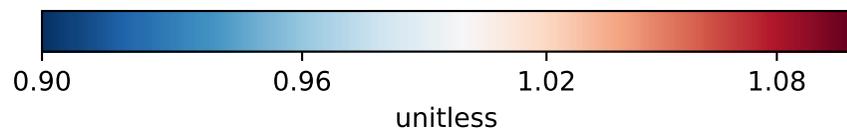
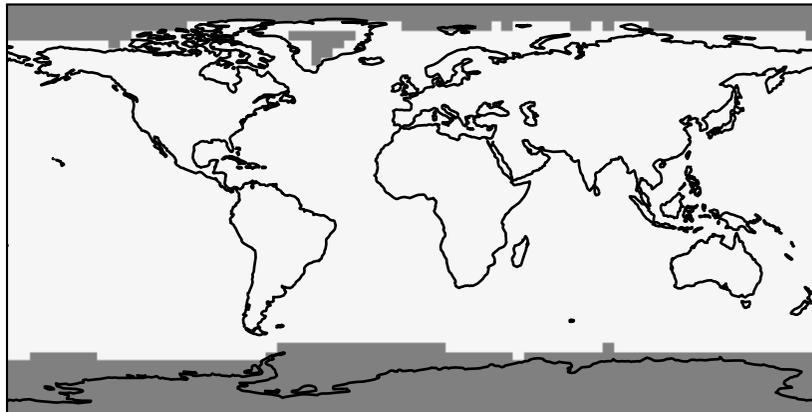
Difference  
Dev - Ref, Dynamic Range



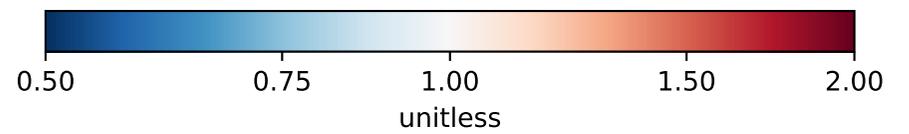
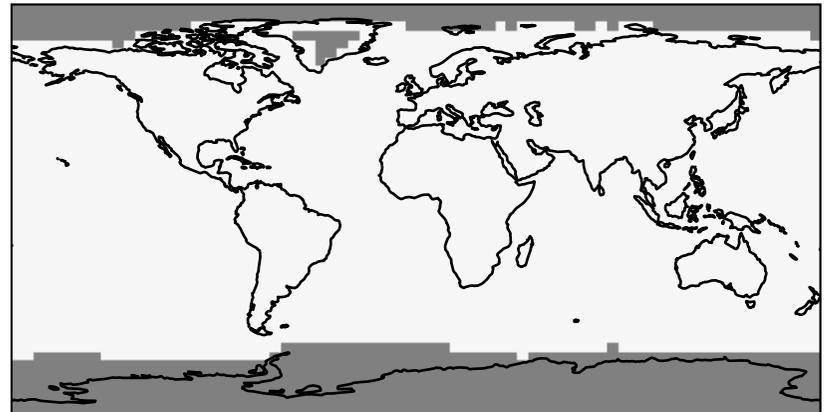
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

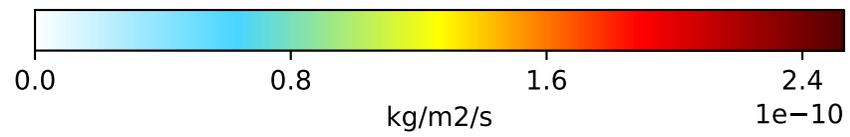
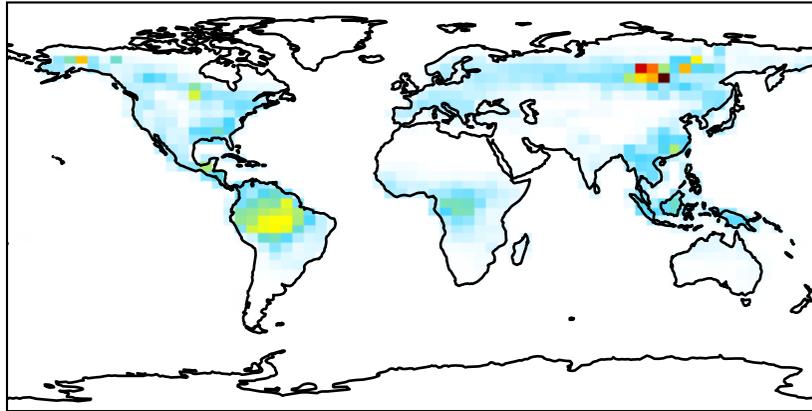


Ratio  
Dev/Ref, Fixed Range

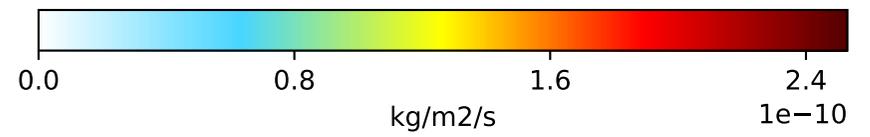
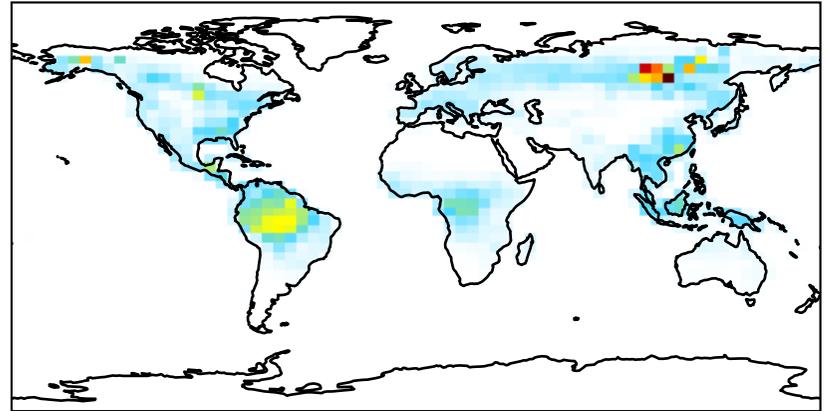


# EmisMTPA\_Total (Jul2019)

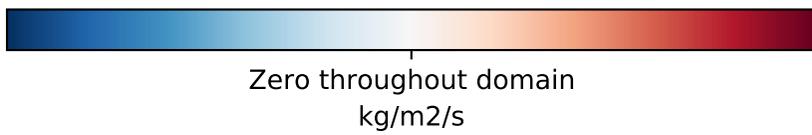
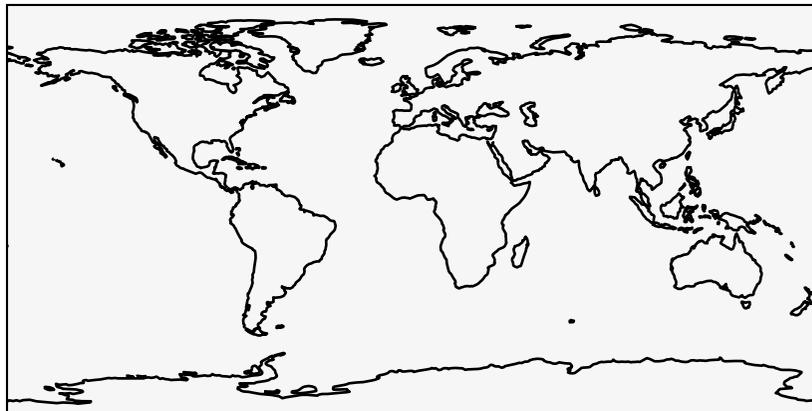
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



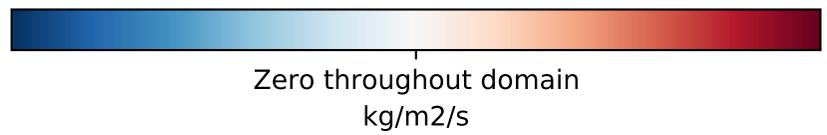
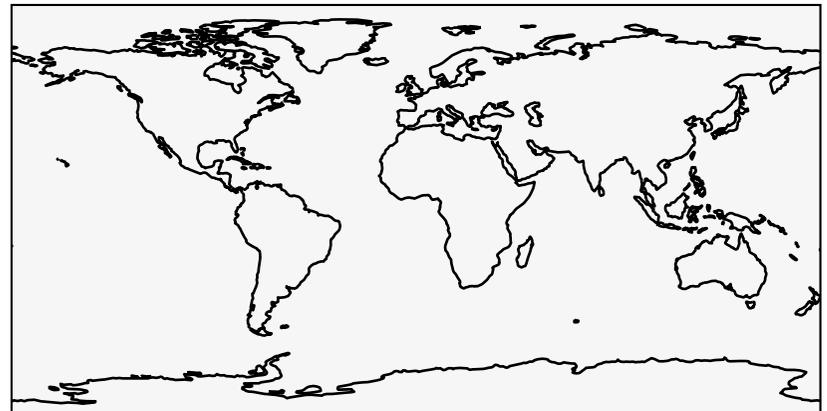
GCC\_13.0.0 (Dev)  
4.0x5.0



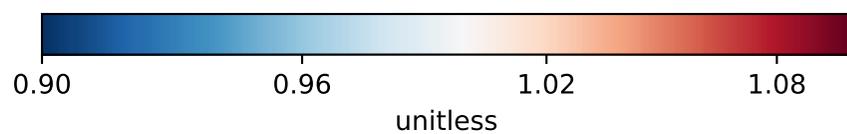
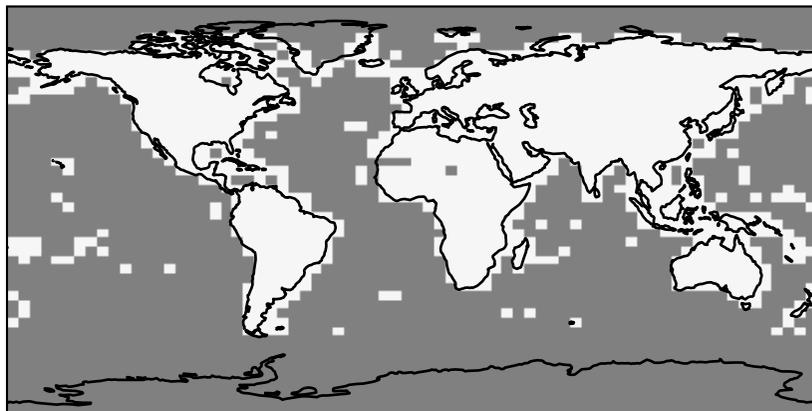
Difference  
Dev - Ref, Dynamic Range



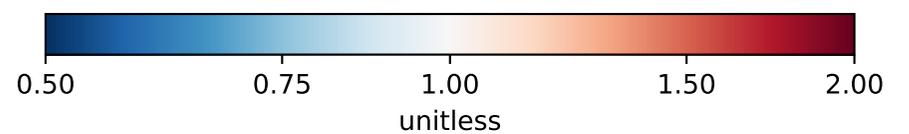
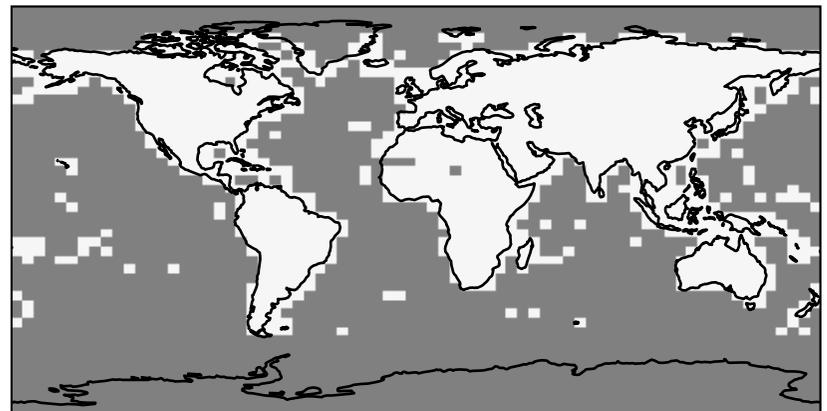
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

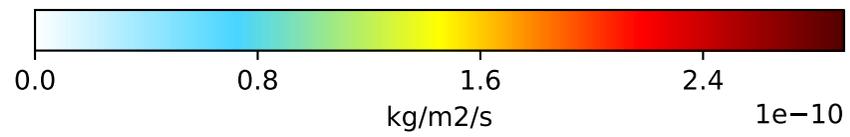
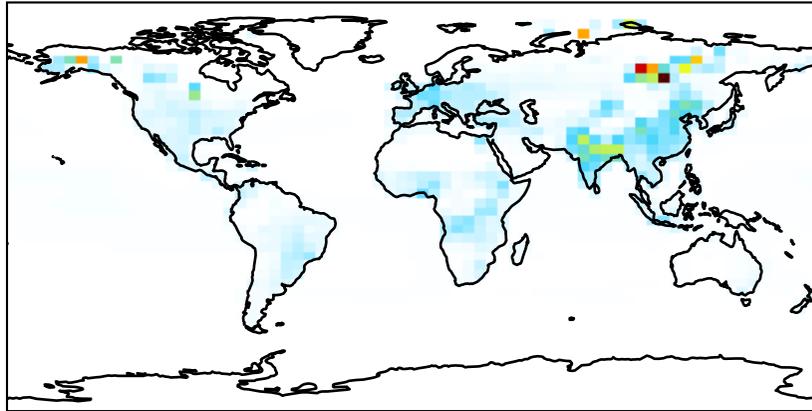


Ratio  
Dev/Ref, Fixed Range

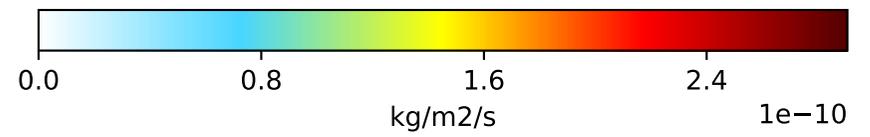
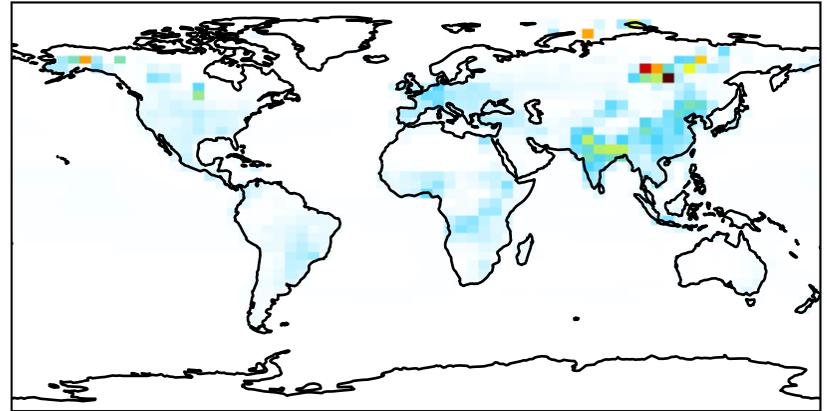


# EmisNH3\_Total (Jul2019)

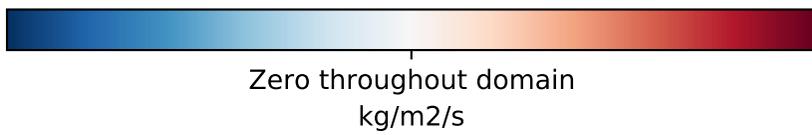
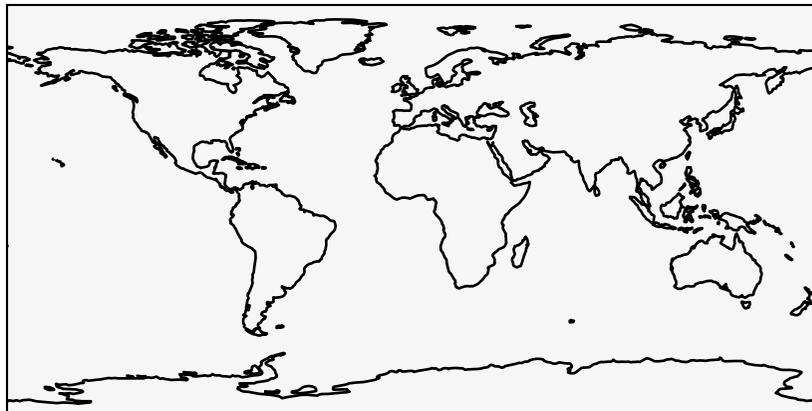
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



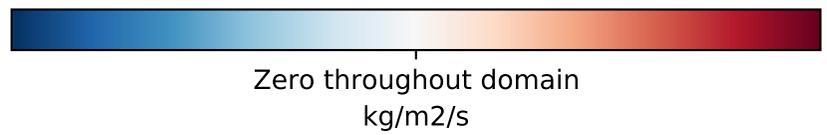
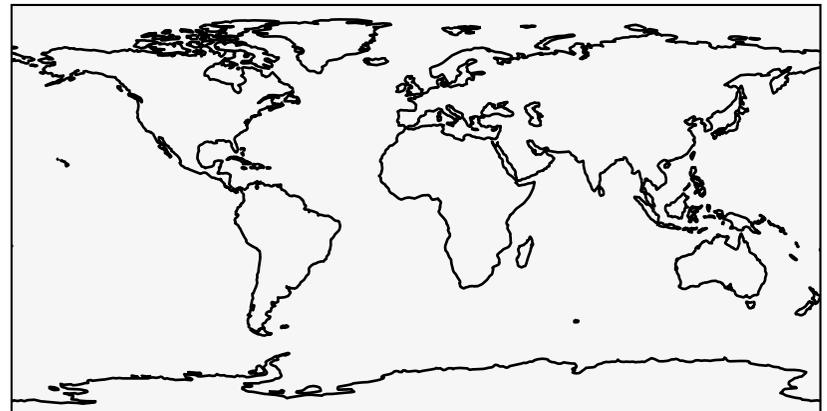
GCC\_13.0.0 (Dev)  
4.0x5.0



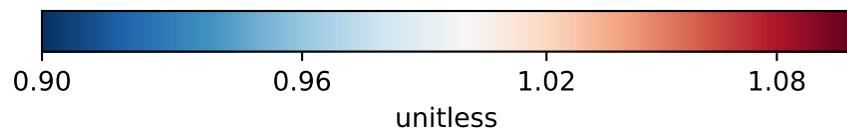
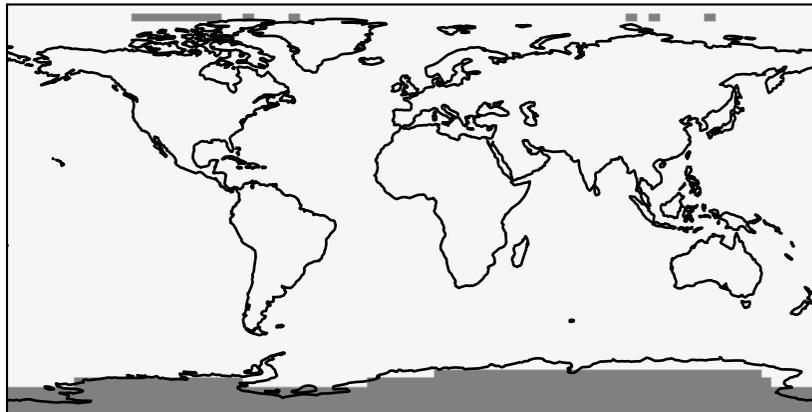
Difference  
Dev - Ref, Dynamic Range



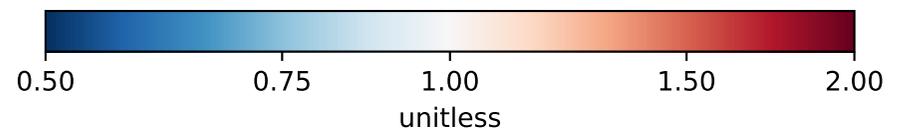
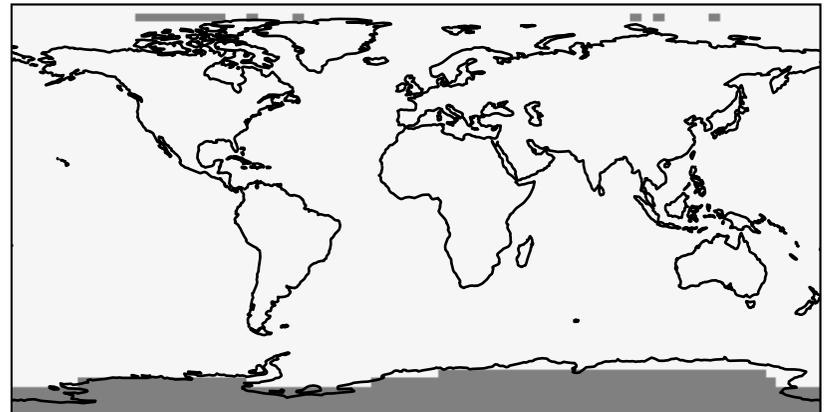
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

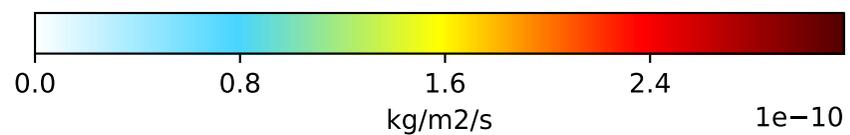
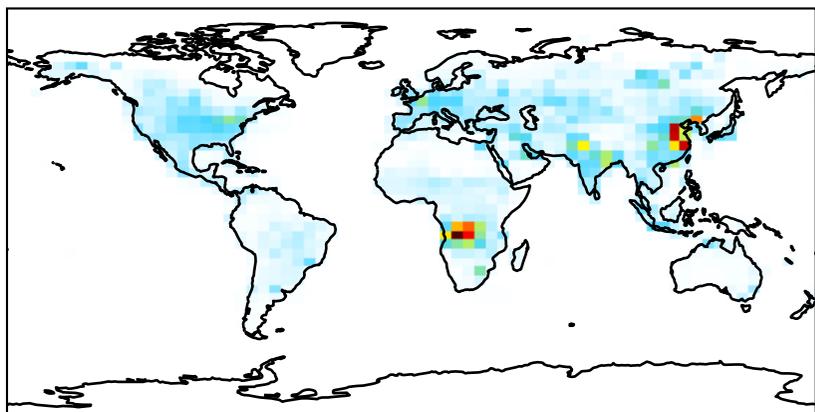


Ratio  
Dev/Ref, Fixed Range

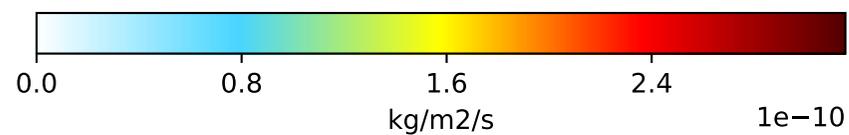
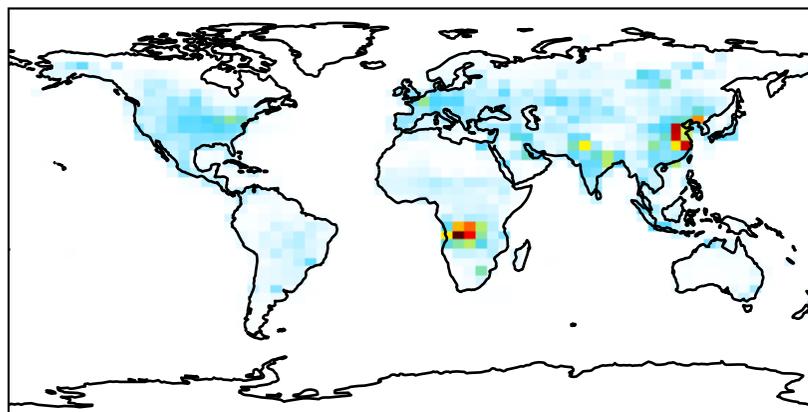


# EmisNO\_Total (Jul2019)

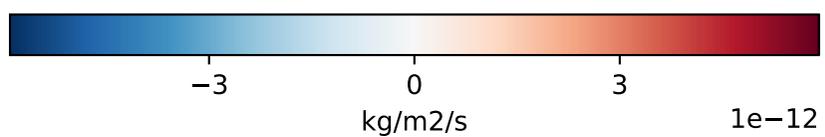
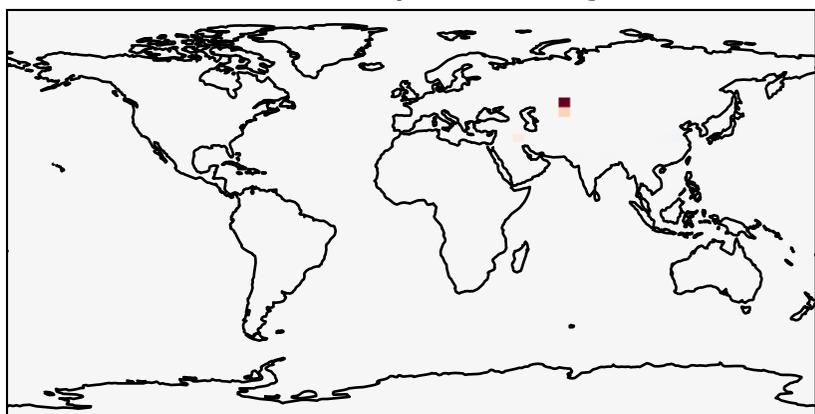
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



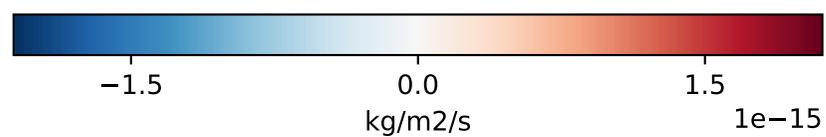
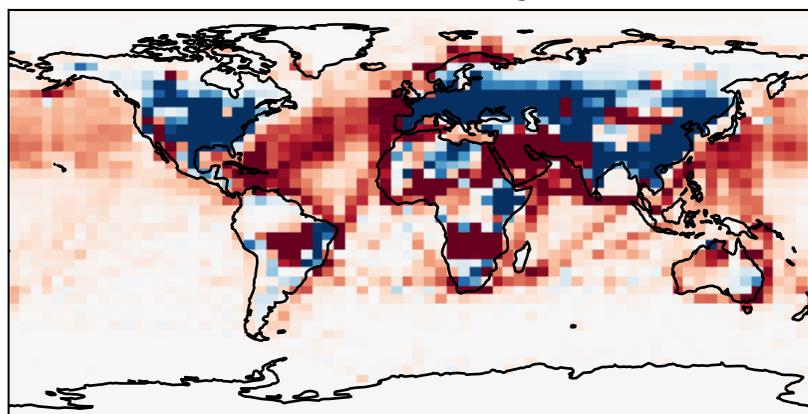
GCC\_13.0.0 (Dev)  
4.0x5.0



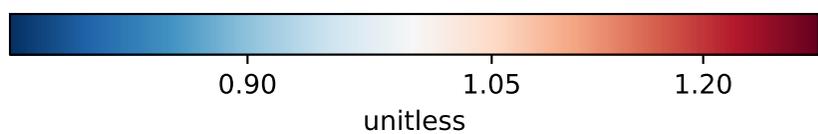
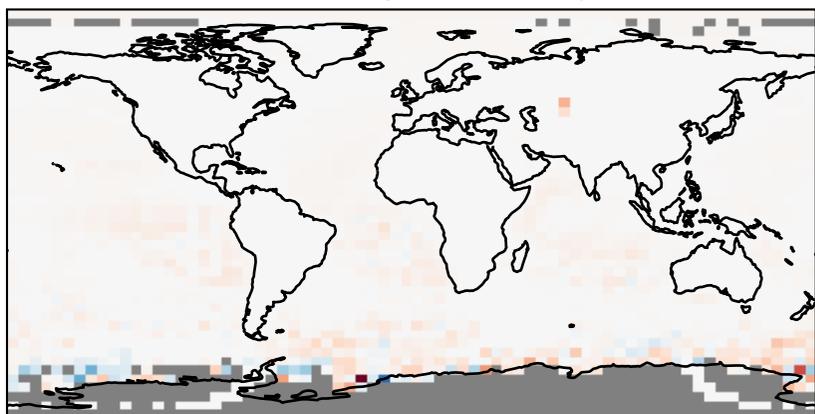
Difference  
Dev - Ref, Dynamic Range



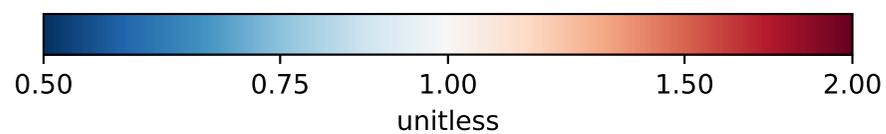
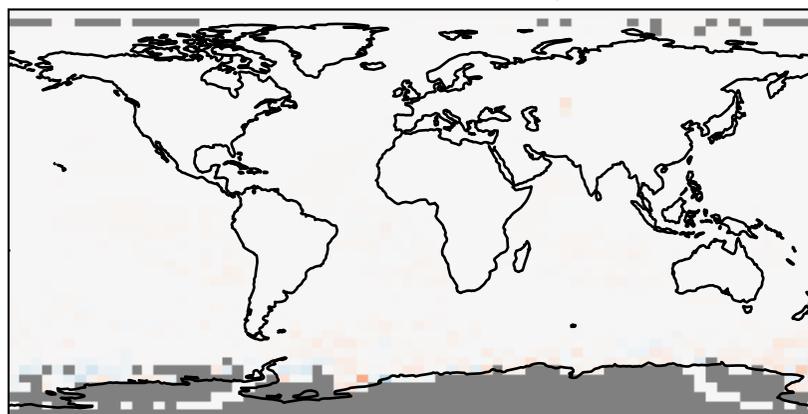
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

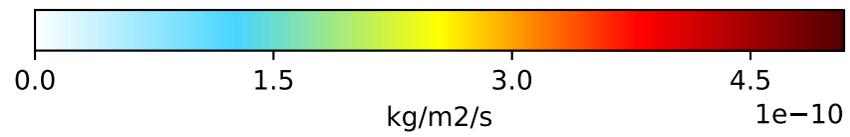
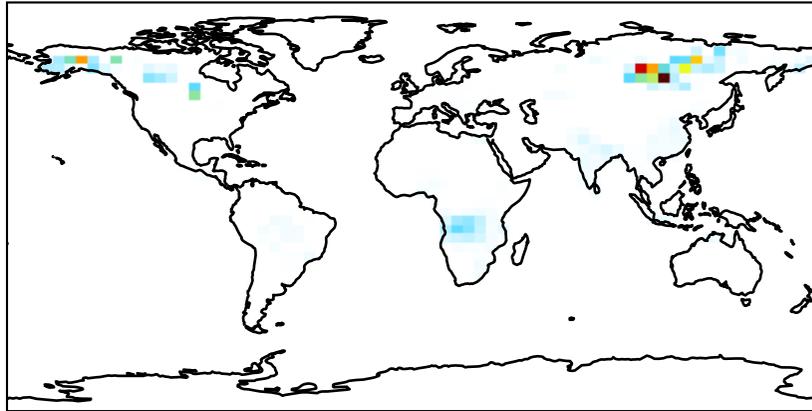


Ratio  
Dev/Ref, Fixed Range

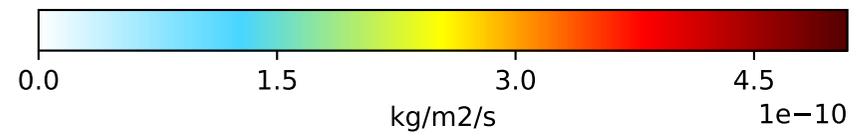
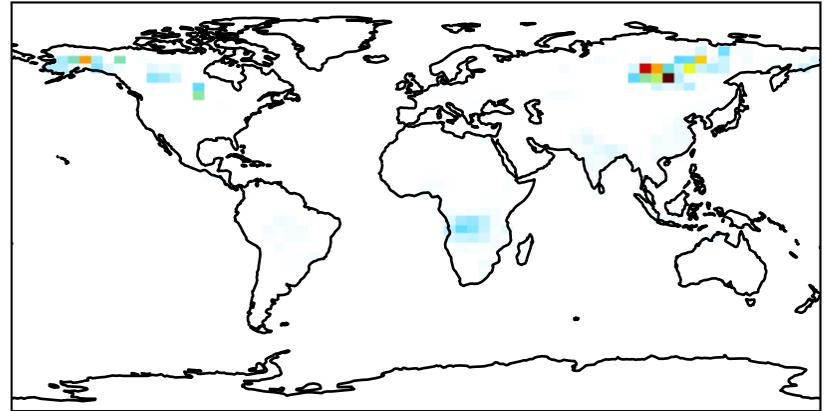


# EmisOCPI\_Total (Jul2019)

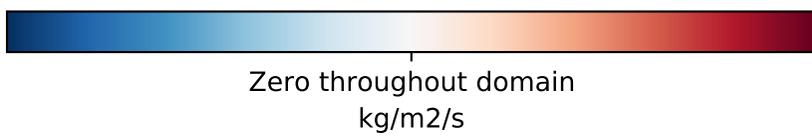
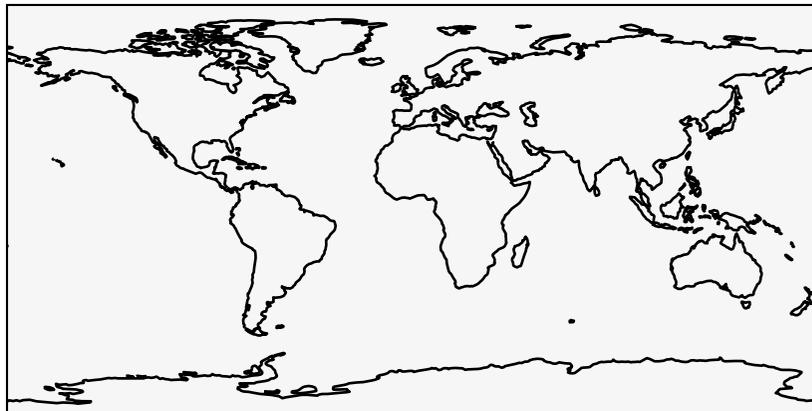
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



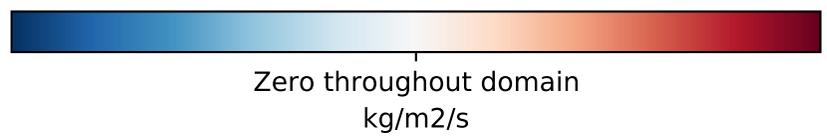
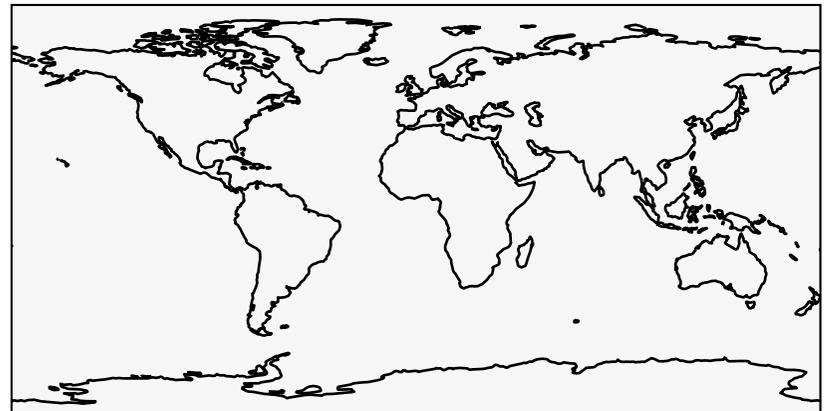
GCC\_13.0.0 (Dev)  
4.0x5.0



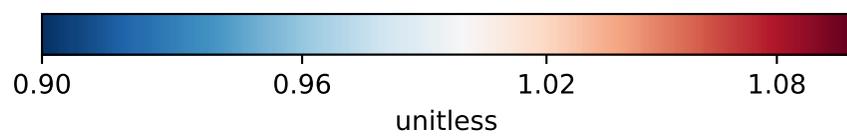
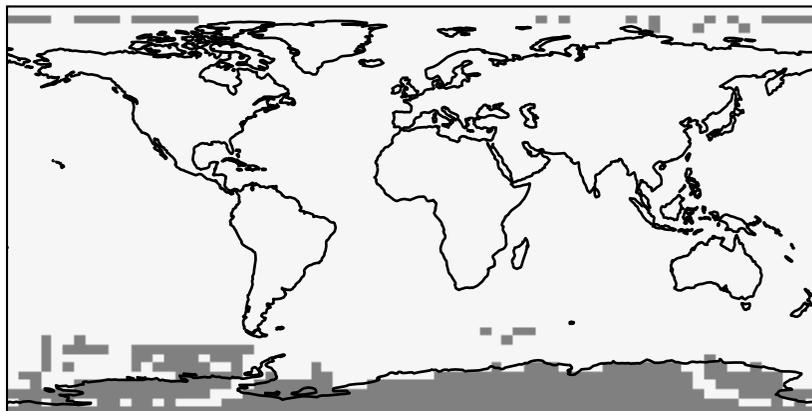
Difference  
Dev - Ref, Dynamic Range



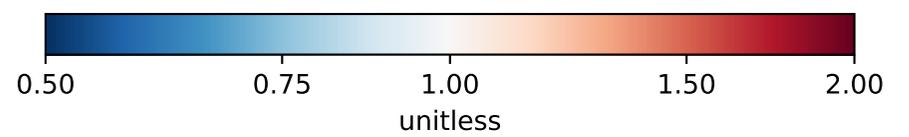
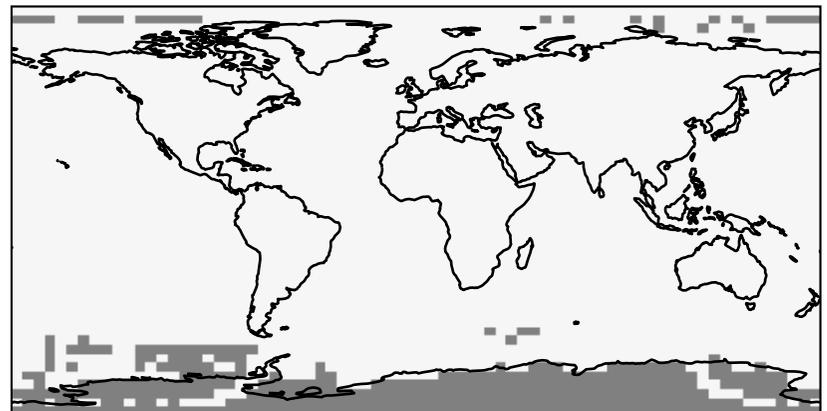
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

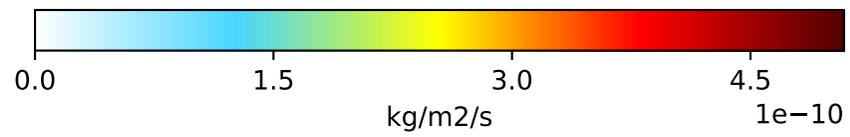
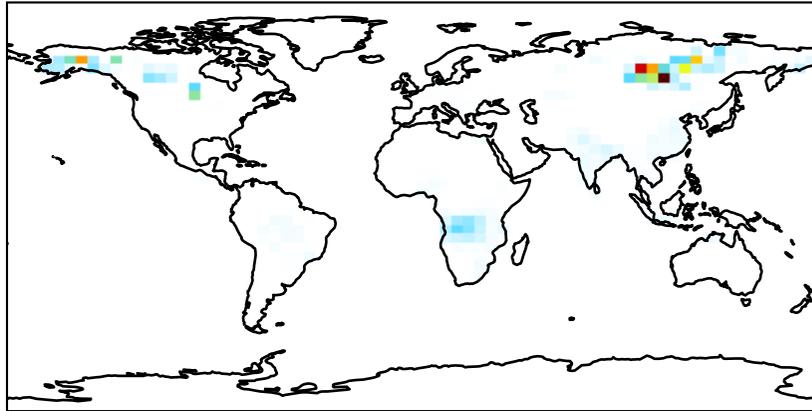


Ratio  
Dev/Ref, Fixed Range

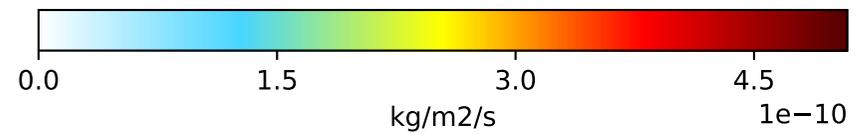
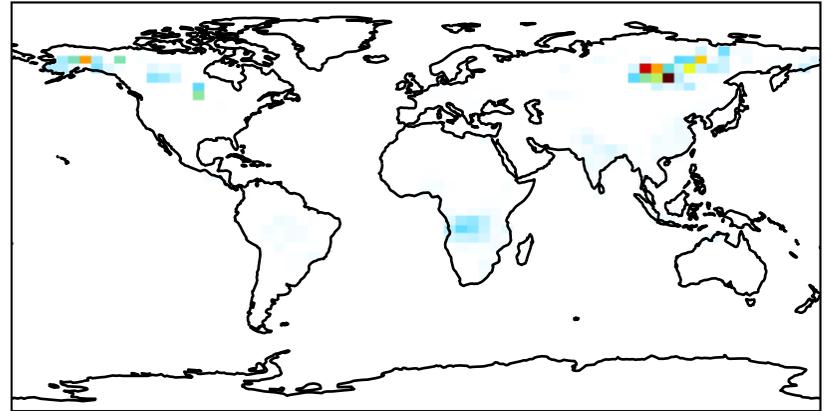


# EmisOCPO\_Total (Jul2019)

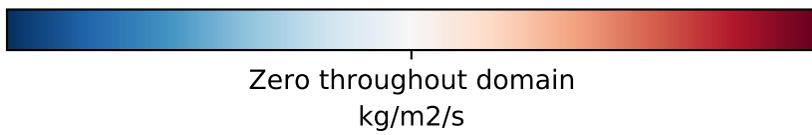
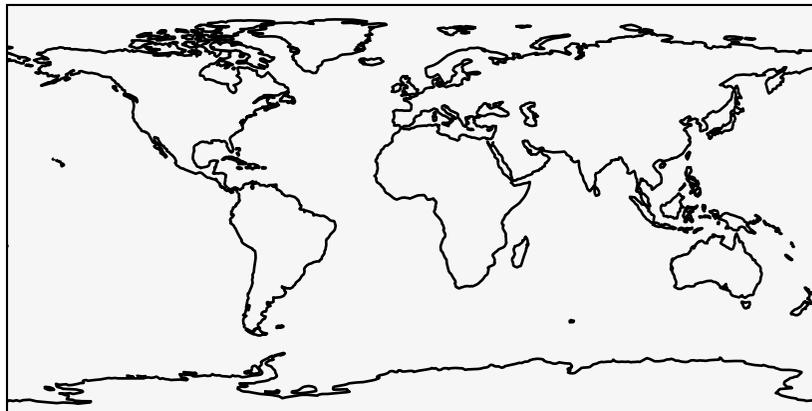
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



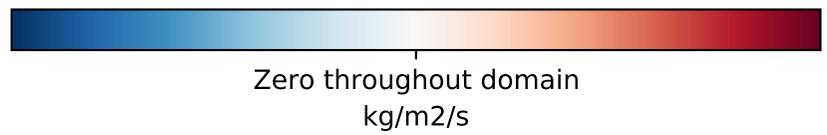
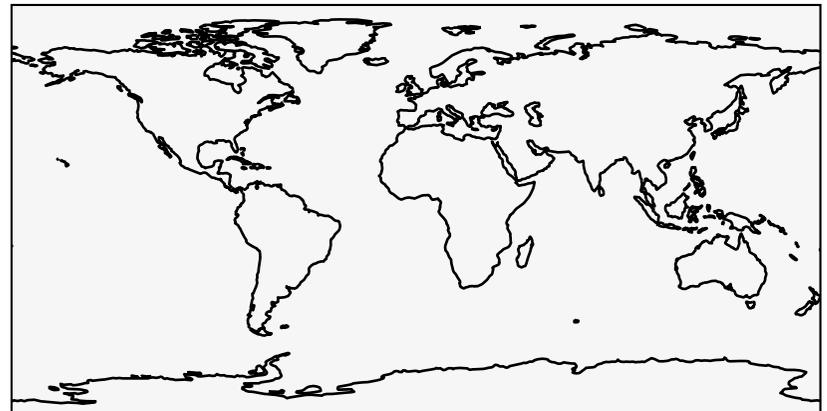
GCC\_13.0.0 (Dev)  
4.0x5.0



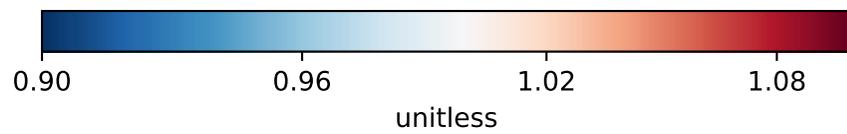
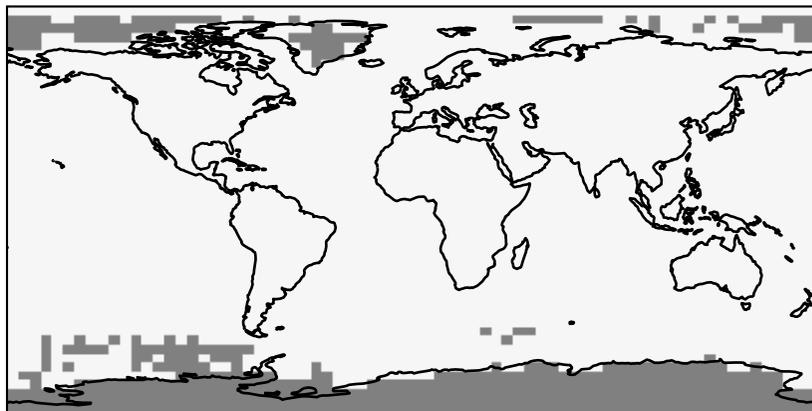
Difference  
Dev - Ref, Dynamic Range



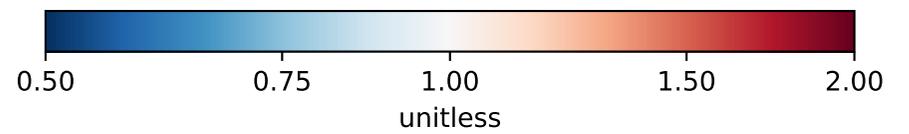
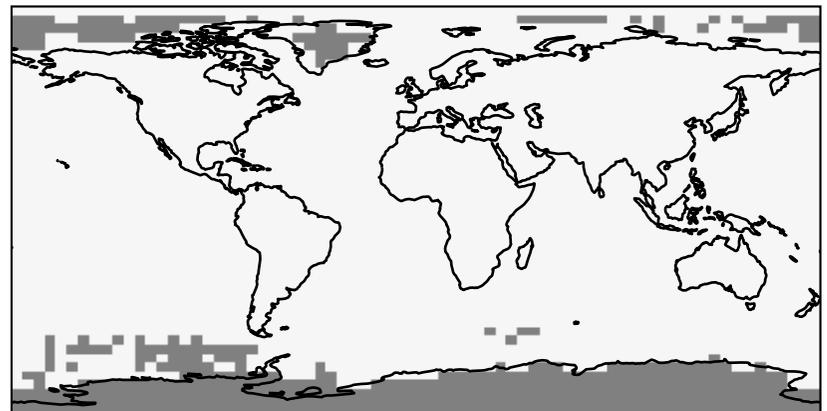
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

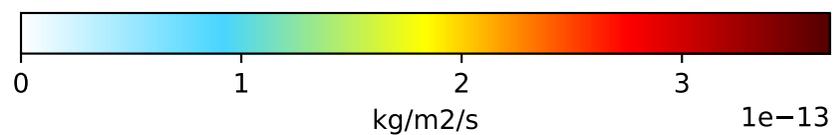
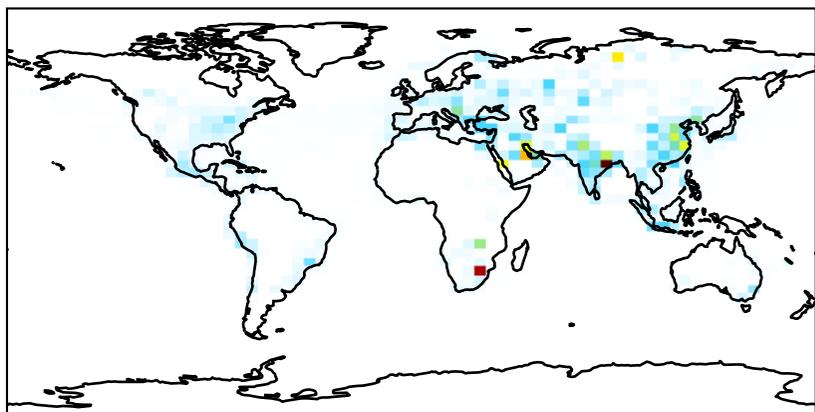


Ratio  
Dev/Ref, Fixed Range

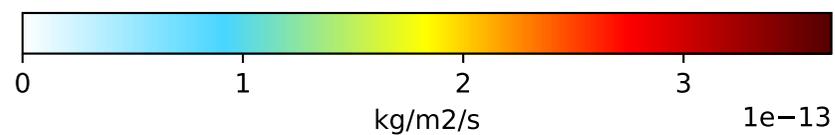
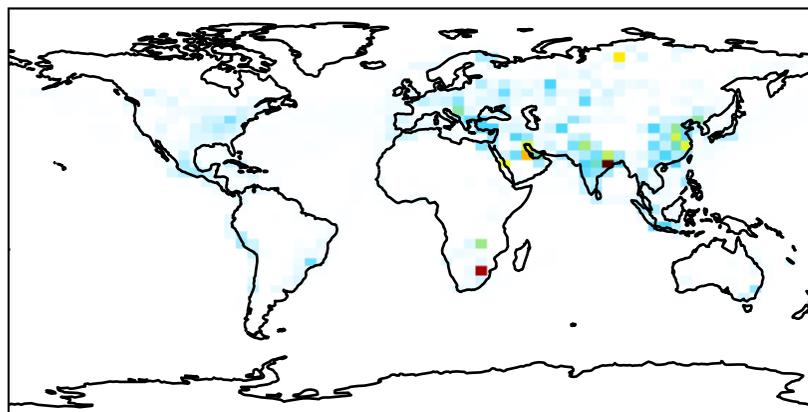


# EmispFe\_Total (Jul2019)

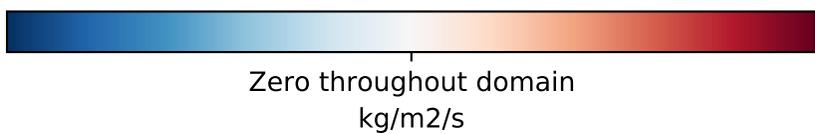
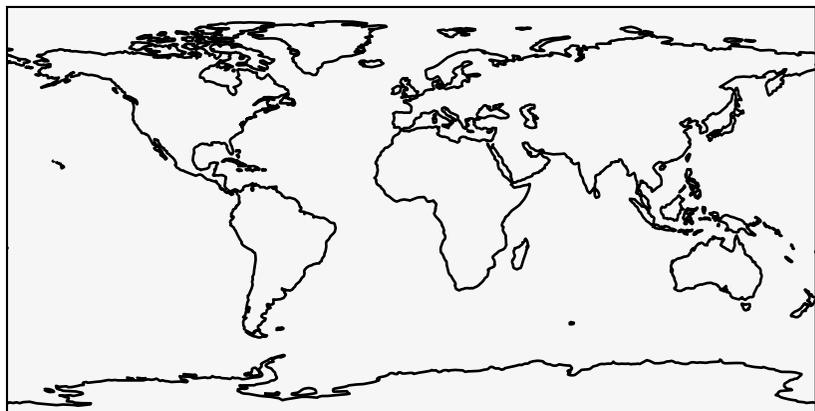
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



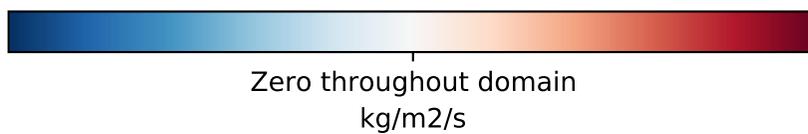
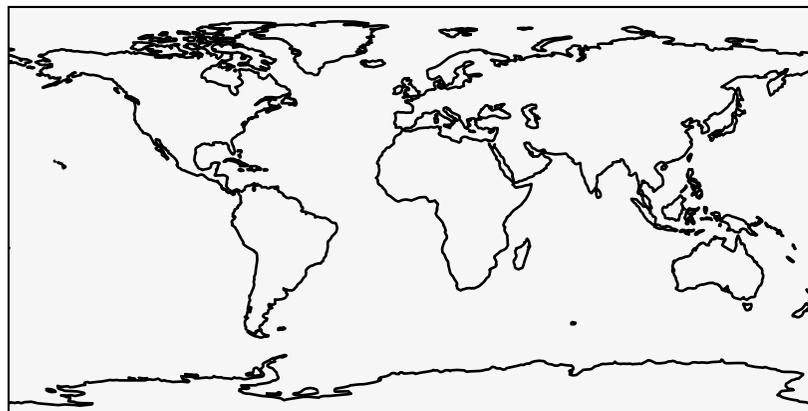
GCC\_13.0.0 (Dev)  
4.0x5.0



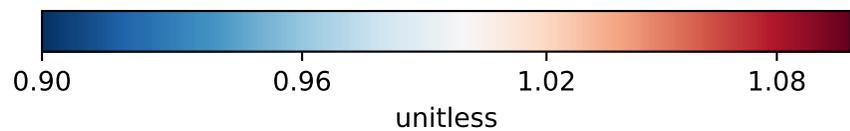
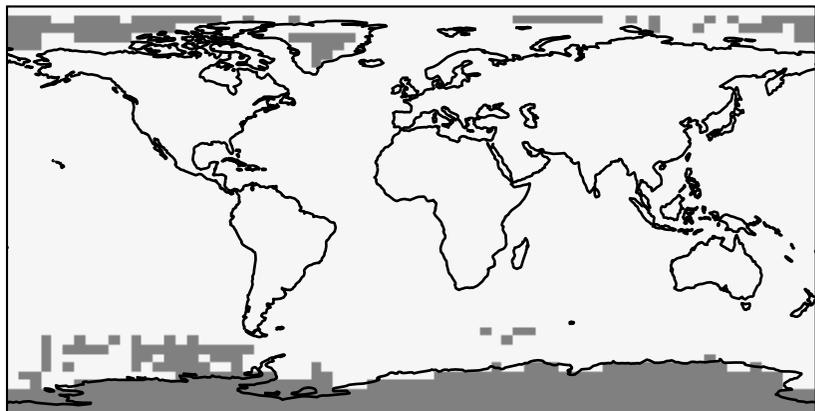
Difference  
Dev - Ref, Dynamic Range



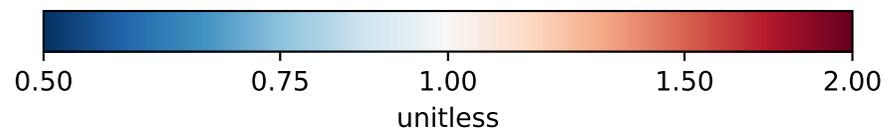
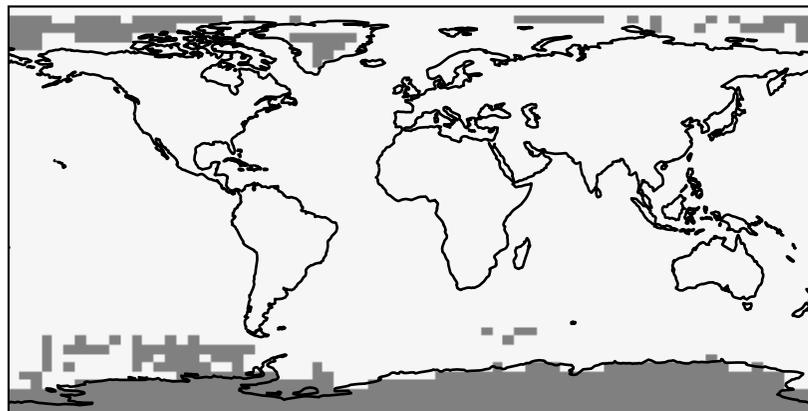
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

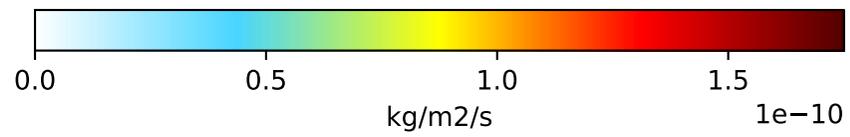
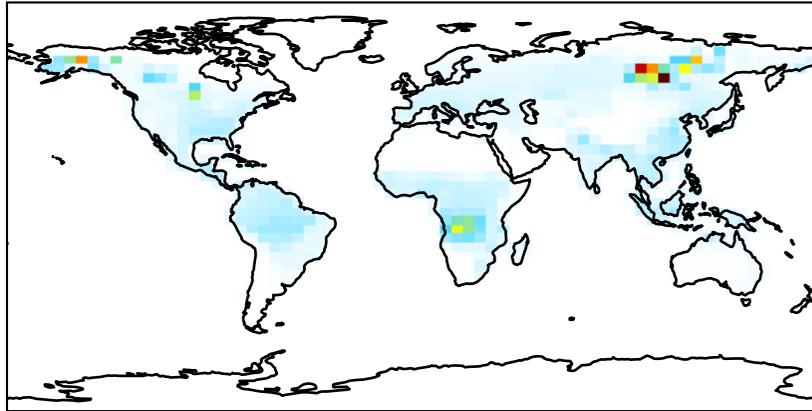


Ratio  
Dev/Ref, Fixed Range

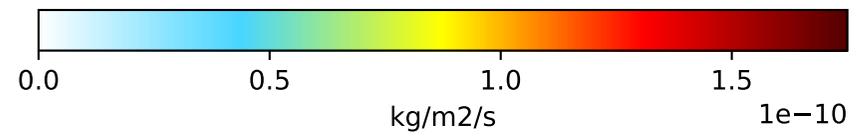
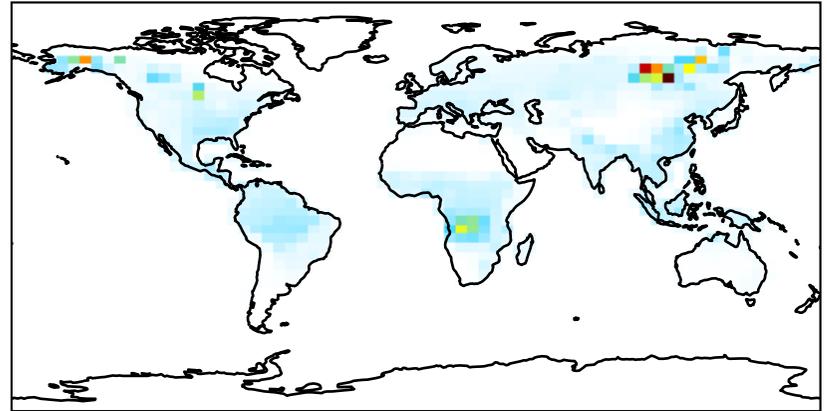


# EmisPRPE\_Total (Jul2019)

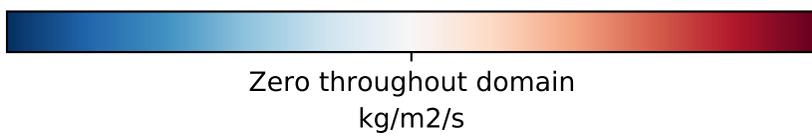
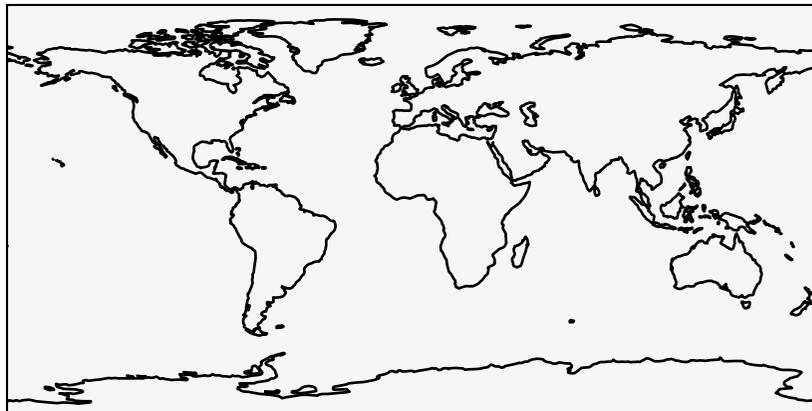
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



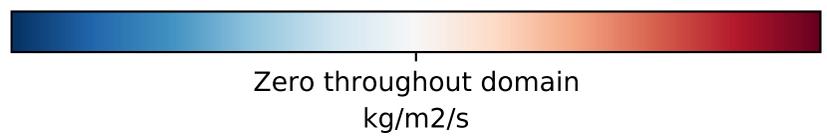
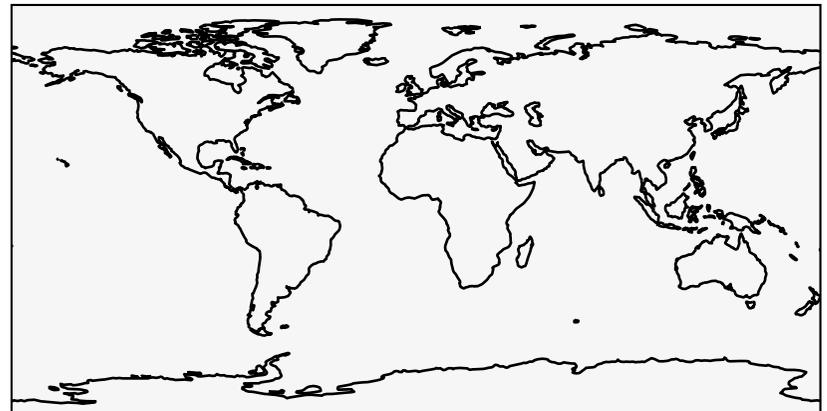
GCC\_13.0.0 (Dev)  
4.0x5.0



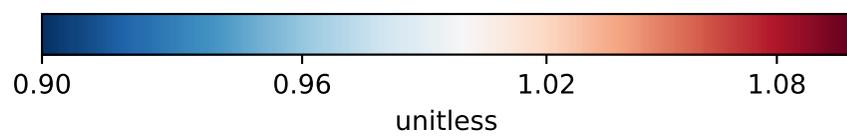
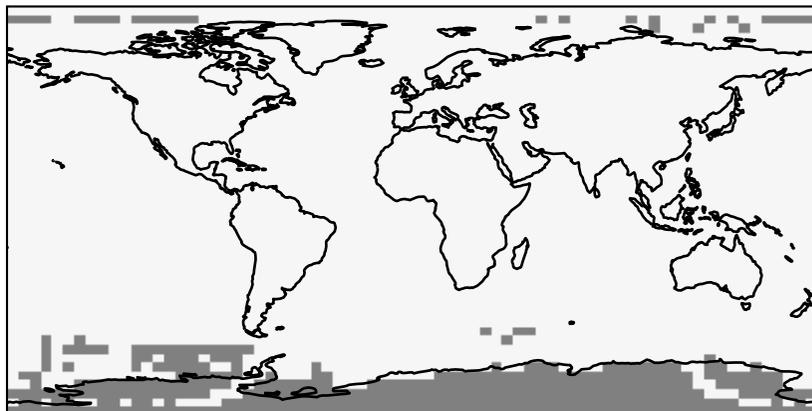
Difference  
Dev - Ref, Dynamic Range



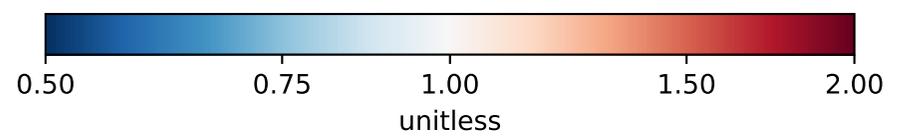
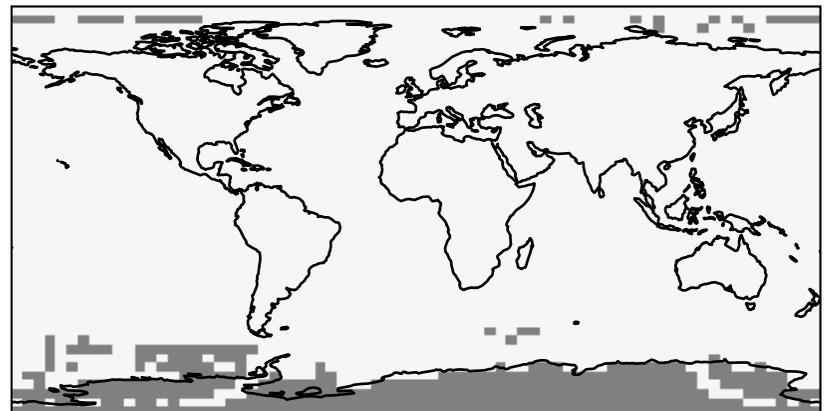
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

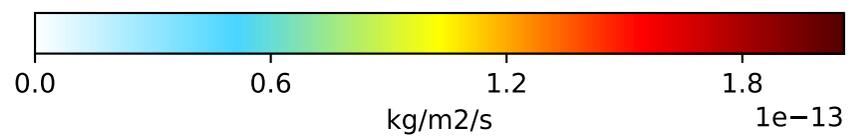
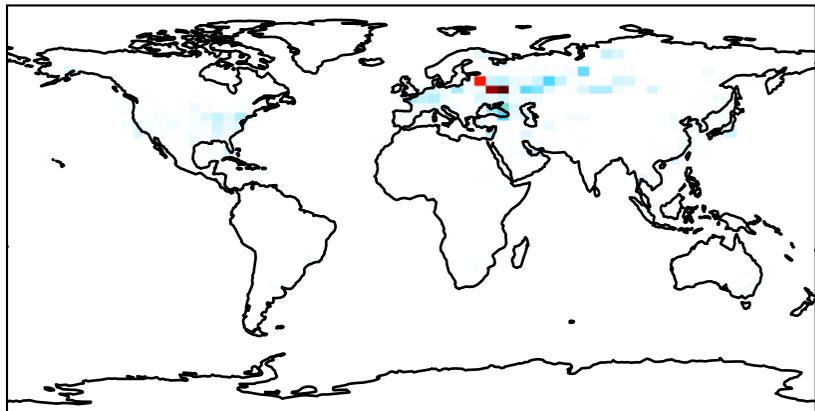


Ratio  
Dev/Ref, Fixed Range

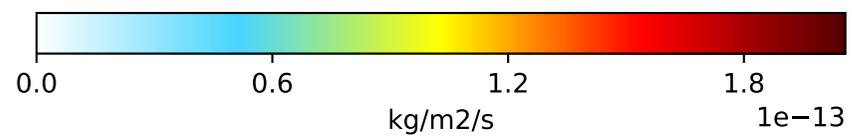
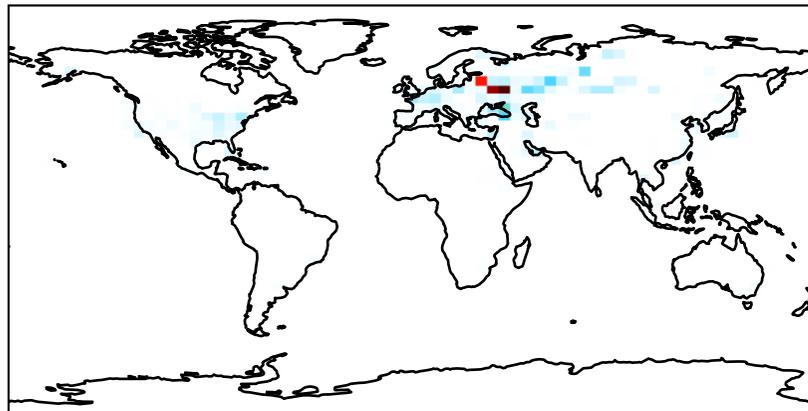


# EmisRCHO\_Total (Jul2019)

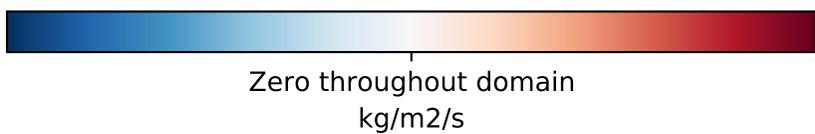
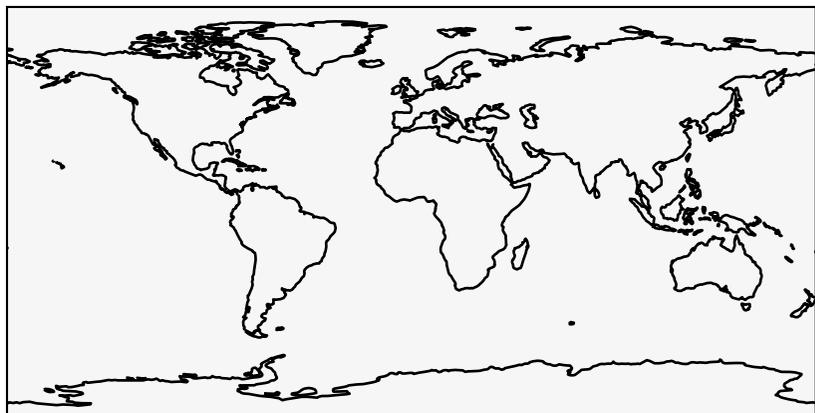
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



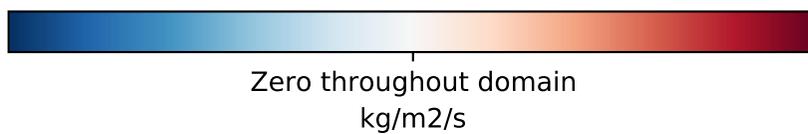
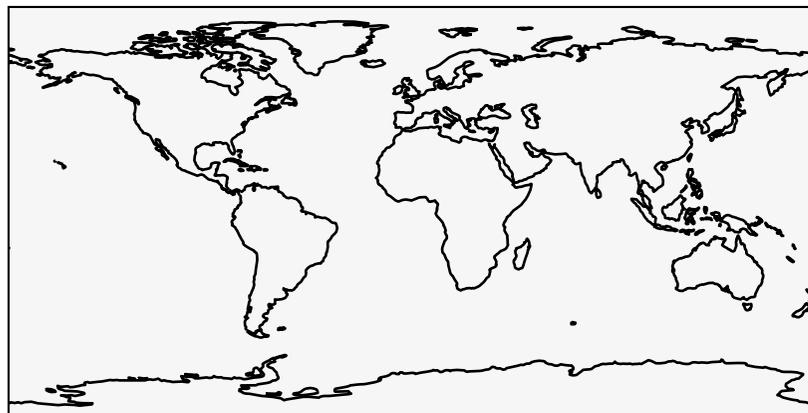
GCC\_13.0.0 (Dev)  
4.0x5.0



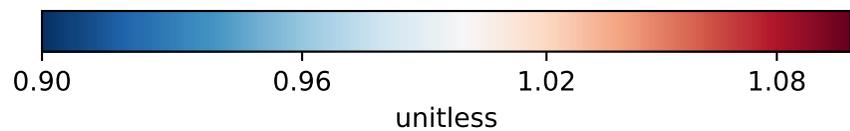
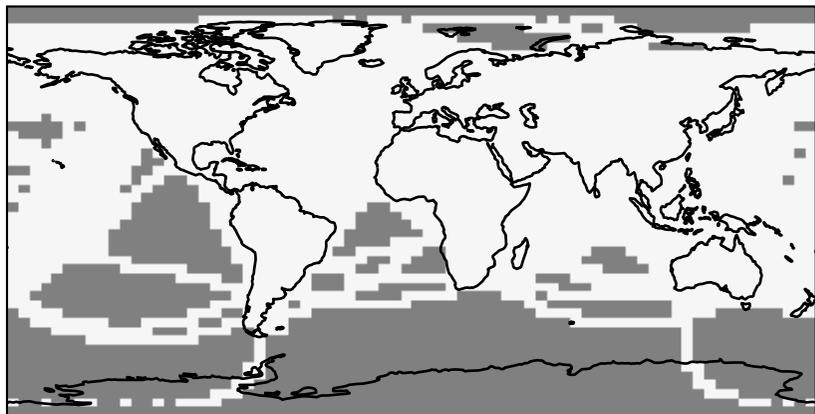
Difference  
Dev - Ref, Dynamic Range



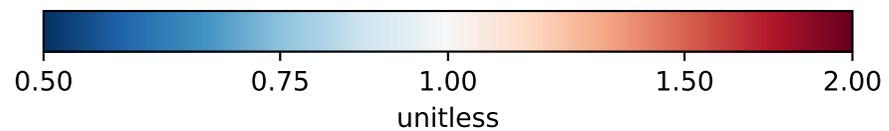
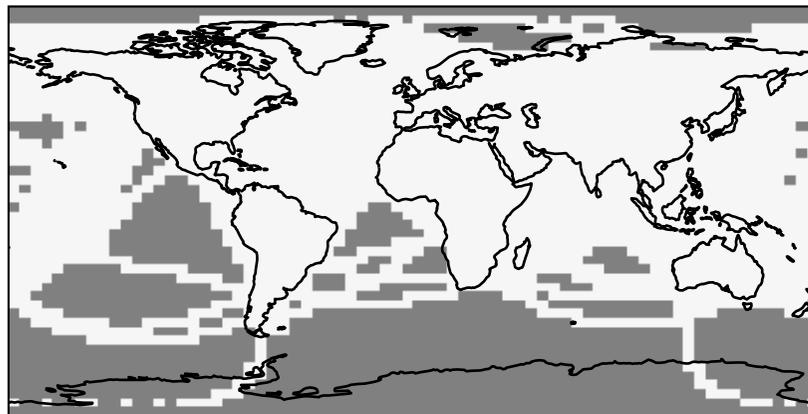
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

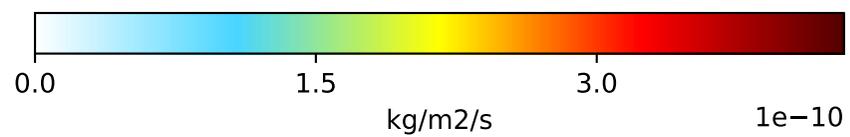
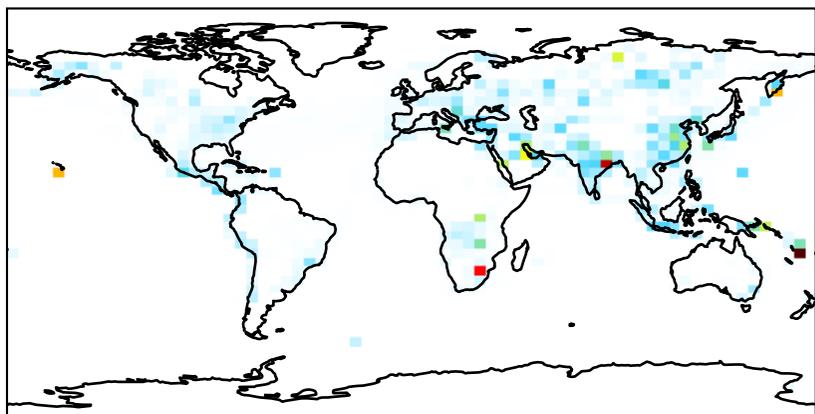


Ratio  
Dev/Ref, Fixed Range

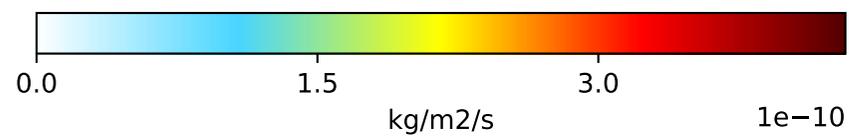
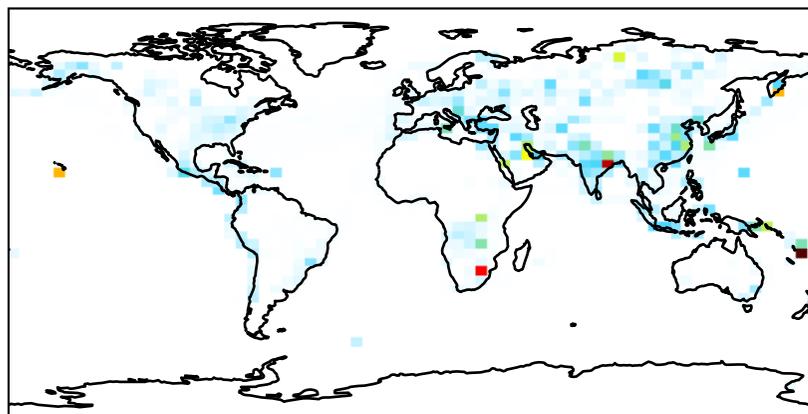


# EmisSO2\_Total (Jul2019)

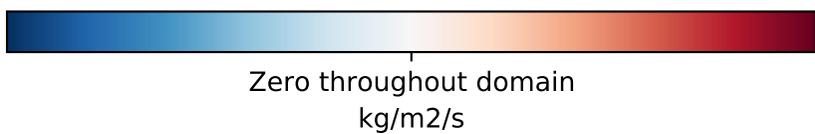
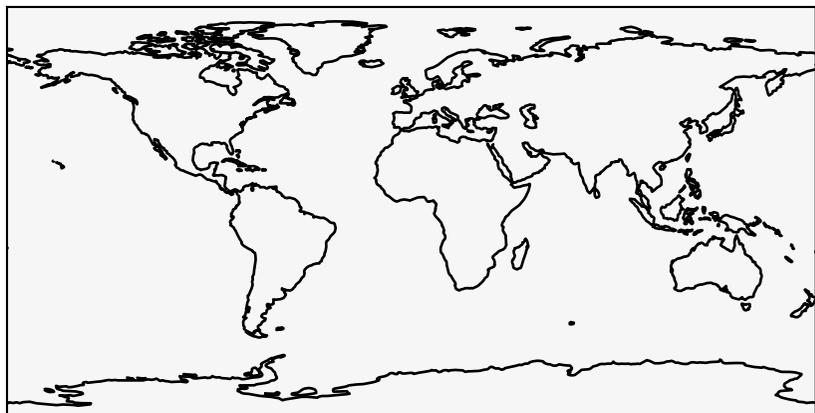
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



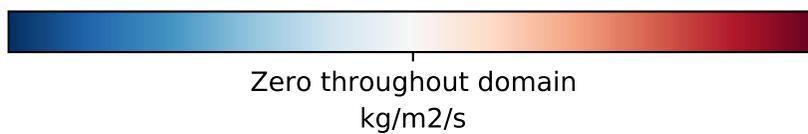
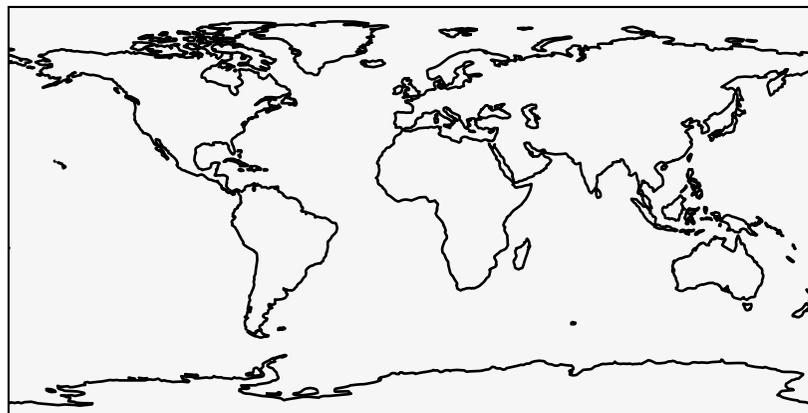
GCC\_13.0.0 (Dev)  
4.0x5.0



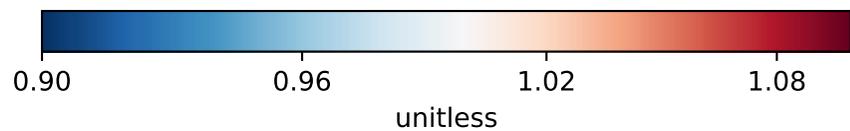
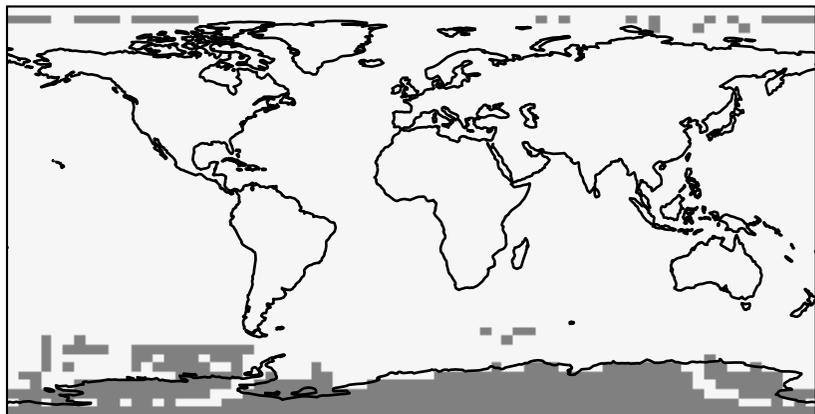
Difference  
Dev - Ref, Dynamic Range



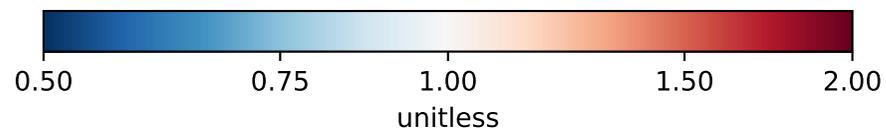
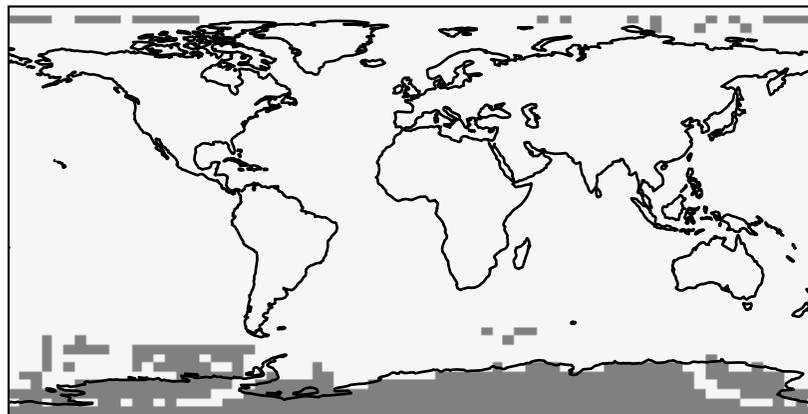
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

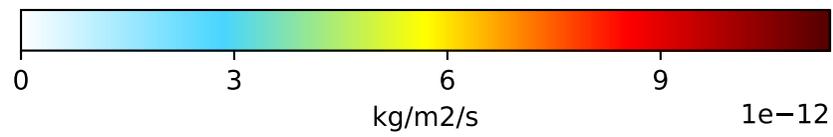
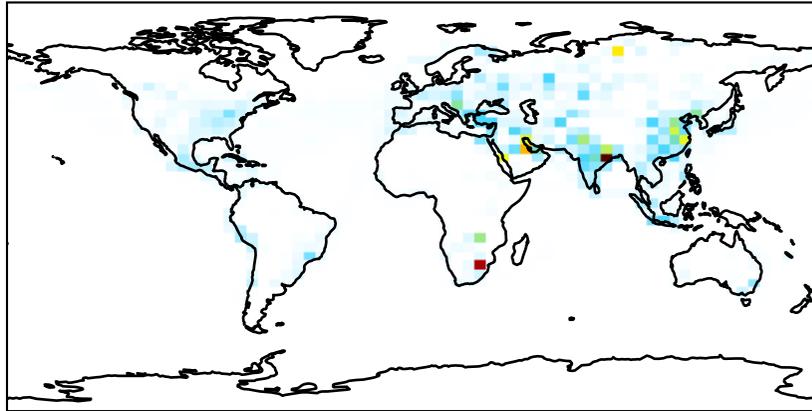


Ratio  
Dev/Ref, Fixed Range

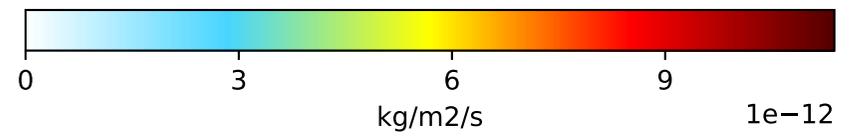
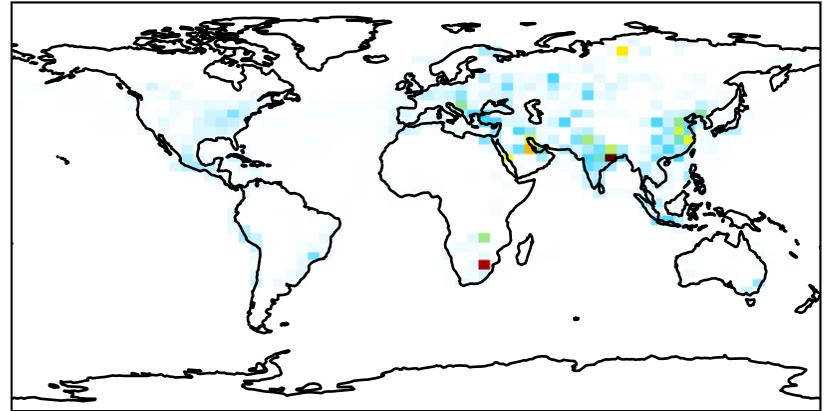


# EmisSO4\_Total (Jul2019)

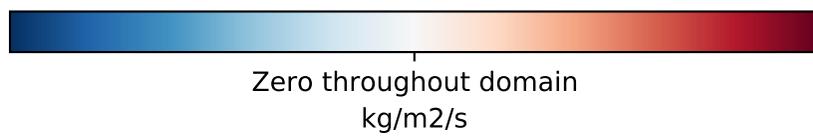
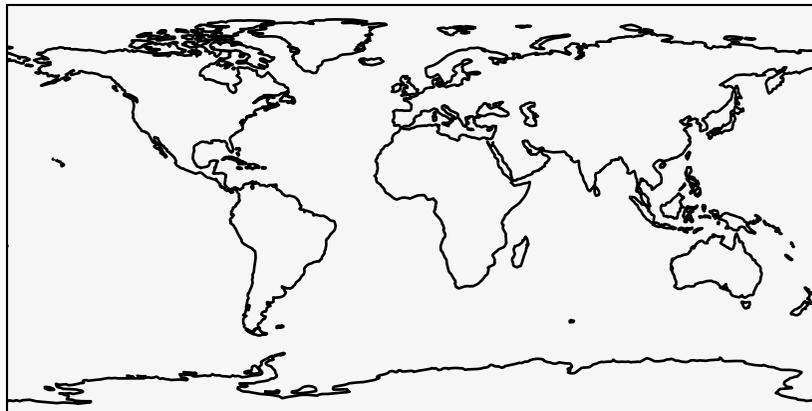
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



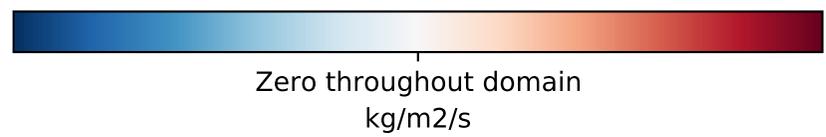
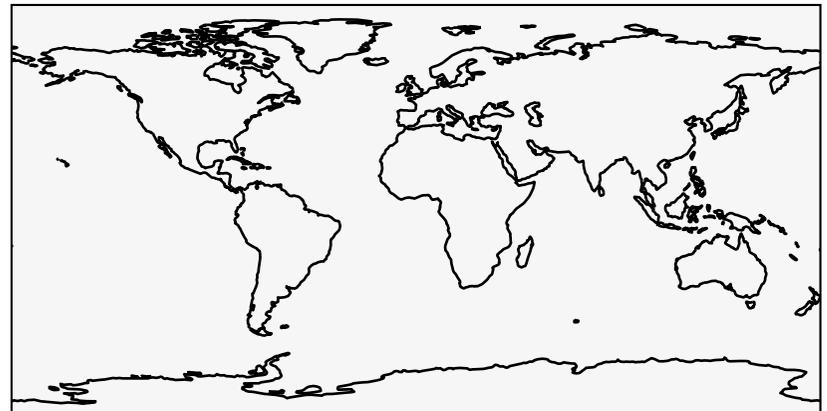
GCC\_13.0.0 (Dev)  
4.0x5.0



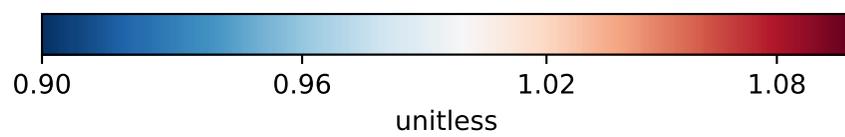
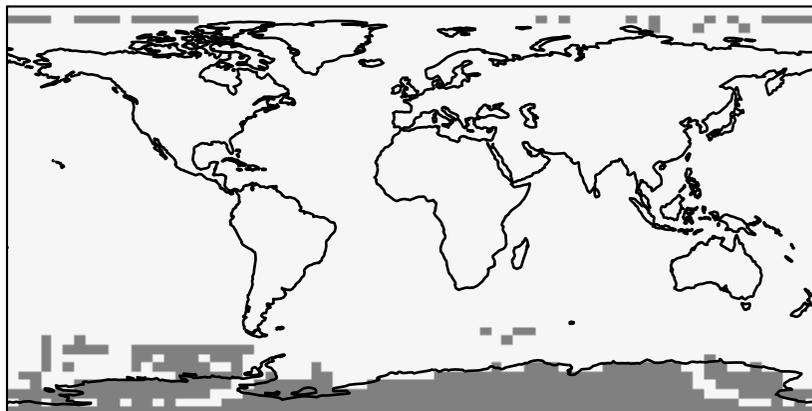
Difference  
Dev - Ref, Dynamic Range



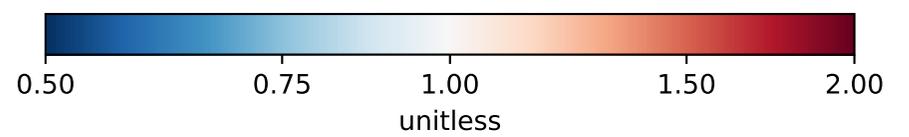
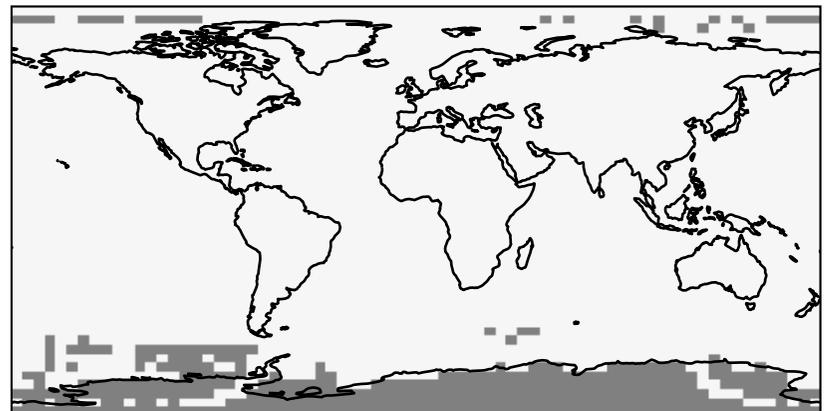
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

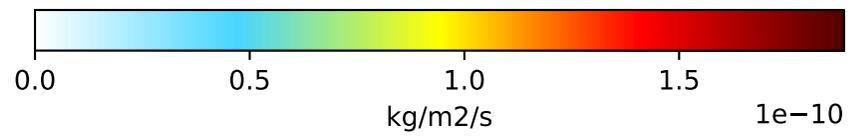
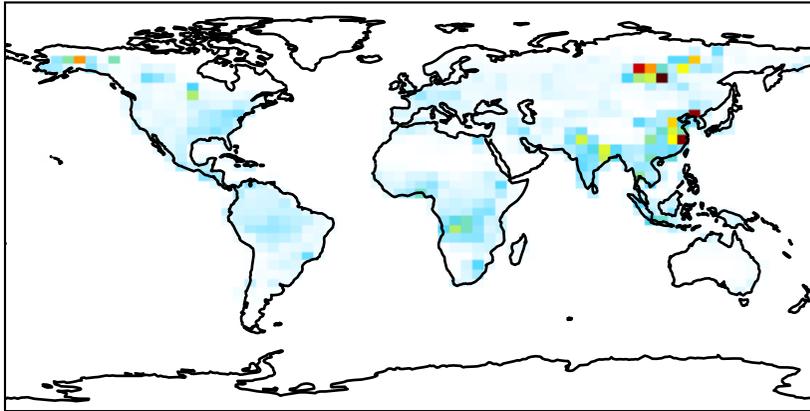


Ratio  
Dev/Ref, Fixed Range

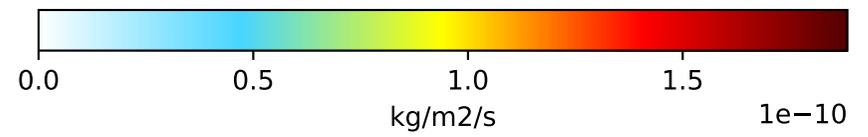
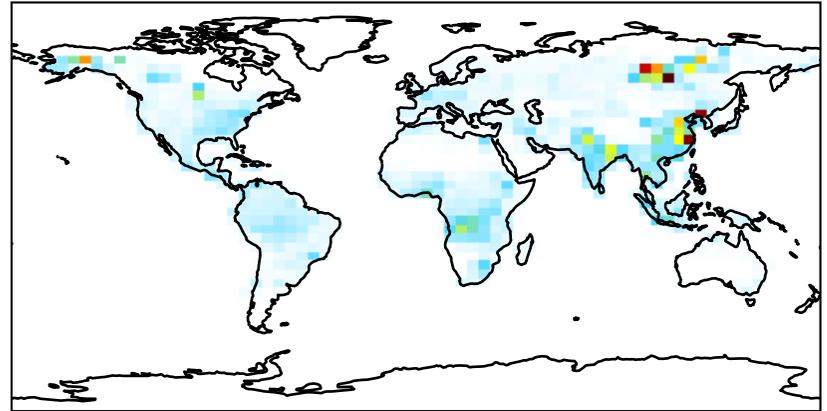


# EmisSOAP\_Total (Jul2019)

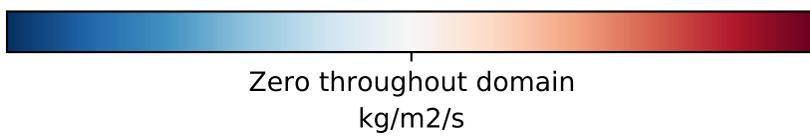
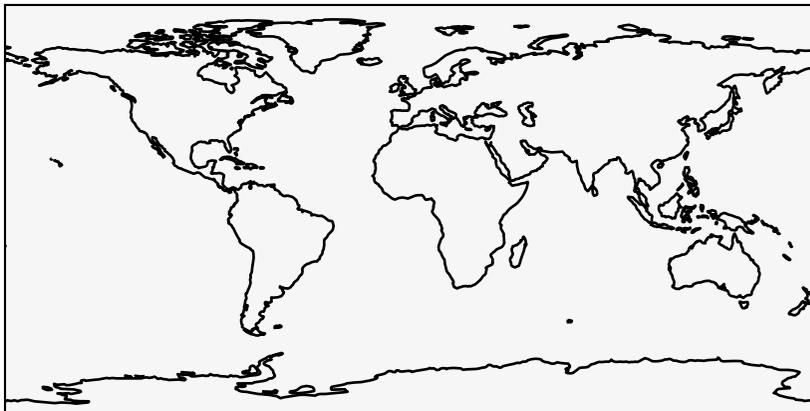
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



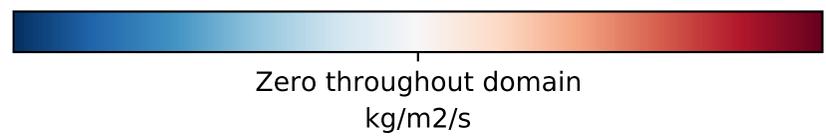
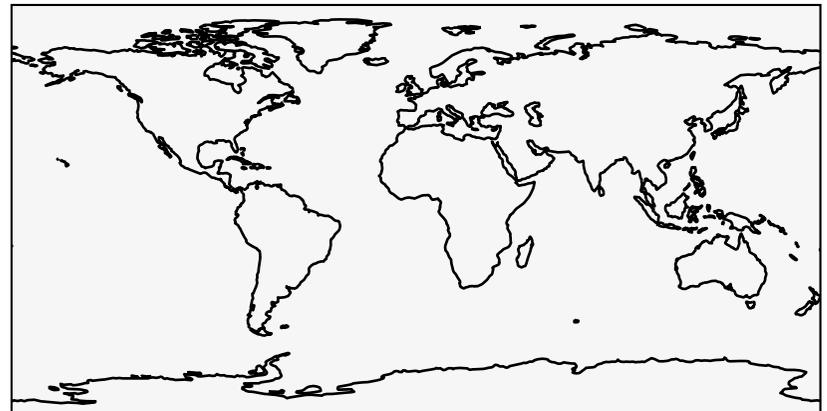
GCC\_13.0.0 (Dev)  
4.0x5.0



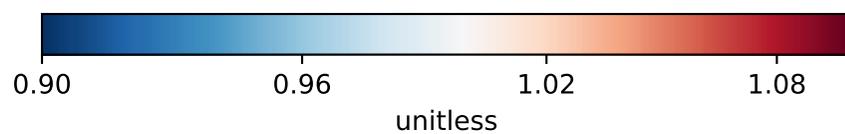
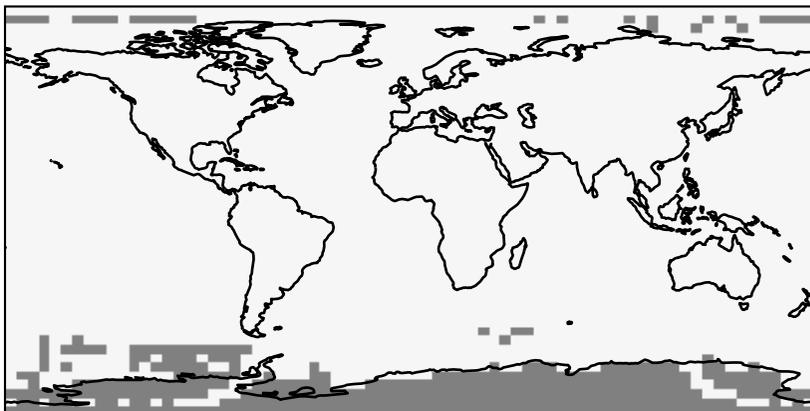
Difference  
Dev - Ref, Dynamic Range



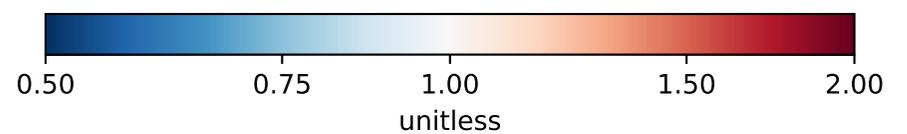
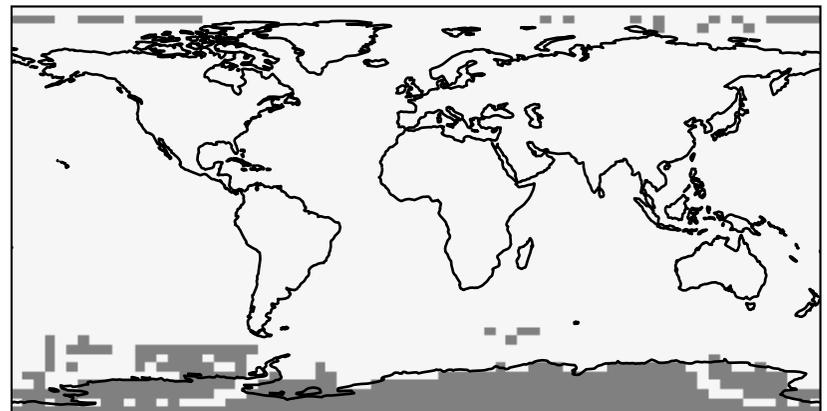
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

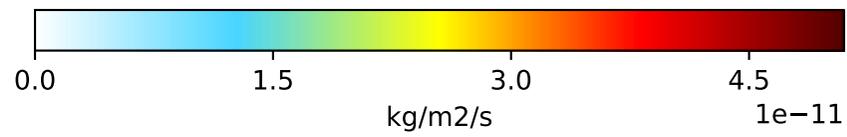
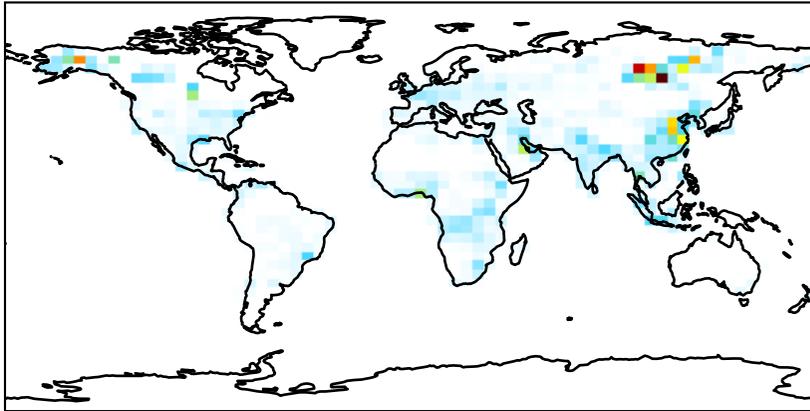


Ratio  
Dev/Ref, Fixed Range

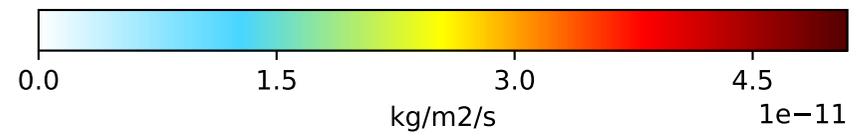
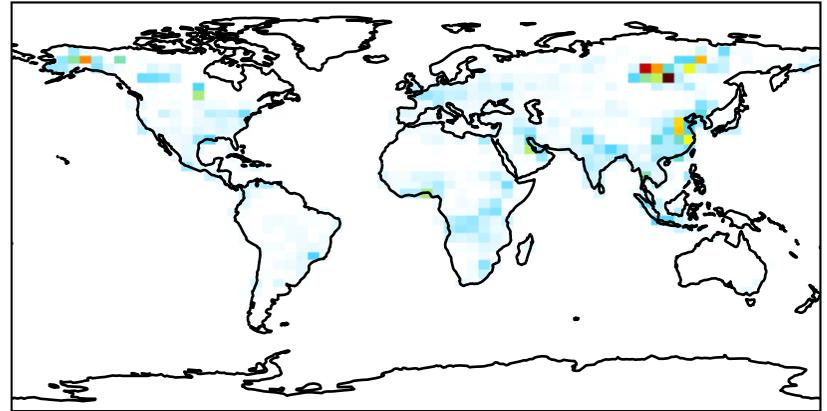


# EmisTOLU\_Total (Jul2019)

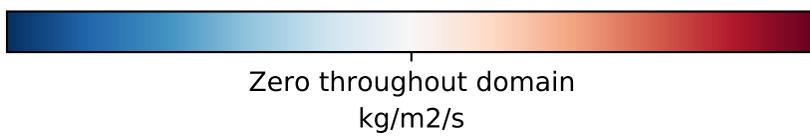
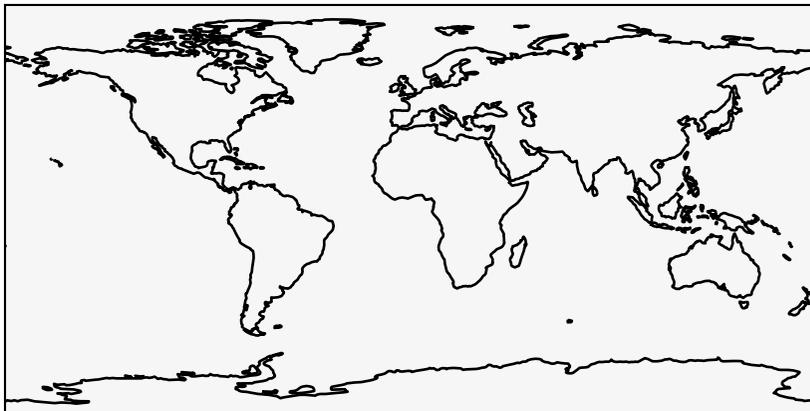
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



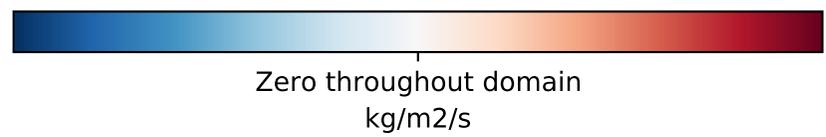
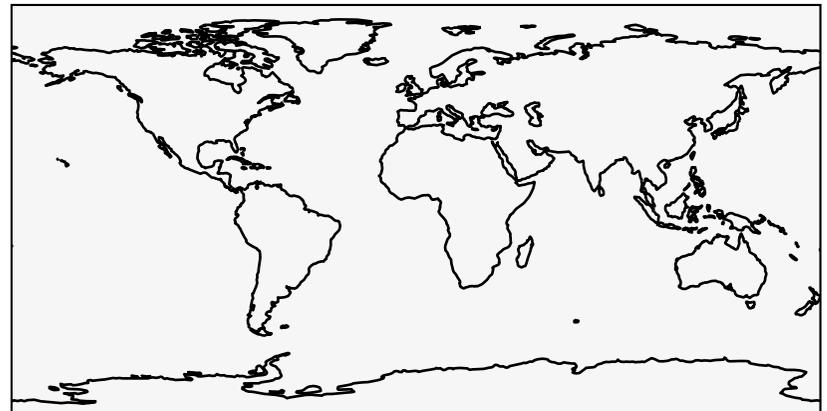
GCC\_13.0.0 (Dev)  
4.0x5.0



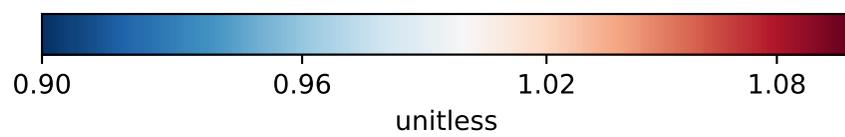
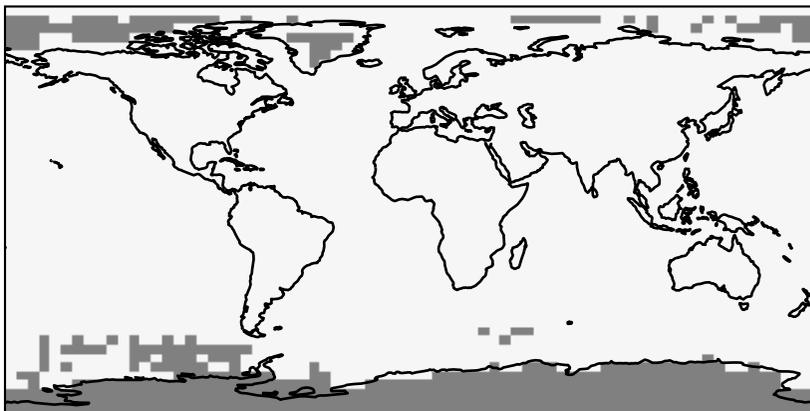
Difference  
Dev - Ref, Dynamic Range



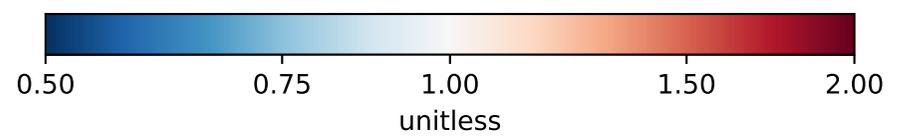
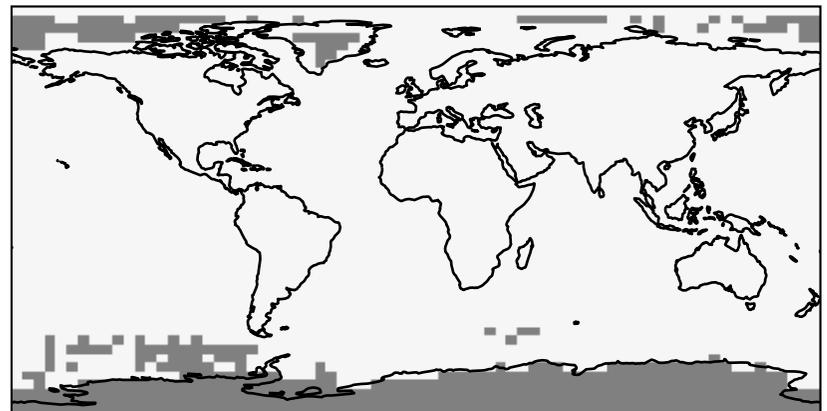
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

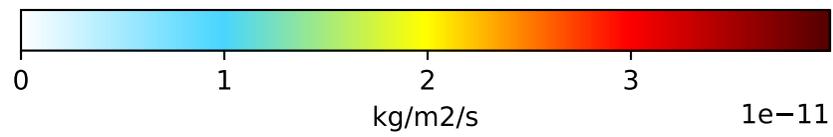
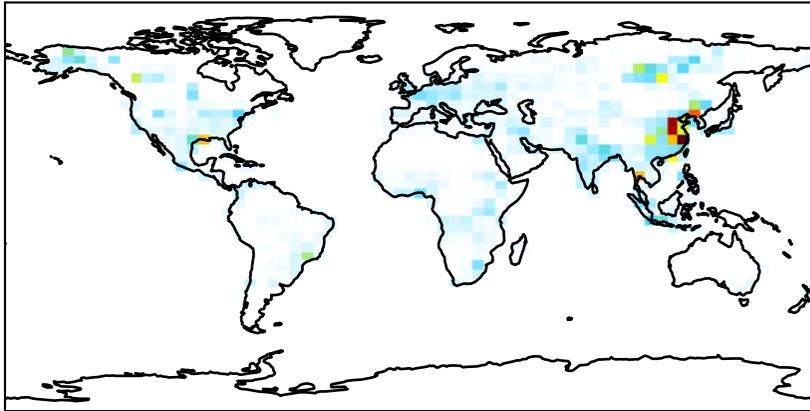


Ratio  
Dev/Ref, Fixed Range

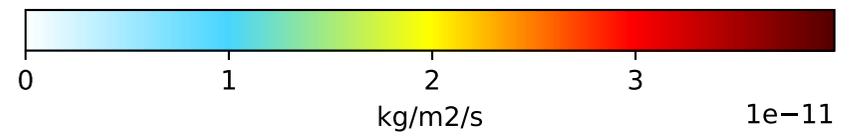
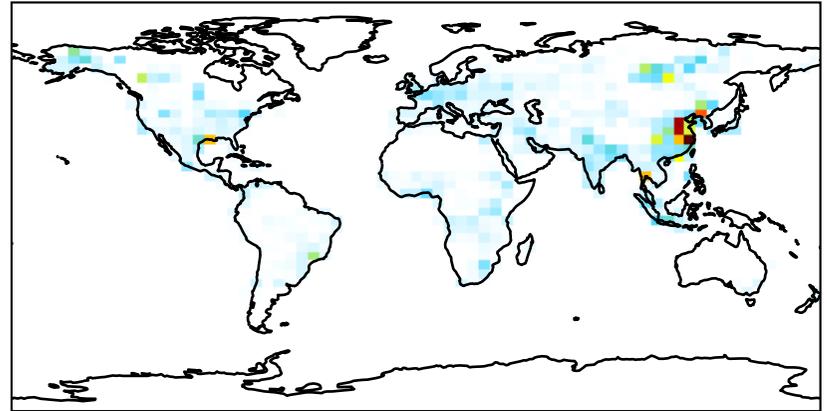


# EmisXYLE\_Total (Jul2019)

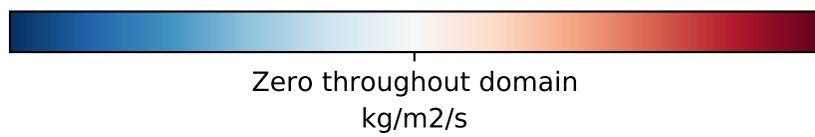
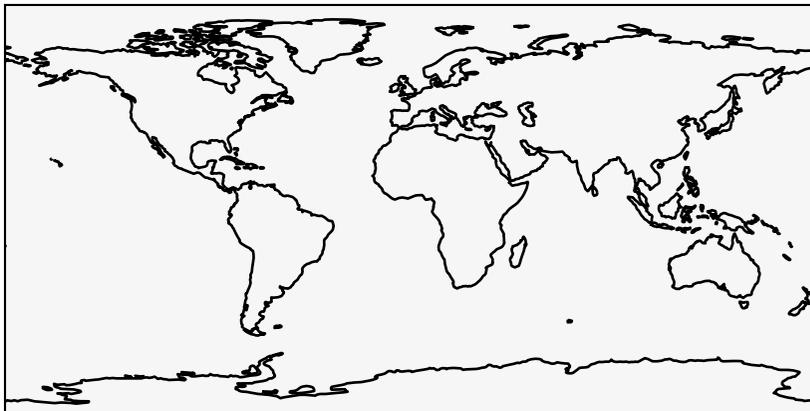
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



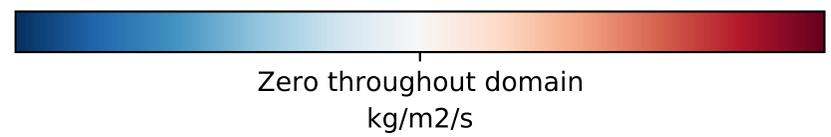
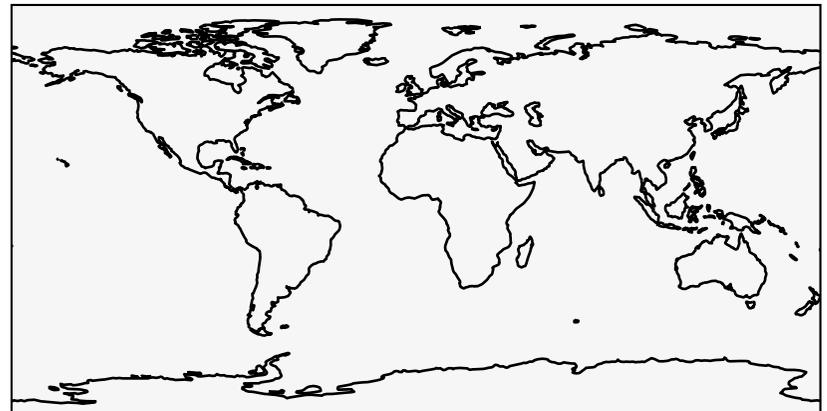
GCC\_13.0.0 (Dev)  
4.0x5.0



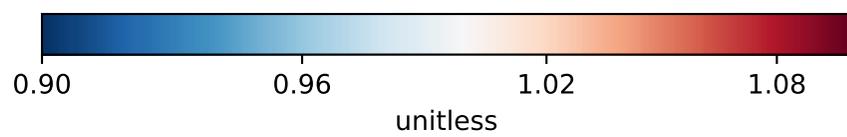
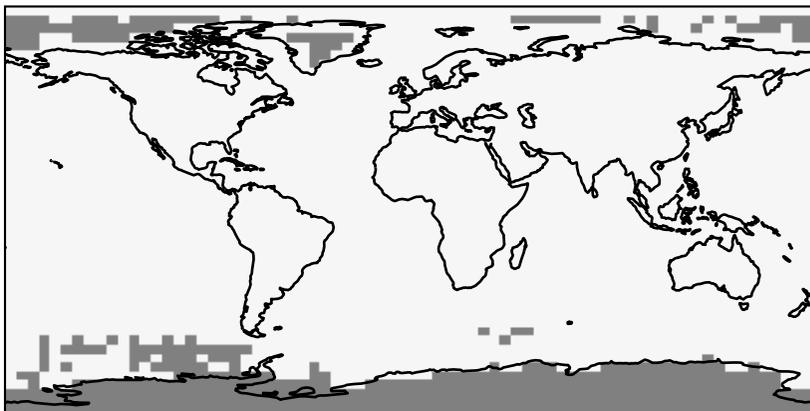
Difference  
Dev - Ref, Dynamic Range



Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

