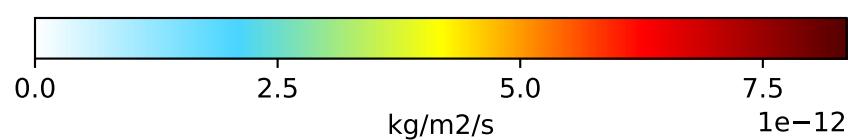
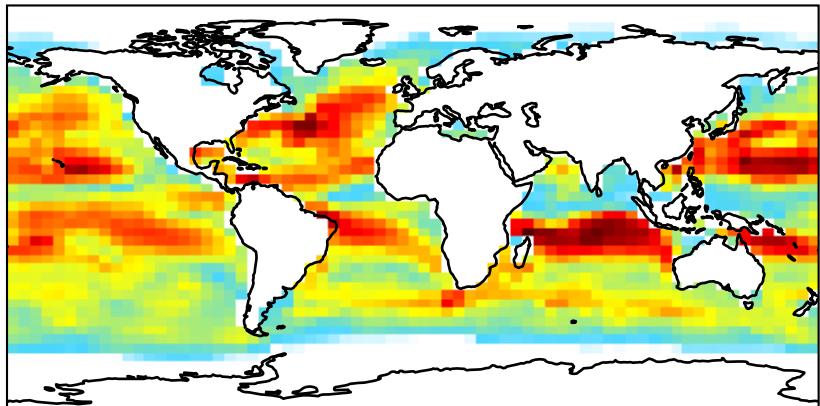
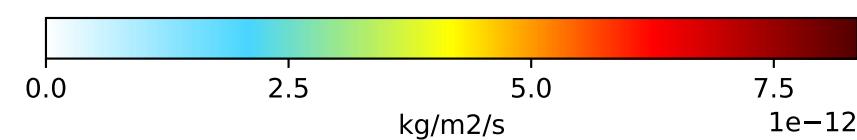
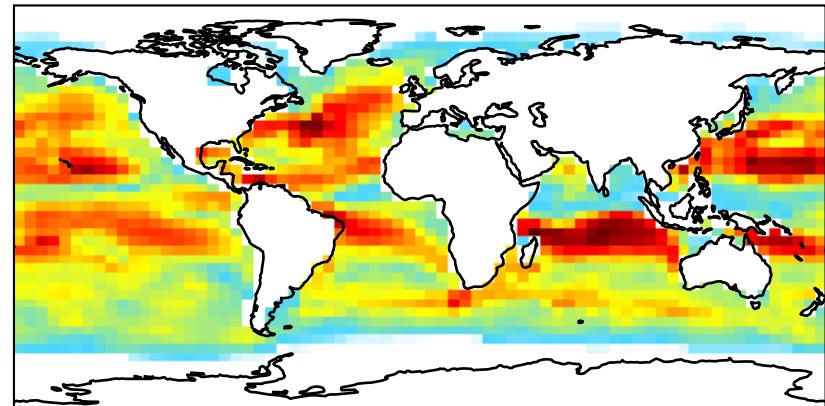


# EmisACET\_Ocean (Oct2019)

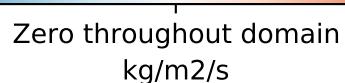
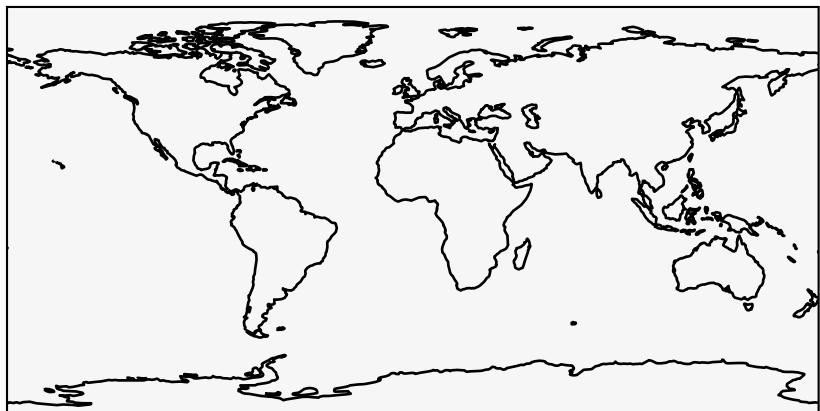
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



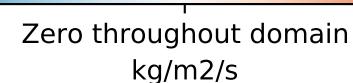
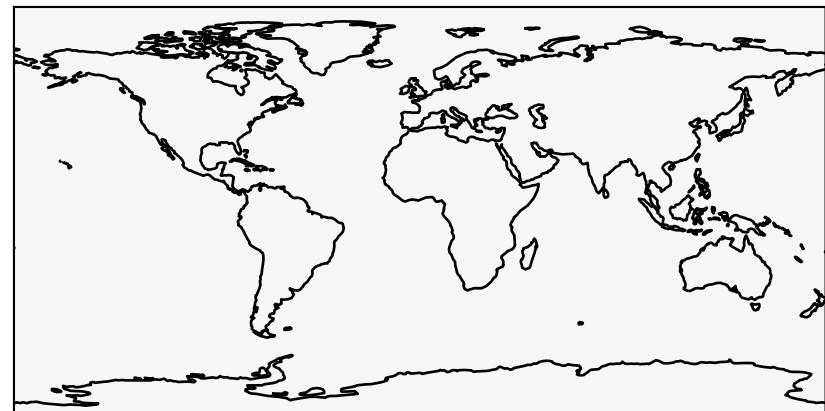
GCC\_13.0.0 (Dev)  
4.0x5.0



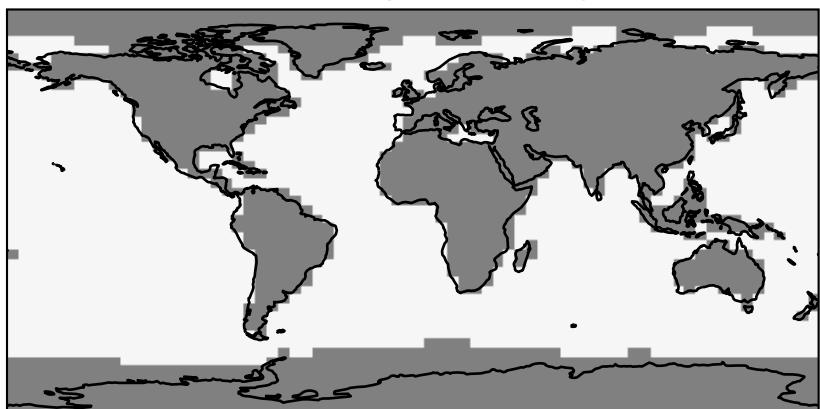
Difference  
Dev - Ref, Dynamic Range



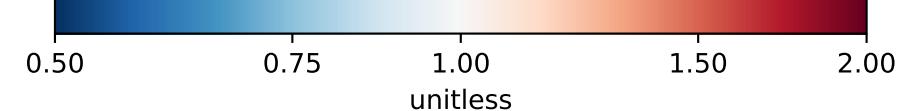
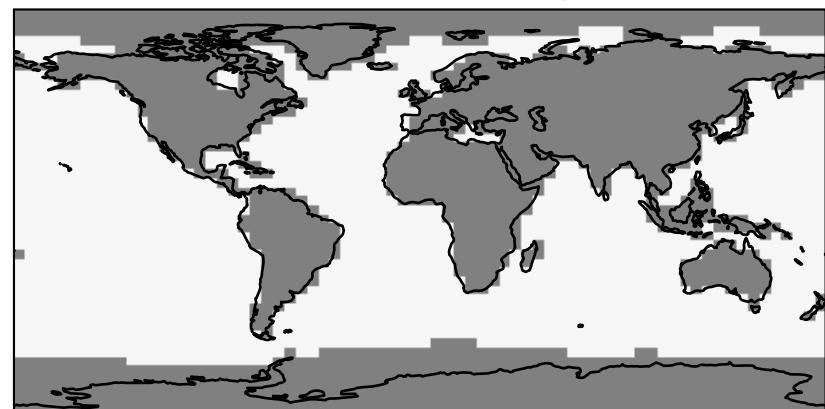
Difference  
Dev - Ref, Restricted Range [5%,95%]



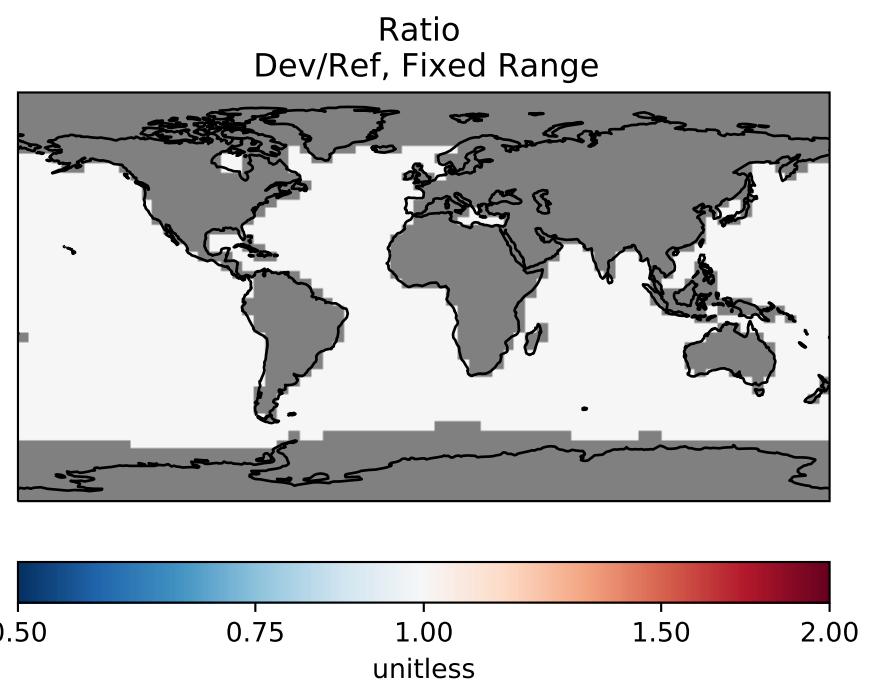
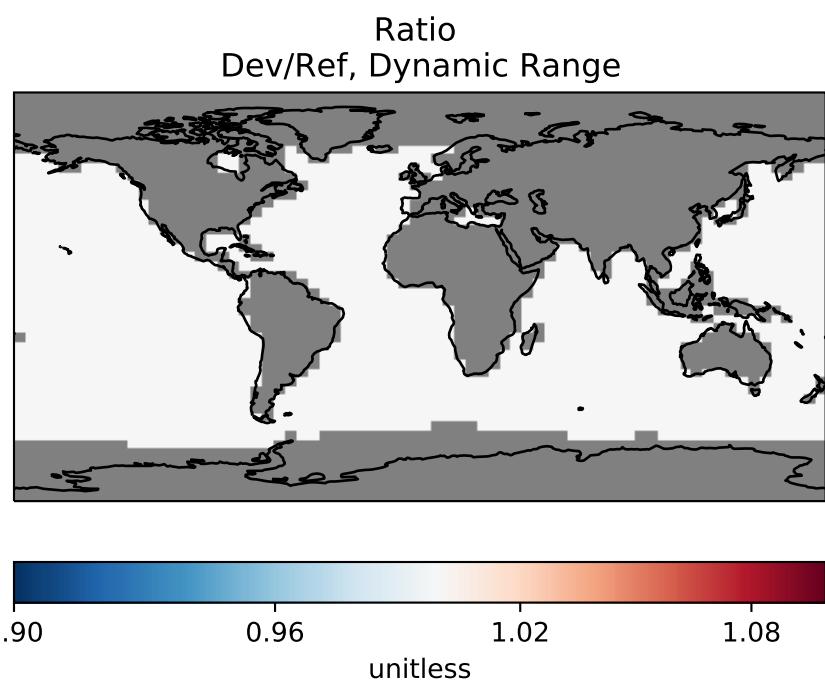
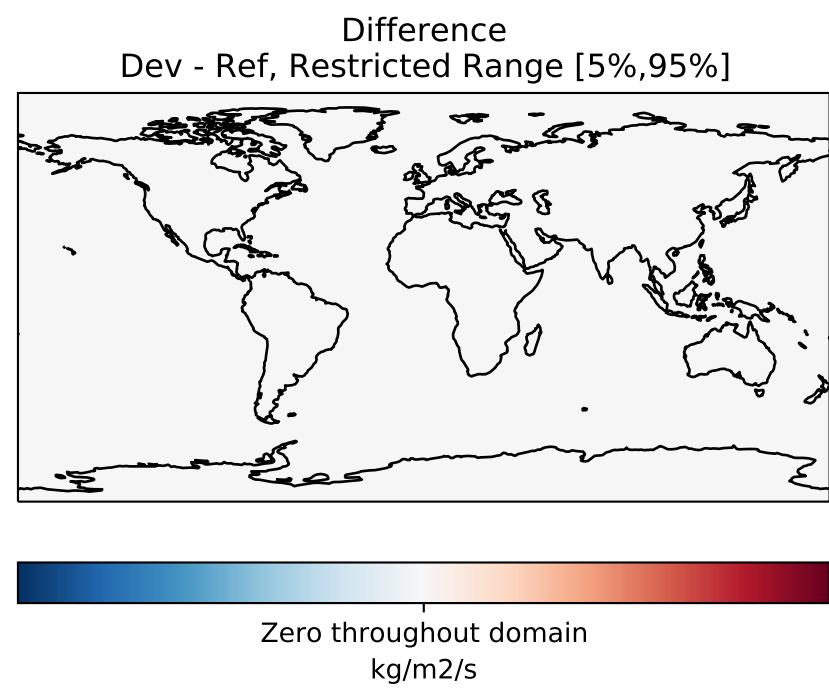
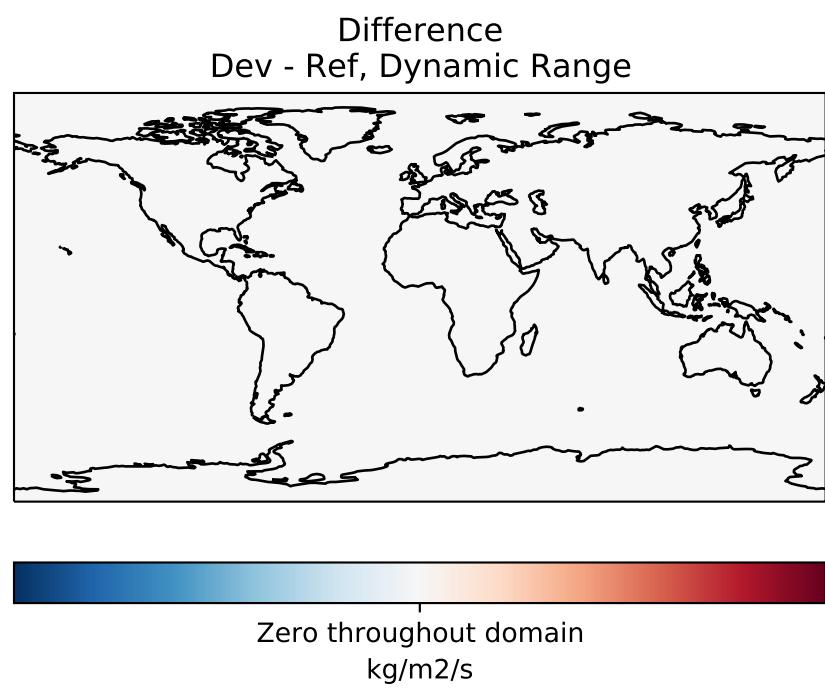
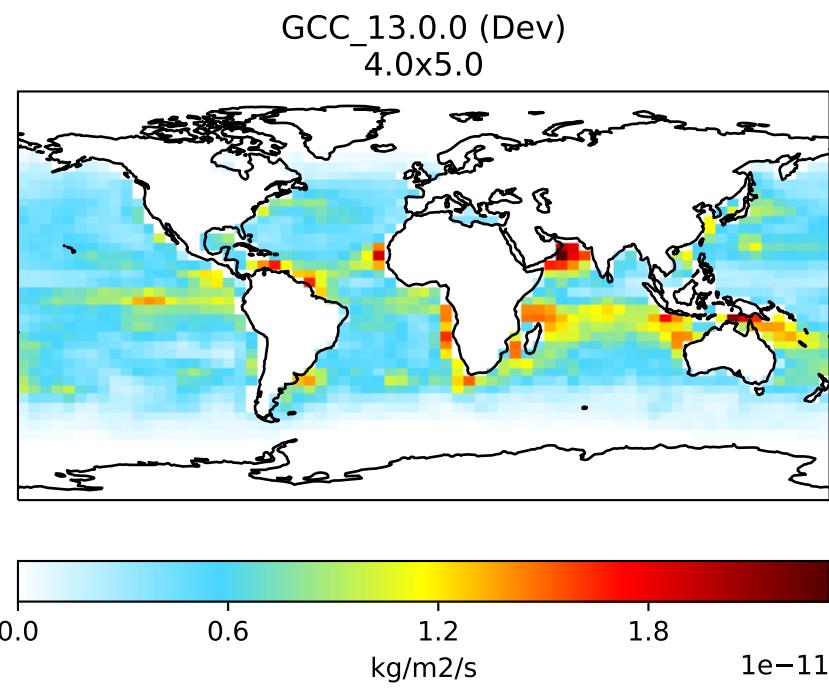
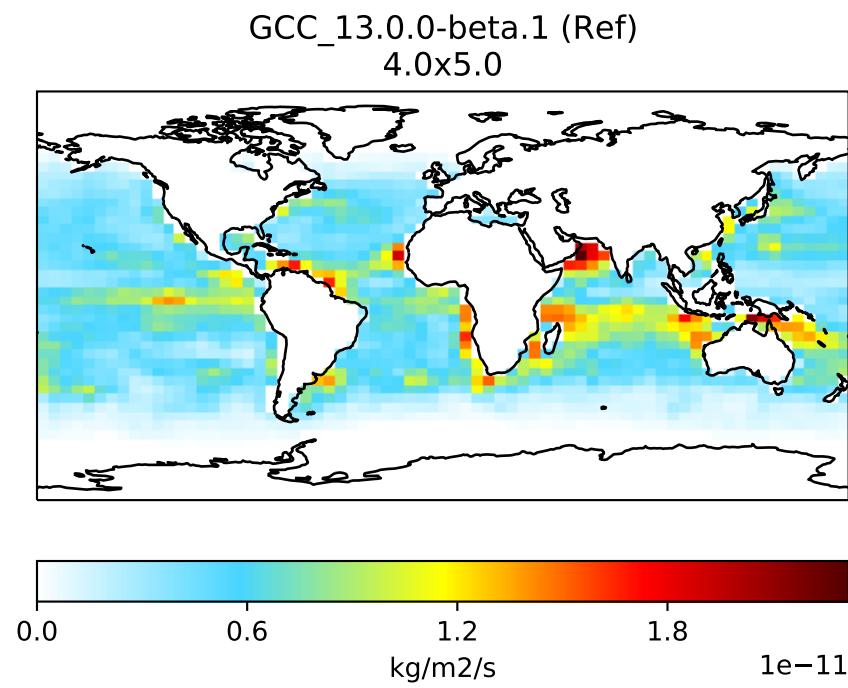
Ratio  
Dev/Ref, Dynamic Range



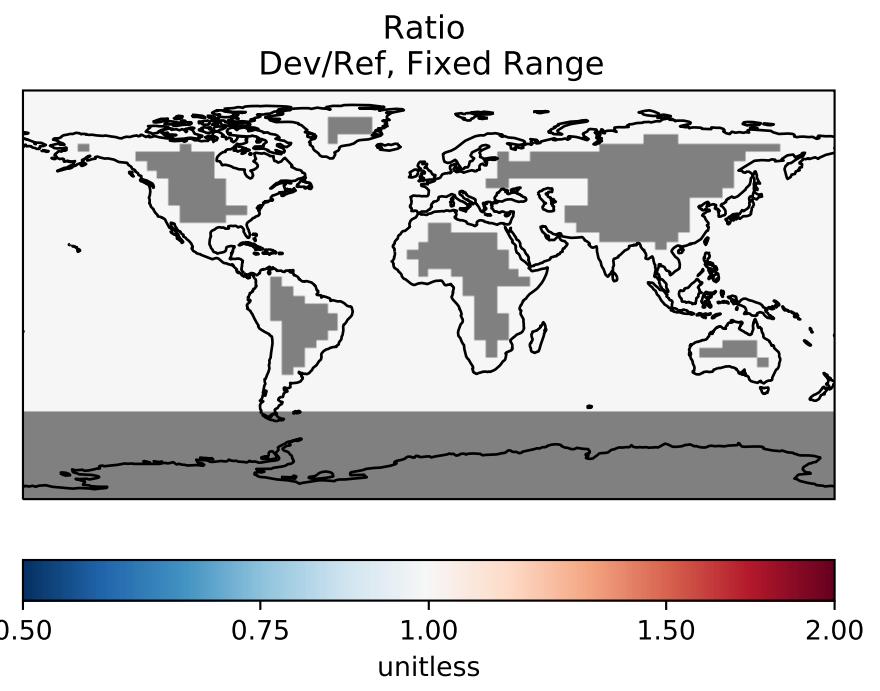
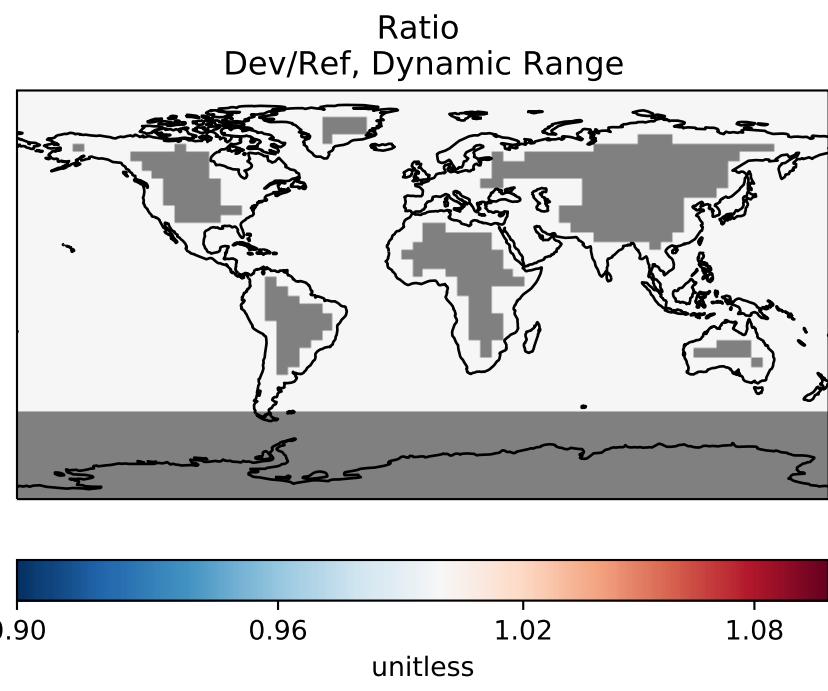
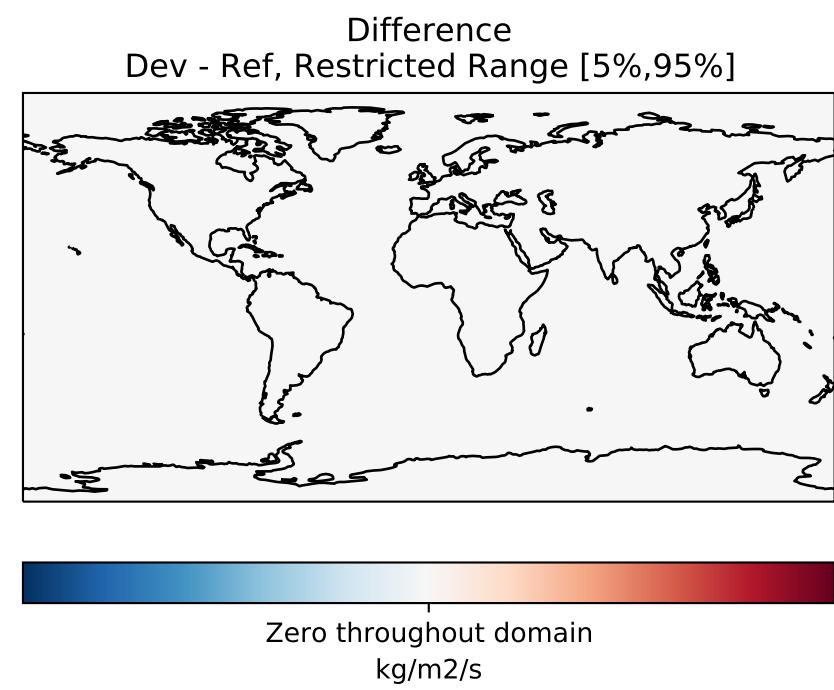
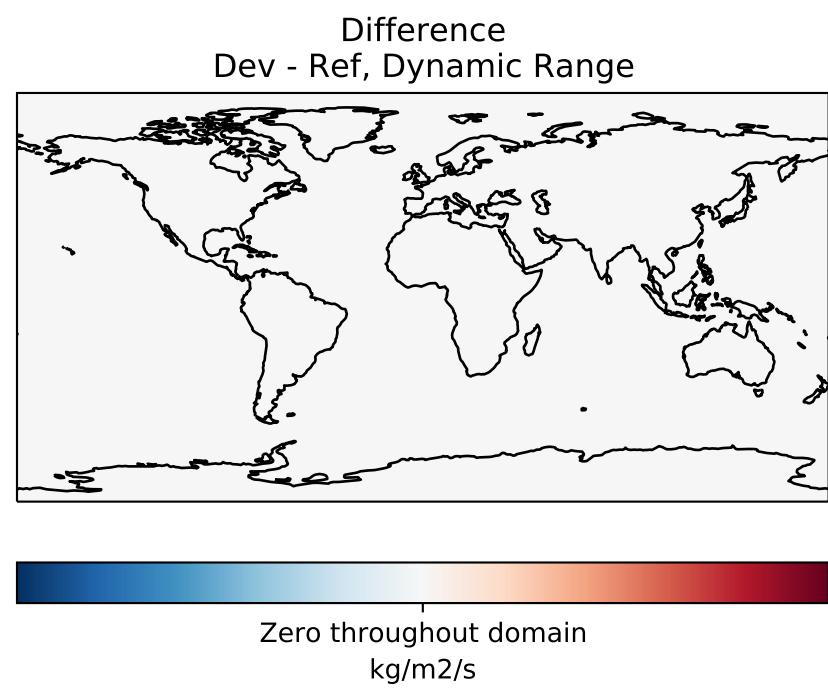
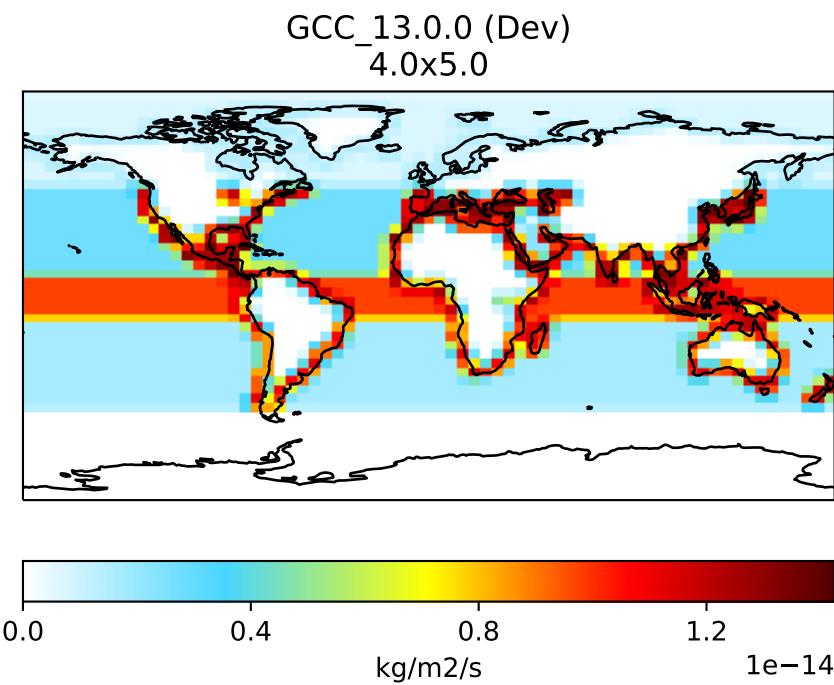
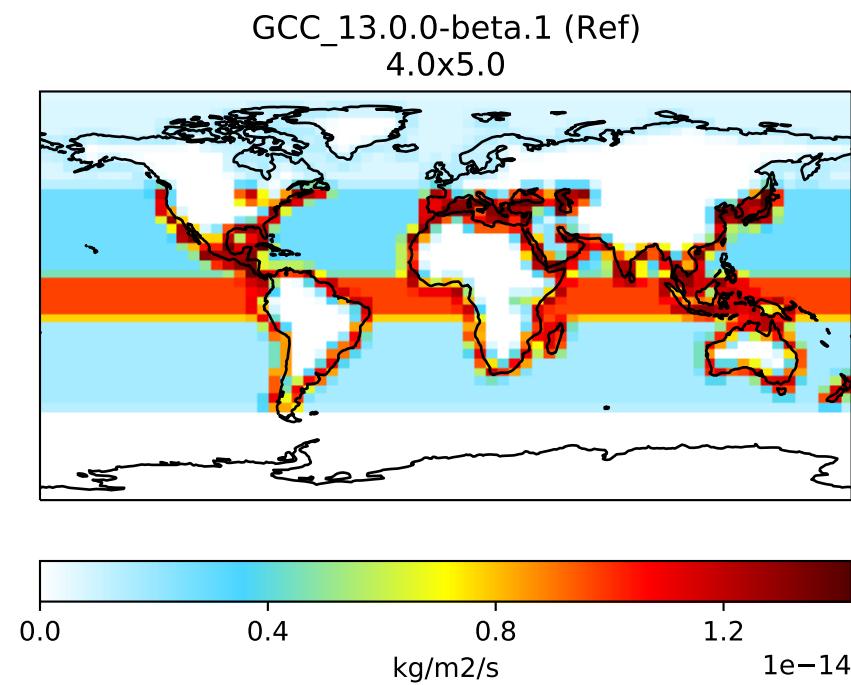
Ratio  
Dev/Ref, Fixed Range



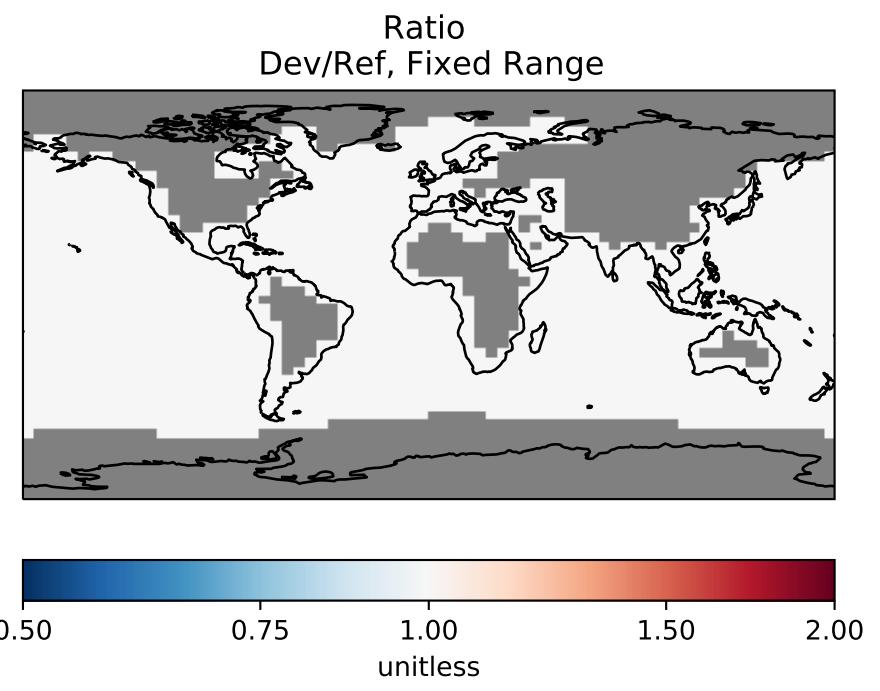
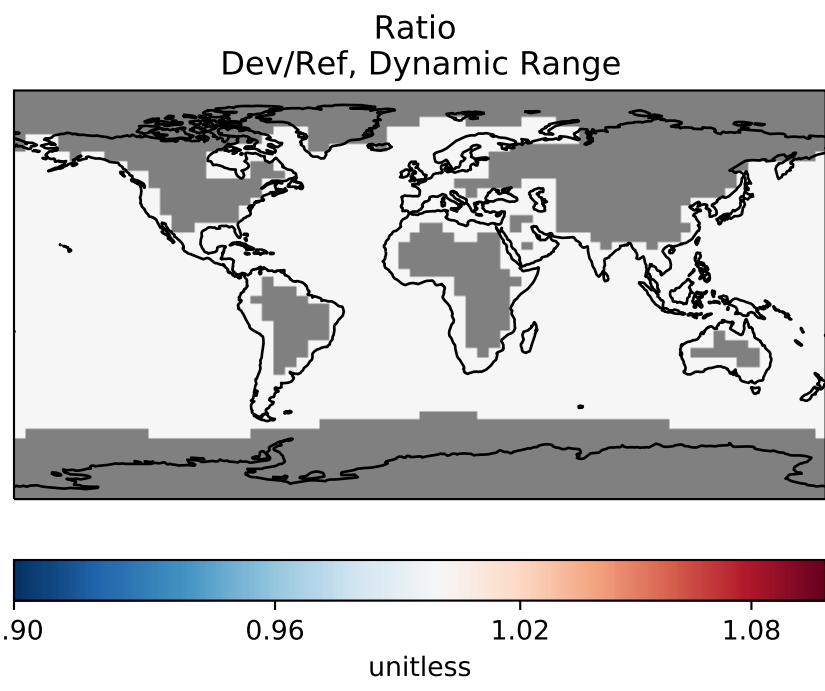
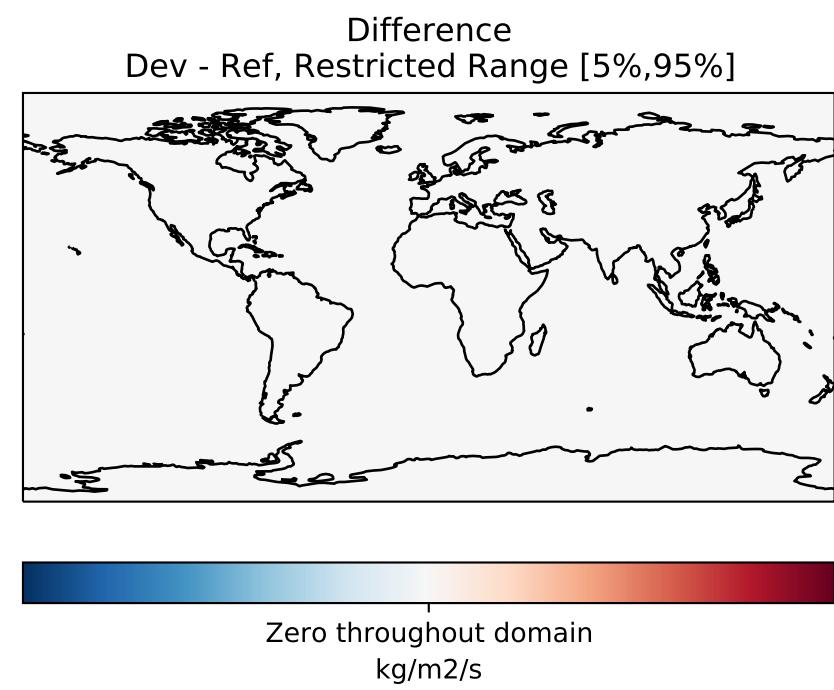
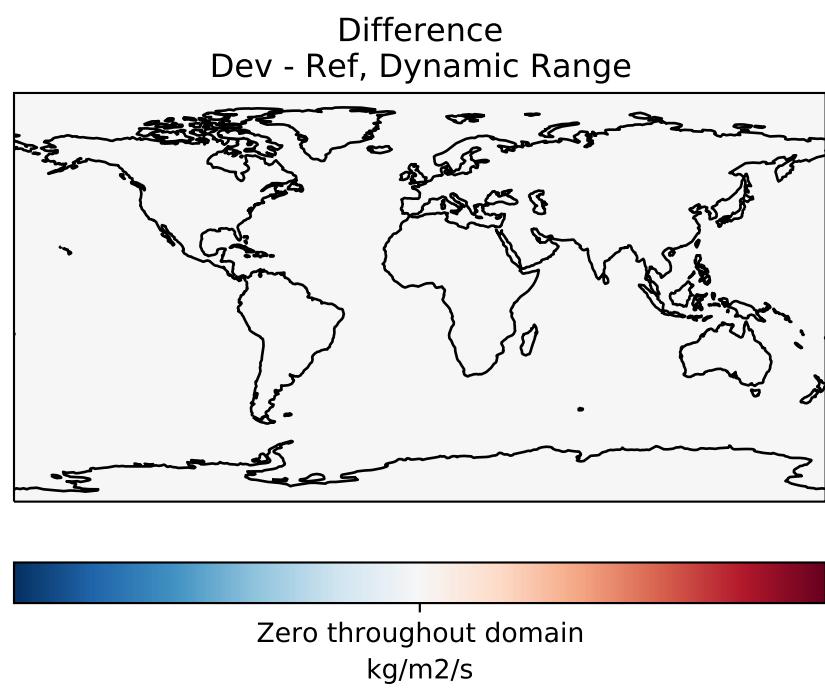
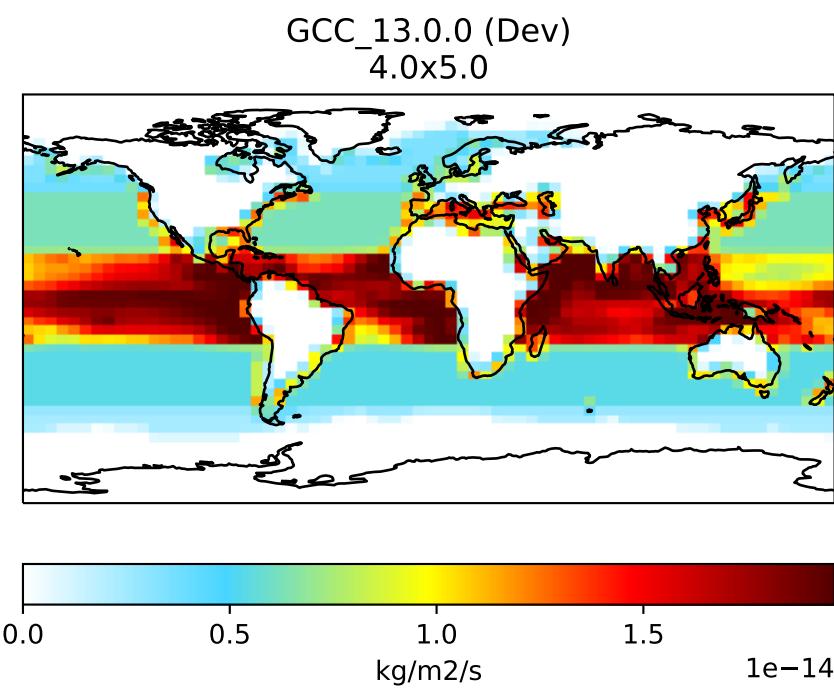
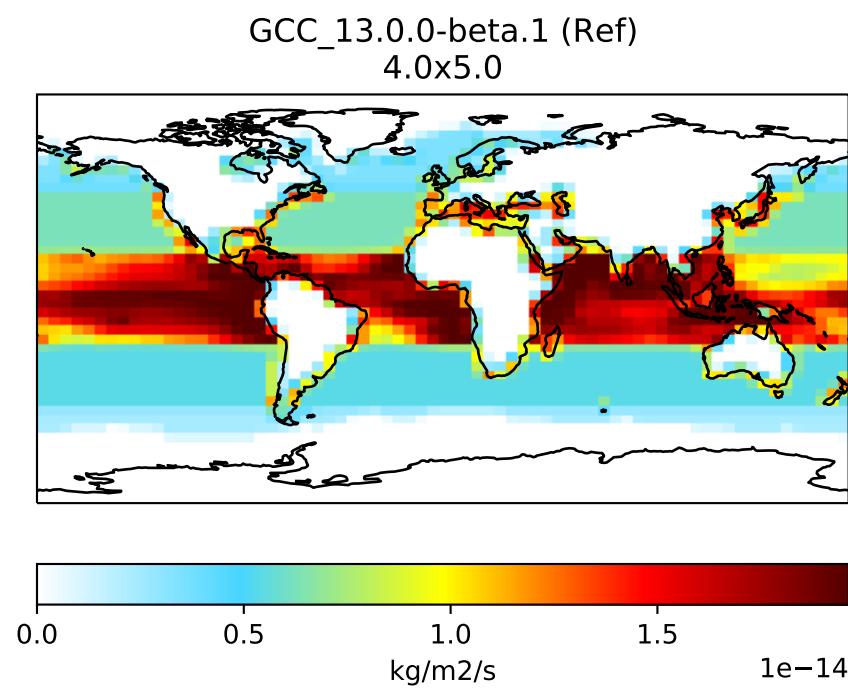
# EmisALD2\_Ocean (Oct2019)



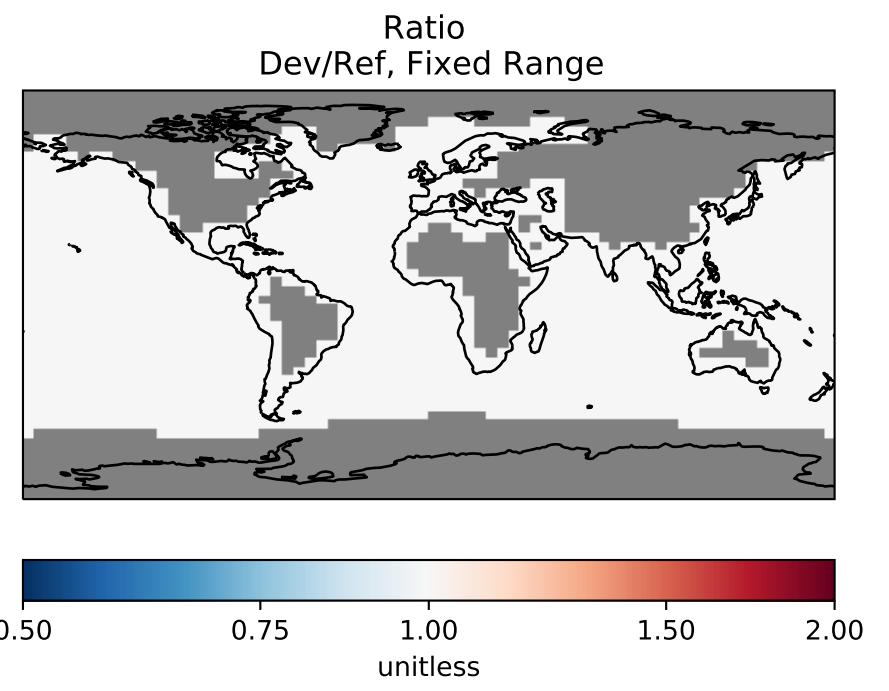
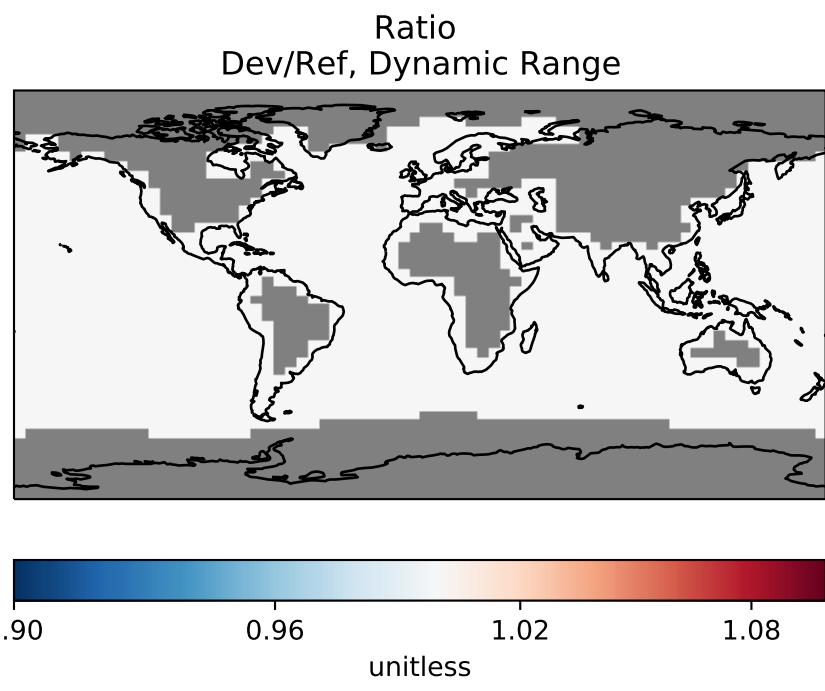
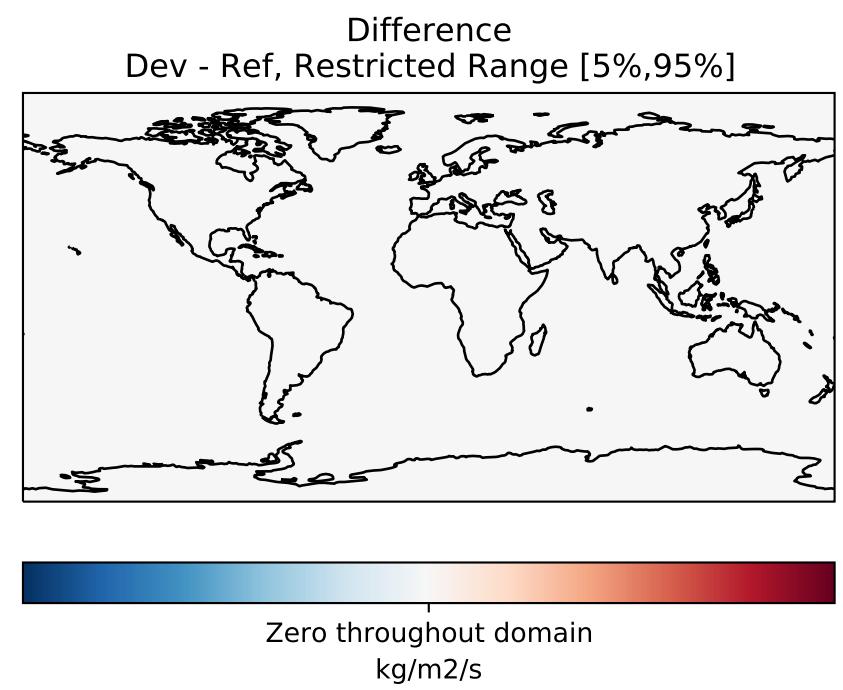
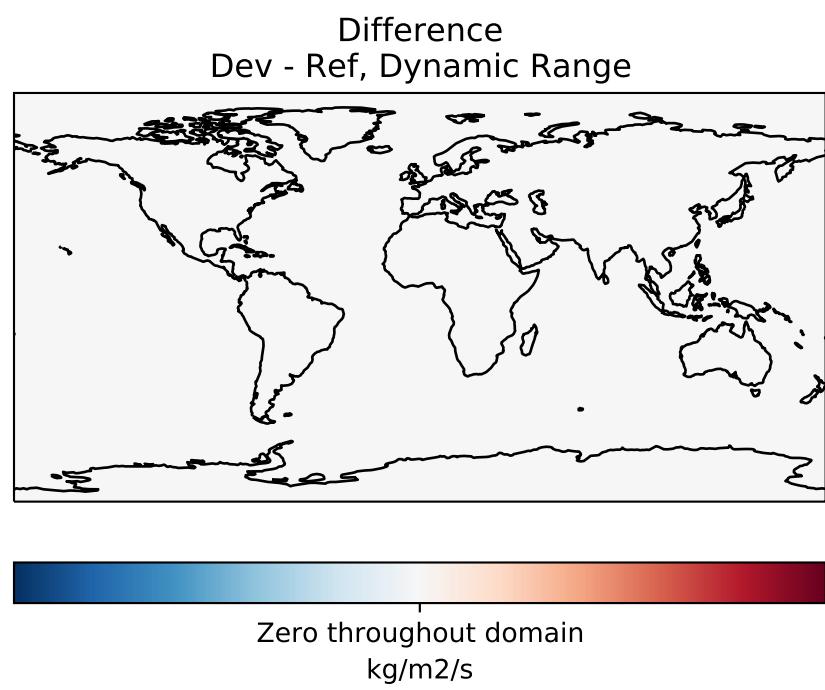
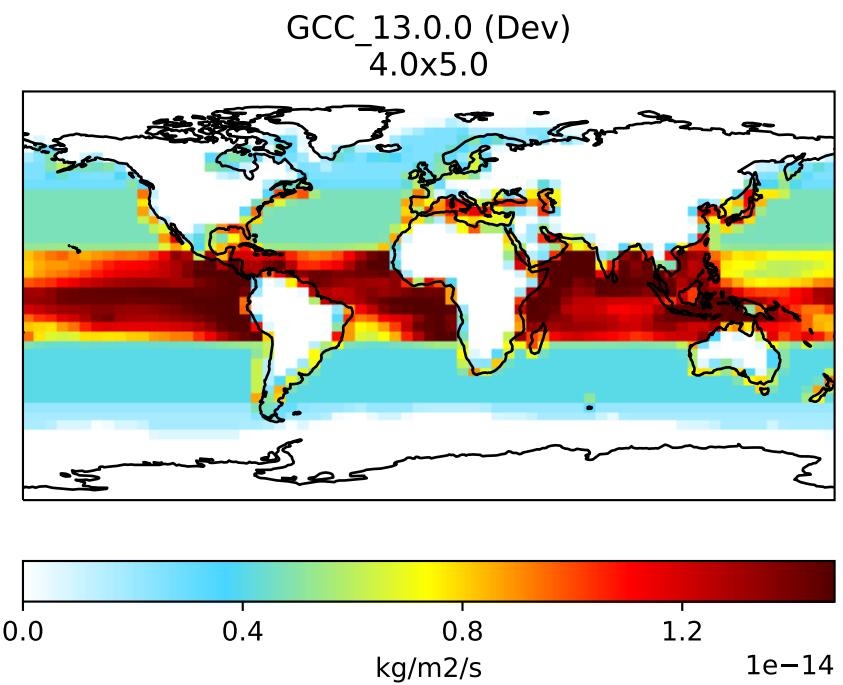
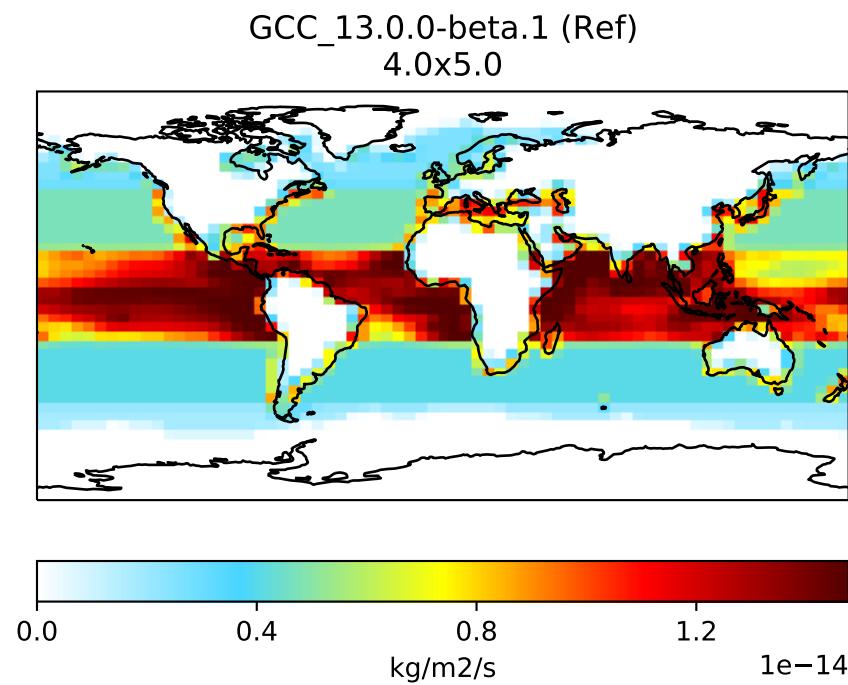
# EmisCH2Br2\_Ocean (Oct2019)



# EmisCH2I2\_Ocean (Oct2019)

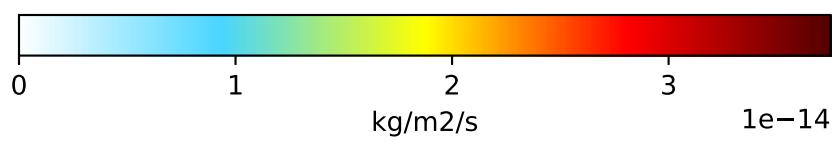
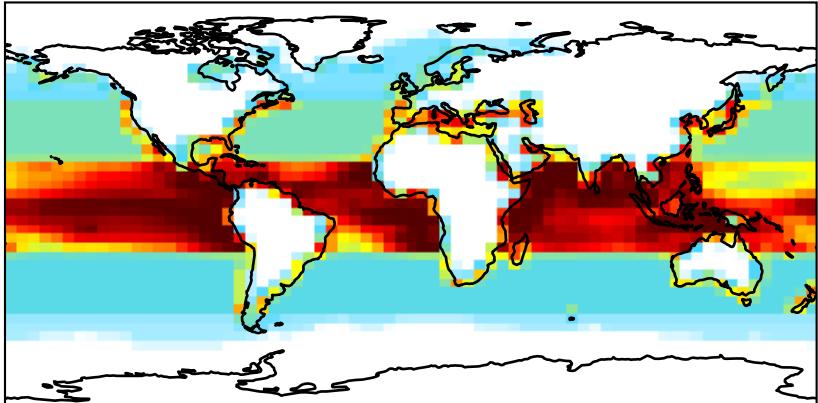


# EmisCH2IBr\_Ocean (Oct2019)

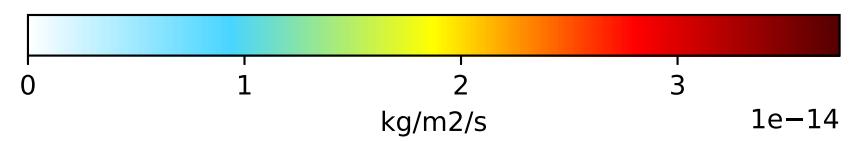
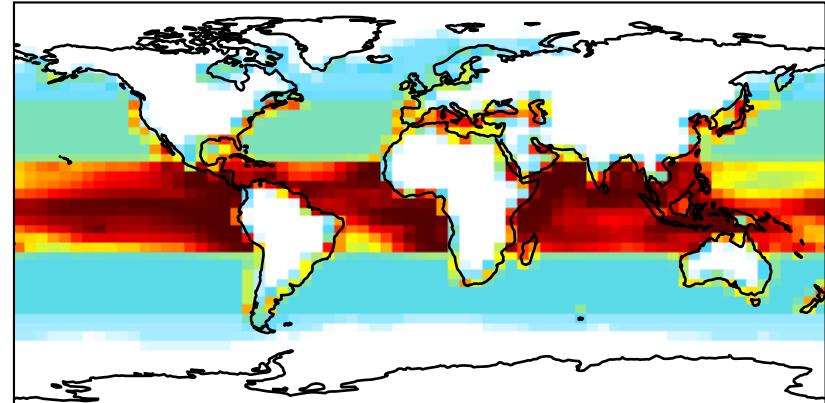


# EmisCH2ICI\_Ocean (Oct2019)

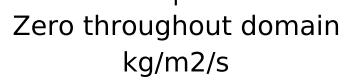
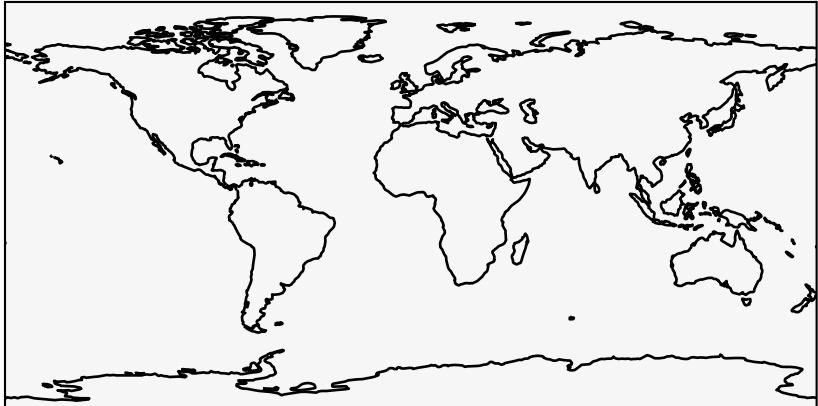
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



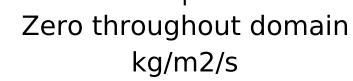
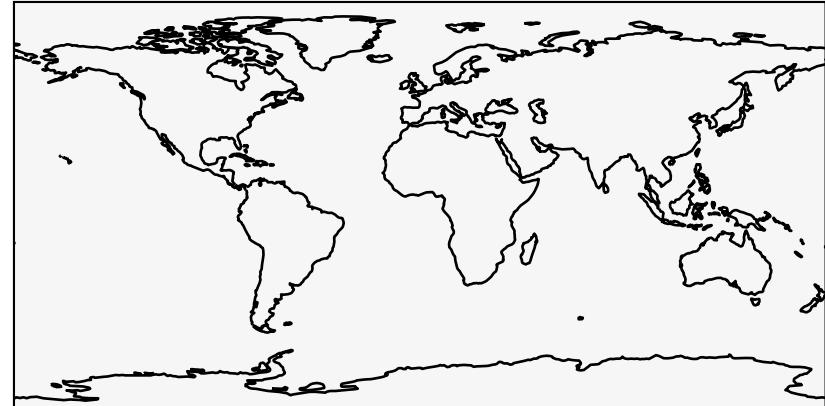
GCC\_13.0.0 (Dev)  
4.0x5.0



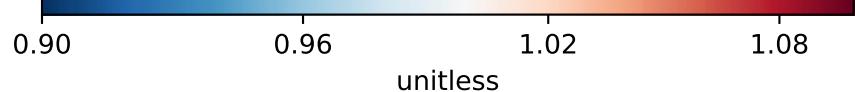
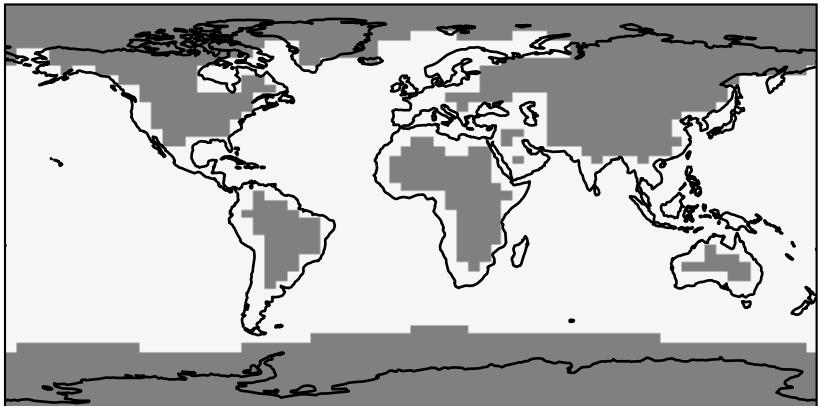
Difference  
Dev - Ref, Dynamic Range



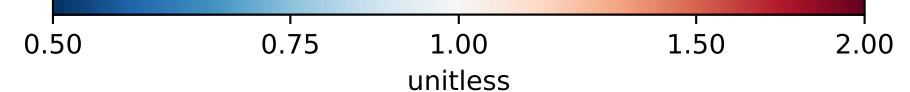
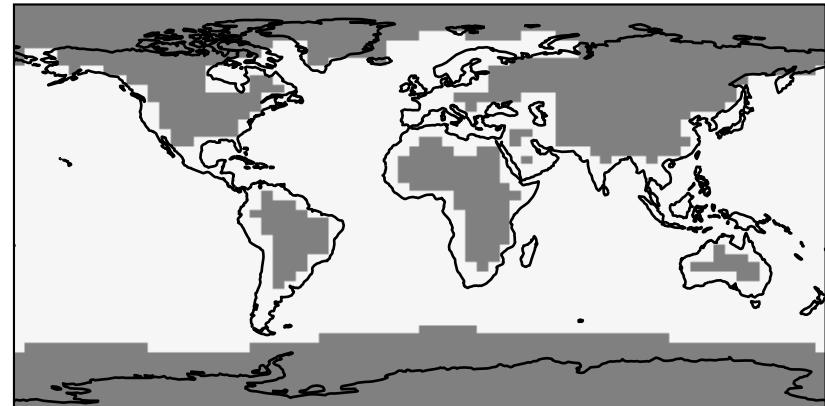
Difference  
Dev - Ref, Restricted Range [5%,95%]



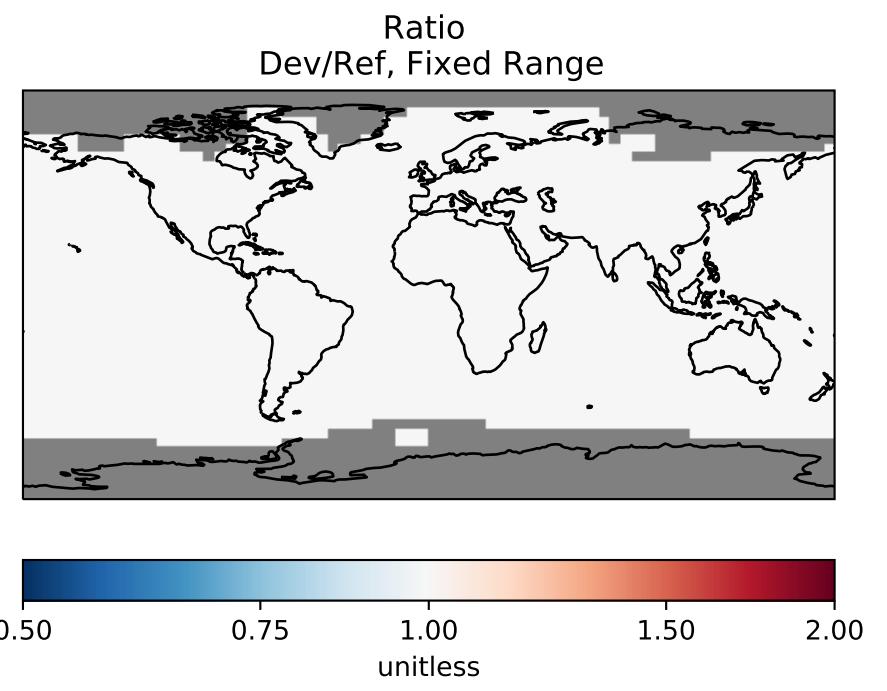
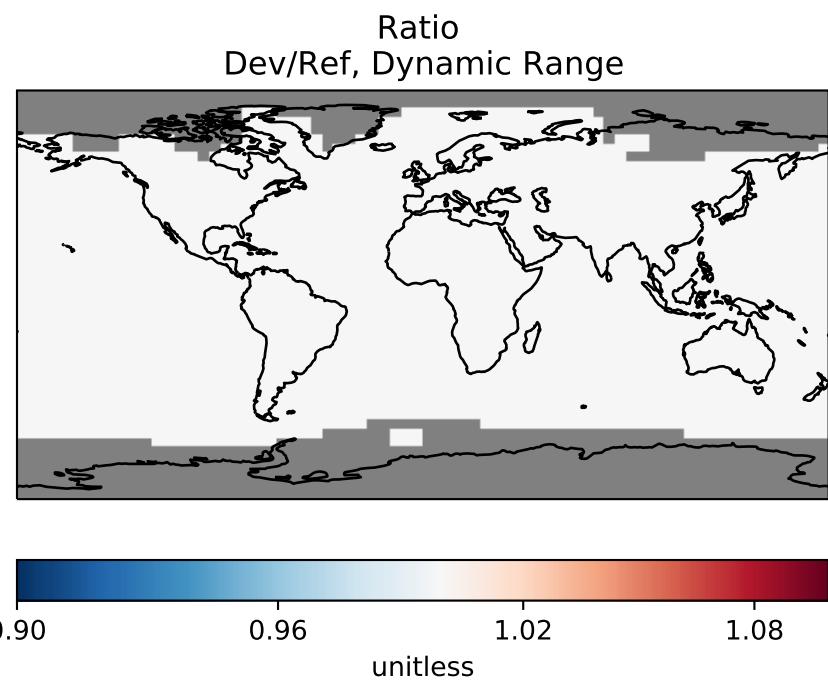
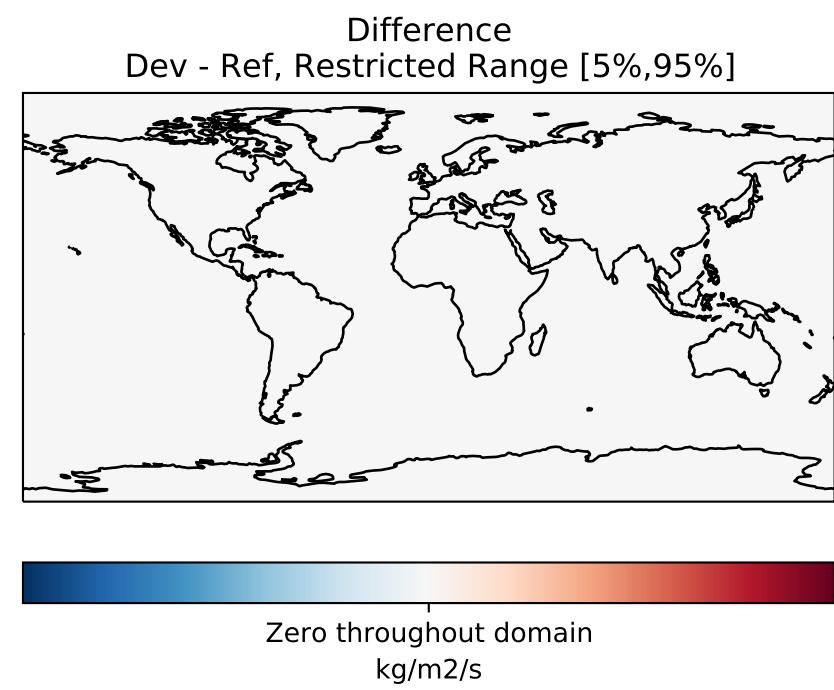
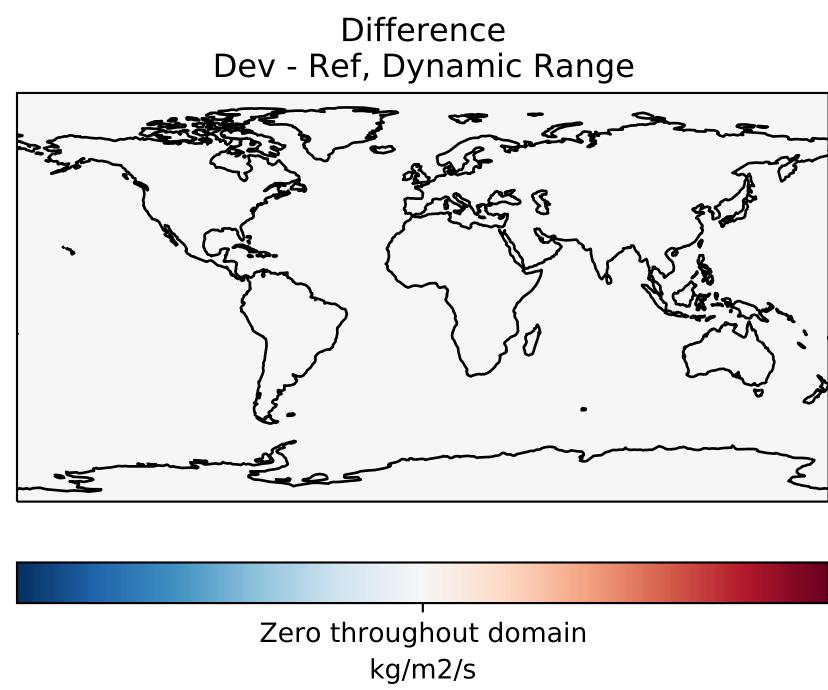
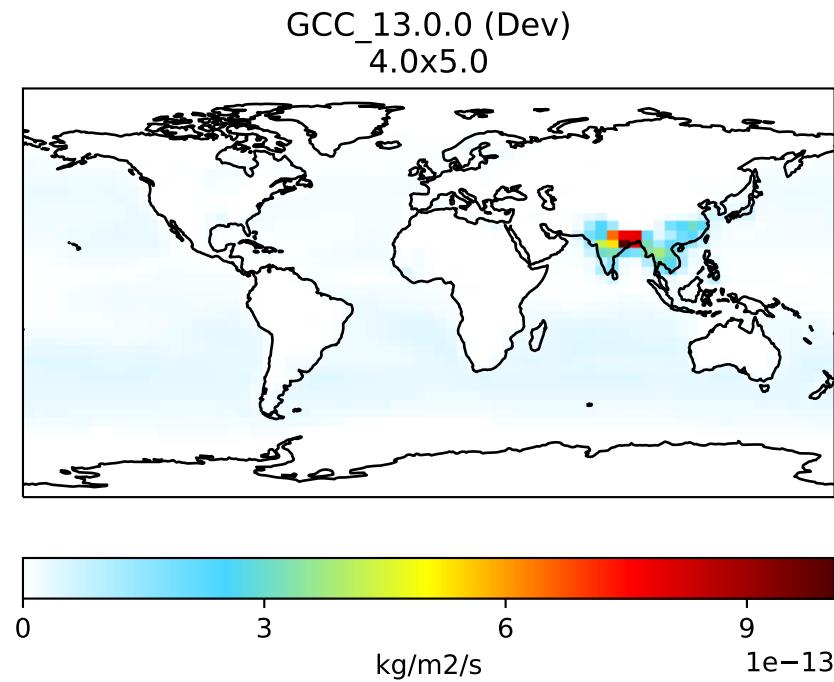
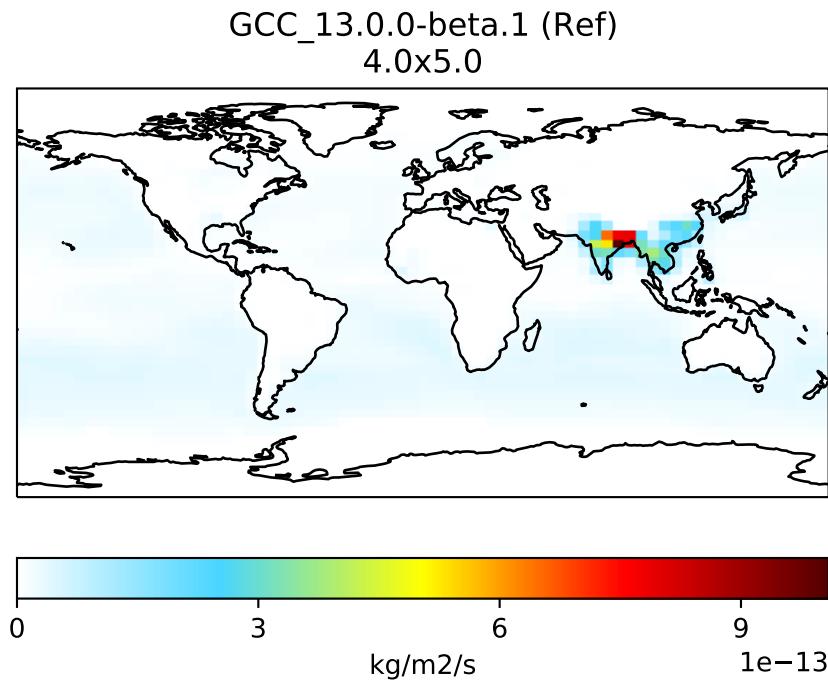
Ratio  
Dev/Ref, Dynamic Range



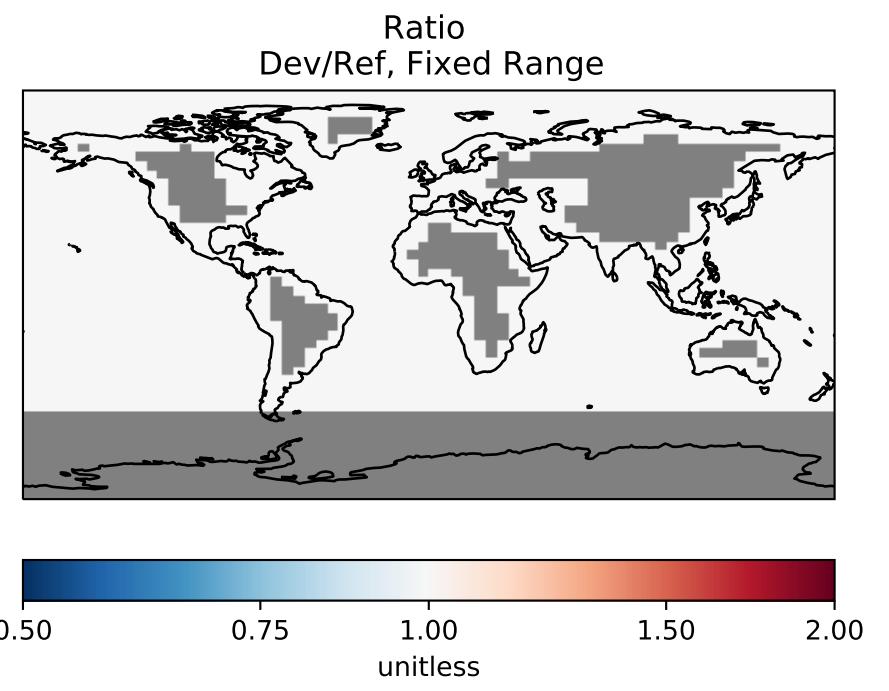
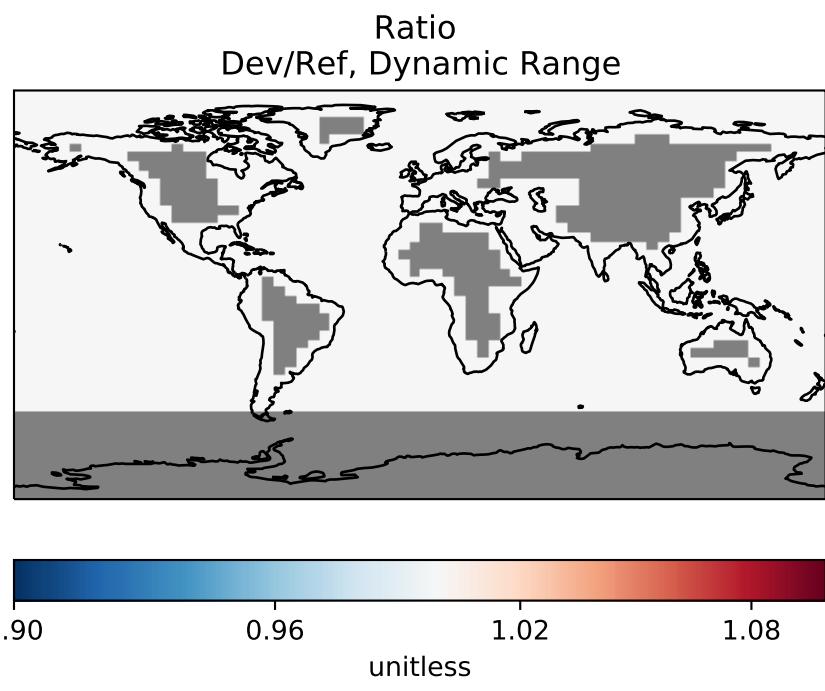
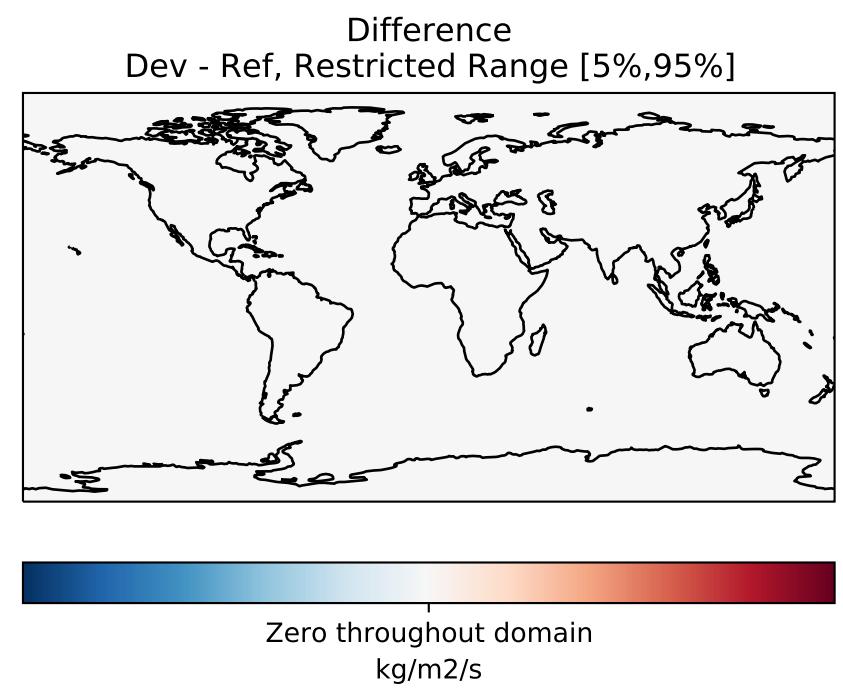
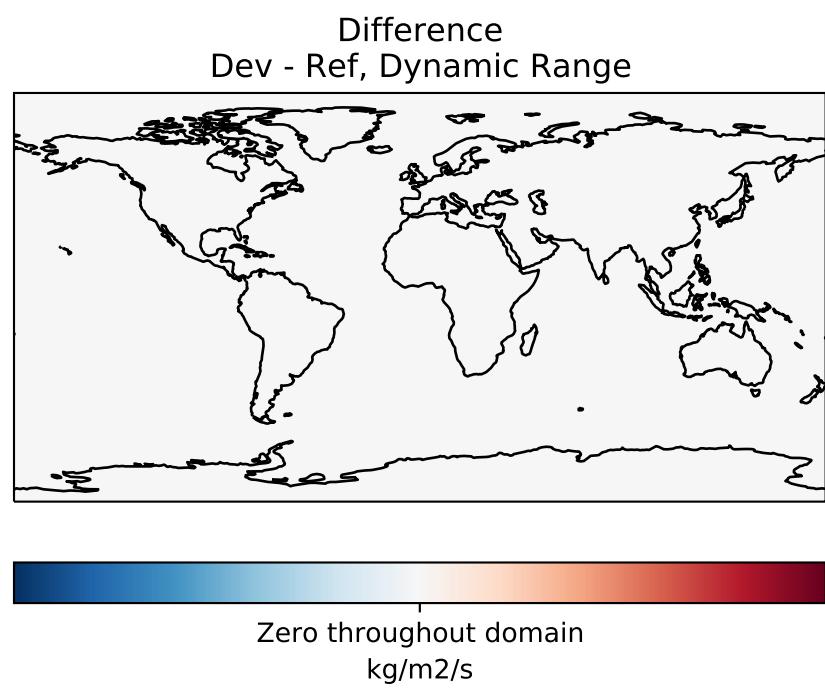
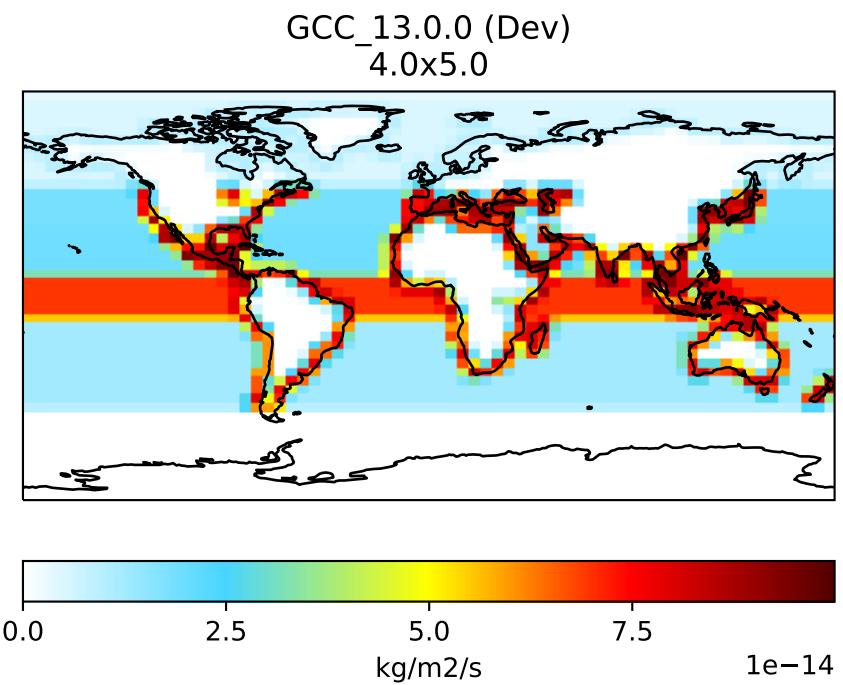
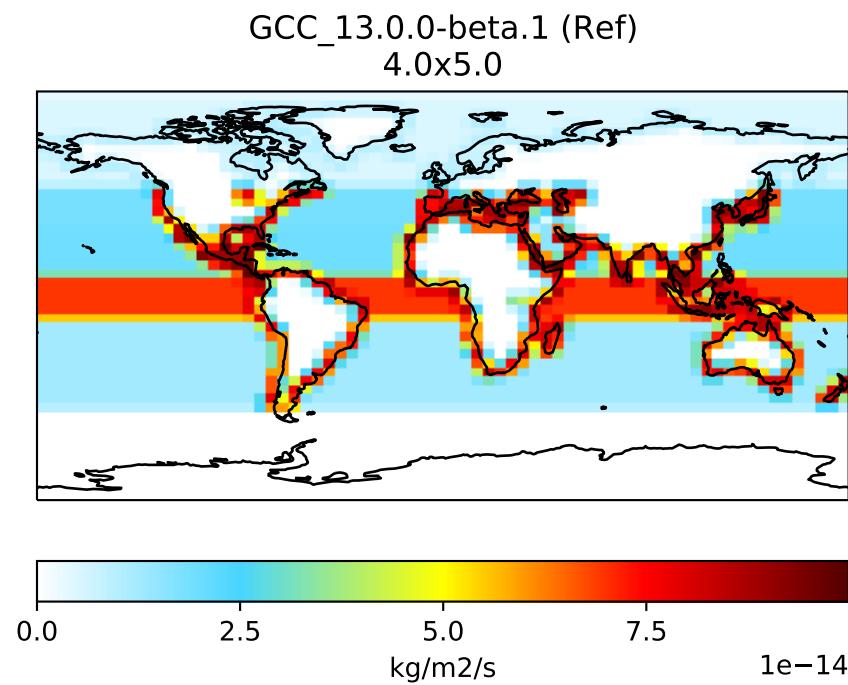
Ratio  
Dev/Ref, Fixed Range



# EmisCH3I\_Ocean (Oct2019)

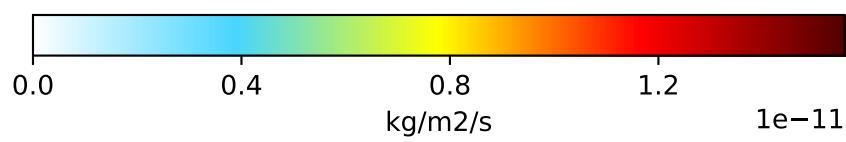
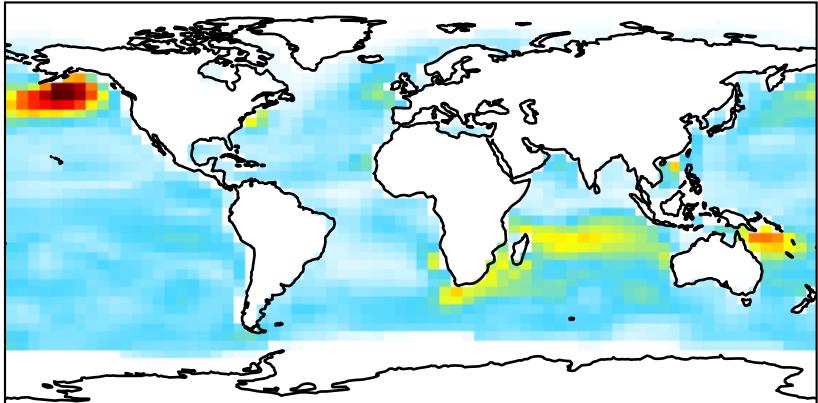


# EmisCHBr3\_Ocean (Oct2019)

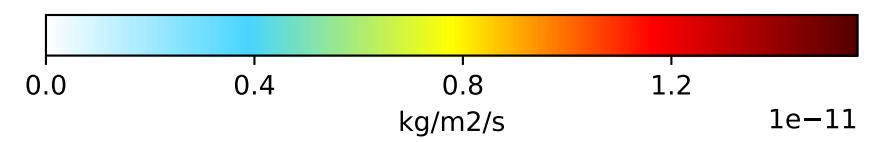
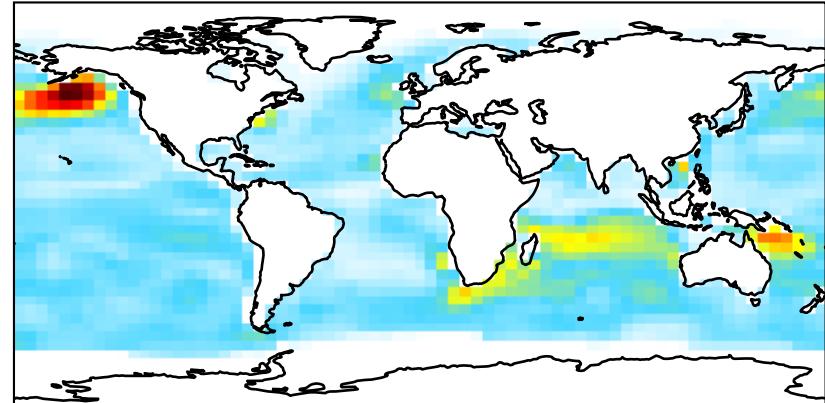


# EmisDMS\_Ocean (Oct2019)

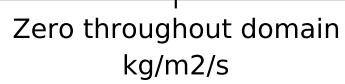
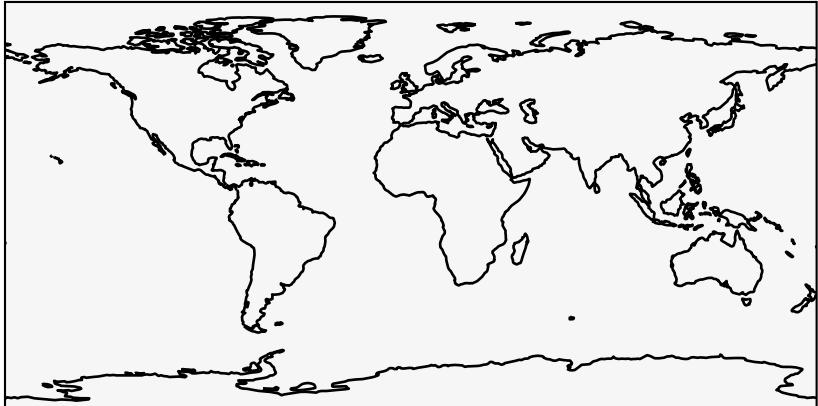
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



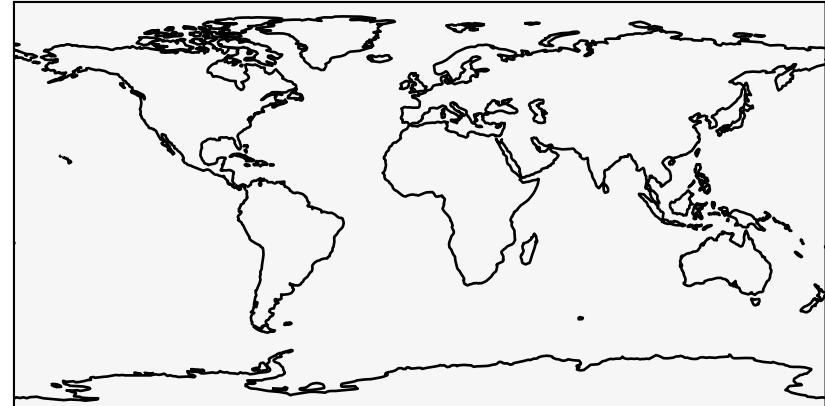
GCC\_13.0.0 (Dev)  
4.0x5.0



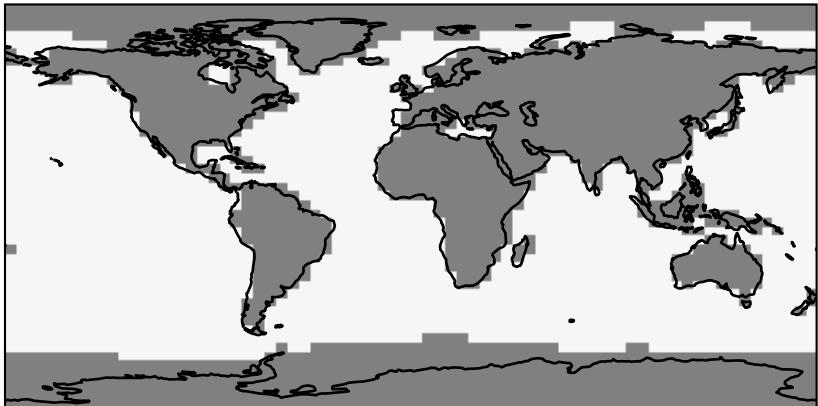
Difference  
Dev - Ref, Dynamic Range



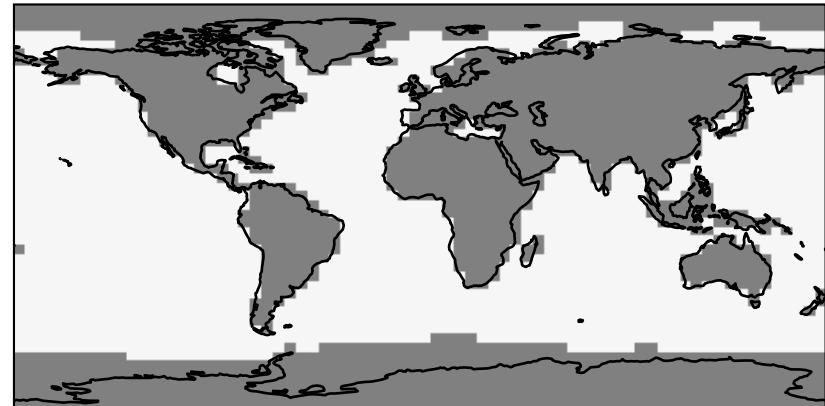
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

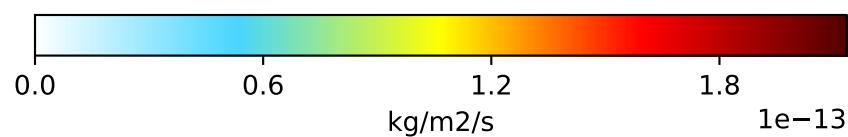
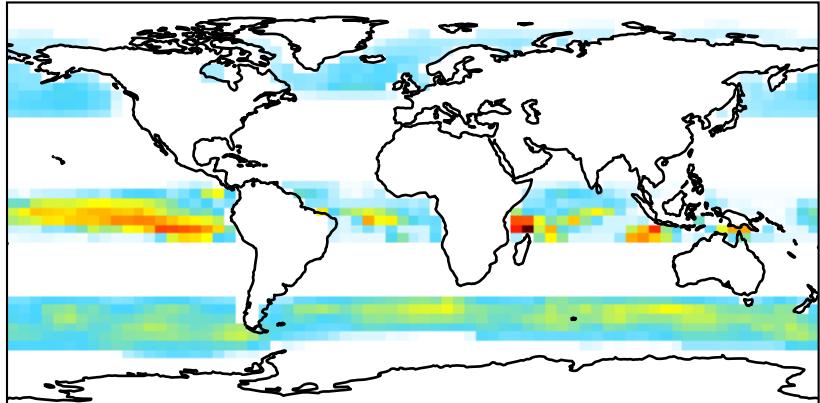


Ratio  
Dev/Ref, Fixed Range

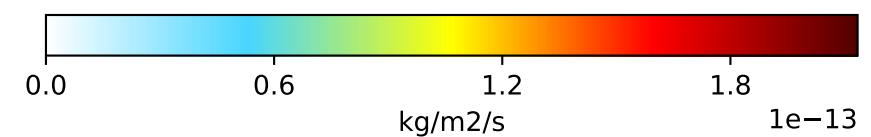
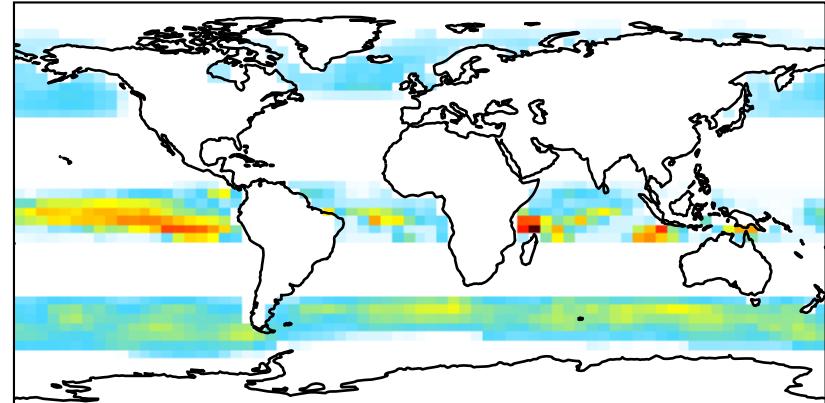


# EmisETNO3\_Ocean (Oct2019)

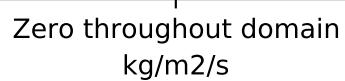
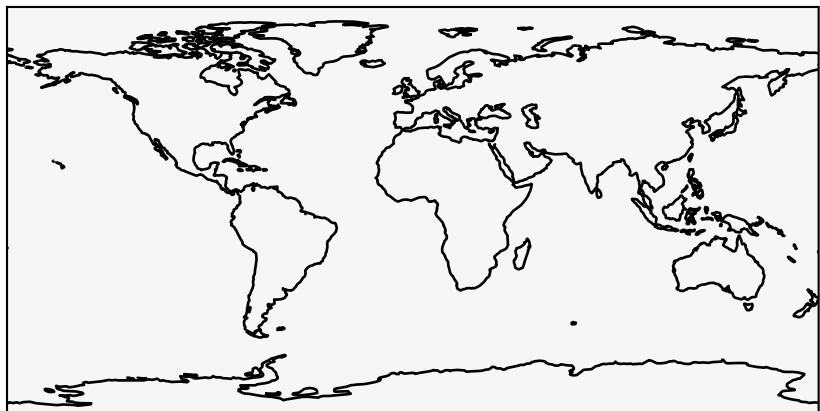
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



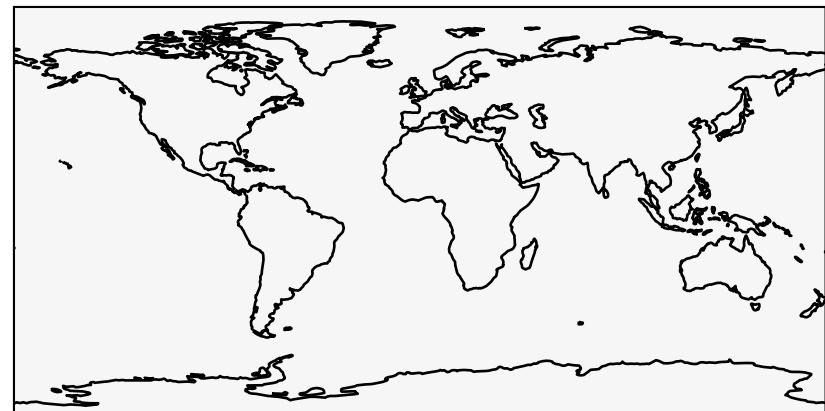
GCC\_13.0.0 (Dev)  
4.0x5.0



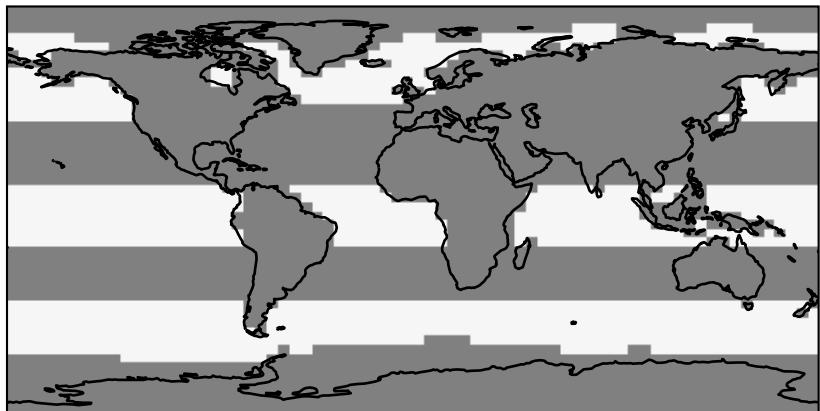
Difference  
Dev - Ref, Dynamic Range



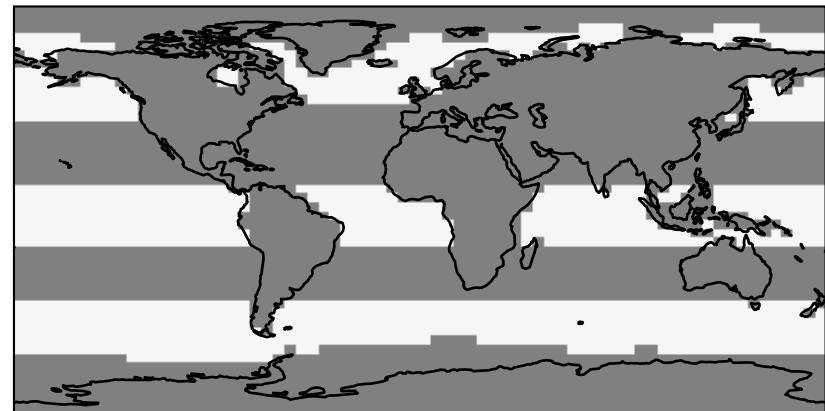
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

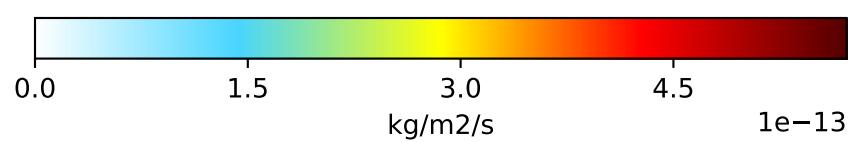
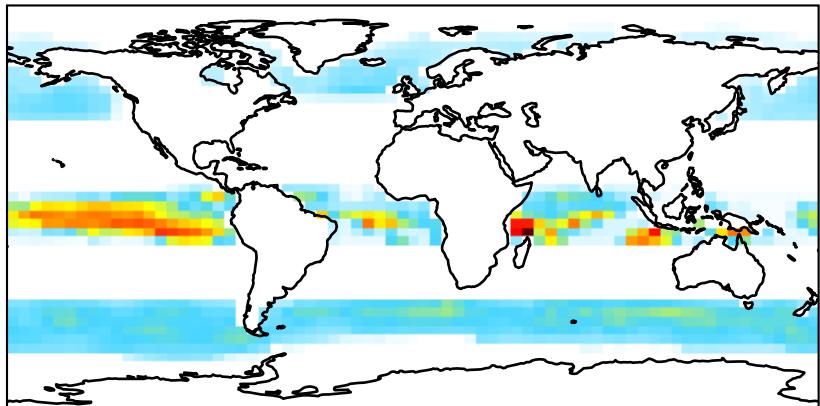


Ratio  
Dev/Ref, Fixed Range

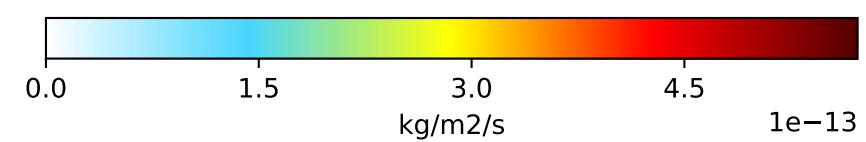
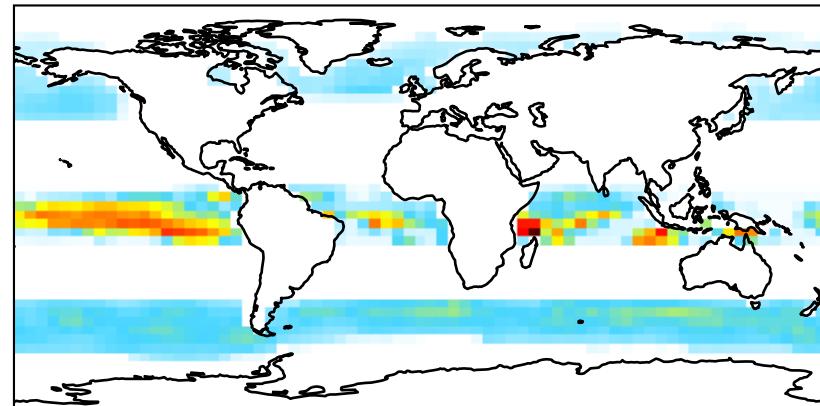


# EmisMENO3\_Ocean (Oct2019)

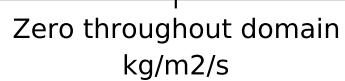
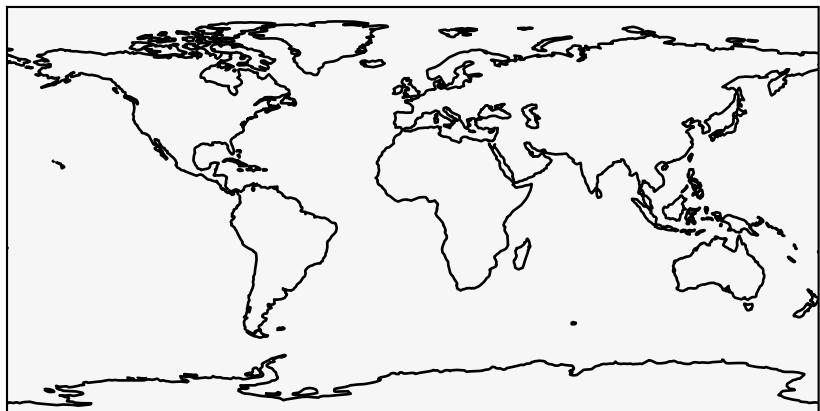
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



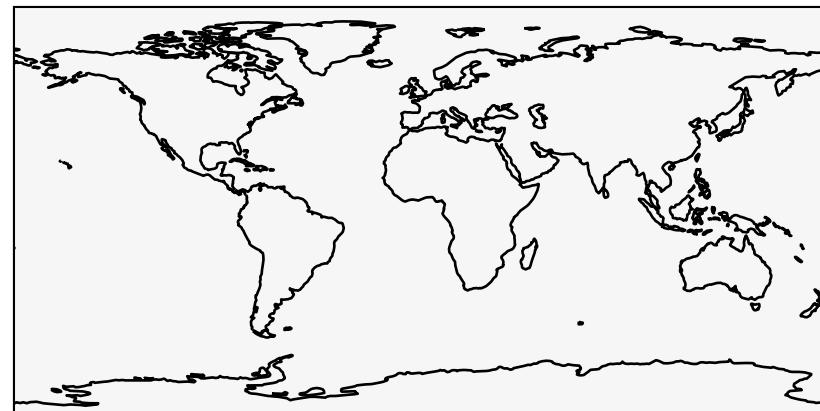
GCC\_13.0.0 (Dev)  
4.0x5.0



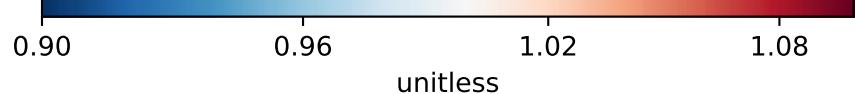
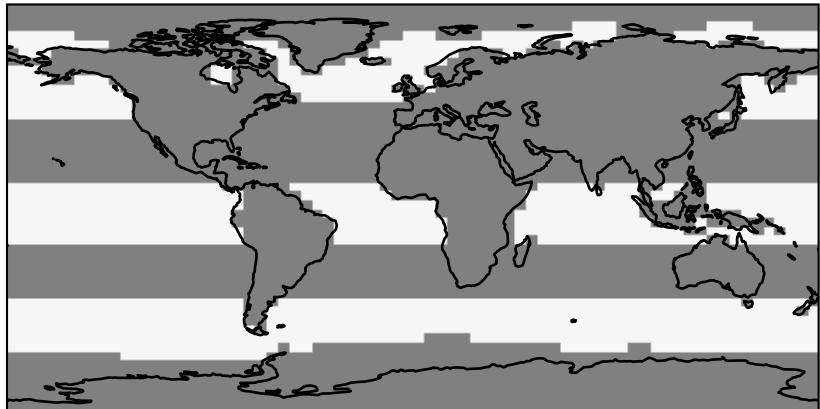
Difference  
Dev - Ref, Dynamic Range



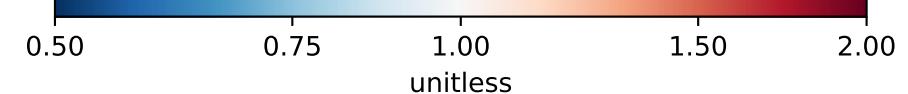
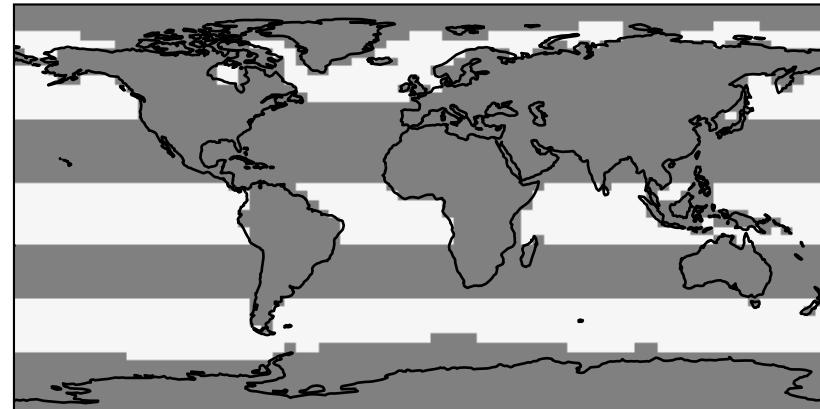
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

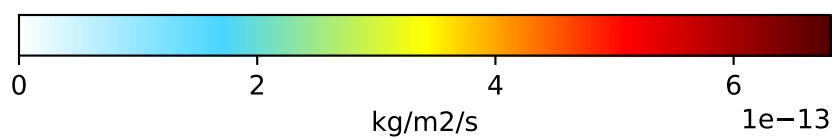
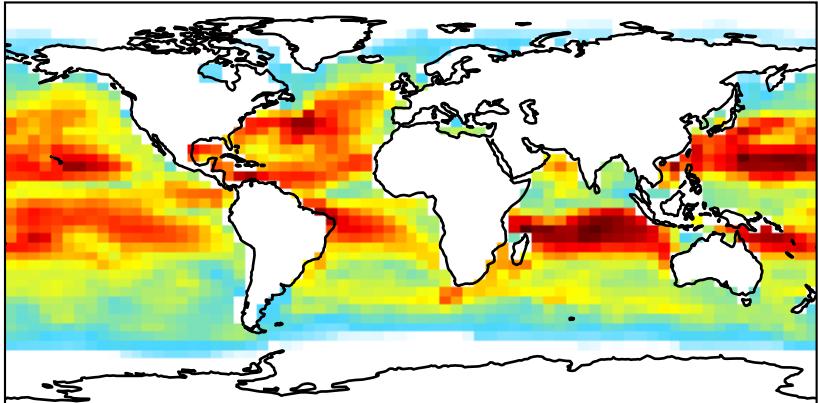


Ratio  
Dev/Ref, Fixed Range

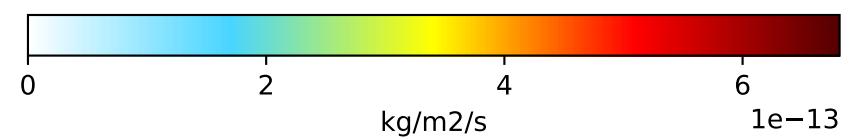
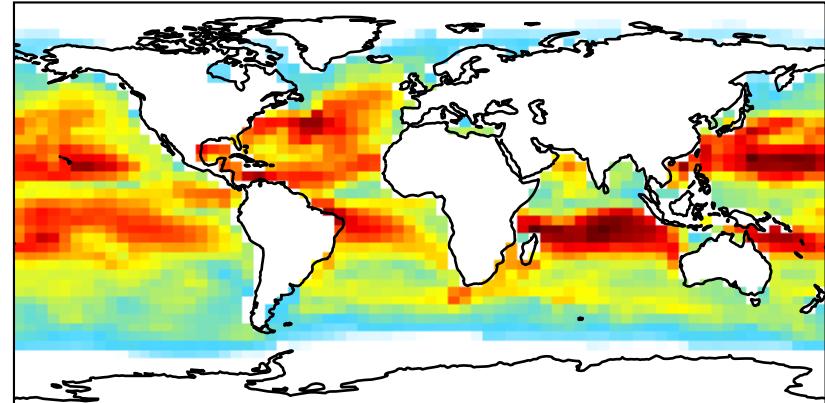


# EmisMOH\_Ocean (Oct2019)

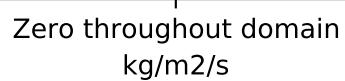
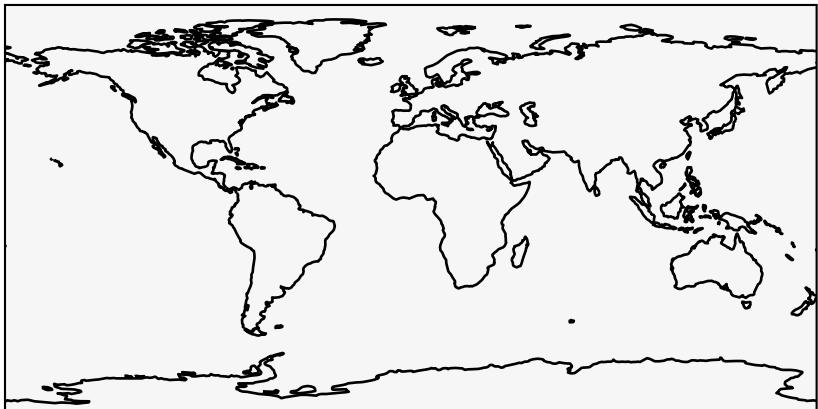
GCC\_13.0.0-beta.1 (Ref)  
4.0x5.0



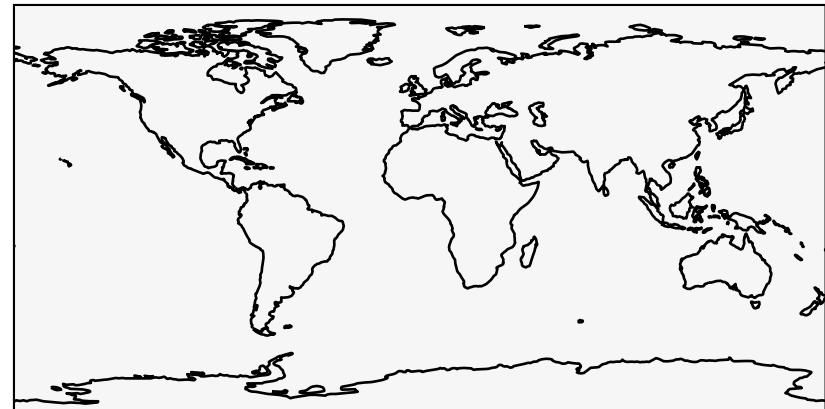
GCC\_13.0.0 (Dev)  
4.0x5.0



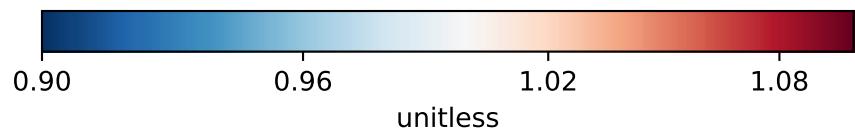
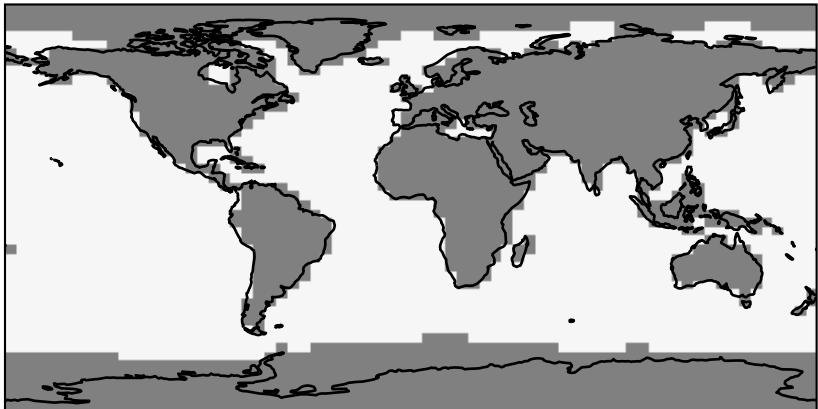
Difference  
Dev - Ref, Dynamic Range



Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

