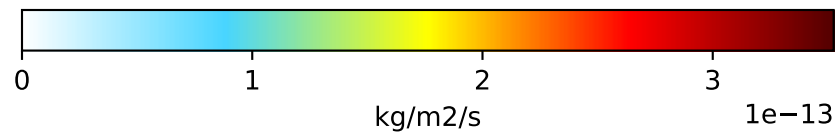
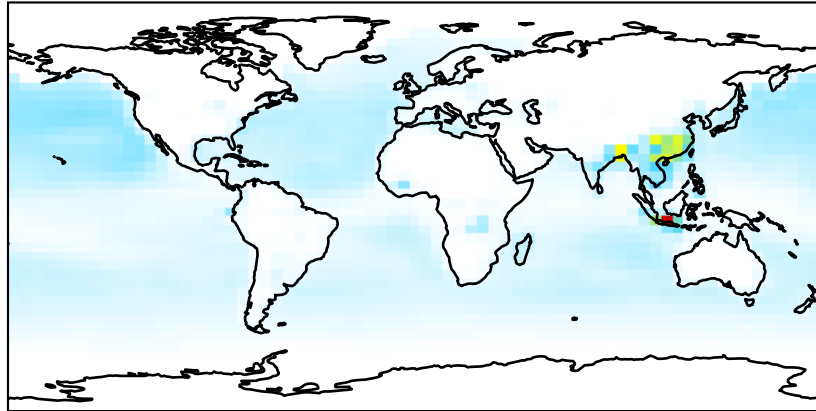
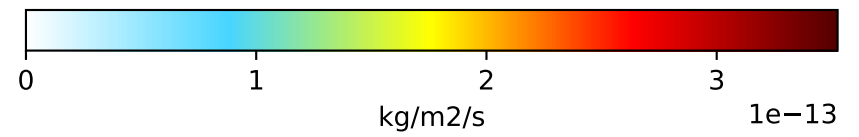
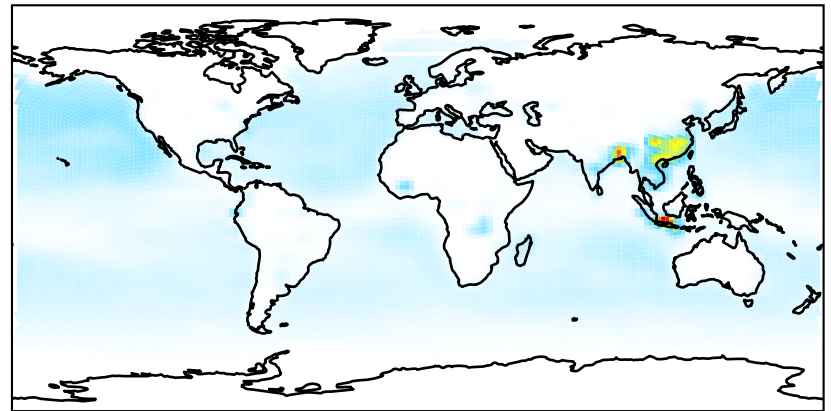


# EmisCH3I\_Ocean (Apr2019)

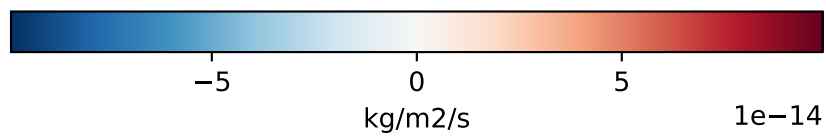
GCC\_13.0.0-rc.0 (Ref)  
4.0x5.0



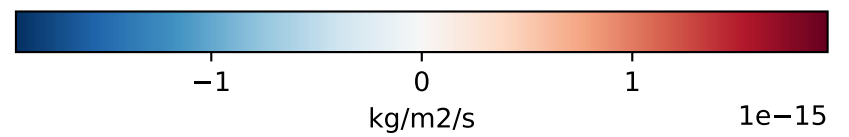
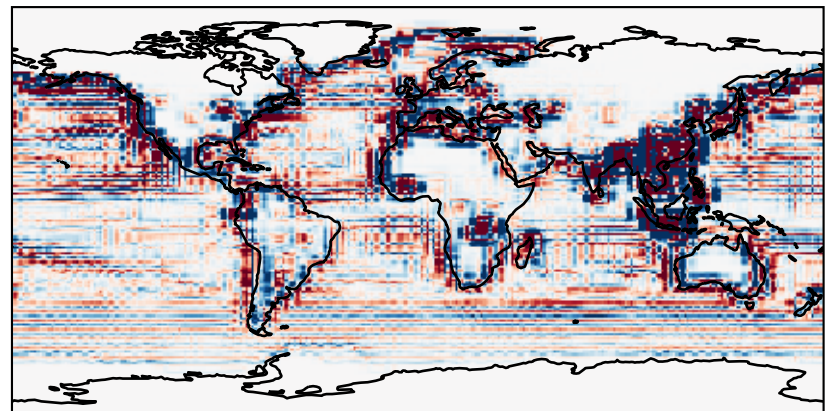
GCHP\_13.0.0-rc.0 (Dev)  
c48



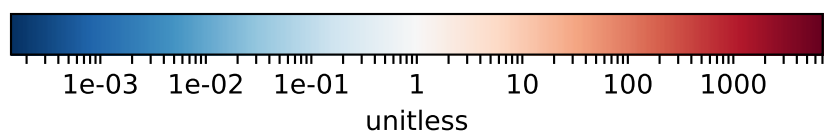
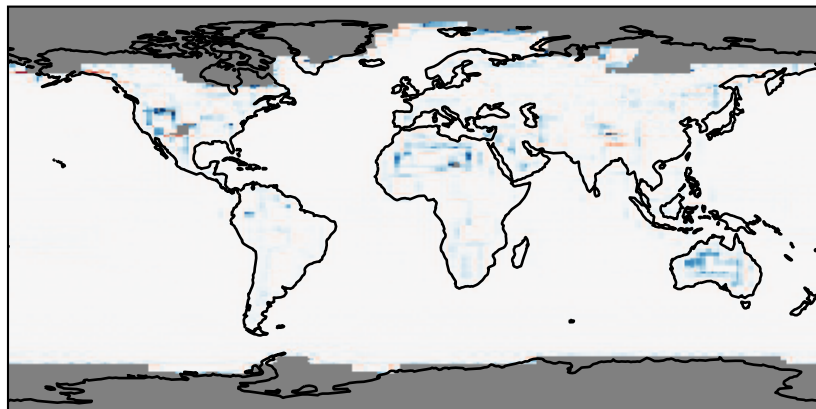
Difference (1x1.25)  
Dev - Ref, Dynamic Range



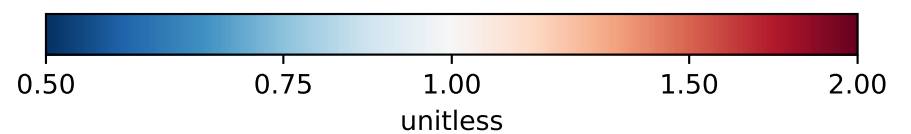
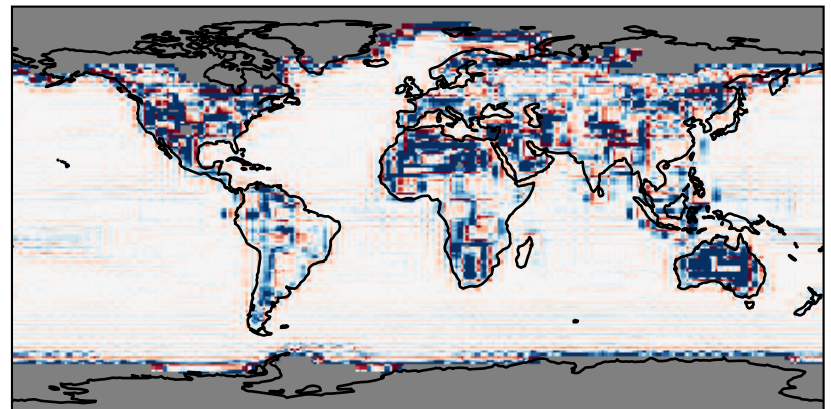
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

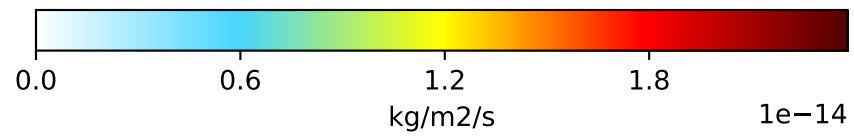
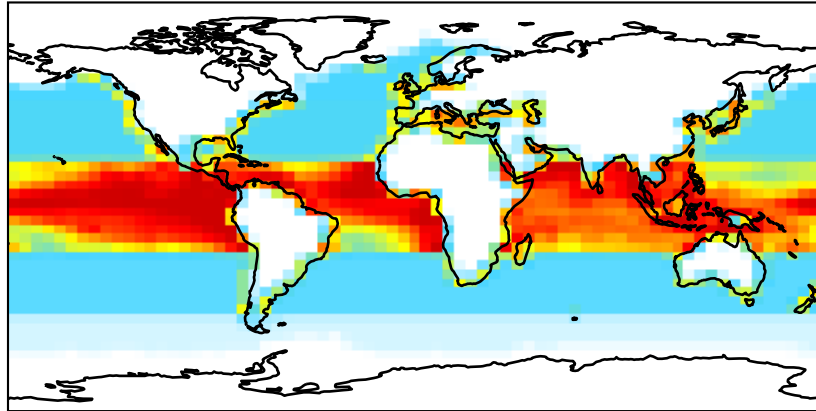


Ratio (1x1.25)  
Dev/Ref, Fixed Range

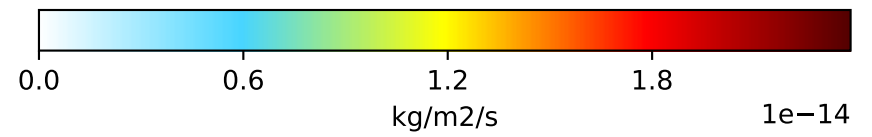
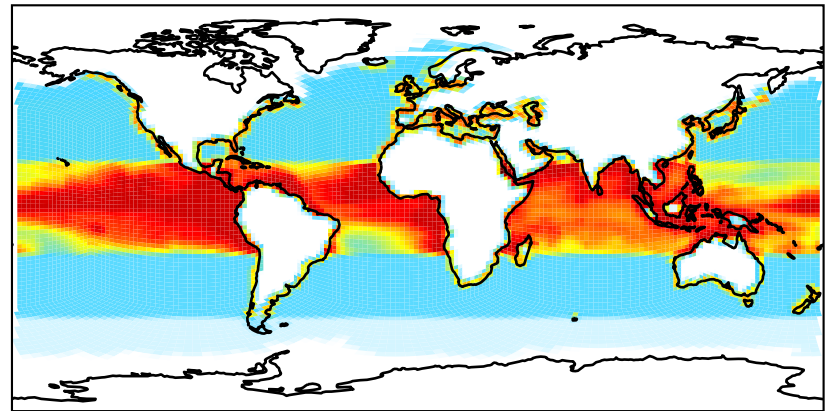


# EmisCH2I2\_Ocean (Apr2019)

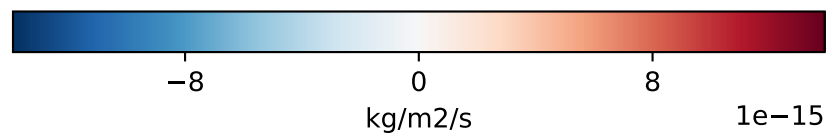
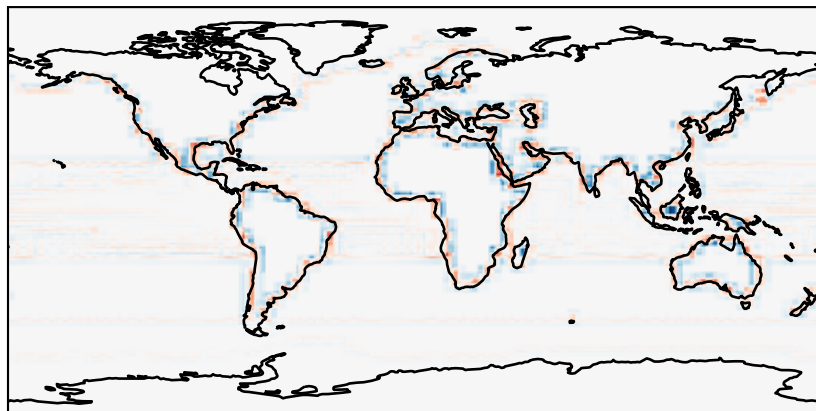
GCC\_13.0.0-rc.0 (Ref)  
4.0x5.0



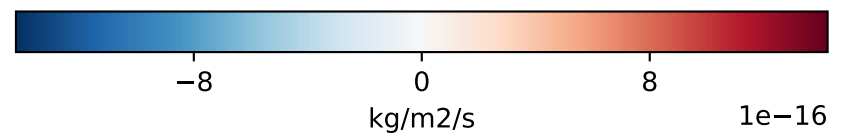
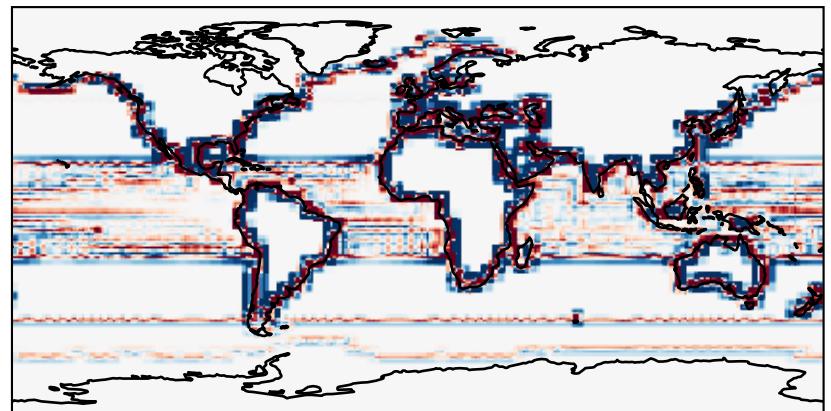
GCHP\_13.0.0-rc.0 (Dev)  
c48



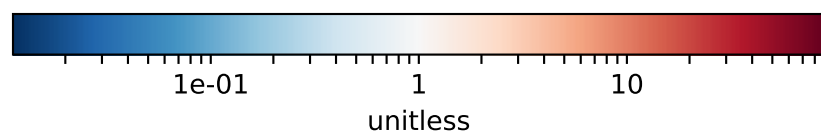
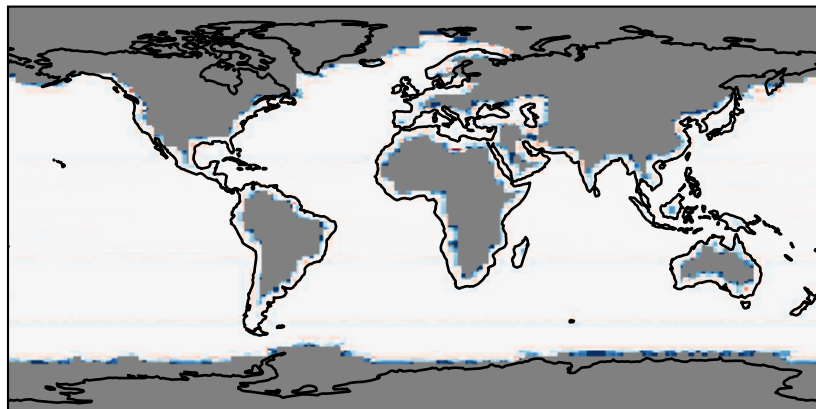
Difference (1x1.25)  
Dev - Ref, Dynamic Range



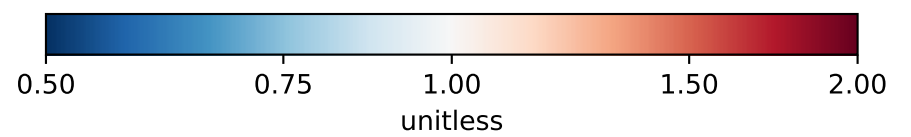
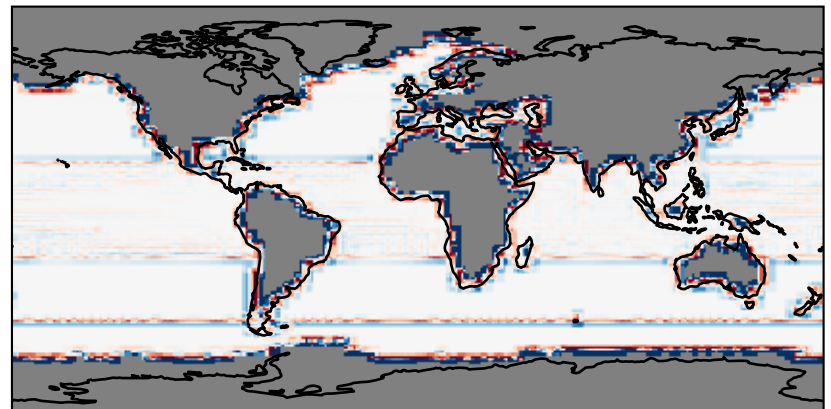
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

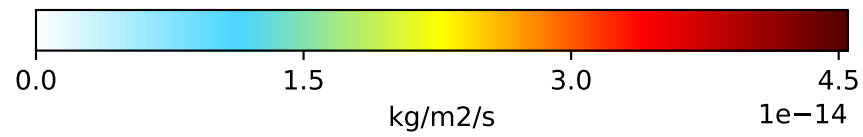
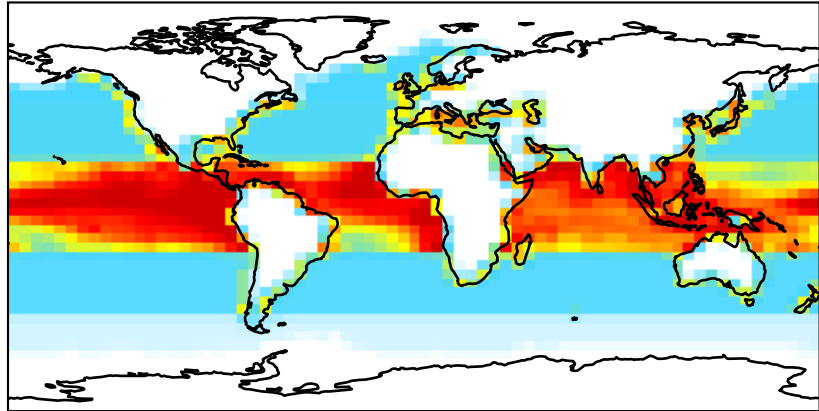


Ratio (1x1.25)  
Dev/Ref, Fixed Range

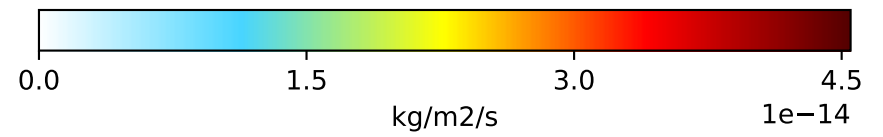
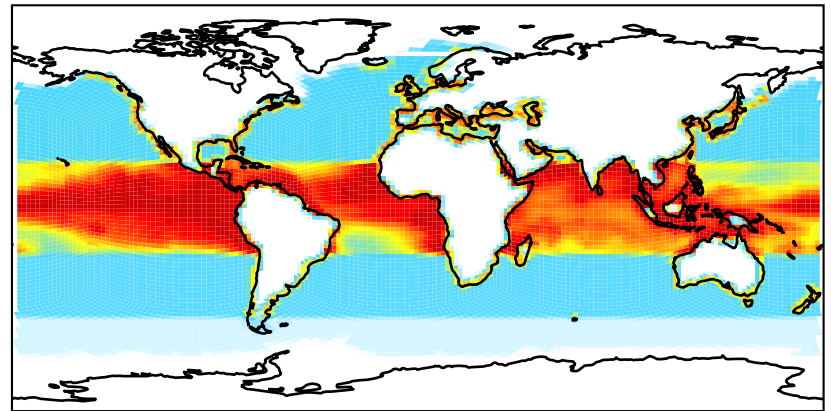


# EmisCH2ICl\_Ocean (Apr2019)

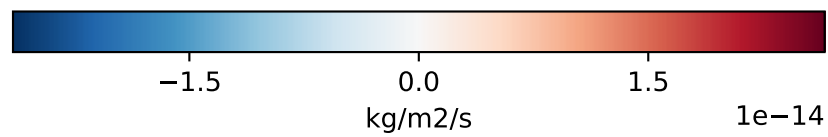
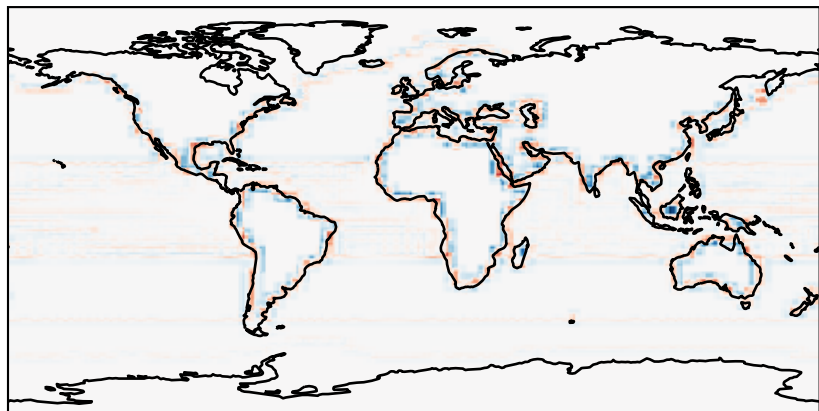
GCC\_13.0.0-rc.0 (Ref)  
4.0x5.0



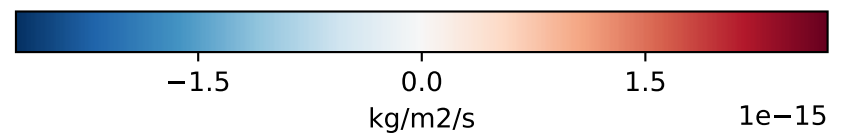
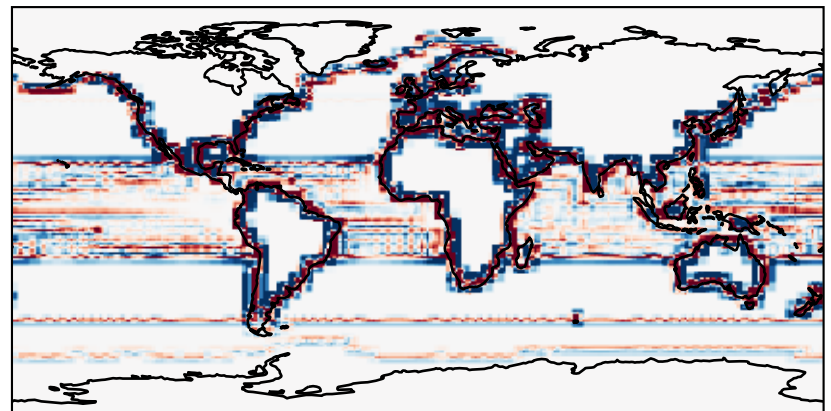
GCHP\_13.0.0-rc.0 (Dev)  
c48



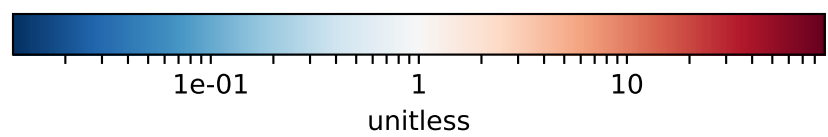
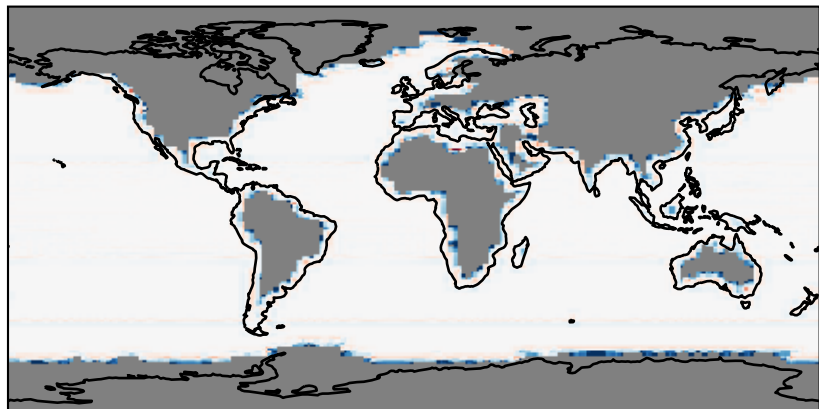
Difference (1x1.25)  
Dev - Ref, Dynamic Range



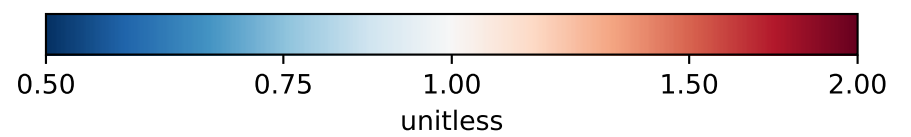
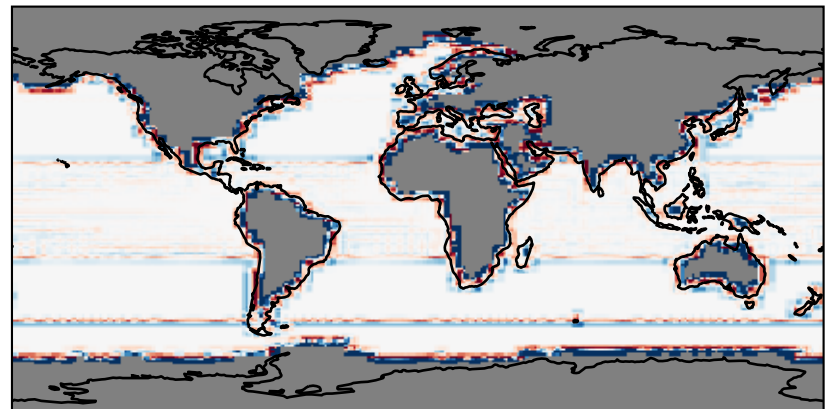
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

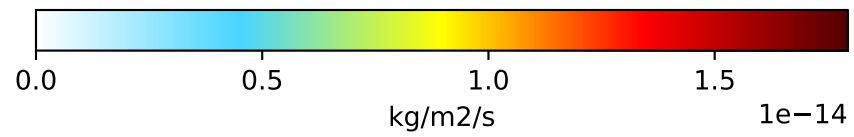
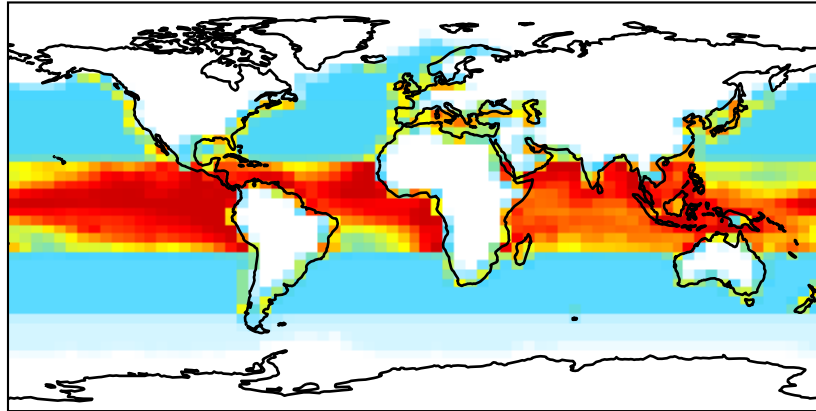


Ratio (1x1.25)  
Dev/Ref, Fixed Range

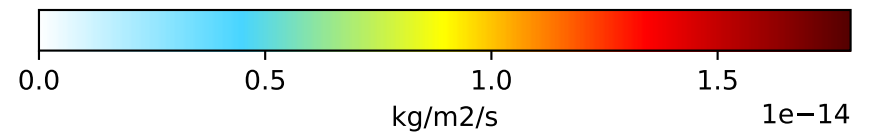
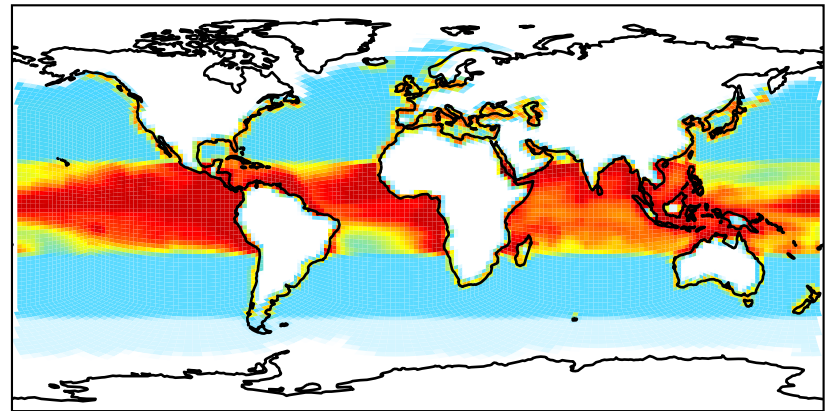


# EmisCH2IBr\_Ocean (Apr2019)

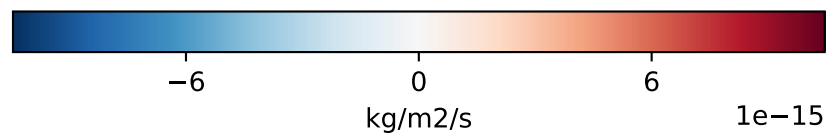
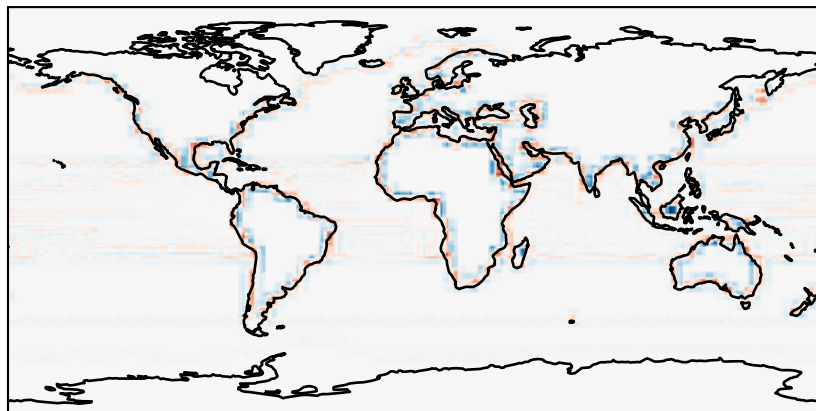
GCC\_13.0.0-rc.0 (Ref)  
4.0x5.0



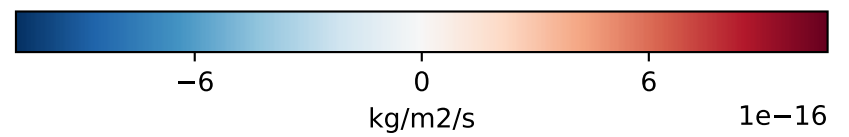
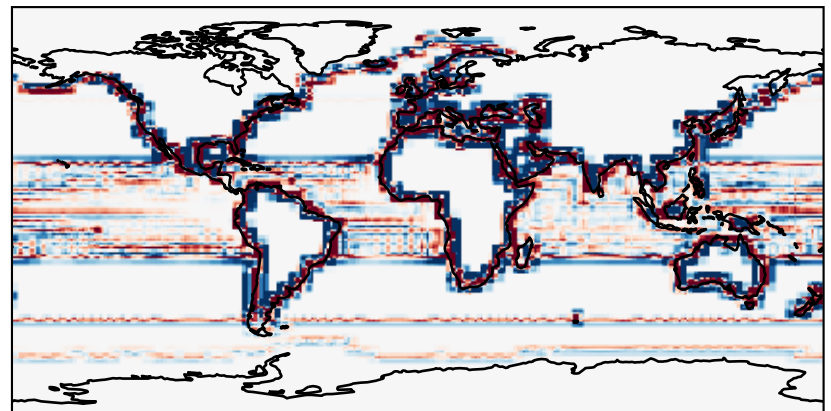
GCHP\_13.0.0-rc.0 (Dev)  
c48



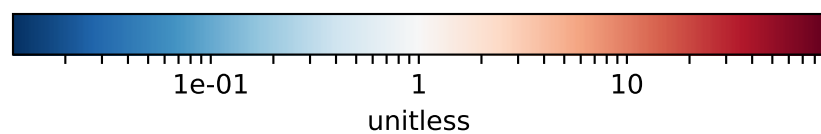
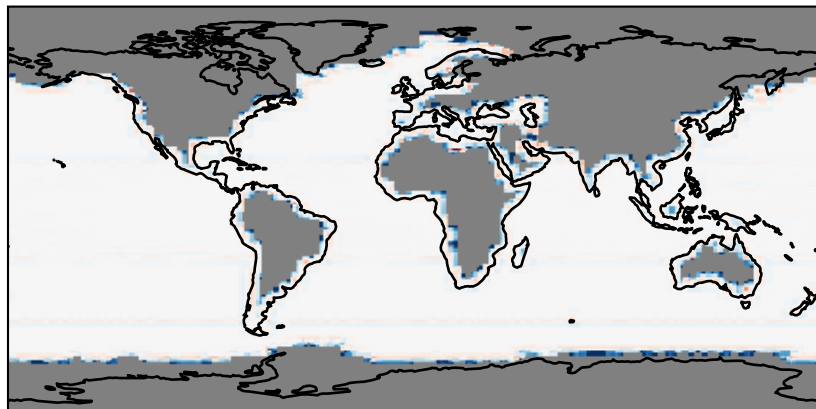
Difference (1x1.25)  
Dev - Ref, Dynamic Range



Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Ratio (1x1.25)  
Dev/Ref, Fixed Range

