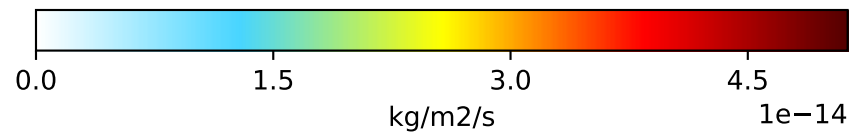
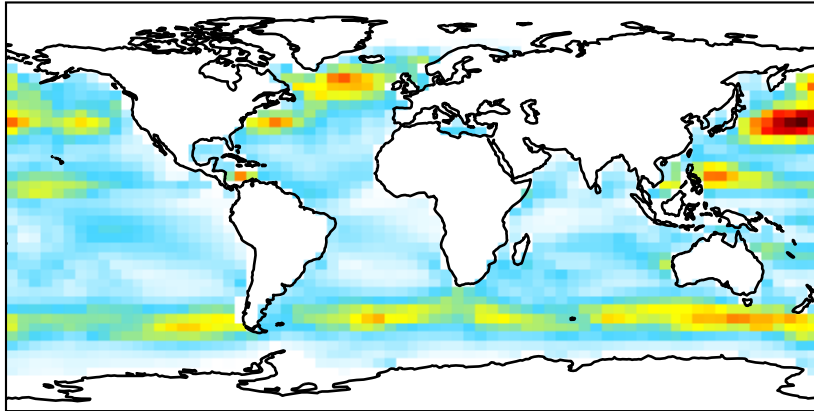
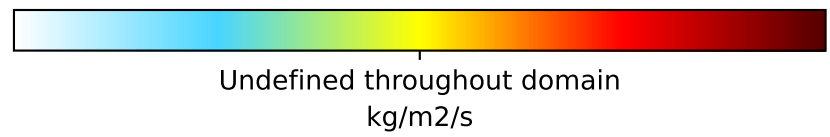
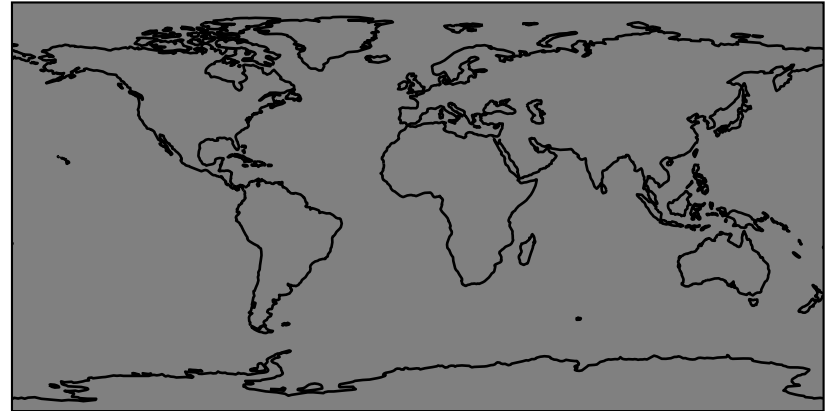


EmisBrSALA_Natural (Jan2019)

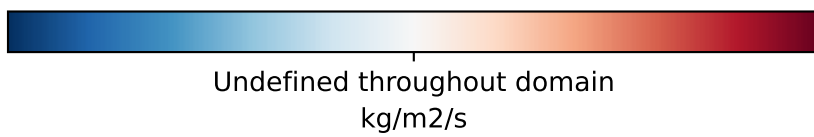
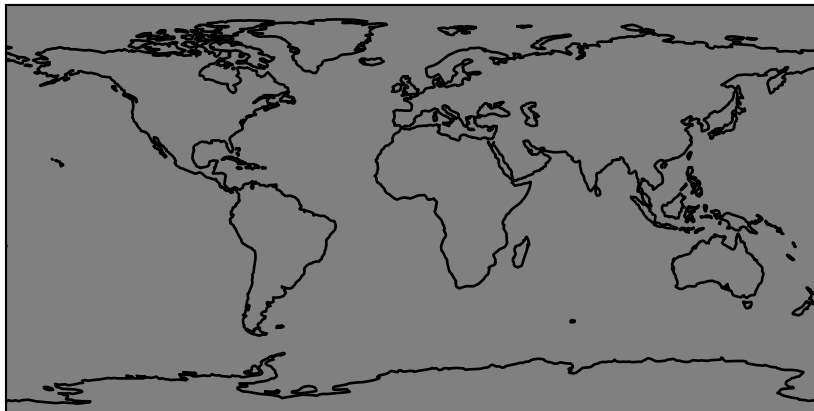
GCC_13.0.0 (Ref)
4.0x5.0



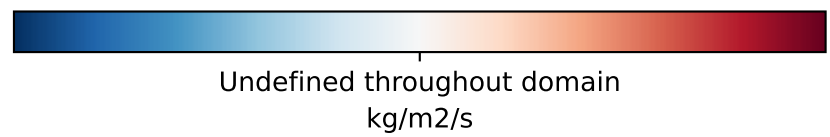
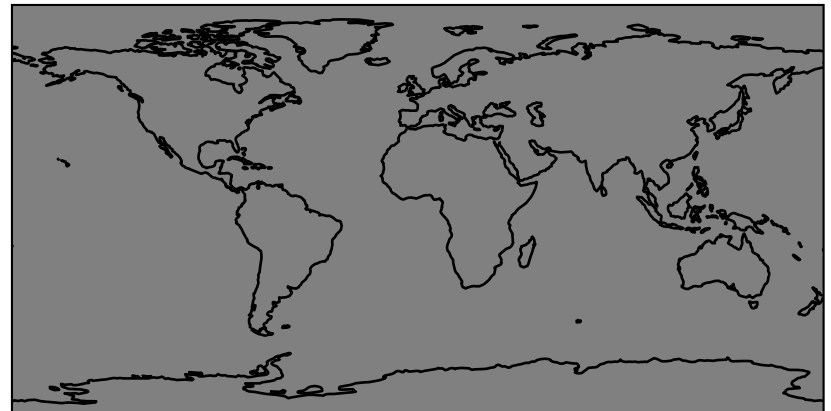
GCC_14.0.0 (Dev)
4.0x5.0



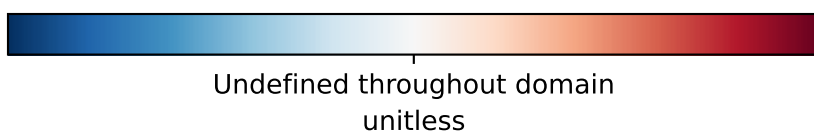
Difference
Dev - Ref, Dynamic Range



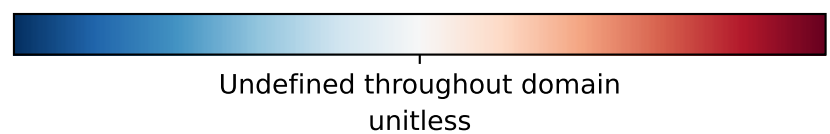
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

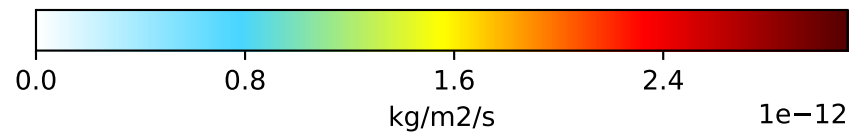
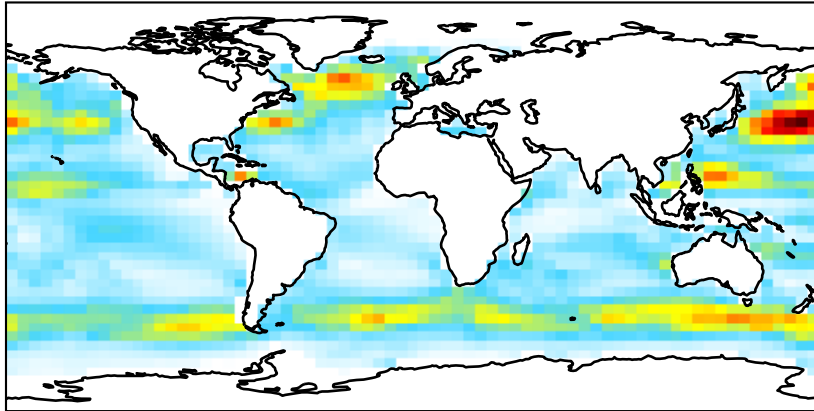


Ratio
Dev/Ref, Fixed Range

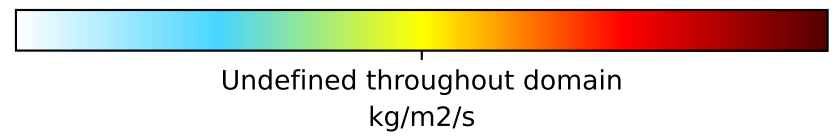
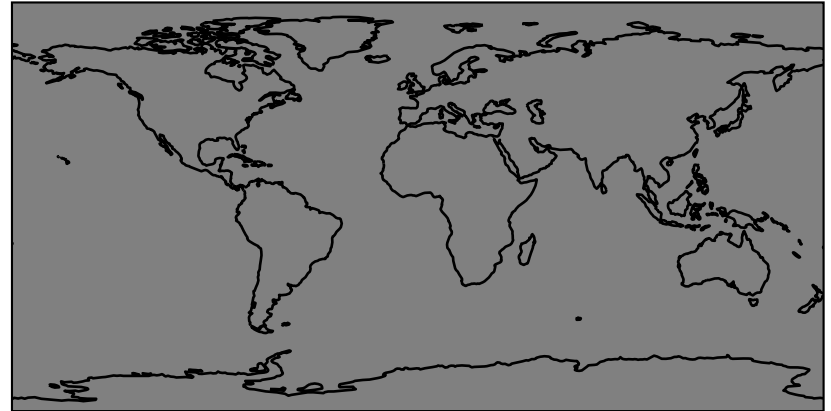


EmisBrSALC_Natural (Jan2019)

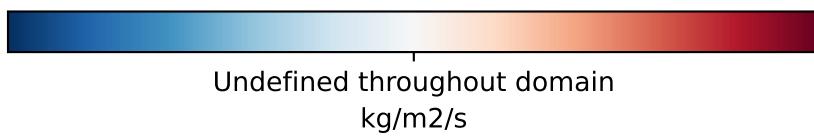
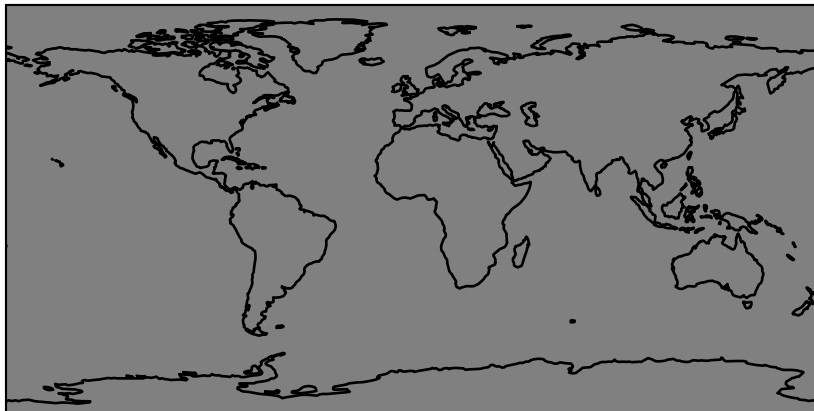
GCC_13.0.0 (Ref)
4.0x5.0



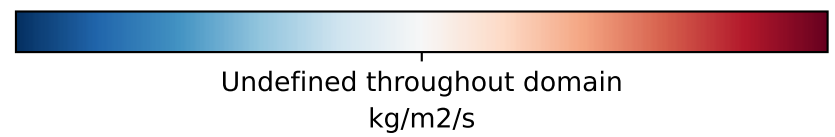
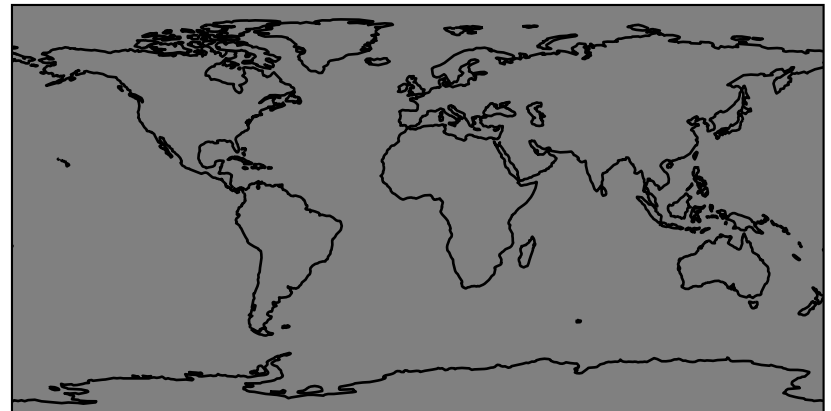
GCC_14.0.0 (Dev)
4.0x5.0



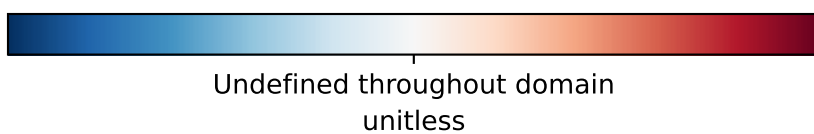
Difference
Dev - Ref, Dynamic Range



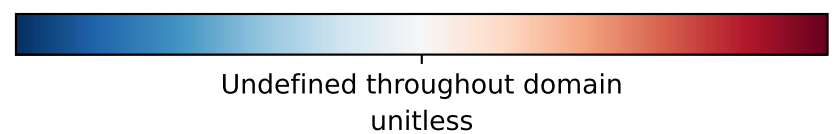
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

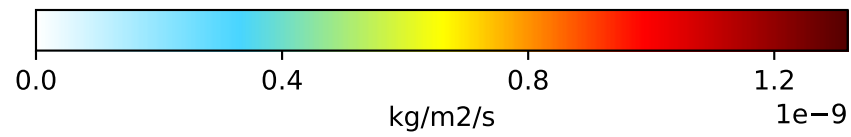
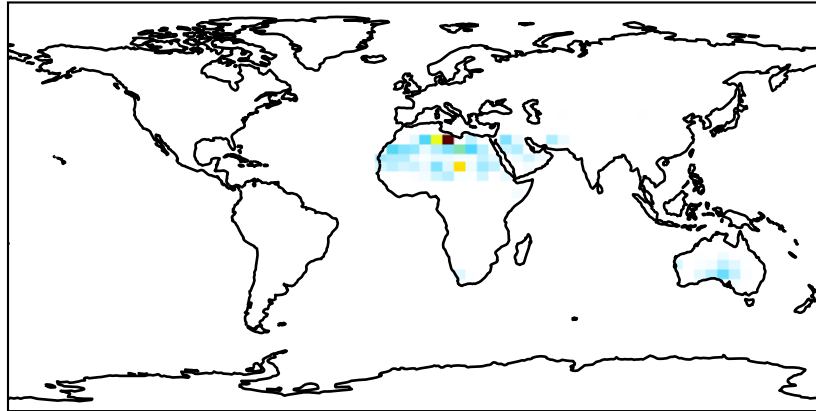


Ratio
Dev/Ref, Fixed Range

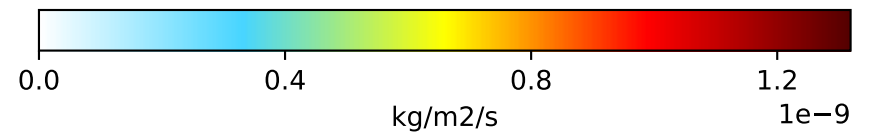
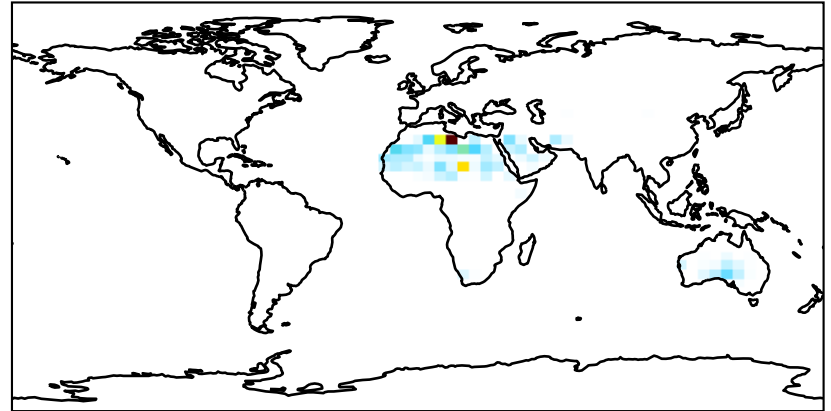


EmisDST1_Natural (Jan2019)

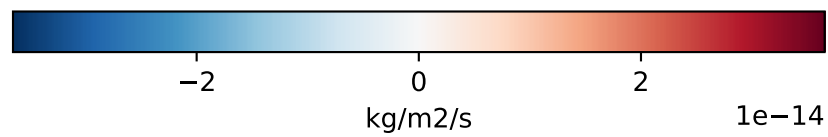
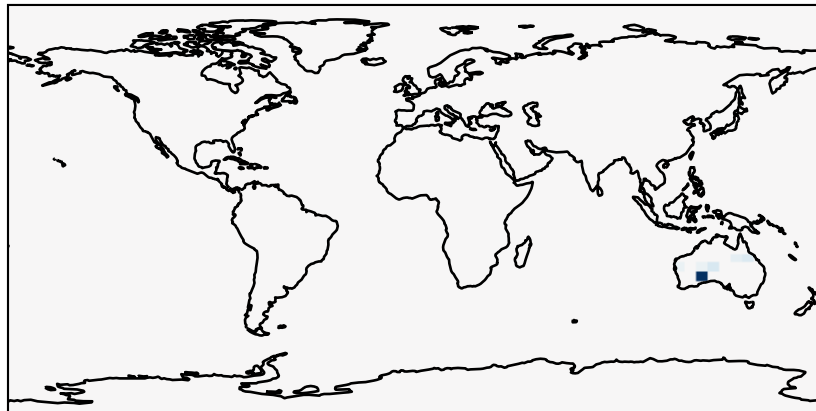
GCC_13.0.0 (Ref)
4.0x5.0



GCC_14.0.0 (Dev)
4.0x5.0



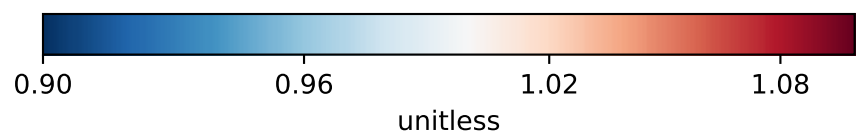
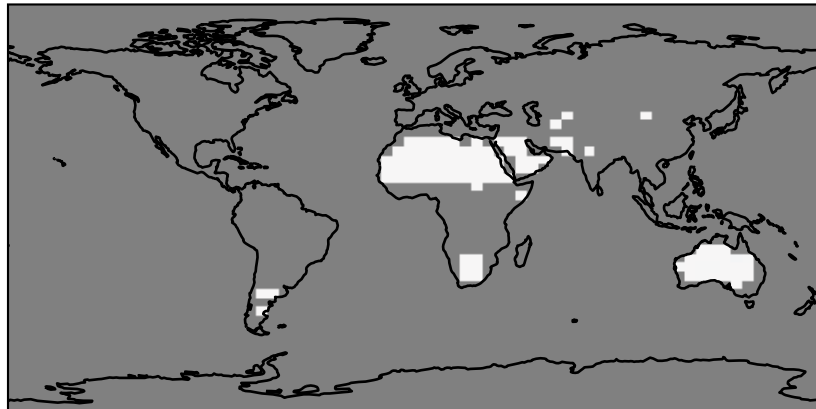
Difference
Dev - Ref, Dynamic Range



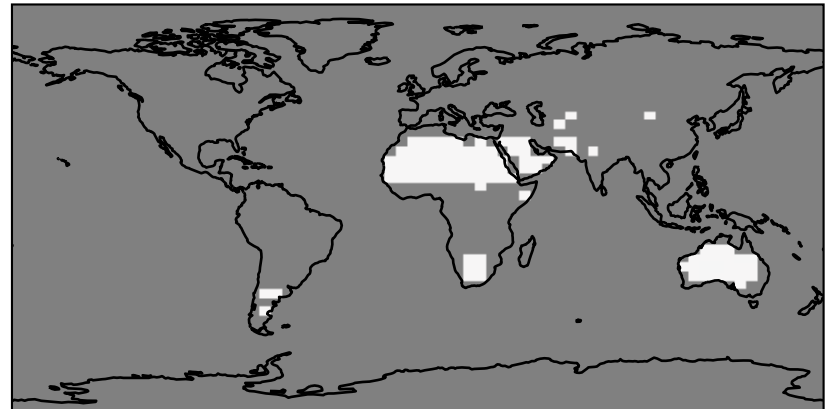
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

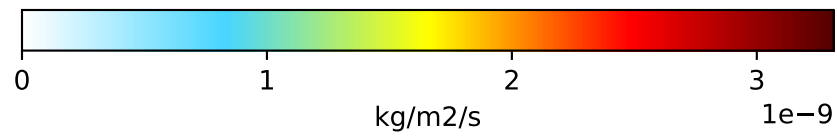
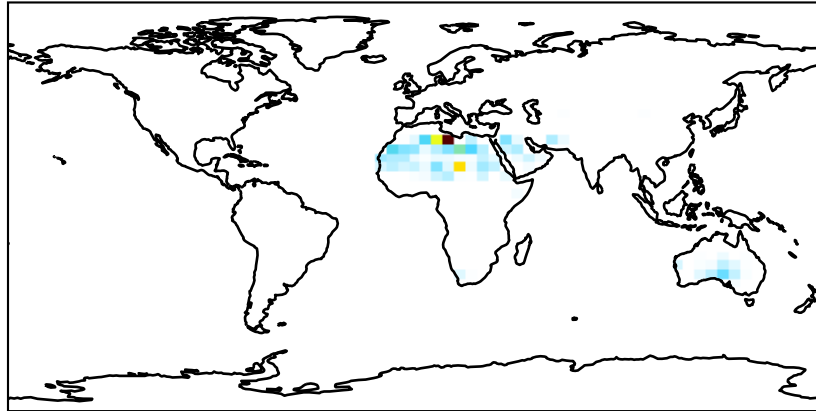


Ratio
Dev/Ref, Fixed Range

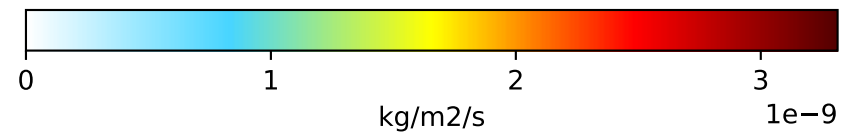
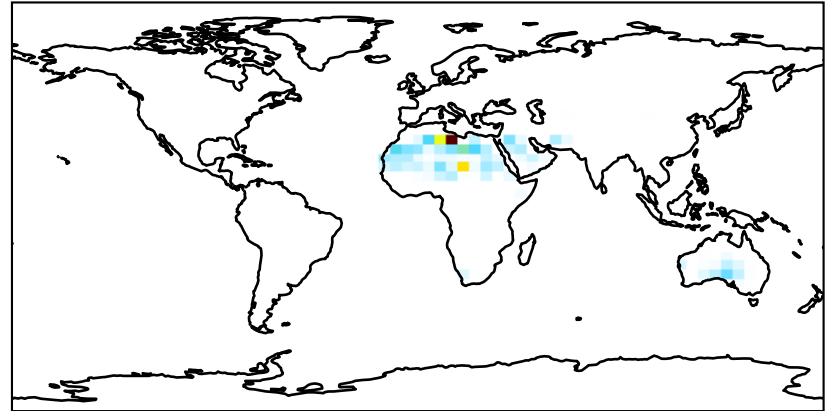


EmisDST2_Natural (Jan2019)

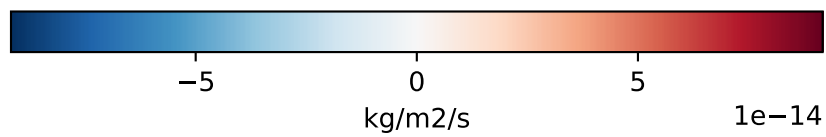
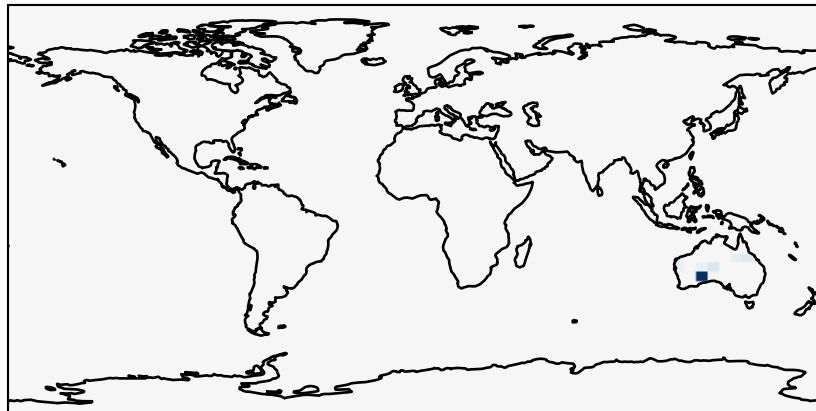
GCC_13.0.0 (Ref)
4.0x5.0



GCC_14.0.0 (Dev)
4.0x5.0



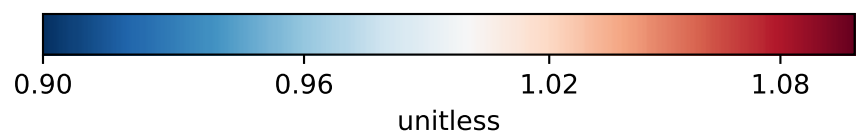
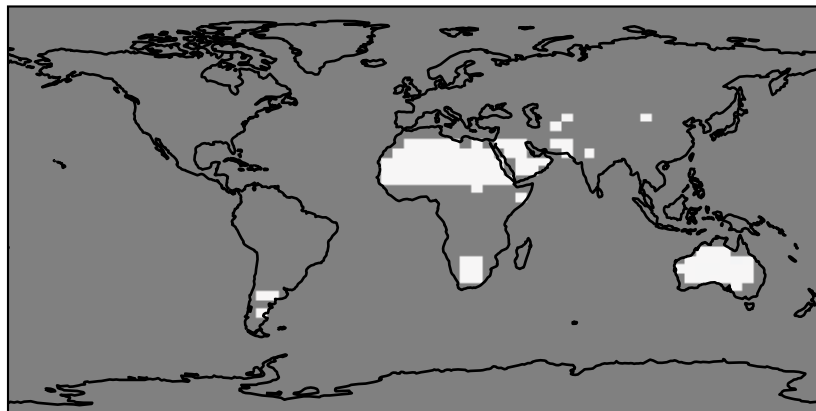
Difference
Dev - Ref, Dynamic Range



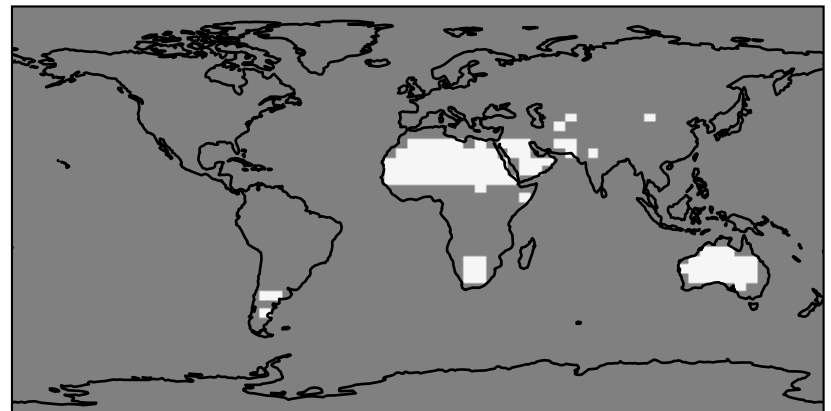
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

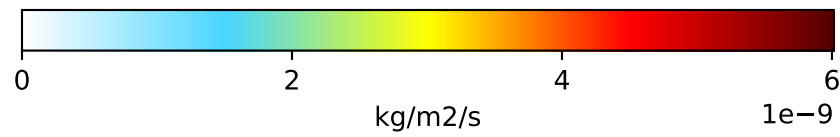
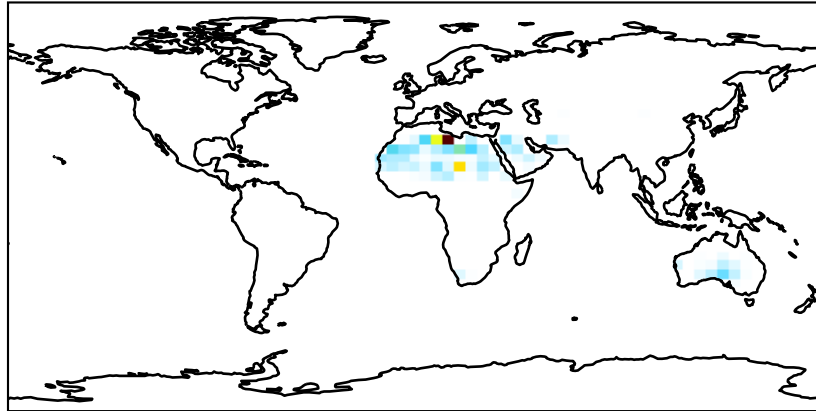


Ratio
Dev/Ref, Fixed Range

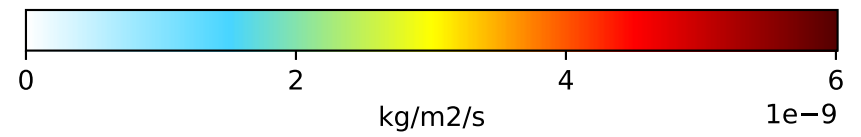
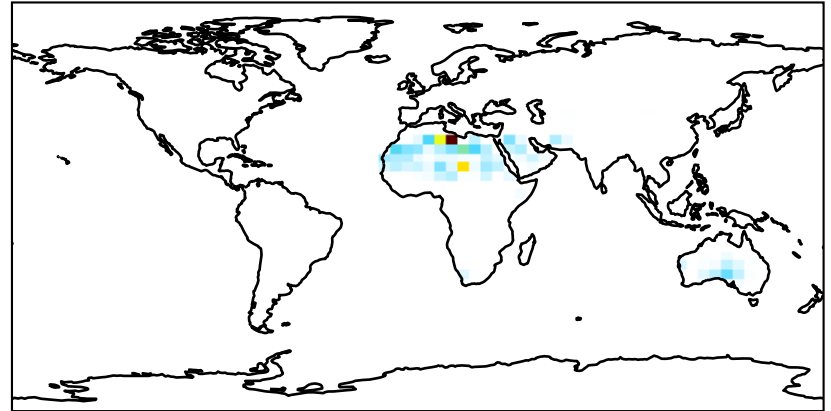


EmisDST3_Natural (Jan2019)

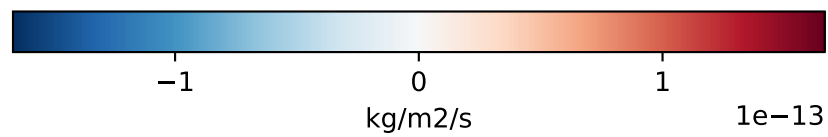
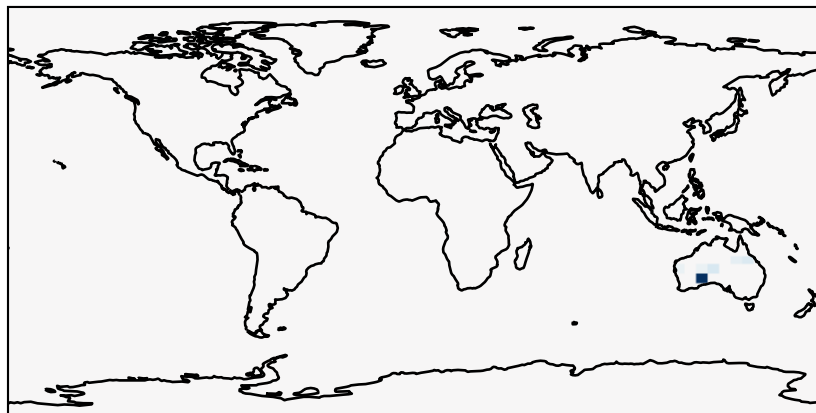
GCC_13.0.0 (Ref)
4.0x5.0



GCC_14.0.0 (Dev)
4.0x5.0



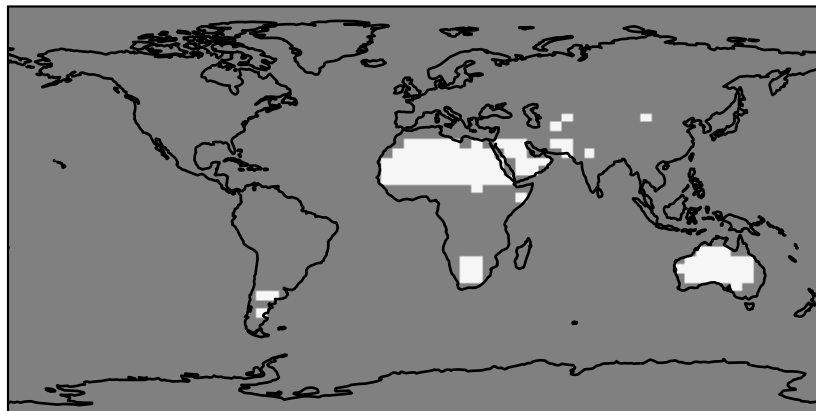
Difference
Dev - Ref, Dynamic Range



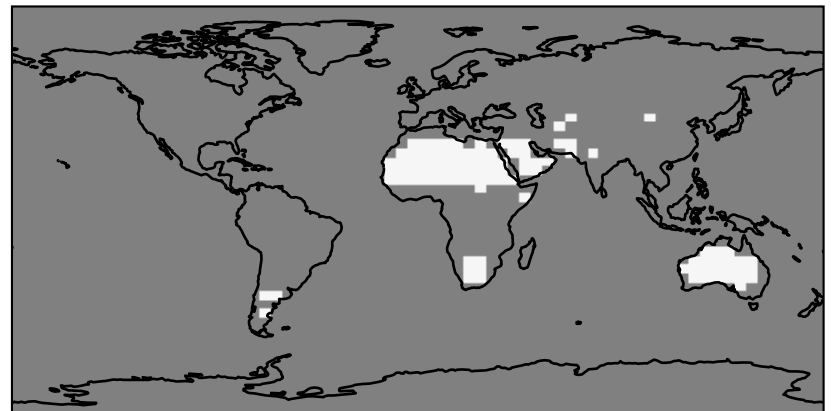
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

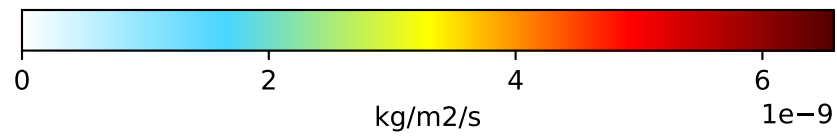
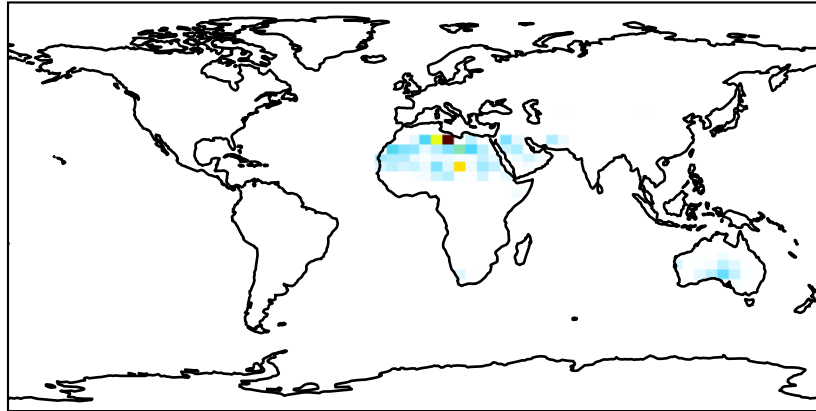


Ratio
Dev/Ref, Fixed Range

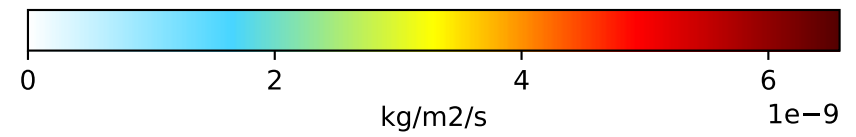
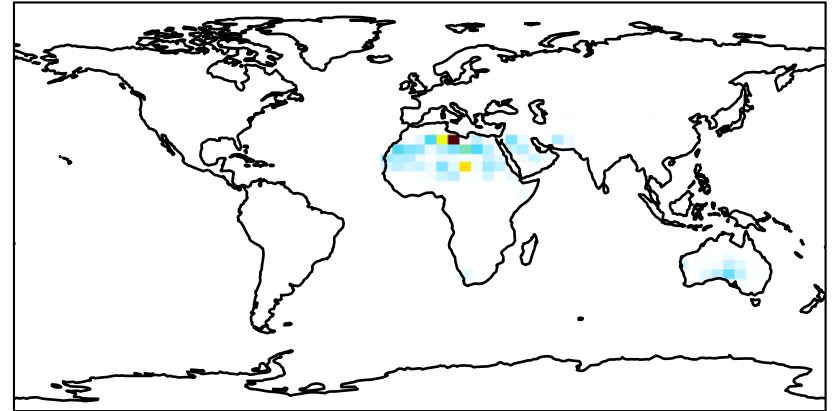


EmisDST4_Natural (Jan2019)

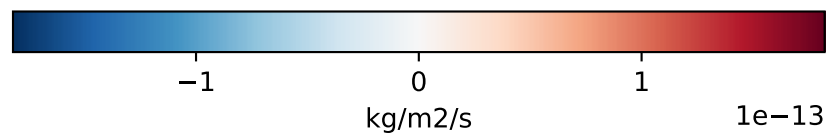
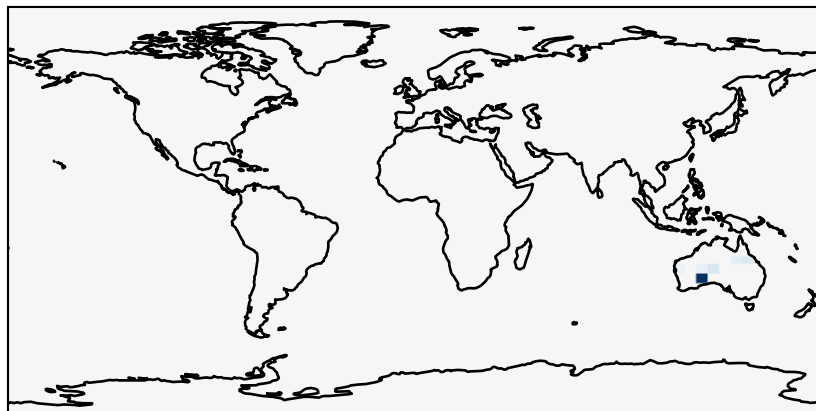
GCC_13.0.0 (Ref)
4.0x5.0



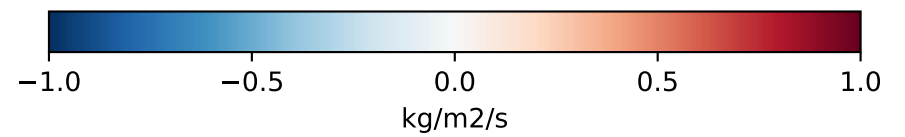
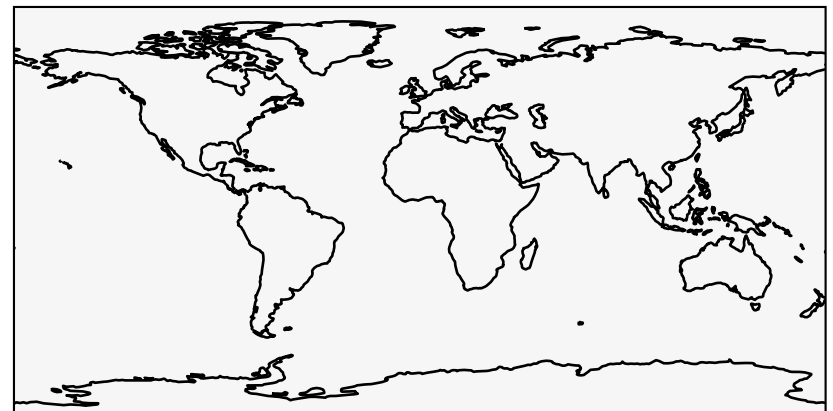
GCC_14.0.0 (Dev)
4.0x5.0



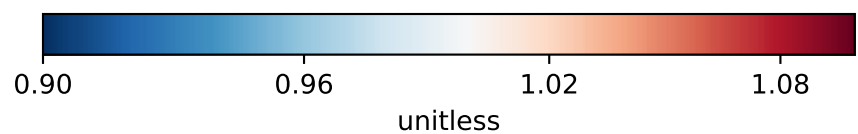
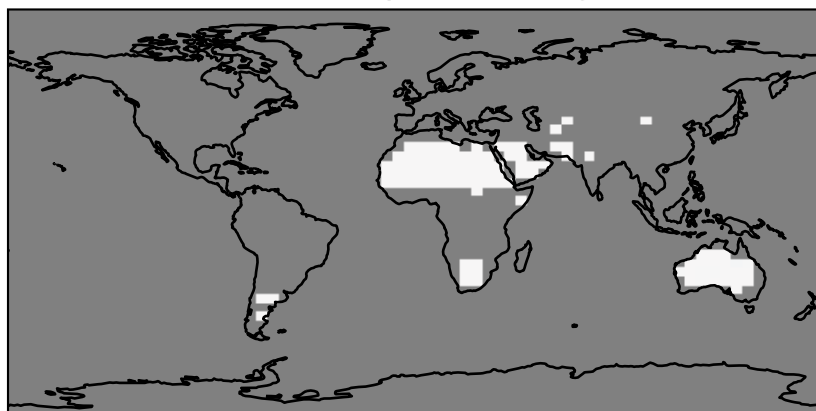
Difference
Dev - Ref, Dynamic Range



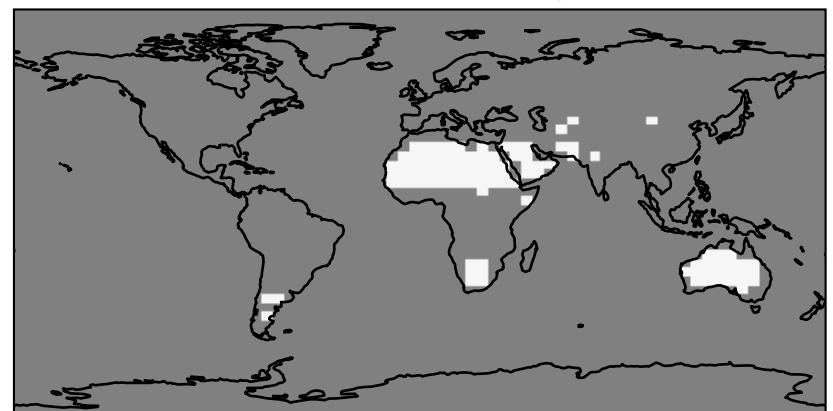
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

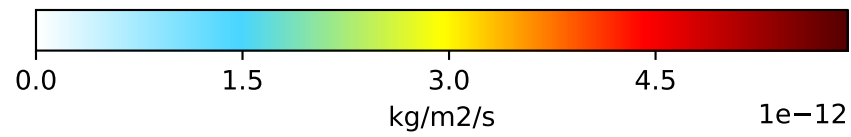
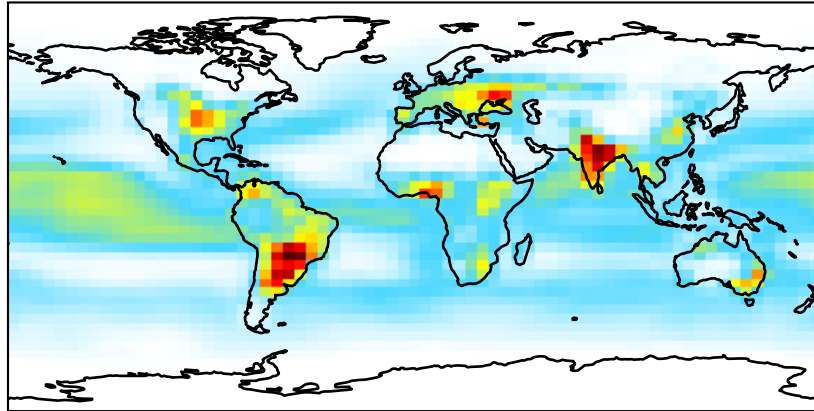


Ratio
Dev/Ref, Fixed Range

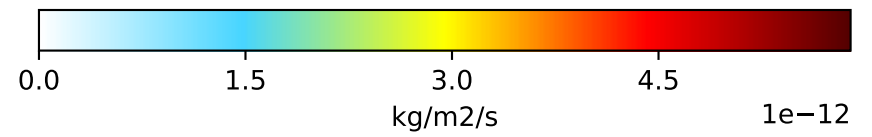
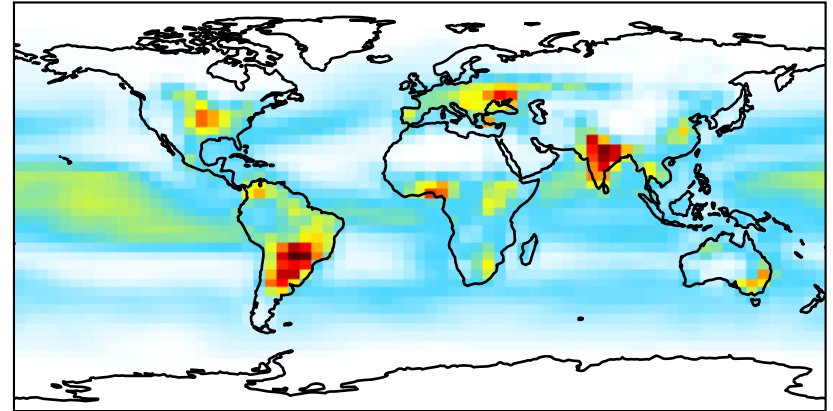


EmisNH3_Natural (Jan2019)

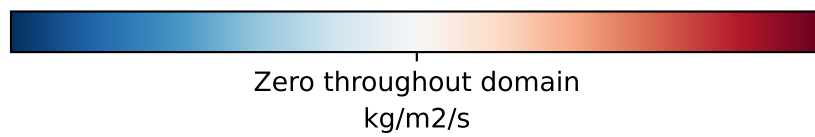
GCC_13.0.0 (Ref)
4.0x5.0



GCC_14.0.0 (Dev)
4.0x5.0



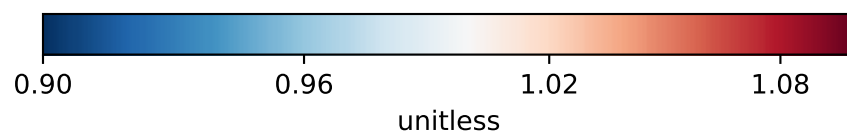
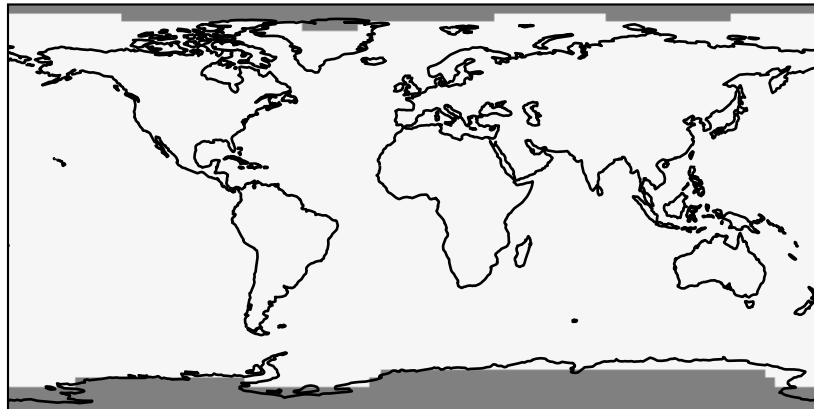
Difference
Dev - Ref, Dynamic Range



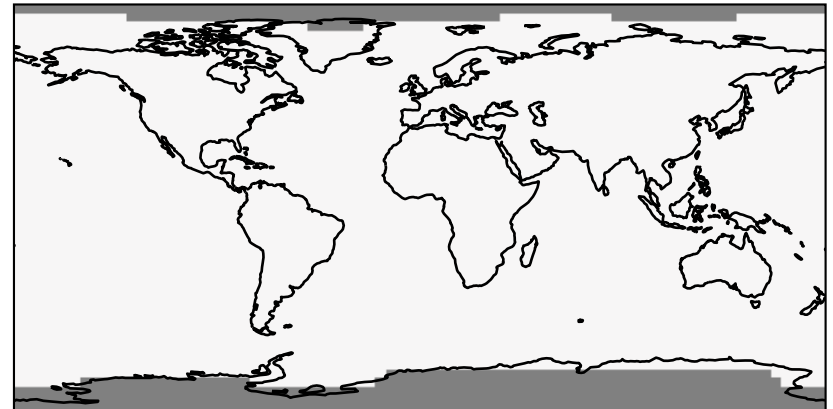
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

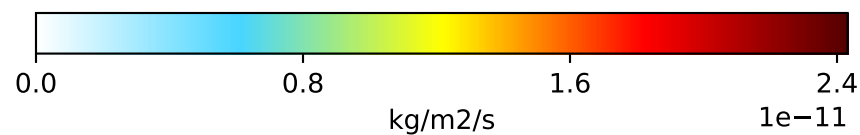
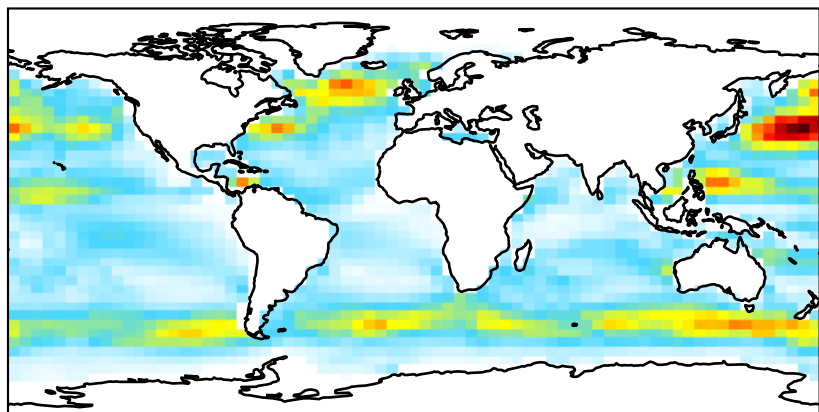


Ratio
Dev/Ref, Fixed Range

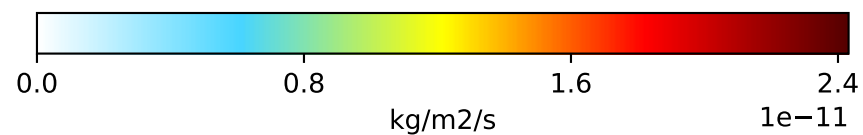
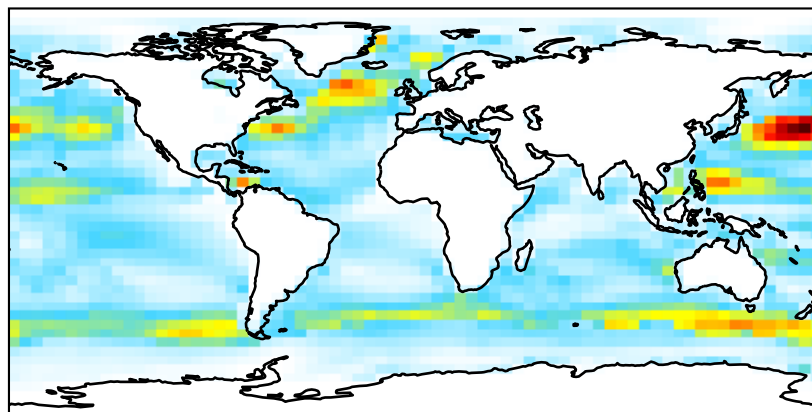


EmisSALA_Natural (Jan2019)

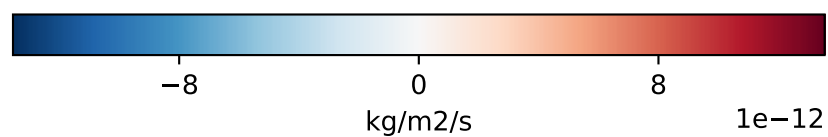
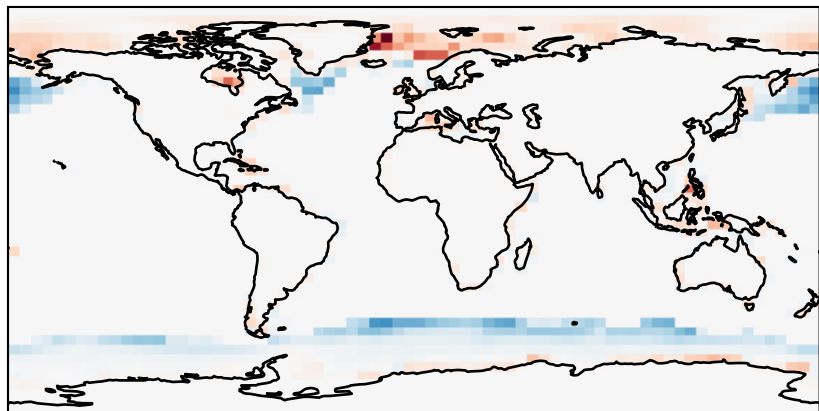
GCC_13.0.0 (Ref)
4.0x5.0



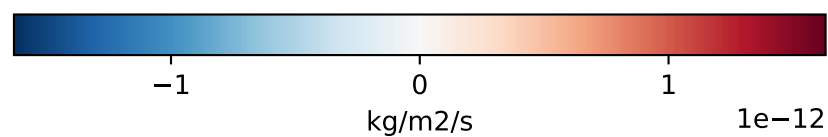
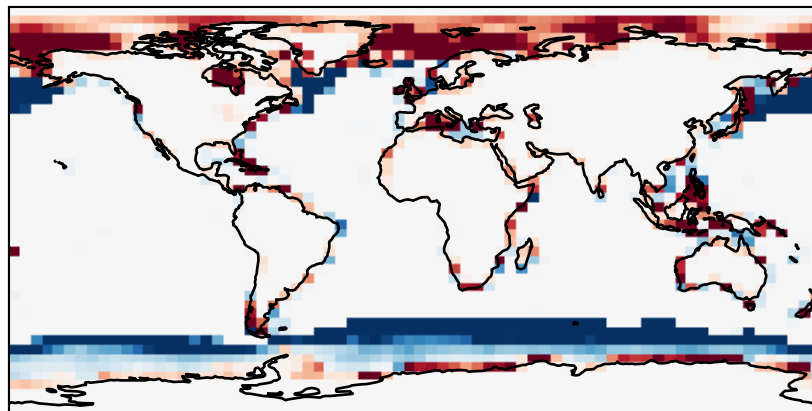
GCC_14.0.0 (Dev)
4.0x5.0



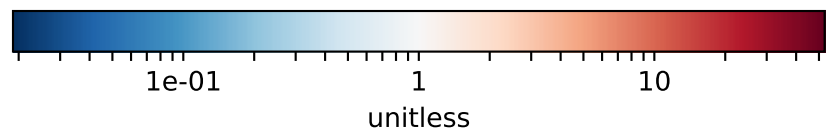
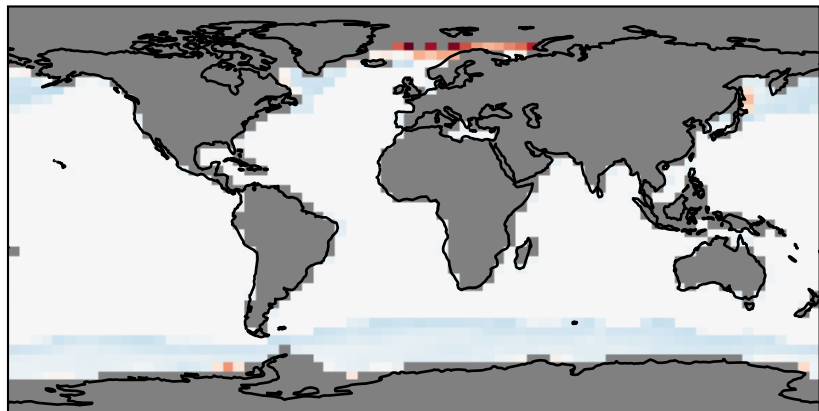
Difference
Dev - Ref, Dynamic Range



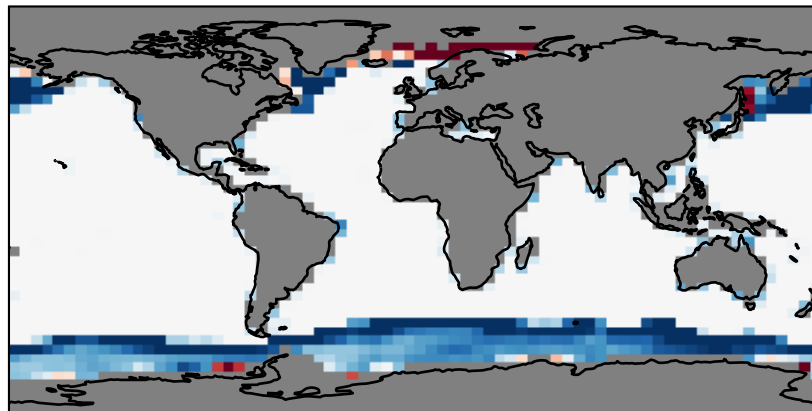
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

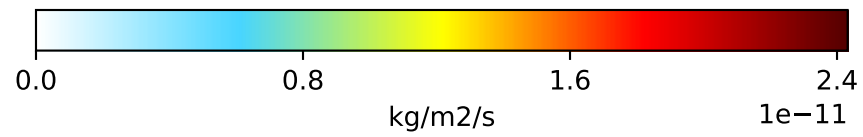
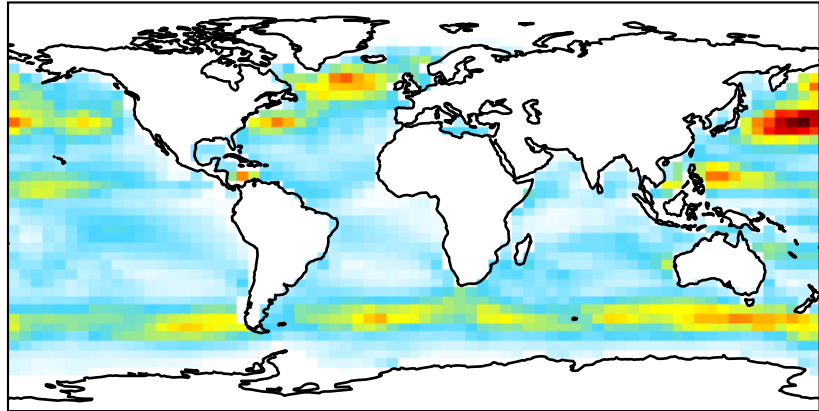


Ratio
Dev/Ref, Fixed Range

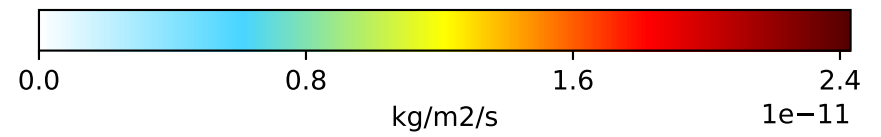
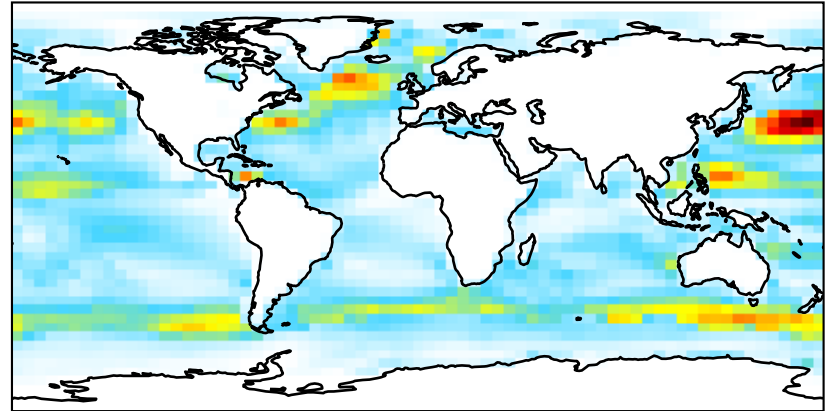


EmisSALAAL_Natural (Jan2019)

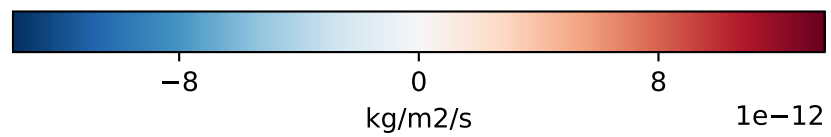
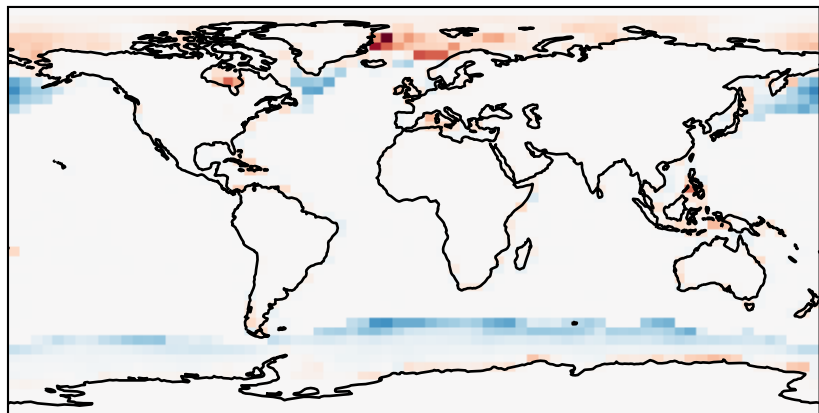
GCC_13.0.0 (Ref)
4.0x5.0



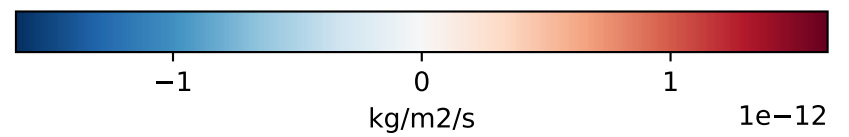
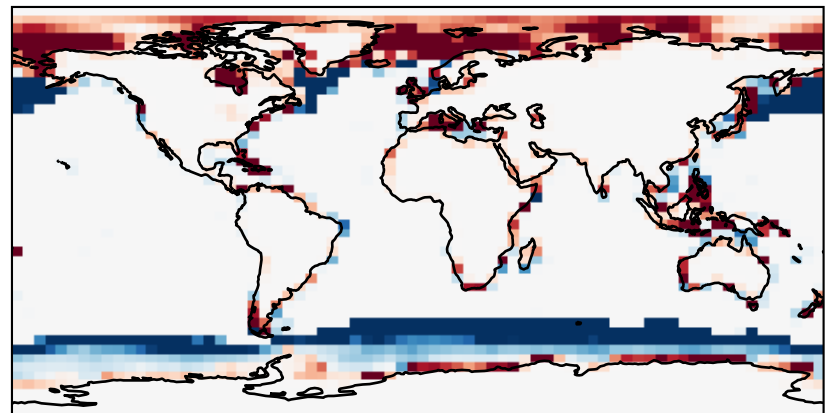
GCC_14.0.0 (Dev)
4.0x5.0



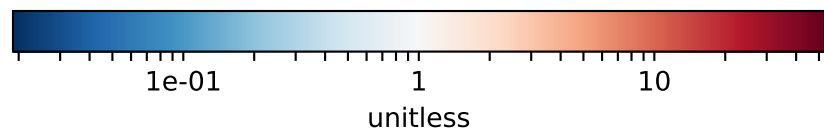
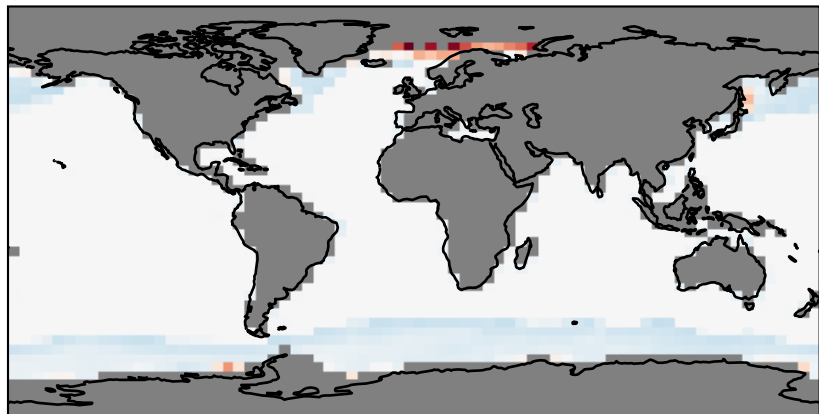
Difference
Dev - Ref, Dynamic Range



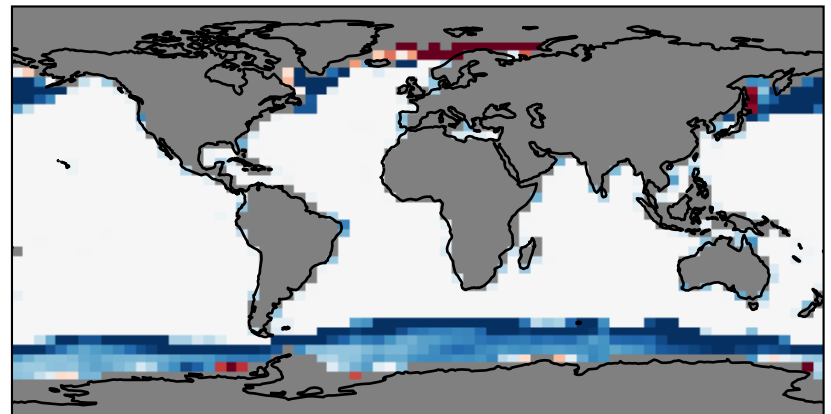
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

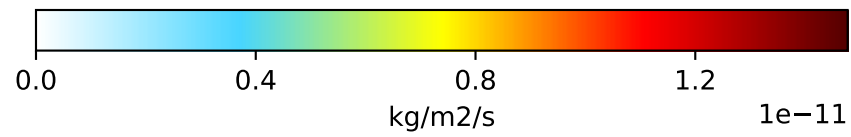
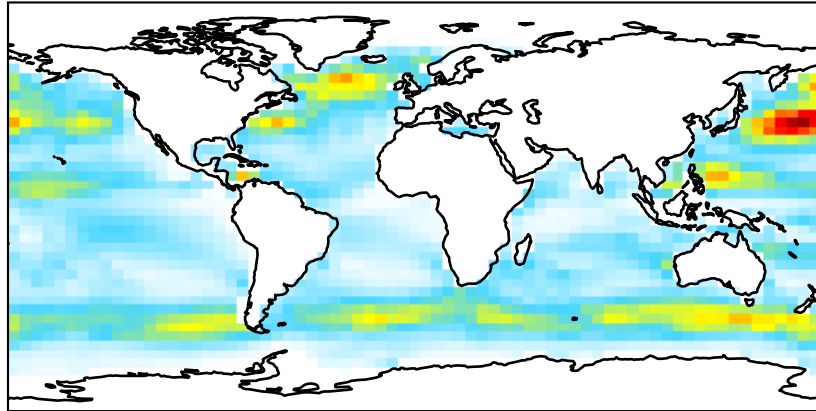


Ratio
Dev/Ref, Fixed Range

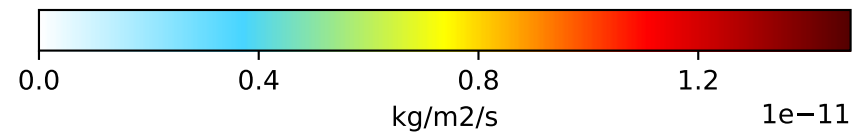
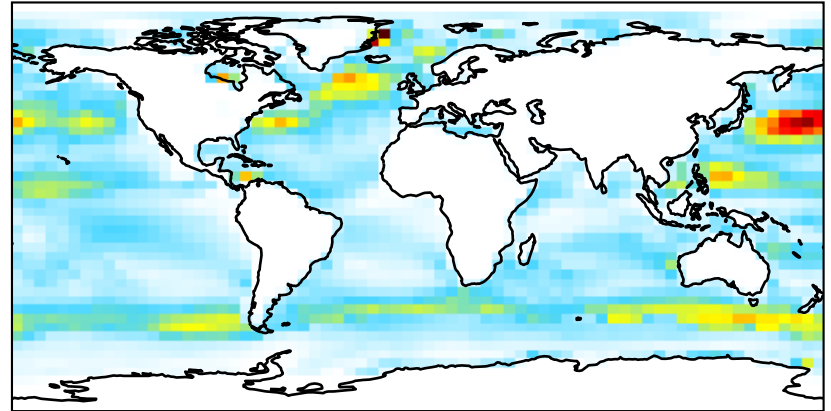


EmisSALACL_Natural (Jan2019)

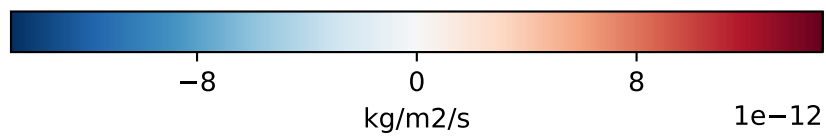
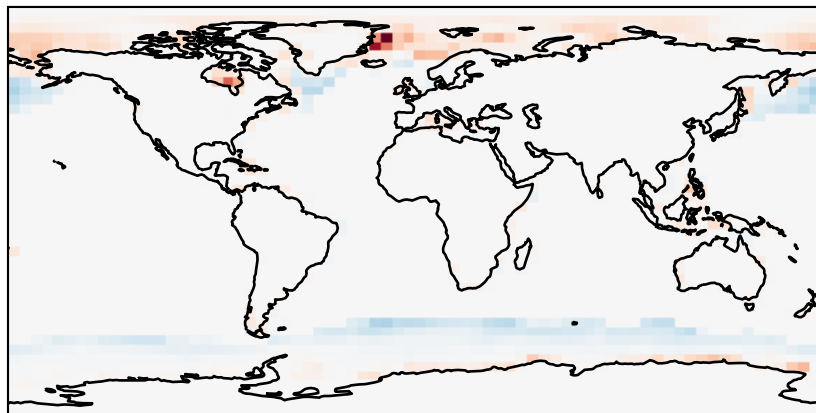
GCC_13.0.0 (Ref)
4.0x5.0



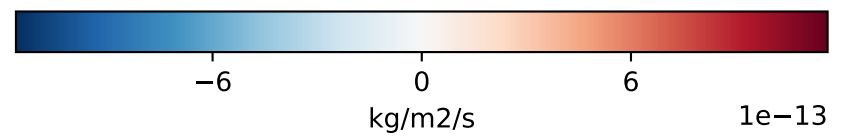
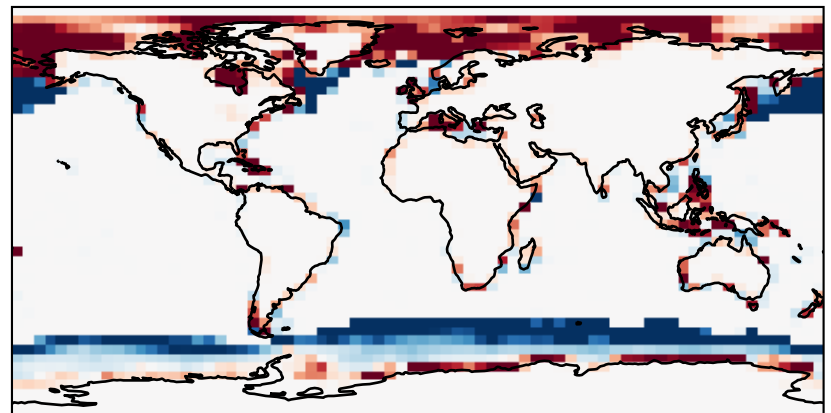
GCC_14.0.0 (Dev)
4.0x5.0



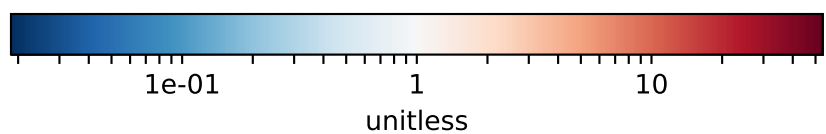
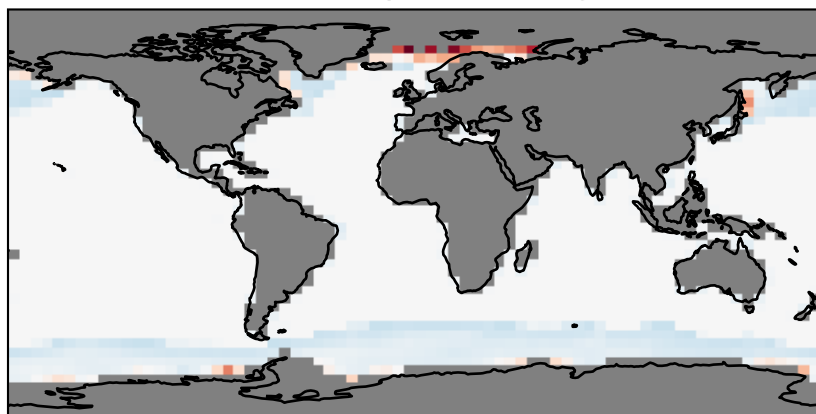
Difference
Dev - Ref, Dynamic Range



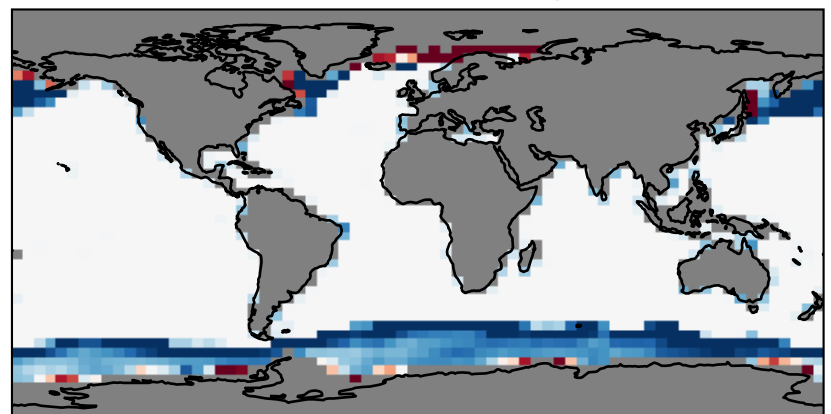
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

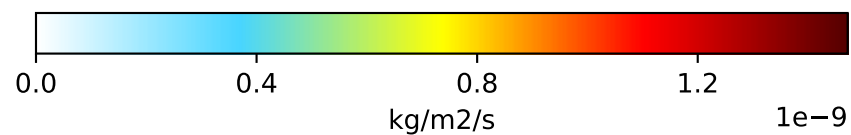
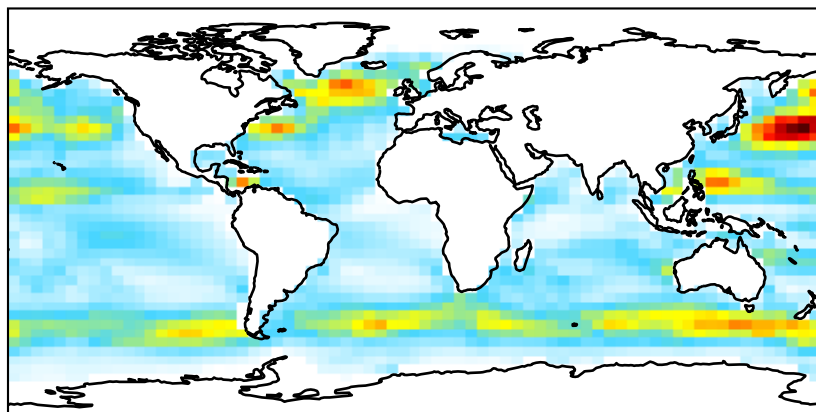


Ratio
Dev/Ref, Fixed Range

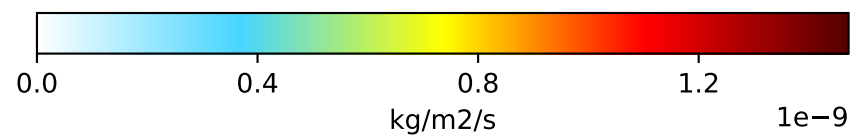
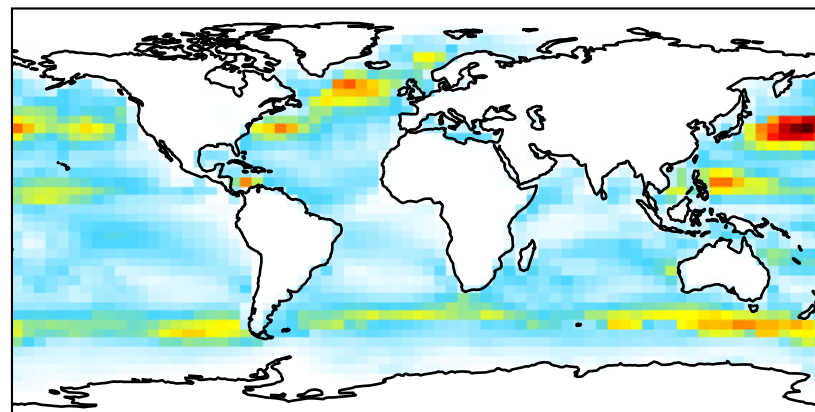


EmisSALC_Natural (Jan2019)

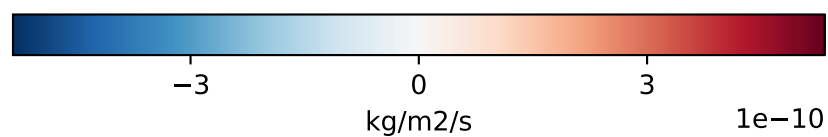
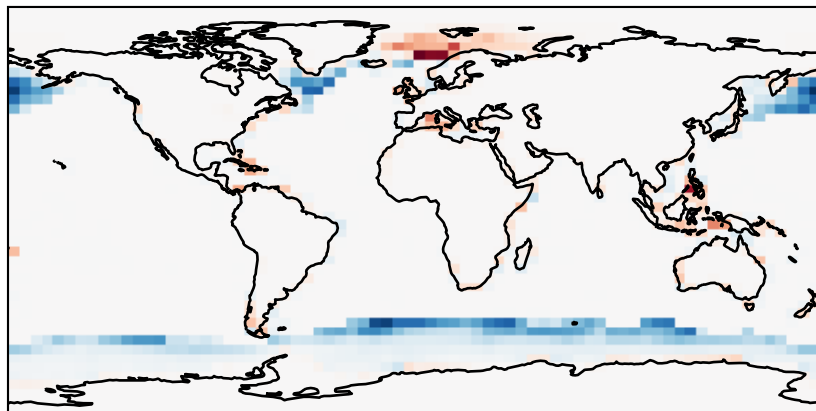
GCC_13.0.0 (Ref)
4.0x5.0



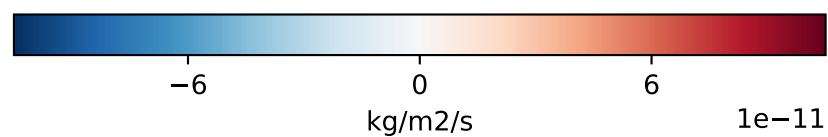
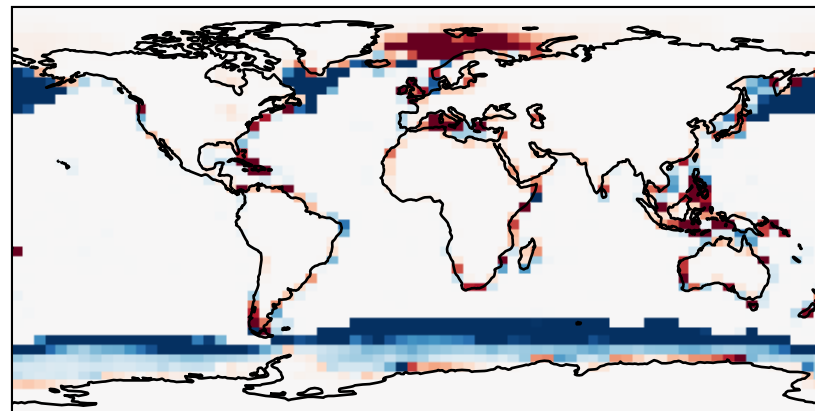
GCC_14.0.0 (Dev)
4.0x5.0



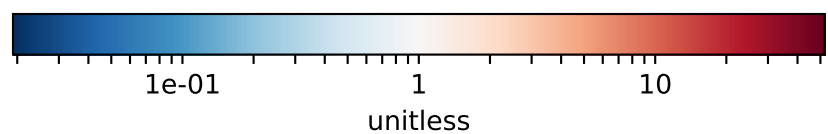
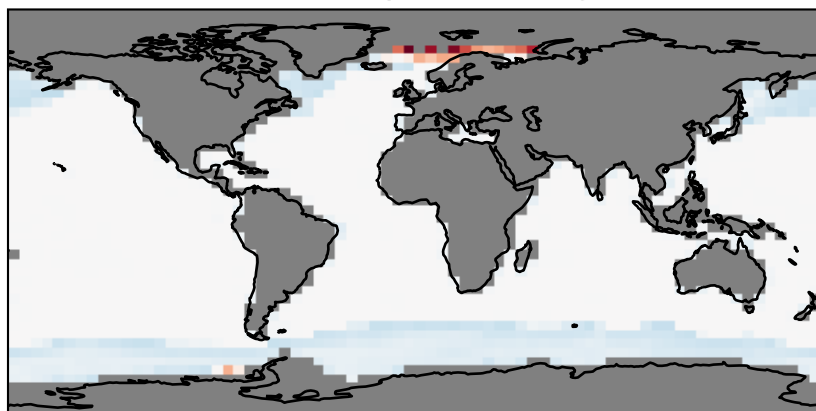
Difference
Dev - Ref, Dynamic Range



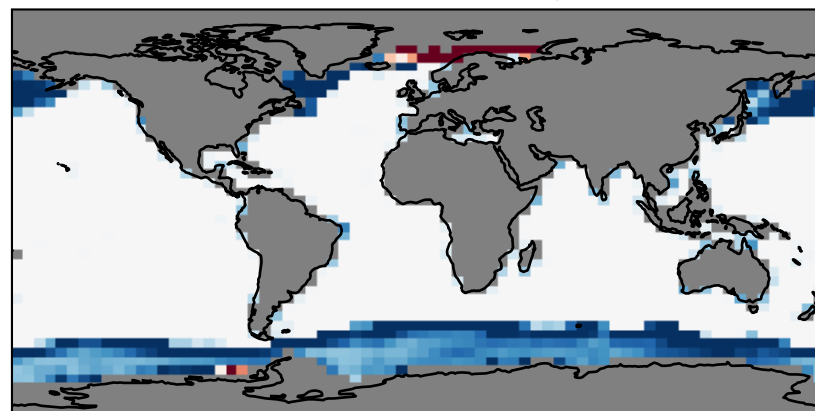
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

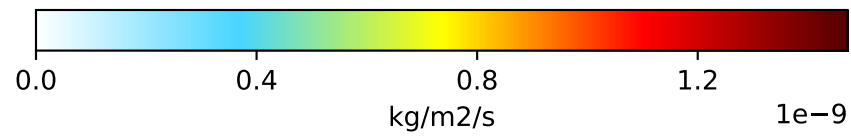
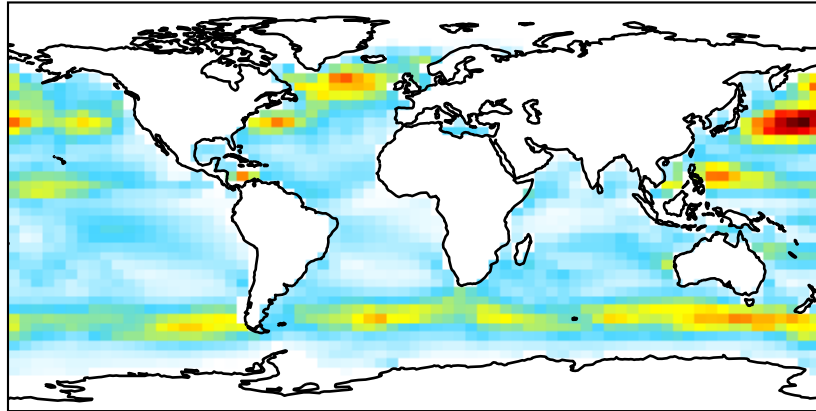


Ratio
Dev/Ref, Fixed Range

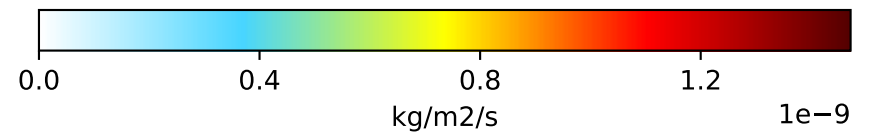
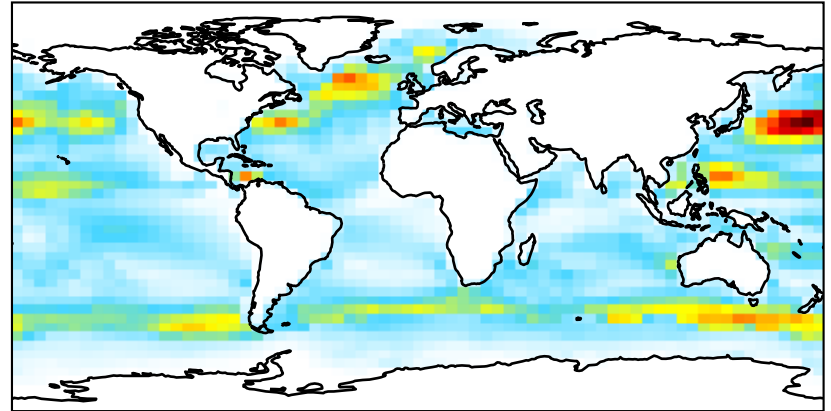


EmisSALCAL_Natural (Jan2019)

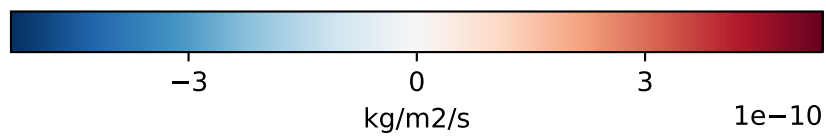
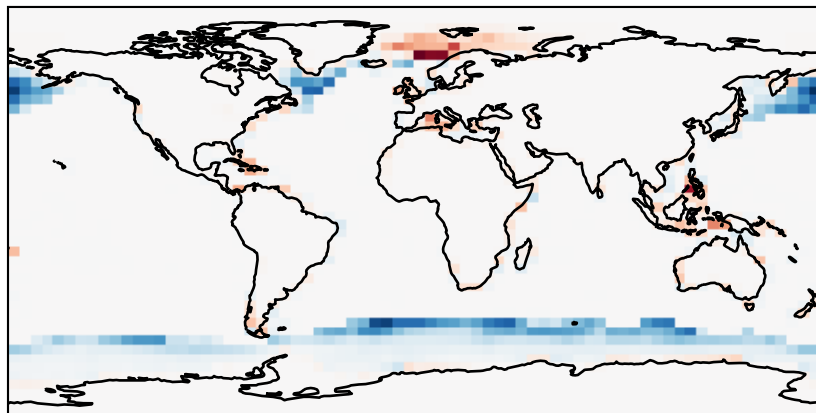
GCC_13.0.0 (Ref)
4.0x5.0



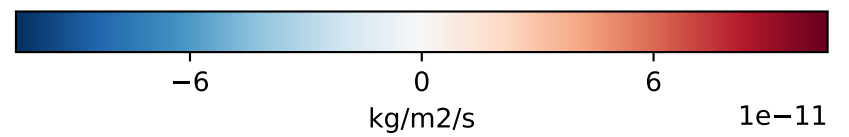
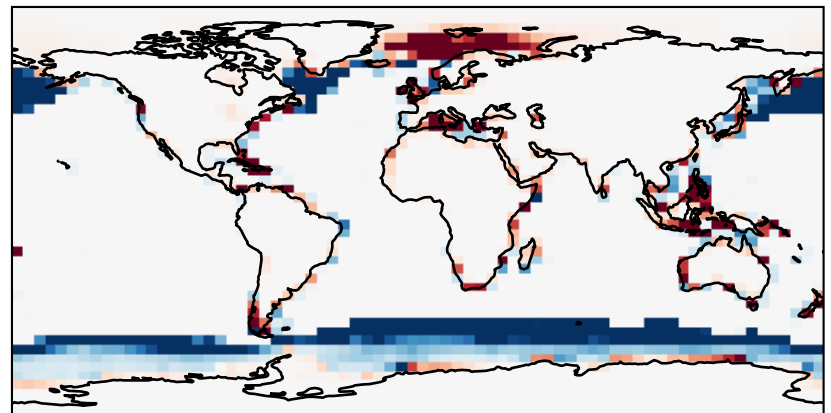
GCC_14.0.0 (Dev)
4.0x5.0



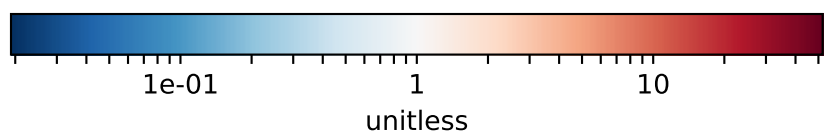
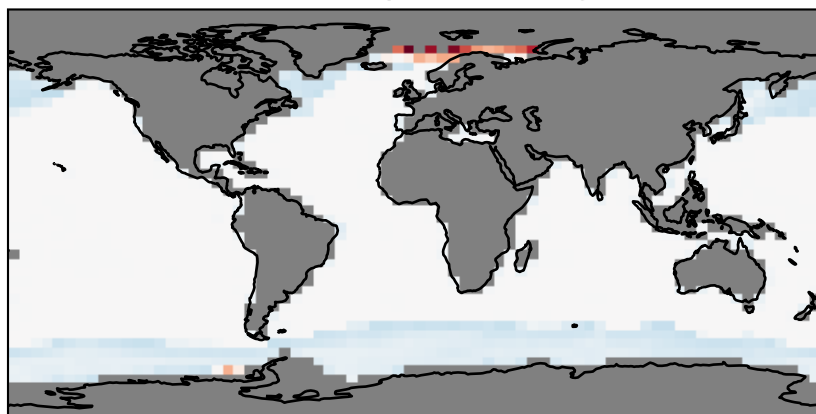
Difference
Dev - Ref, Dynamic Range



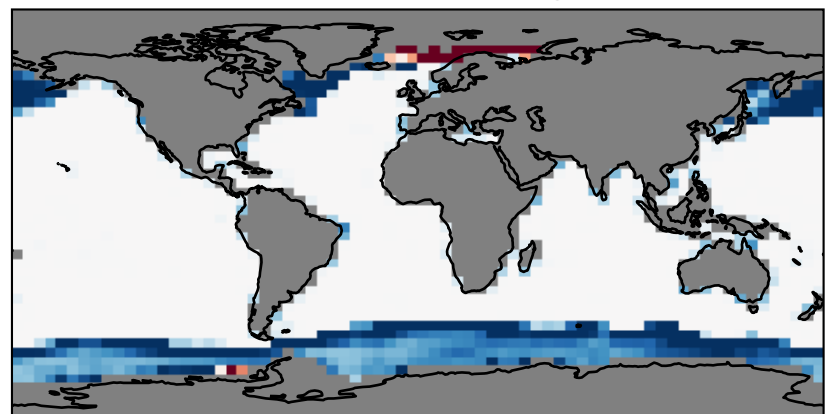
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

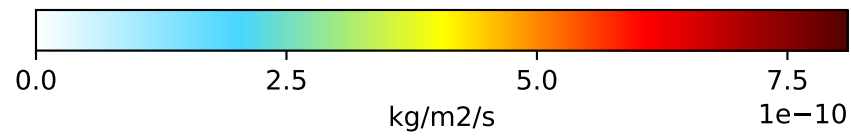
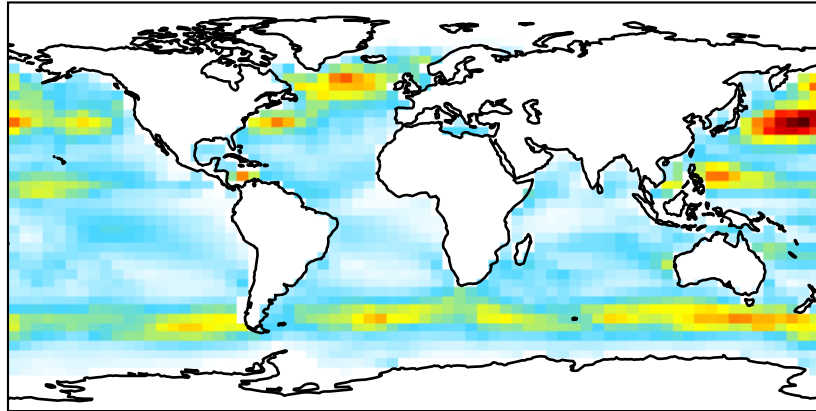


Ratio
Dev/Ref, Fixed Range

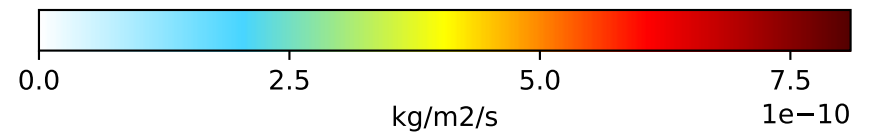
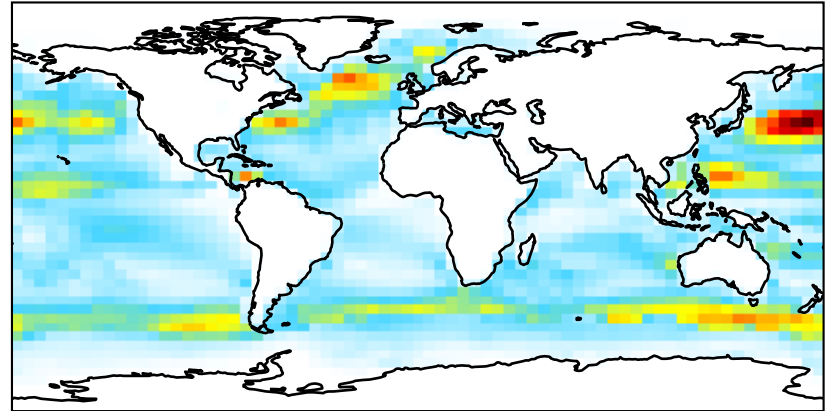


EmisSALCCL_Natural (Jan2019)

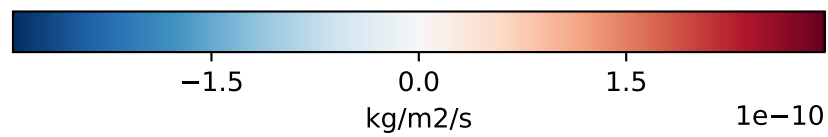
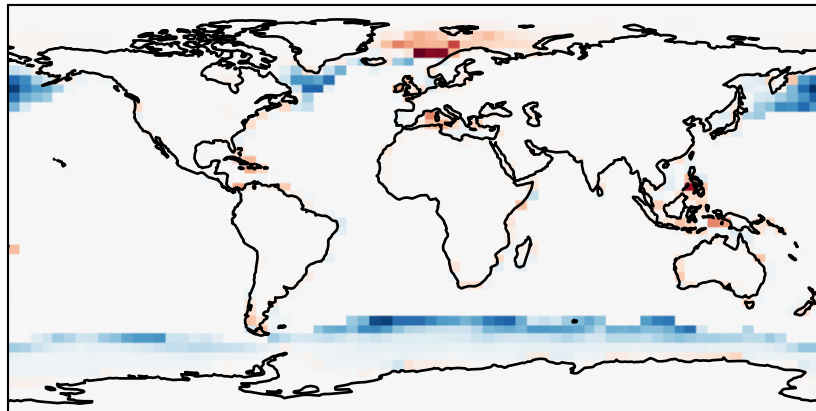
GCC_13.0.0 (Ref)
4.0x5.0



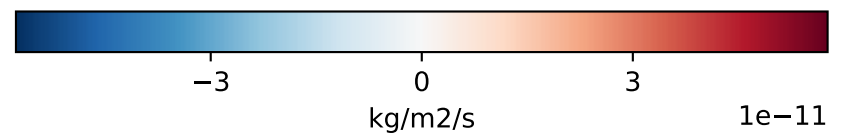
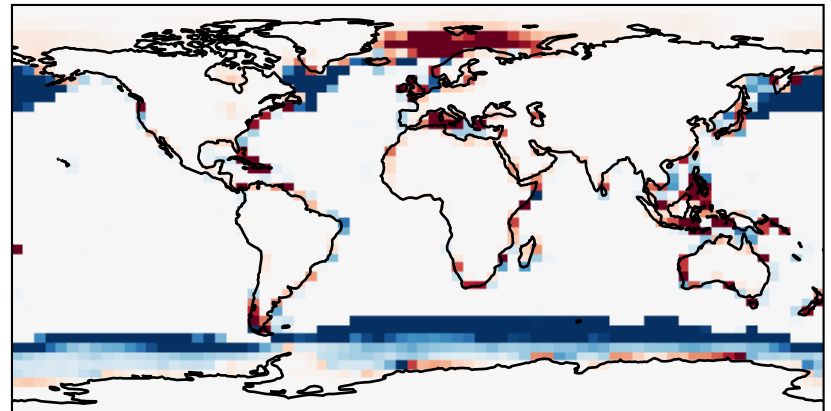
GCC_14.0.0 (Dev)
4.0x5.0



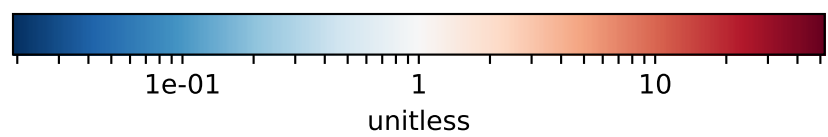
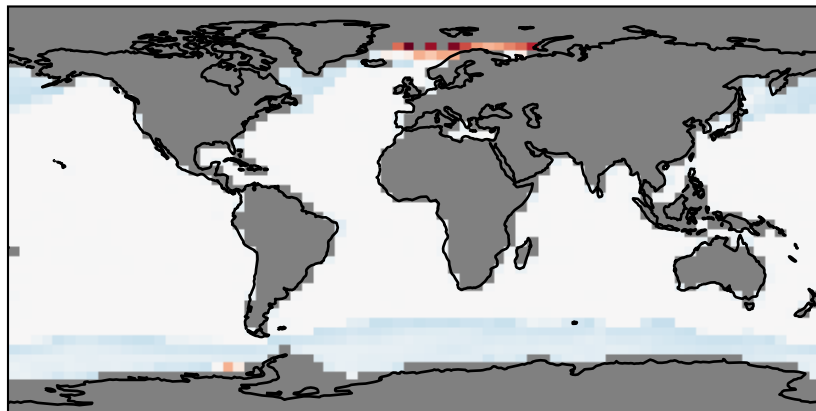
Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range



Ratio
Dev/Ref, Fixed Range

