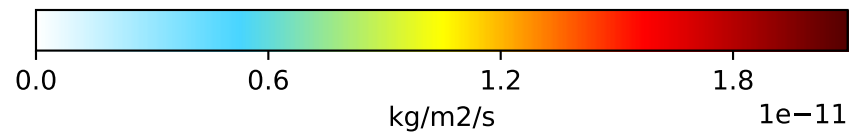
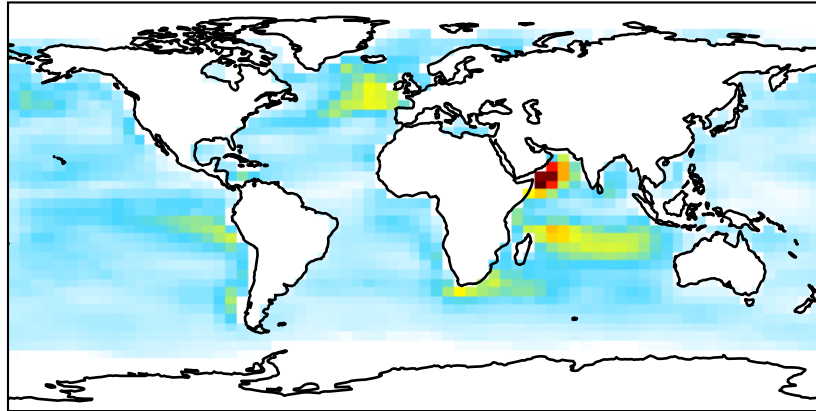
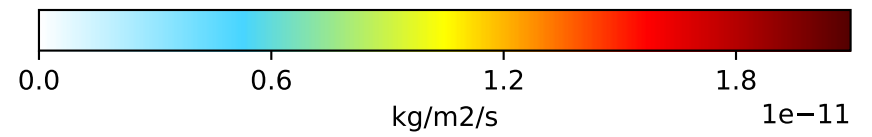
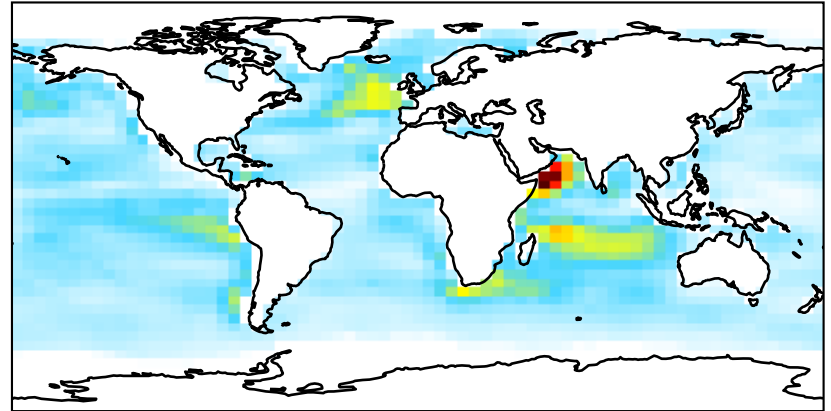


# EmisDMS\_Ocean (Jul2019)

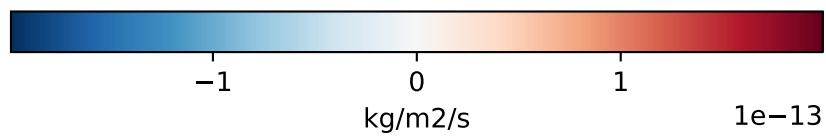
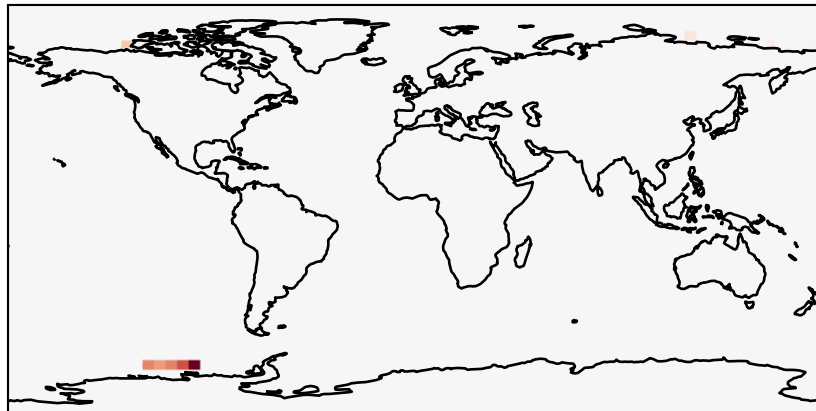
GCC\_13.0.0 (Ref)  
4.0x5.0



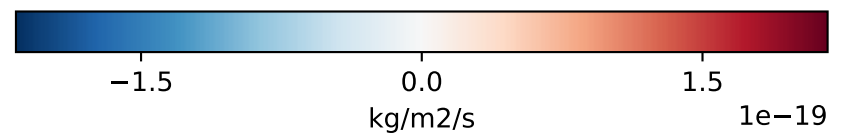
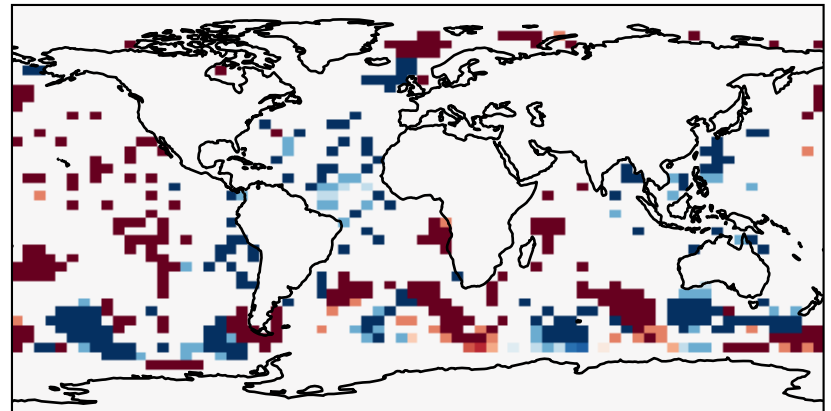
GCC\_14.0.0 (Dev)  
4.0x5.0



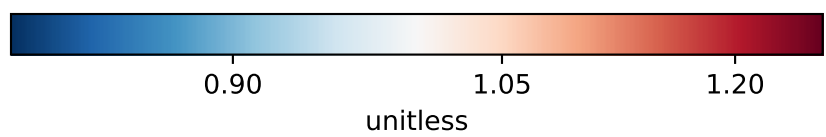
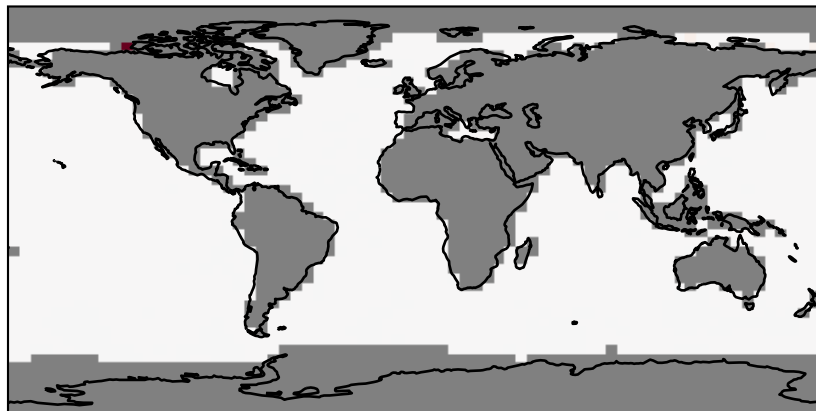
Difference  
Dev - Ref, Dynamic Range



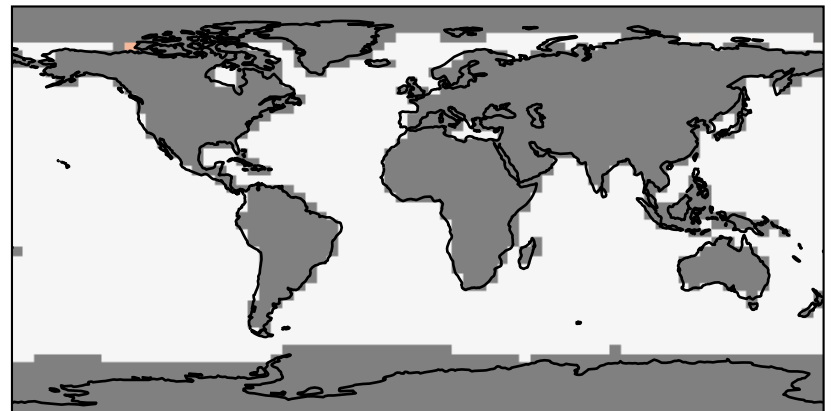
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

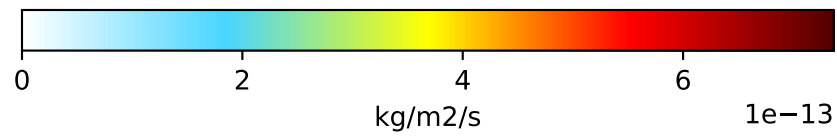
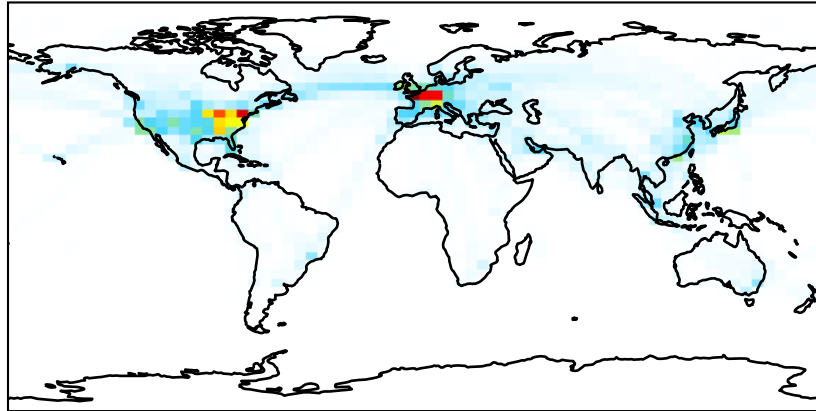


Ratio  
Dev/Ref, Fixed Range

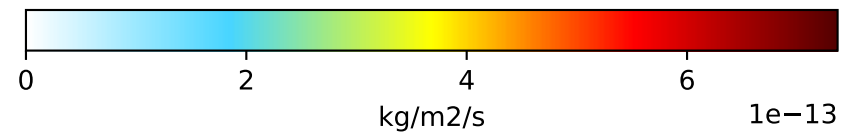
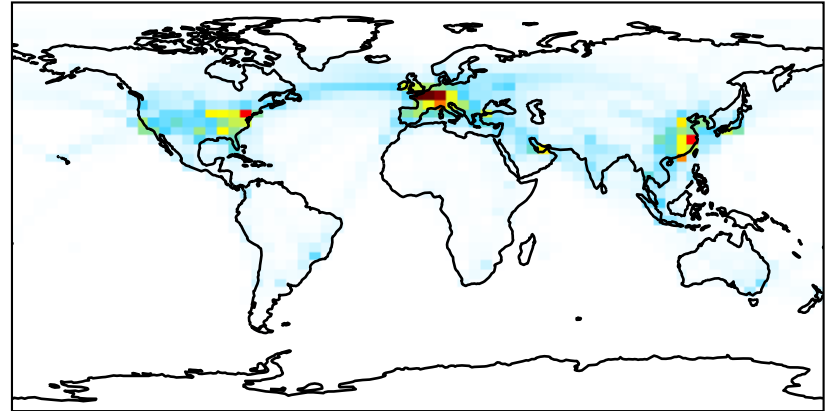


# EmisSO2\_Aircraft (Jul2019)

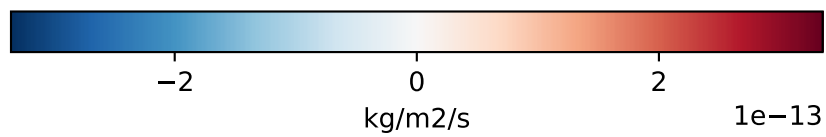
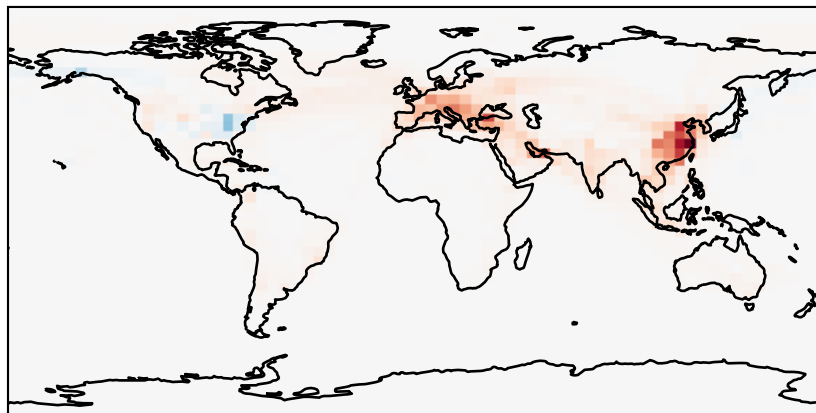
GCC\_13.0.0 (Ref)  
4.0x5.0



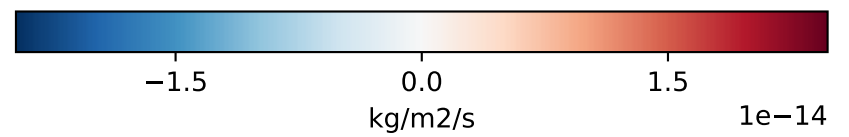
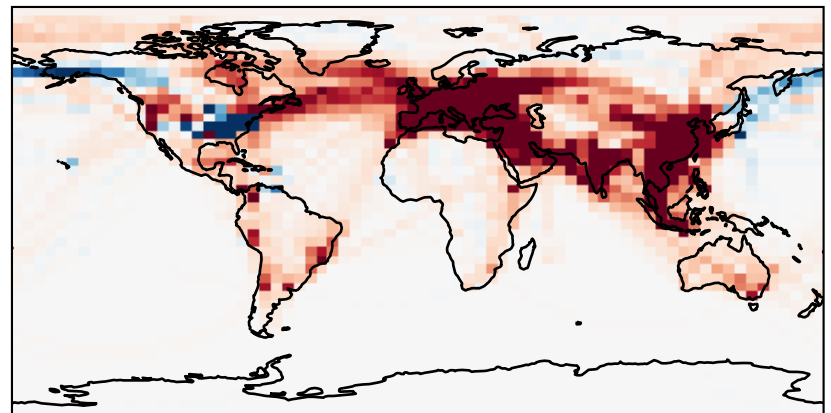
GCC\_14.0.0 (Dev)  
4.0x5.0



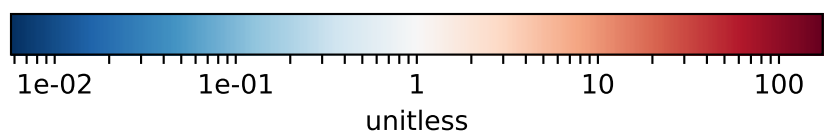
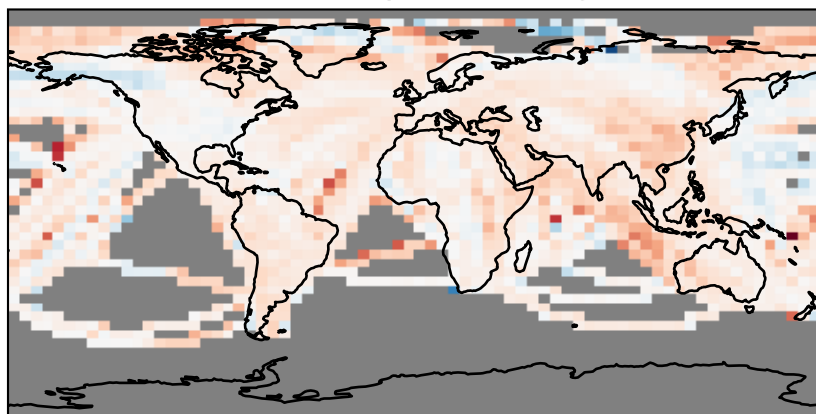
Difference  
Dev - Ref, Dynamic Range



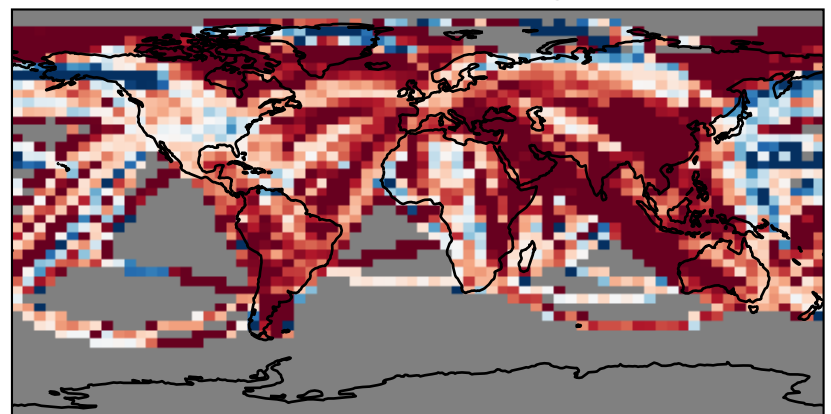
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

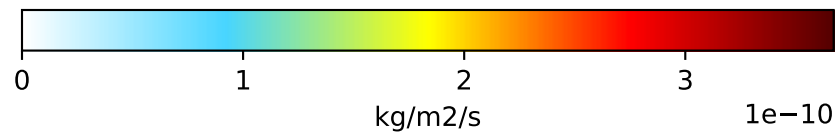
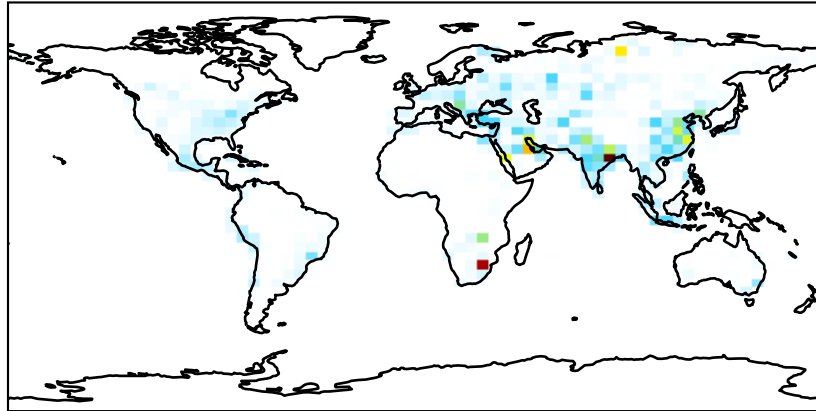


Ratio  
Dev/Ref, Fixed Range

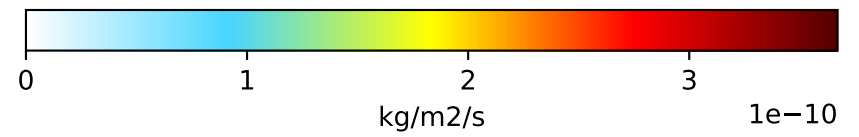
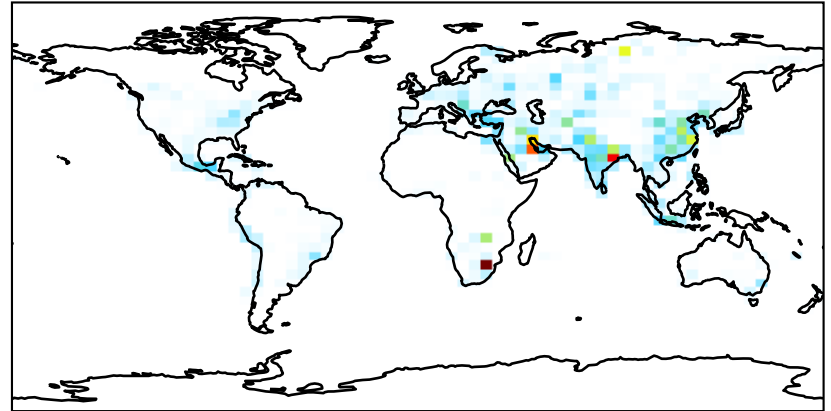


# EmisSO2\_Anthro (Jul2019)

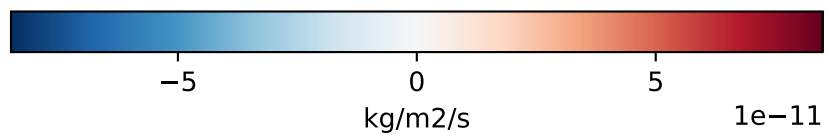
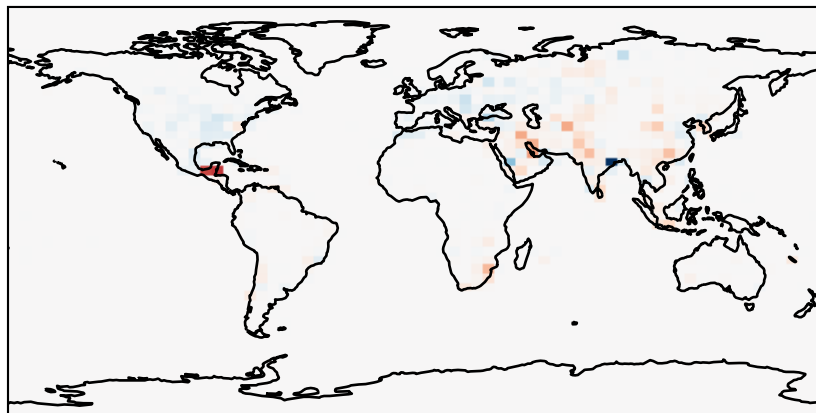
GCC\_13.0.0 (Ref)  
4.0x5.0



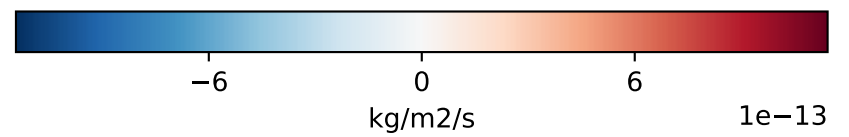
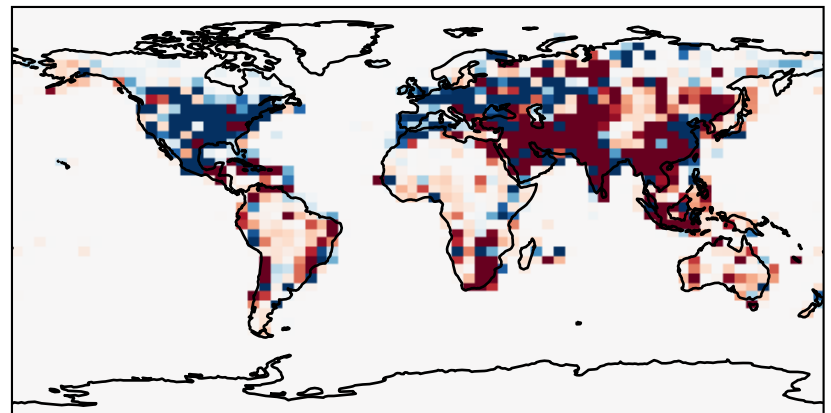
GCC\_14.0.0 (Dev)  
4.0x5.0



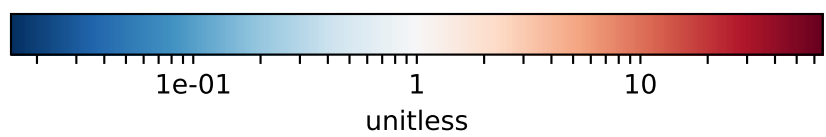
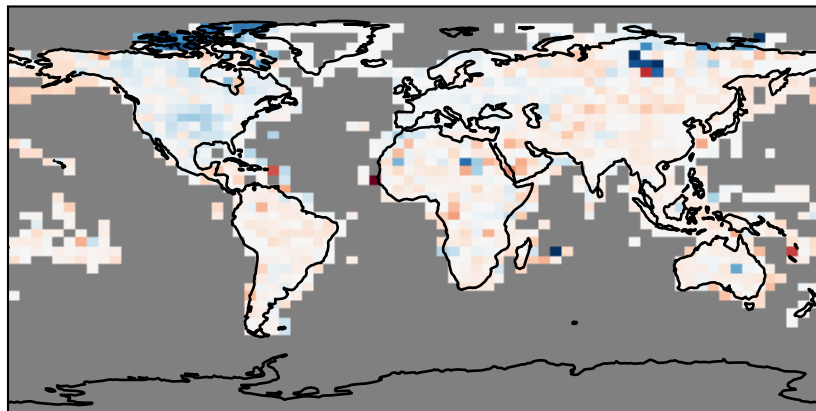
Difference  
Dev - Ref, Dynamic Range



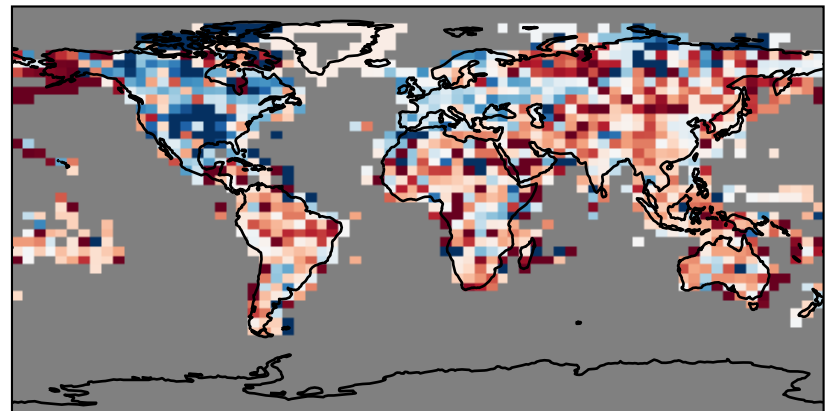
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

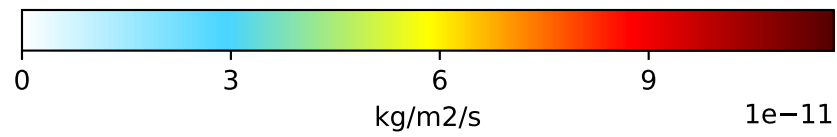
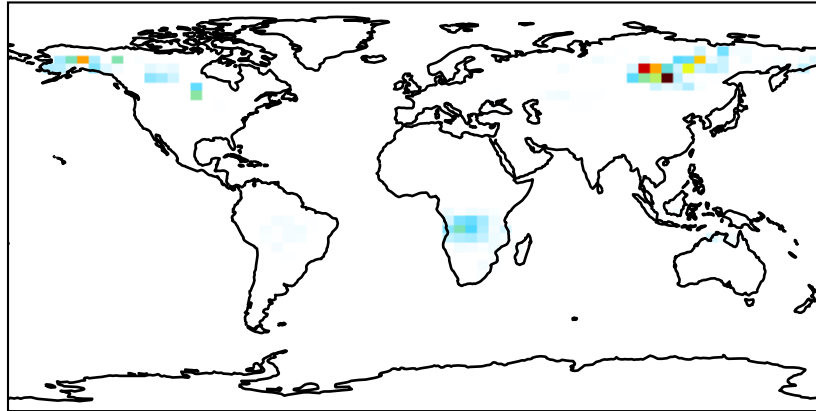


Ratio  
Dev/Ref, Fixed Range

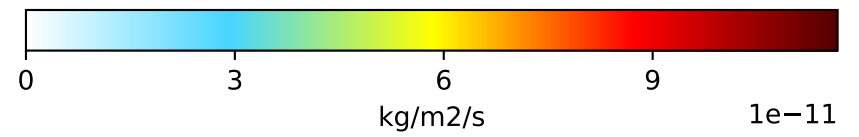
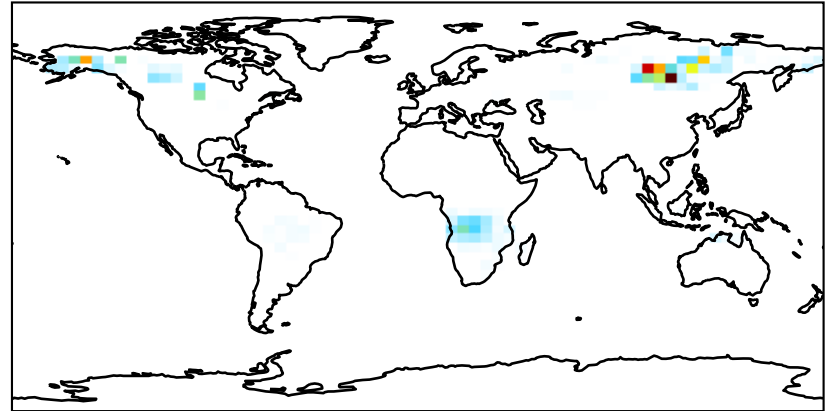


# EmisSO2\_BioBurn (Jul2019)

GCC\_13.0.0 (Ref)  
4.0x5.0



GCC\_14.0.0 (Dev)  
4.0x5.0



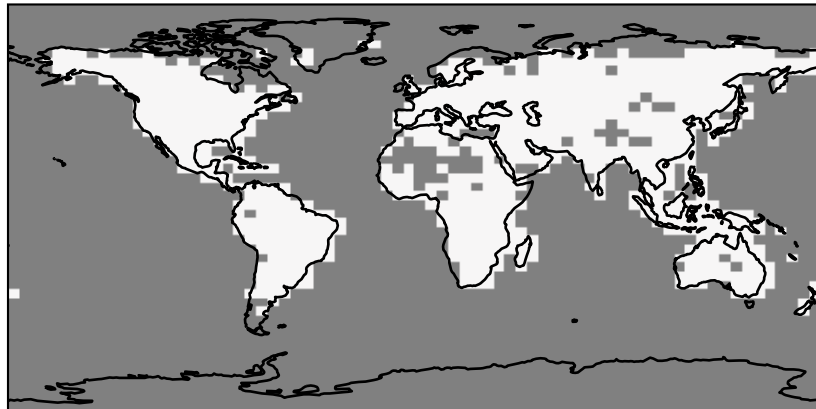
Difference  
Dev - Ref, Dynamic Range



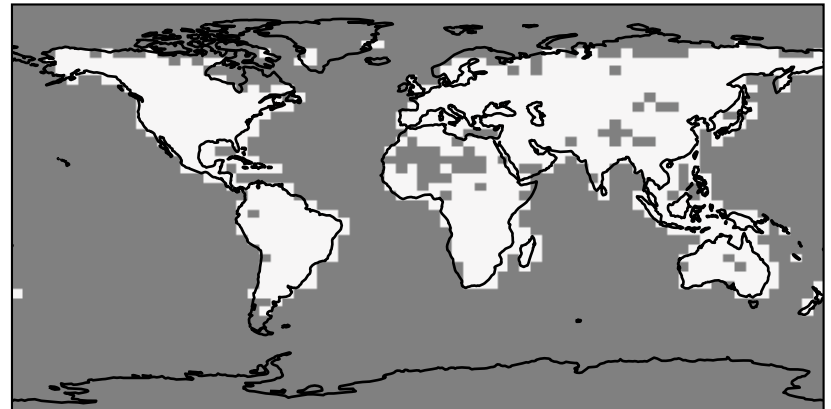
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

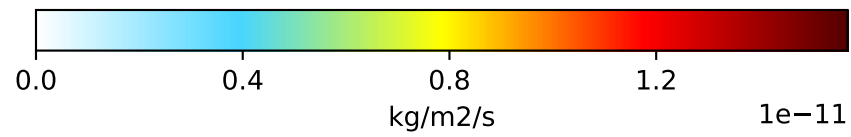
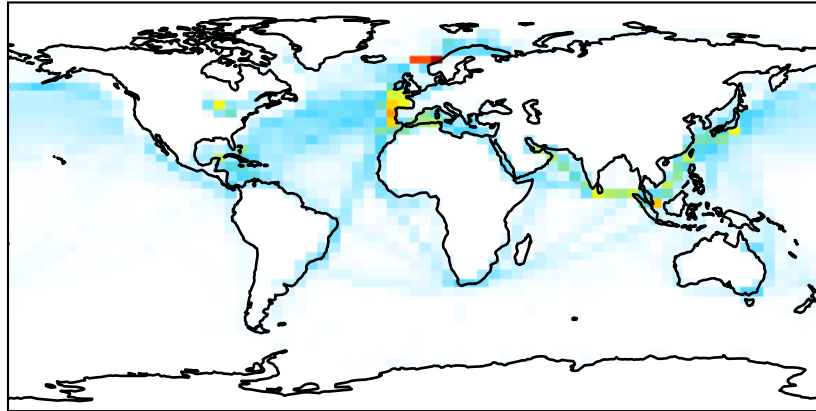


Ratio  
Dev/Ref, Fixed Range

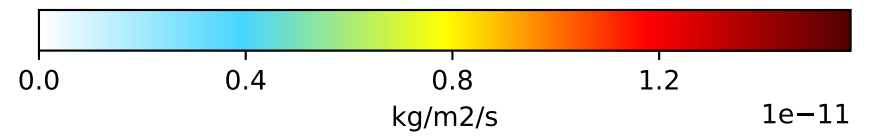
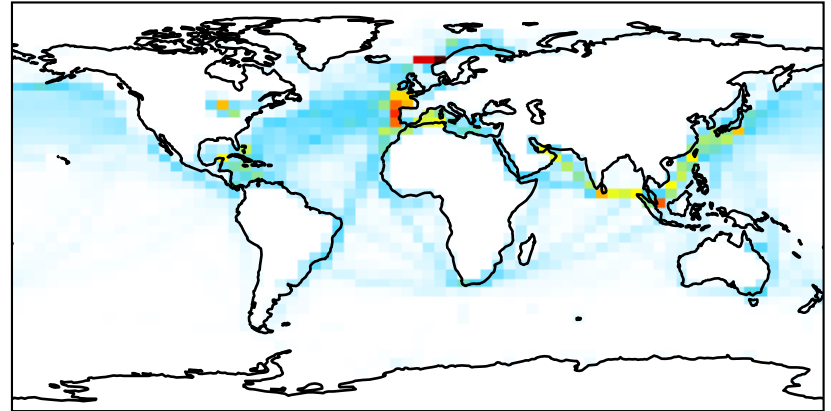


# EmisSO2\_Ship (Jul2019)

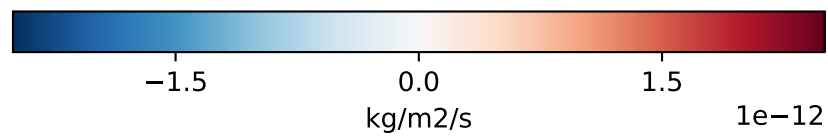
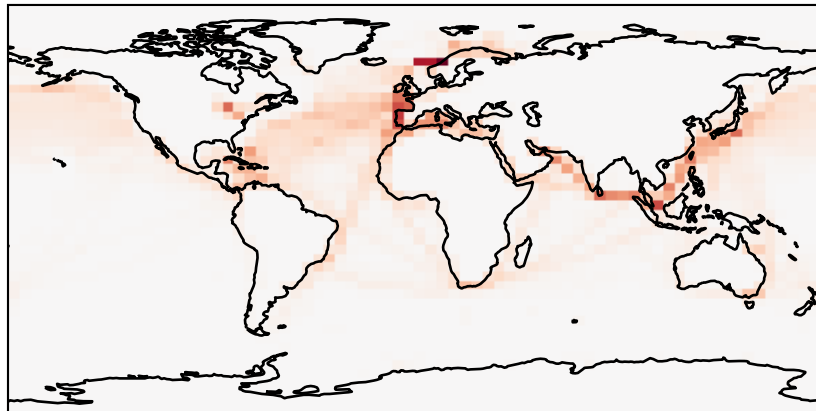
GCC\_13.0.0 (Ref)  
4.0x5.0



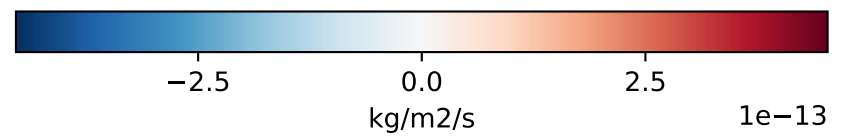
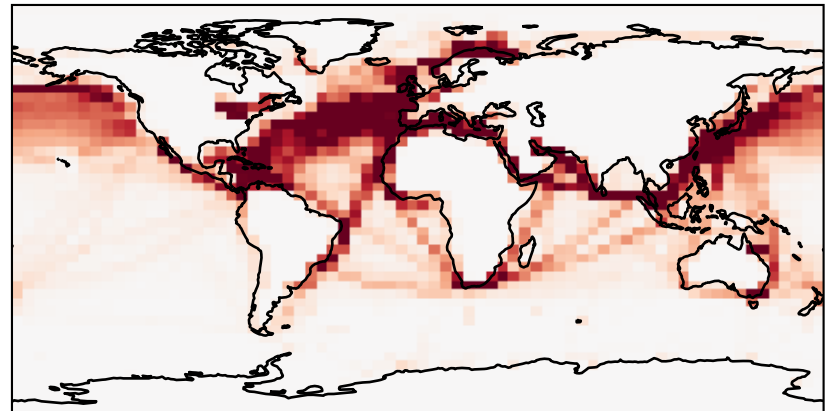
GCC\_14.0.0 (Dev)  
4.0x5.0



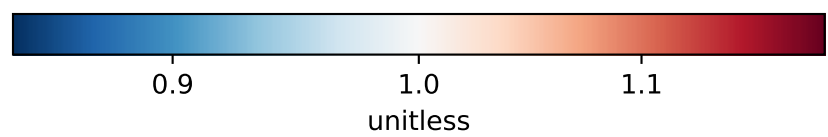
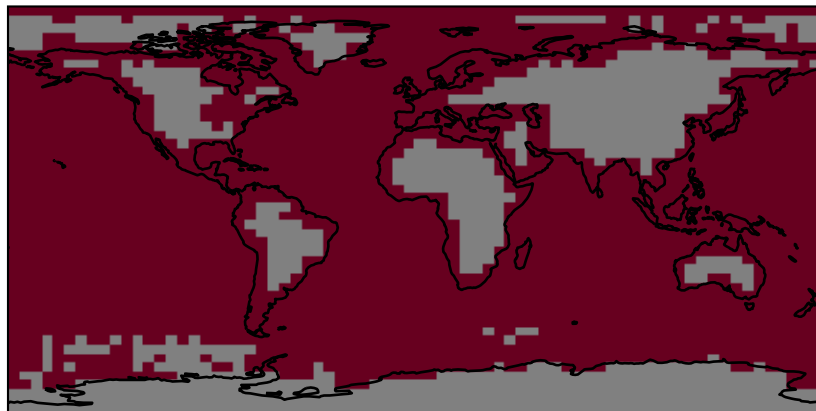
Difference  
Dev - Ref, Dynamic Range



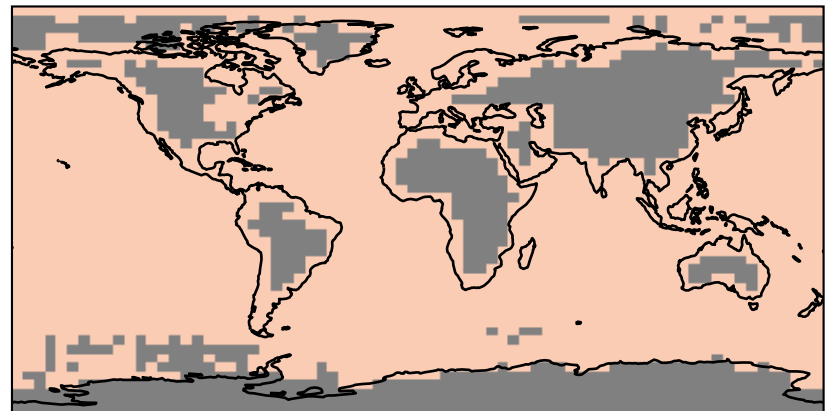
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

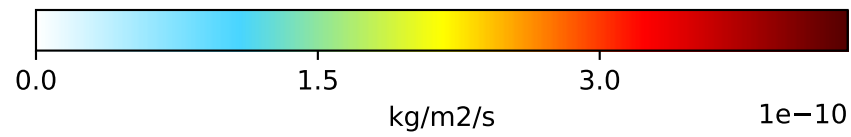
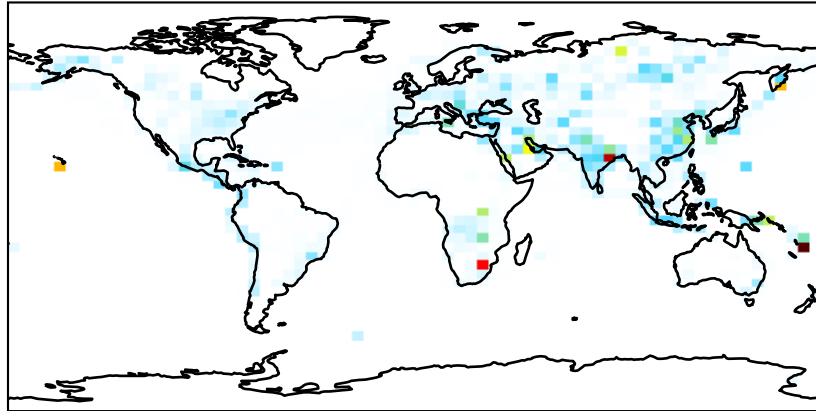


Ratio  
Dev/Ref, Fixed Range

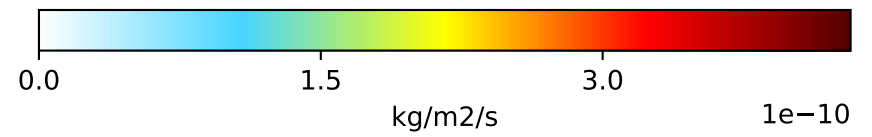
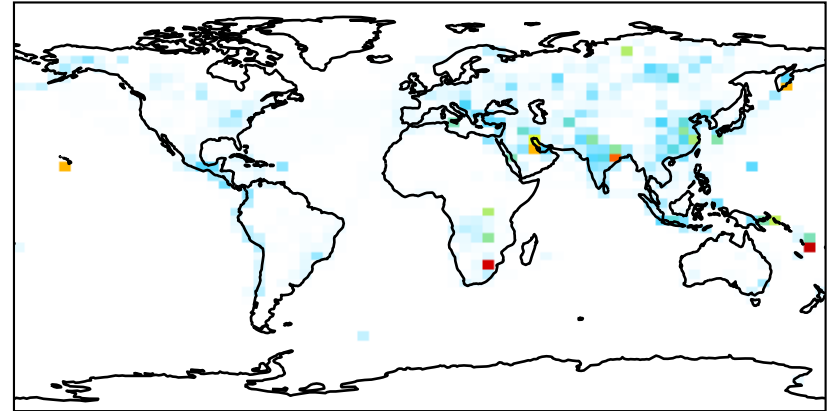


# EmisSO2\_Total (Jul2019)

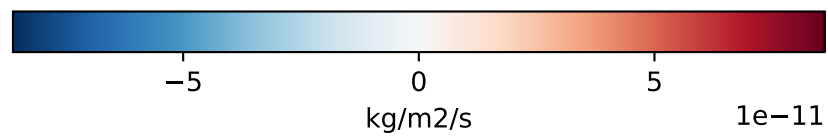
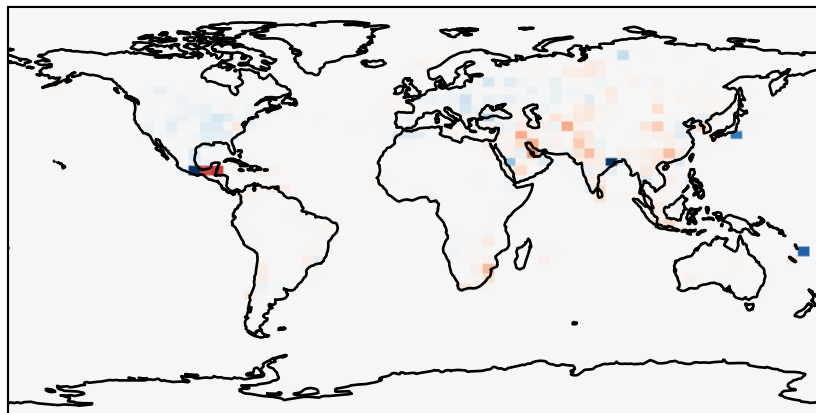
GCC\_13.0.0 (Ref)  
4.0x5.0



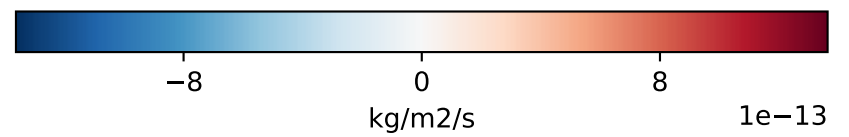
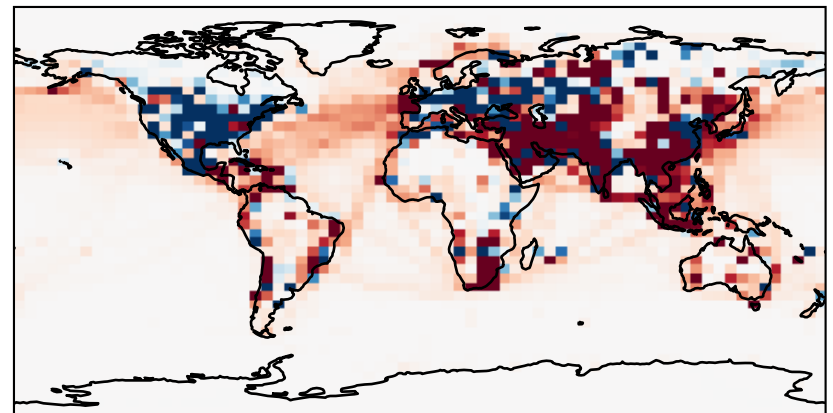
GCC\_14.0.0 (Dev)  
4.0x5.0



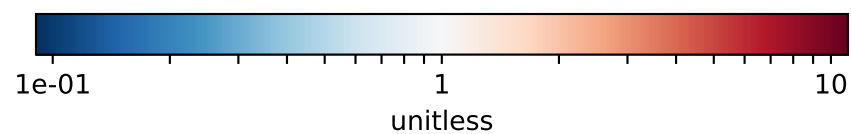
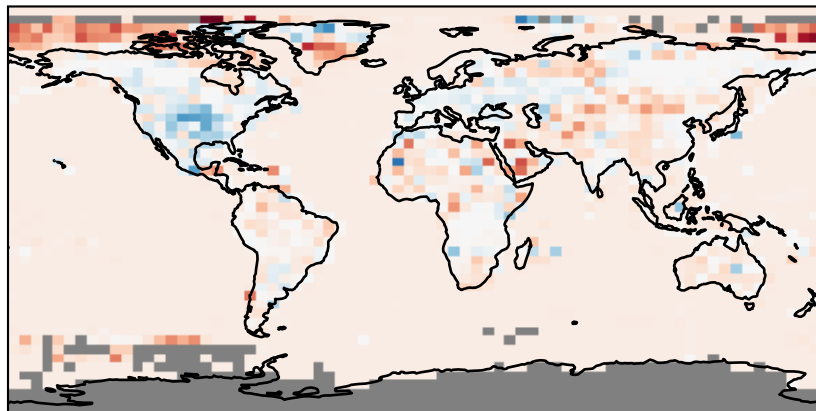
Difference  
Dev - Ref, Dynamic Range



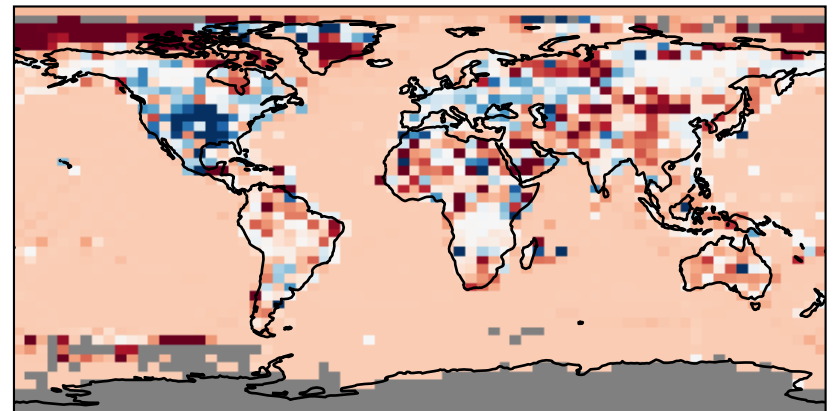
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

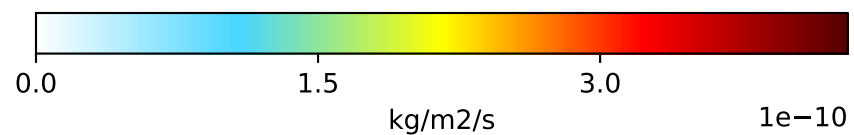
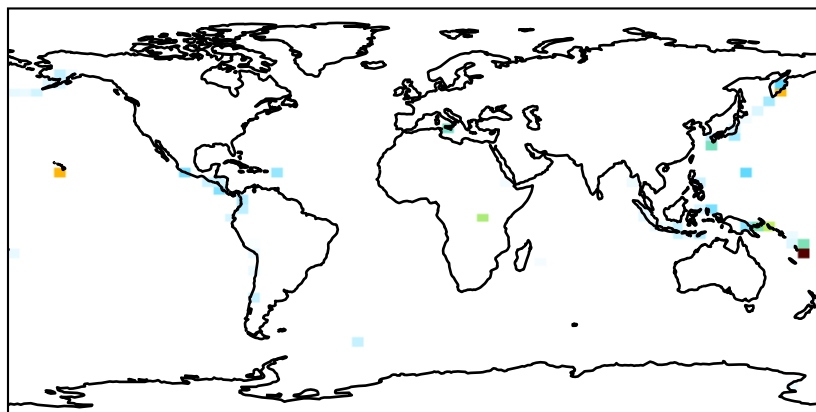


Ratio  
Dev/Ref, Fixed Range

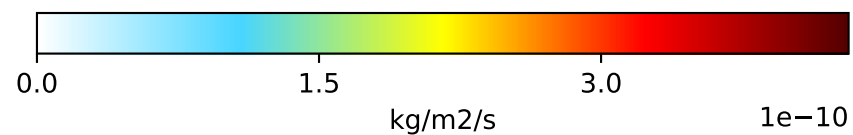
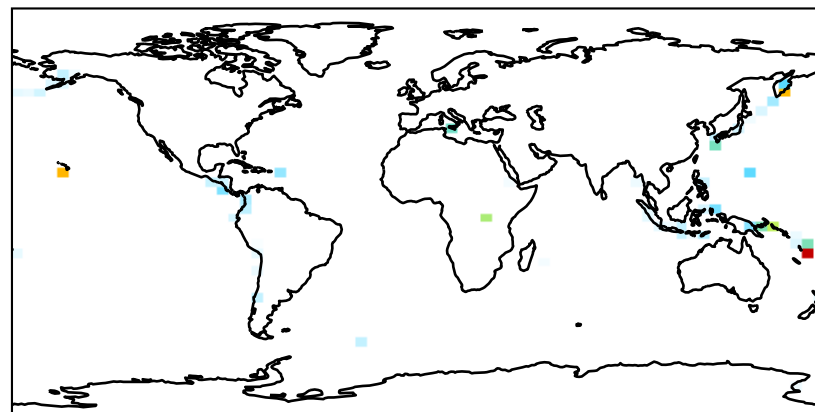


# EmisSO2\_VolcDegas (Jul2019)

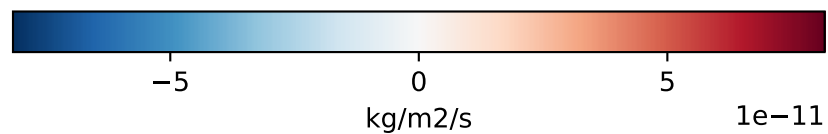
GCC\_13.0.0 (Ref)  
4.0x5.0



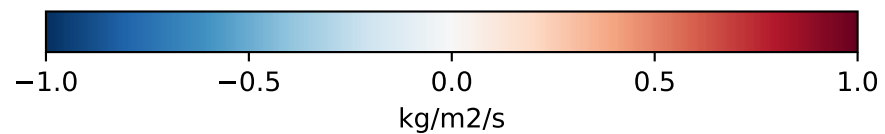
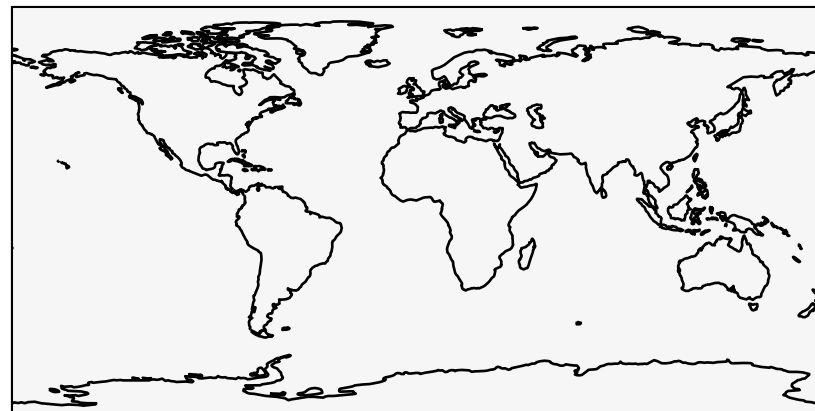
GCC\_14.0.0 (Dev)  
4.0x5.0



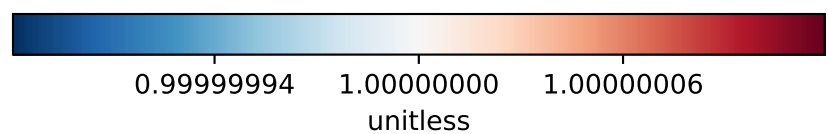
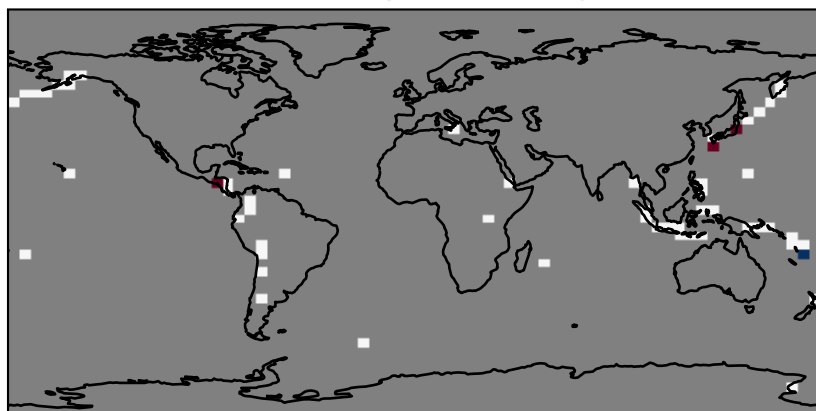
Difference  
Dev - Ref, Dynamic Range



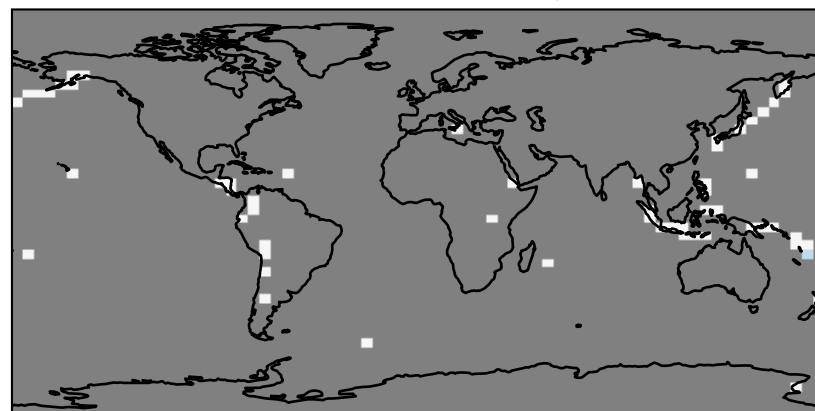
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

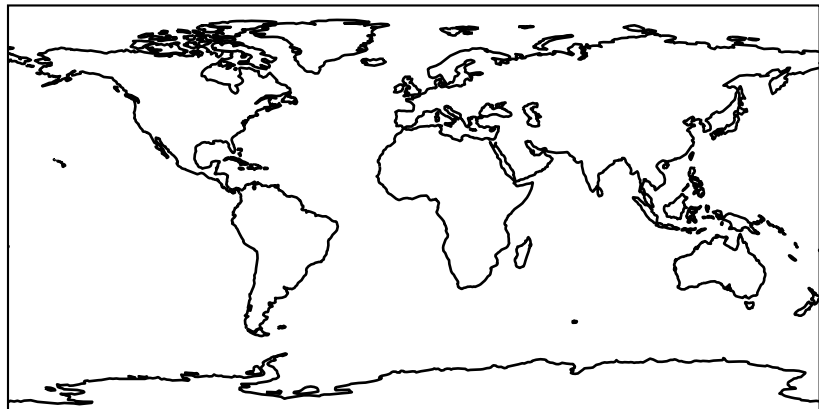


Ratio  
Dev/Ref, Fixed Range



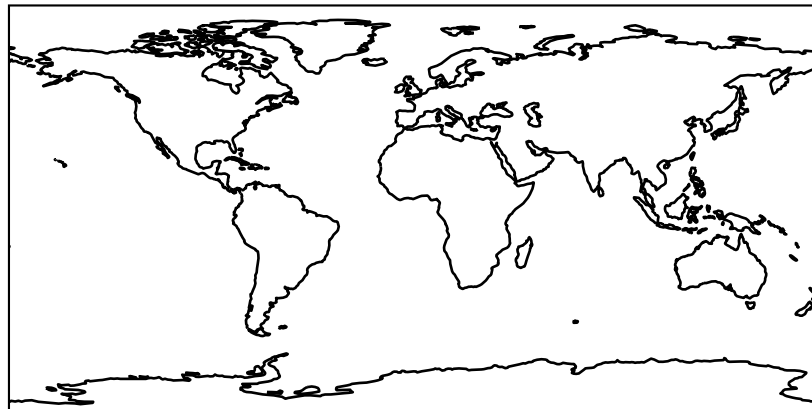
# EmisSO2\_VolcErupt (Jul2019)

GCC\_13.0.0 (Ref)  
4.0x5.0



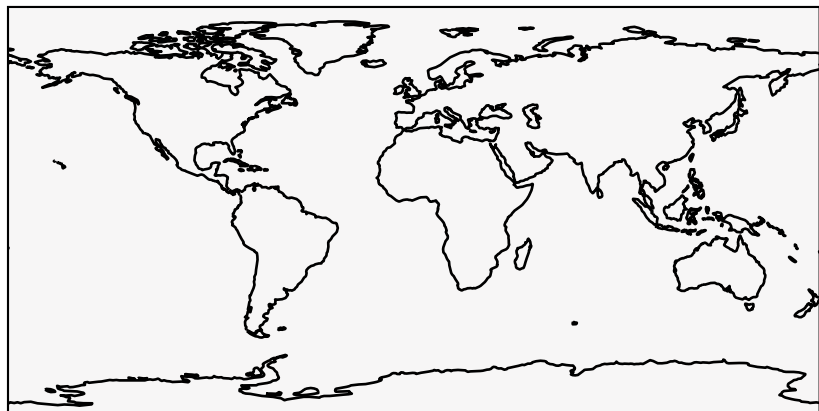
Zero throughout domain  
kg/m2/s

GCC\_14.0.0 (Dev)  
4.0x5.0



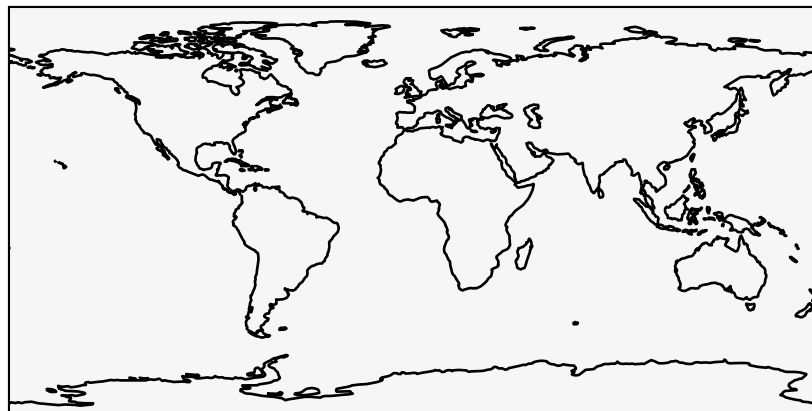
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Dynamic Range



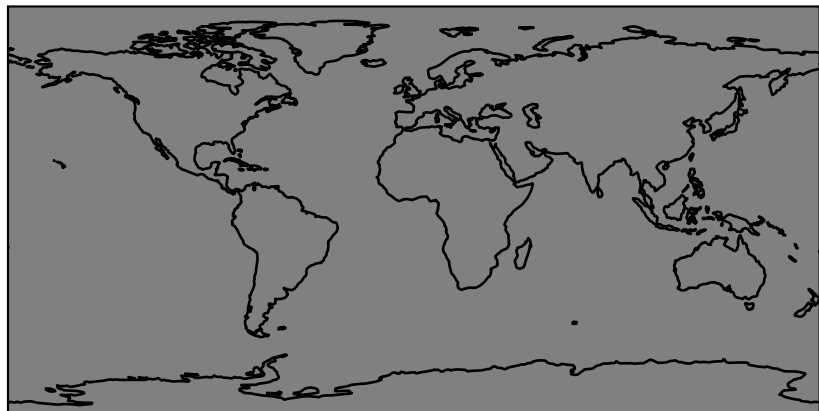
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



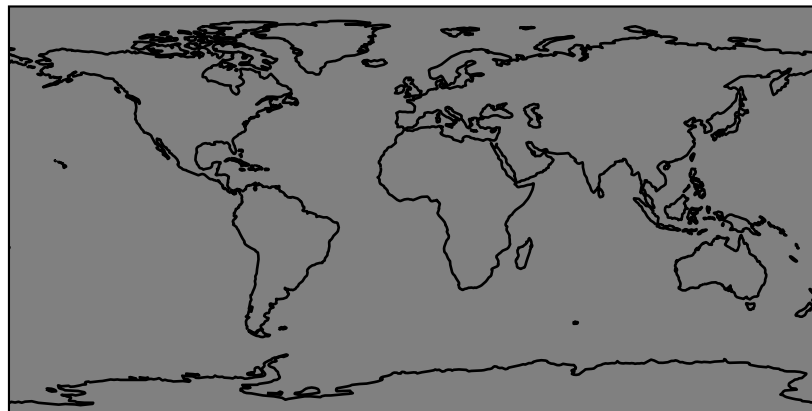
Zero throughout domain  
kg/m2/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

Ratio  
Dev/Ref, Fixed Range

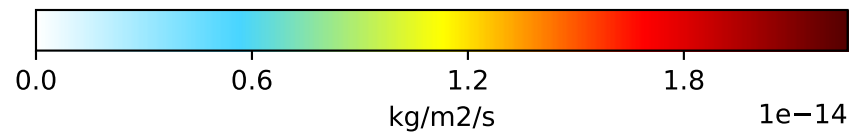
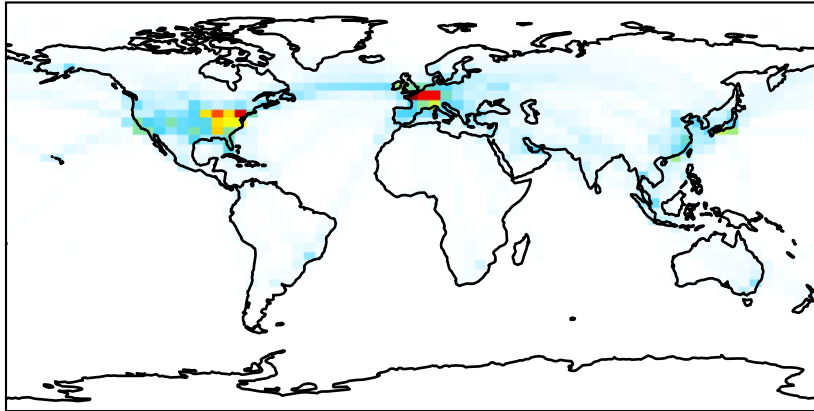


Undefined throughout domain  
unitless

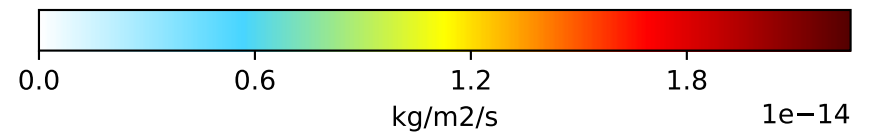
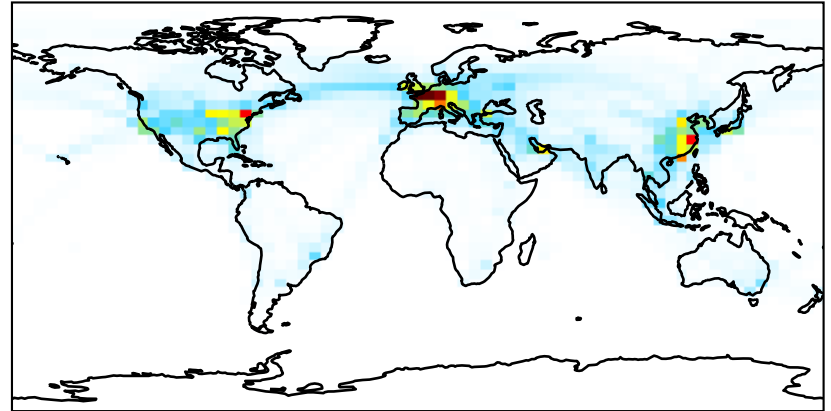


# EmisSO4\_Aircraft (Jul2019)

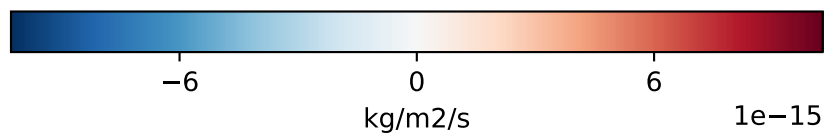
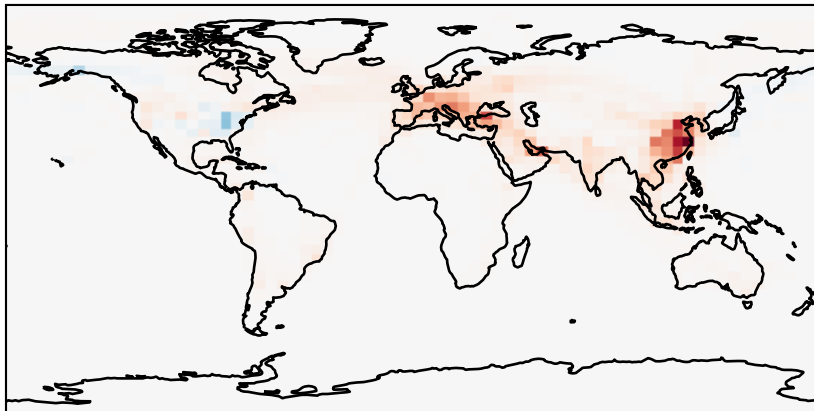
GCC\_13.0.0 (Ref)  
4.0x5.0



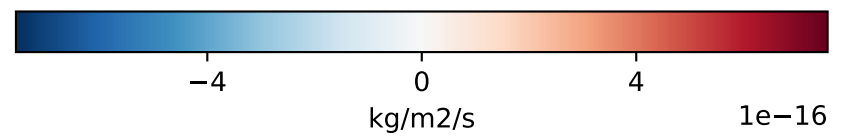
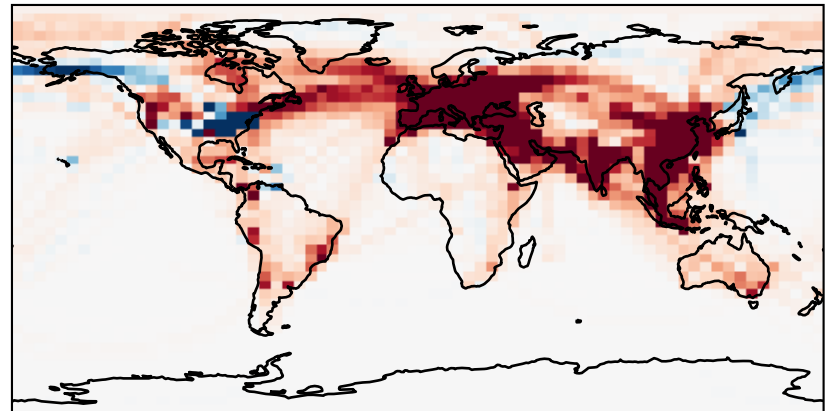
GCC\_14.0.0 (Dev)  
4.0x5.0



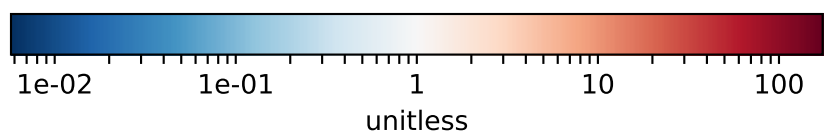
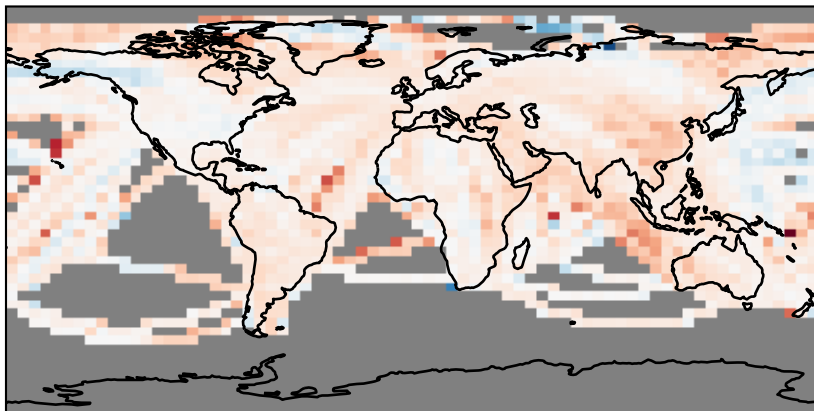
Difference  
Dev - Ref, Dynamic Range



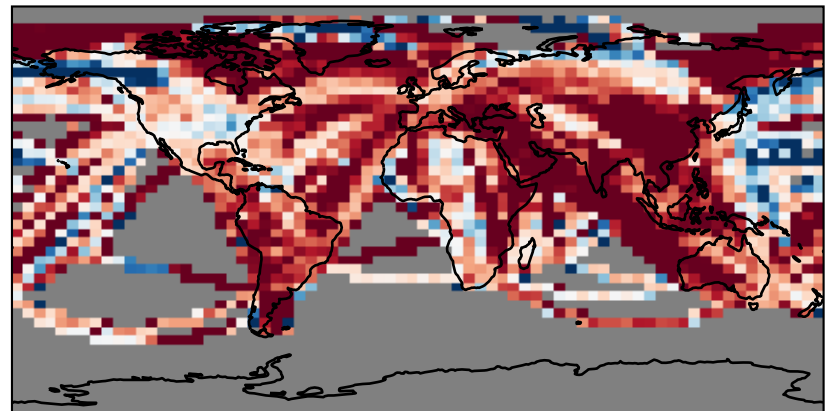
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

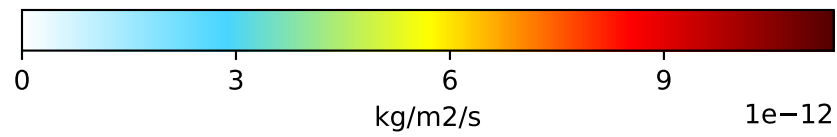
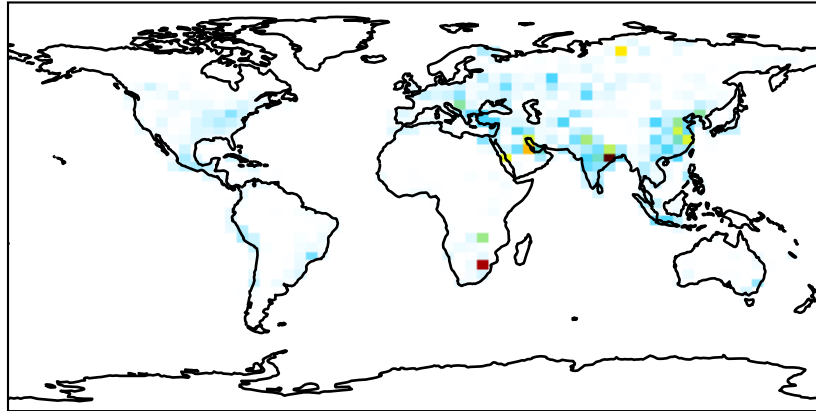


Ratio  
Dev/Ref, Fixed Range

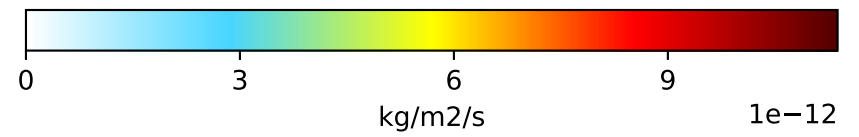
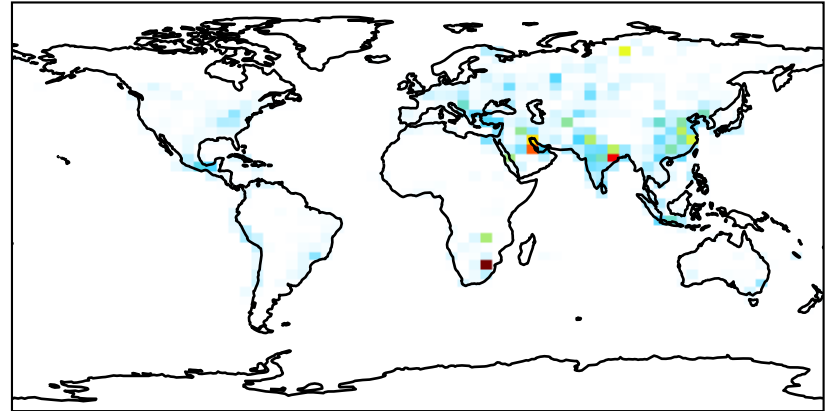


# EmisSO4\_Anthro (Jul2019)

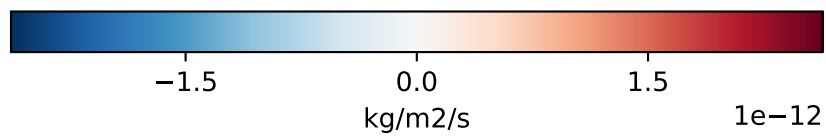
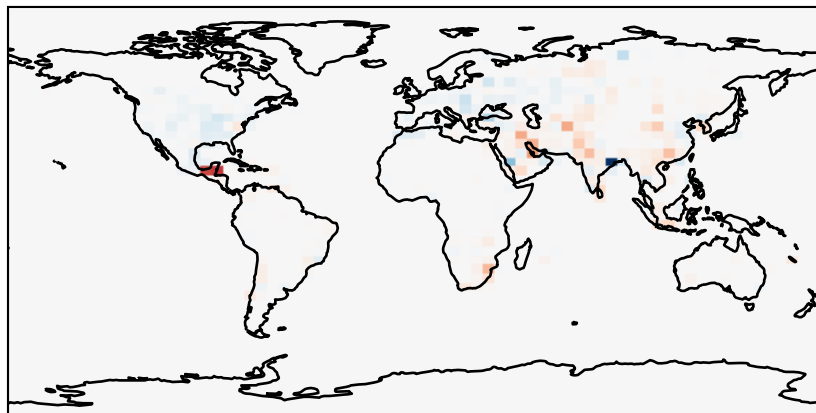
GCC\_13.0.0 (Ref)  
4.0x5.0



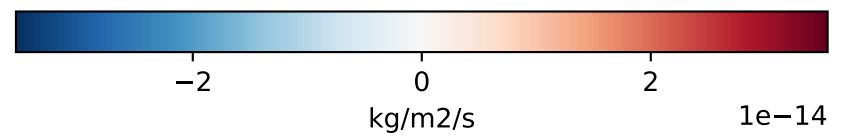
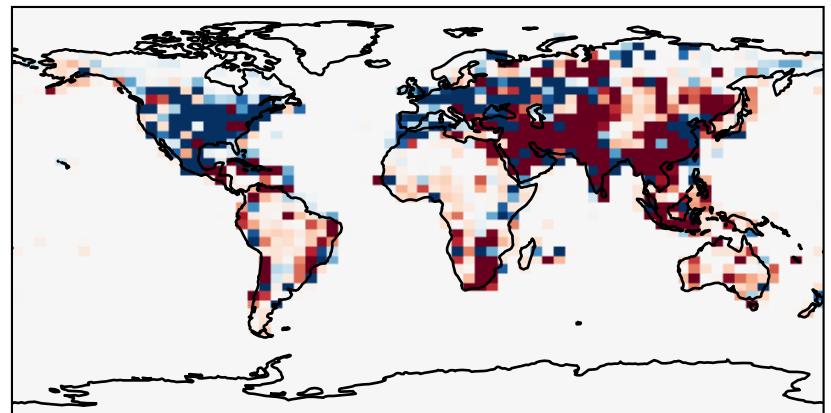
GCC\_14.0.0 (Dev)  
4.0x5.0



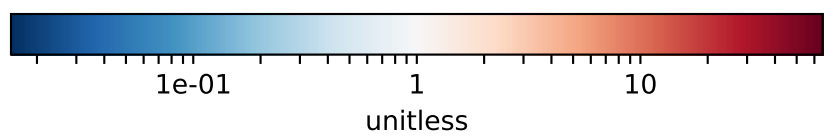
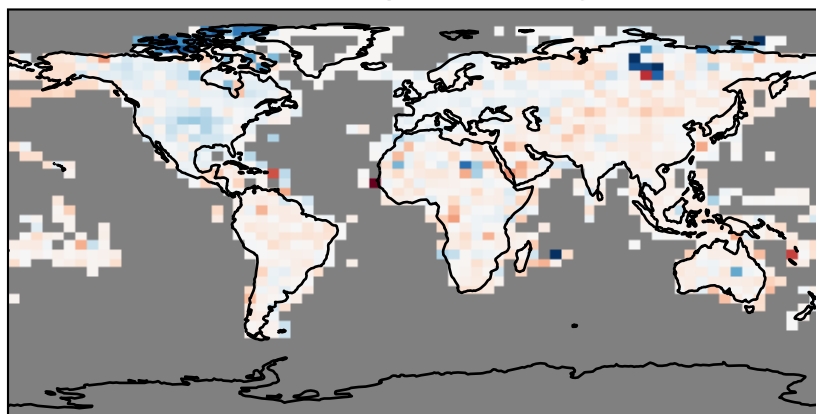
Difference  
Dev - Ref, Dynamic Range



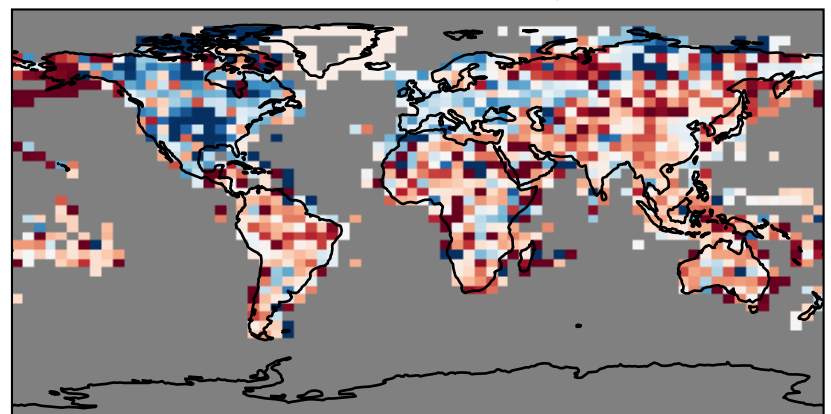
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

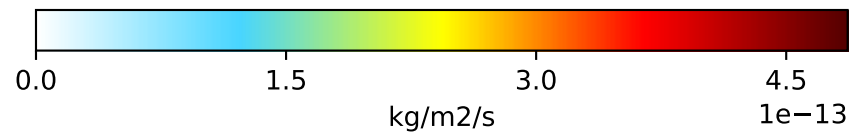
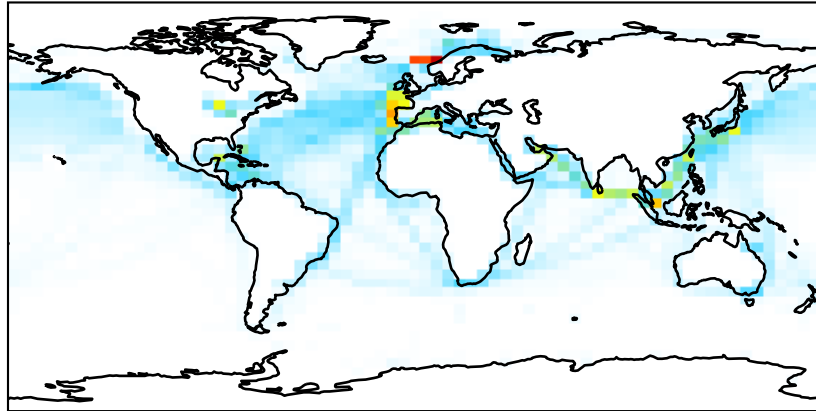


Ratio  
Dev/Ref, Fixed Range

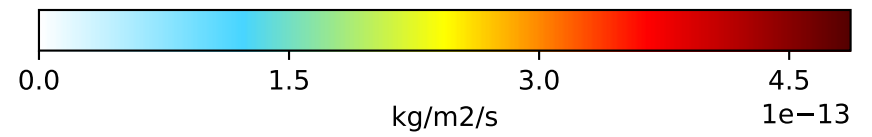
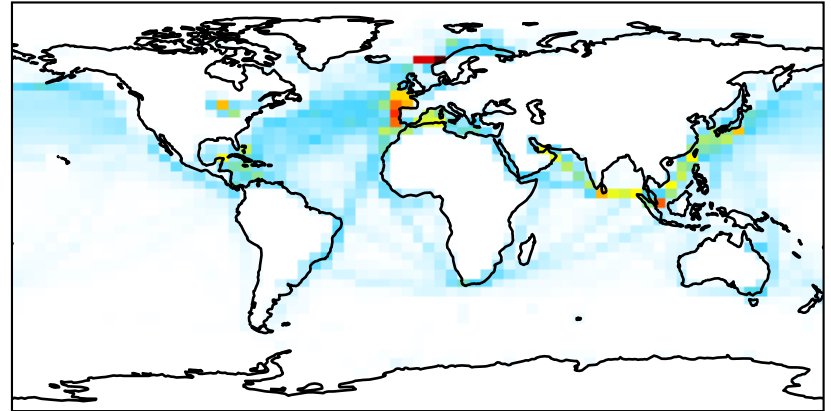


# EmisSO4\_Ship (Jul2019)

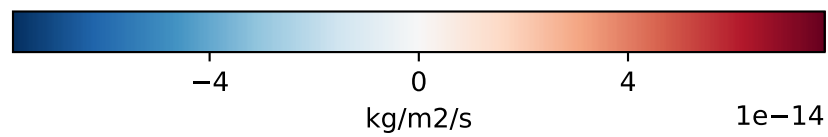
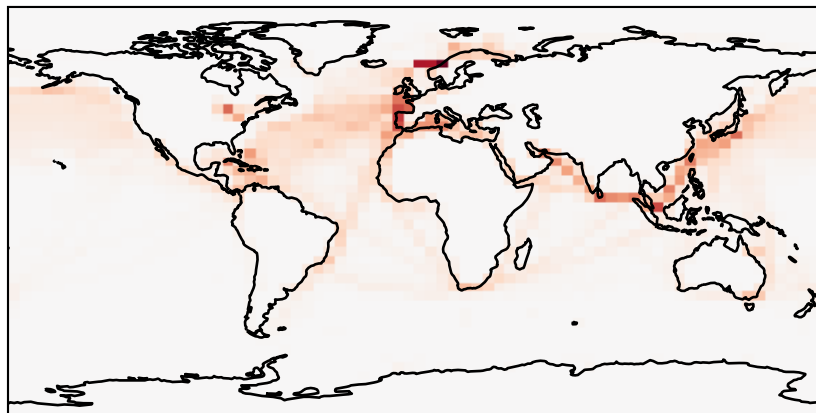
GCC\_13.0.0 (Ref)  
4.0x5.0



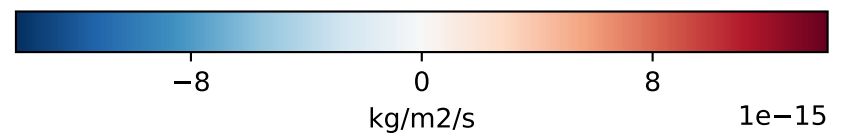
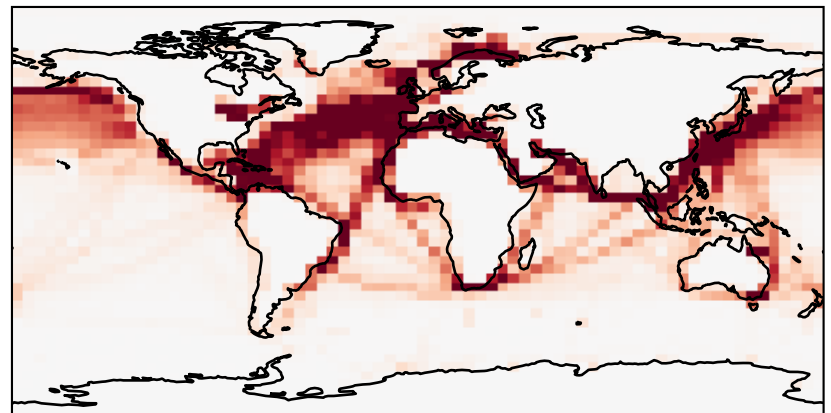
GCC\_14.0.0 (Dev)  
4.0x5.0



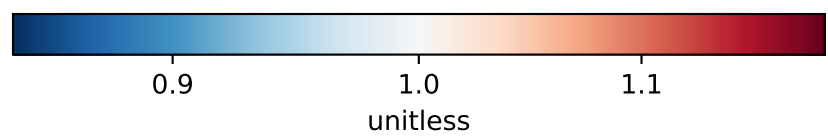
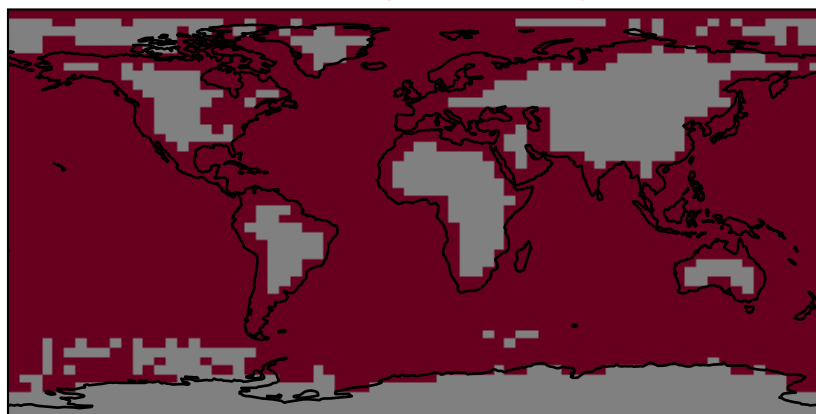
Difference  
Dev - Ref, Dynamic Range



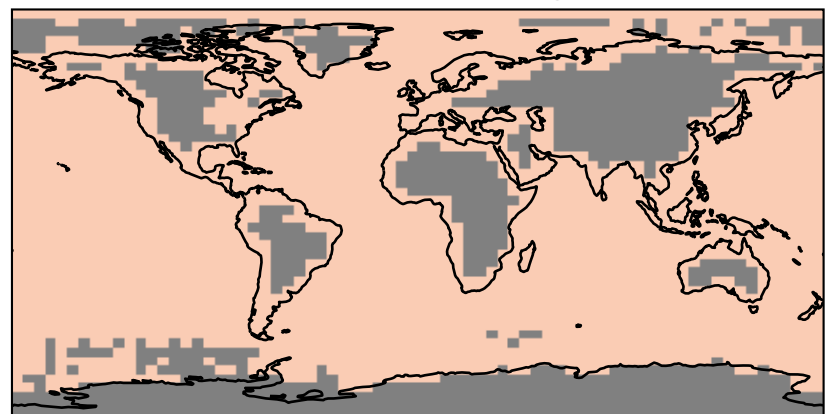
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

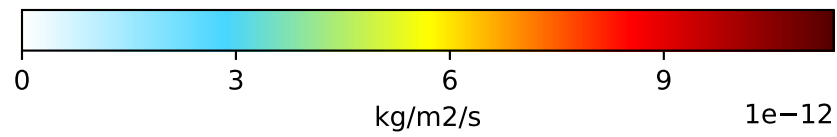
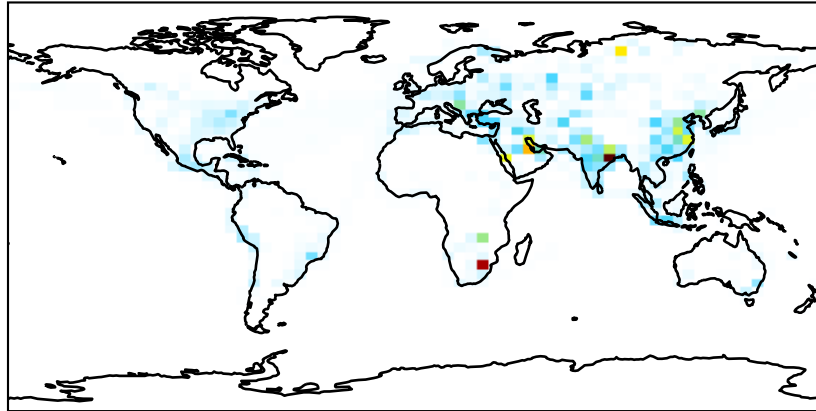


Ratio  
Dev/Ref, Fixed Range

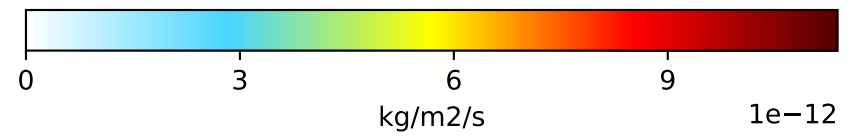
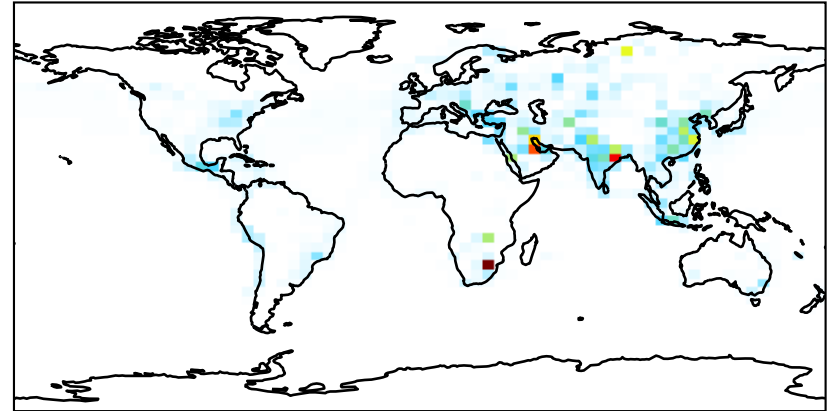


# EmisSO4\_Total (Jul2019)

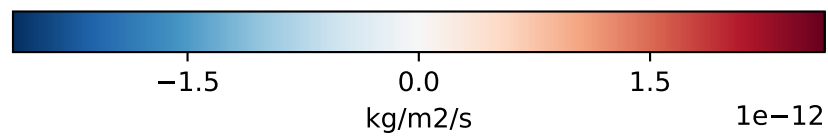
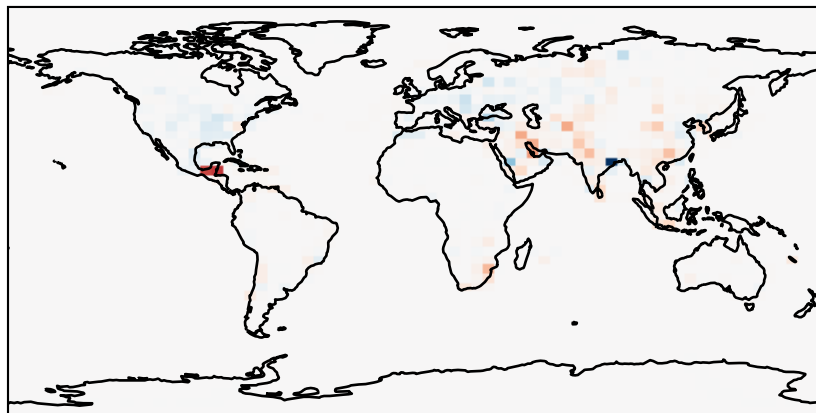
GCC\_13.0.0 (Ref)  
4.0x5.0



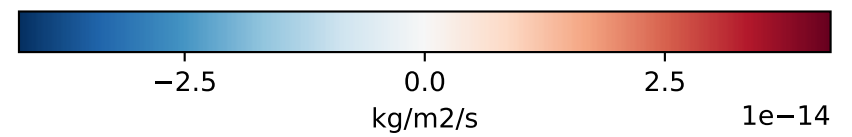
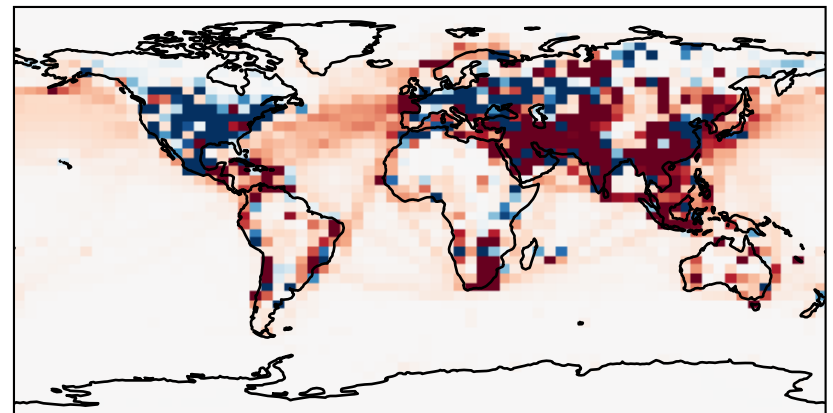
GCC\_14.0.0 (Dev)  
4.0x5.0



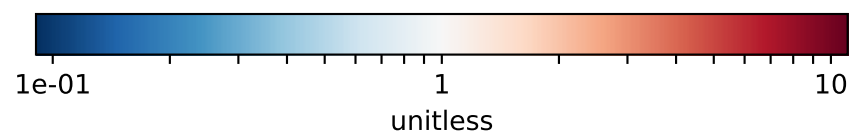
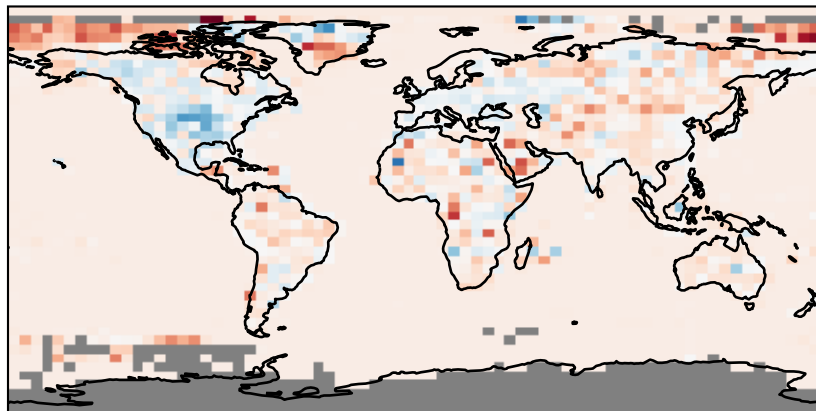
Difference  
Dev - Ref, Dynamic Range



Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

