

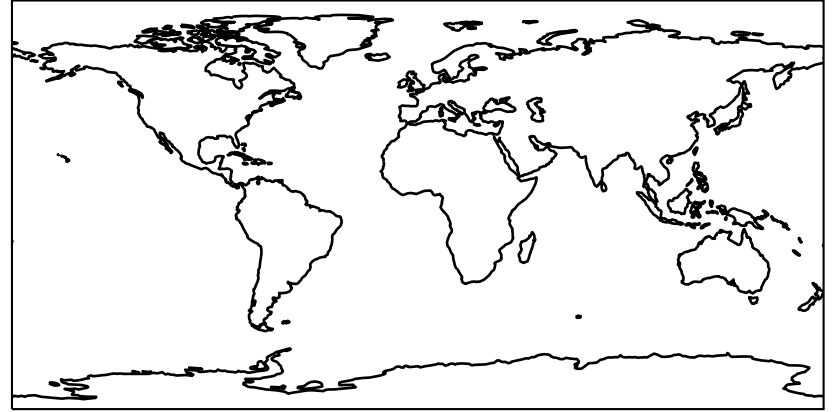
# EmisALD2\_Ship (Apr2019)

GCC\_14.0.0 (Ref)  
4.0x5.0



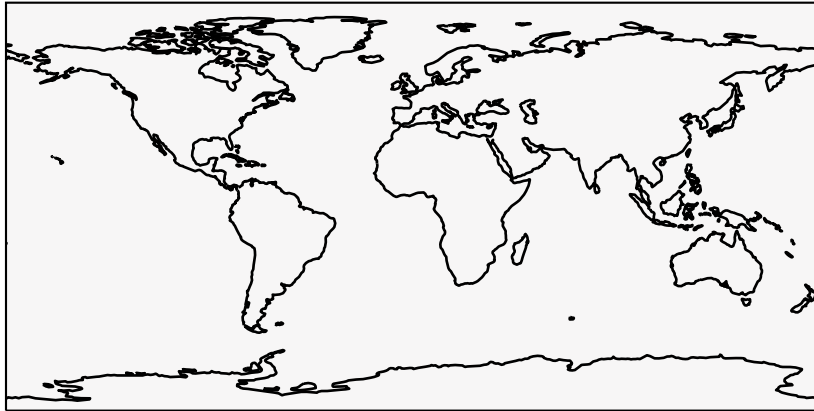
Zero throughout domain  
kg/m2/s

GCHP\_14.0.0 (Dev)  
c24



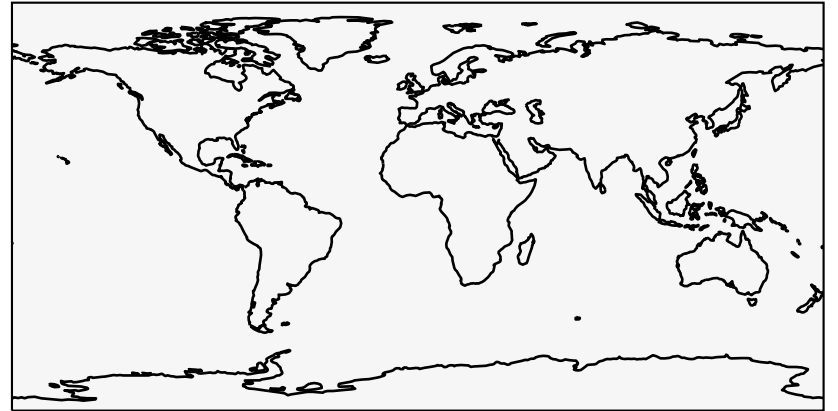
Zero throughout domain  
kg/m2/s

Difference (1x1.25)  
Dev - Ref, Dynamic Range



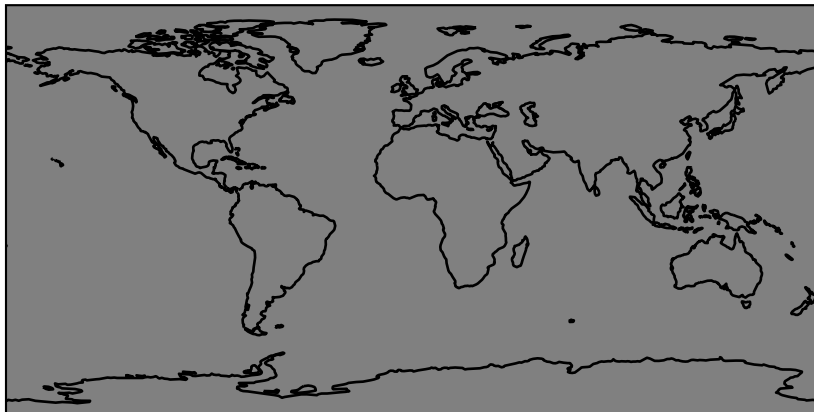
Zero throughout domain  
kg/m2/s

Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



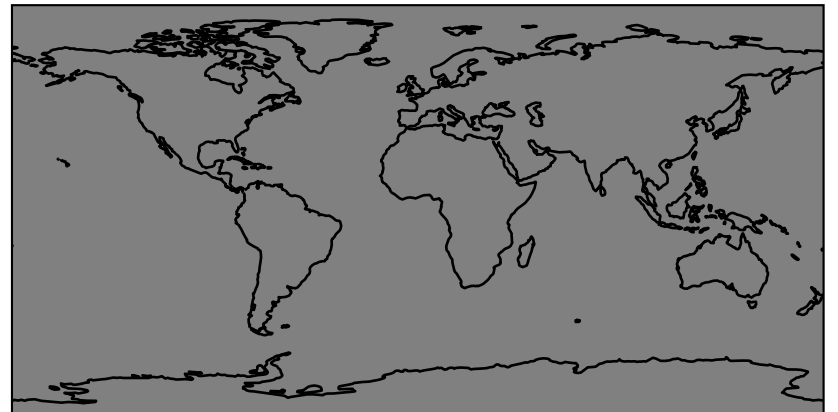
Zero throughout domain  
kg/m2/s

Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

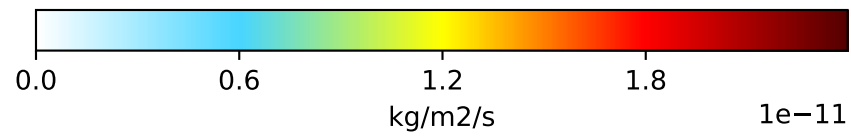
Ratio (1x1.25)  
Dev/Ref, Fixed Range



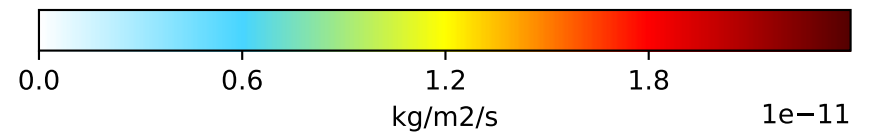
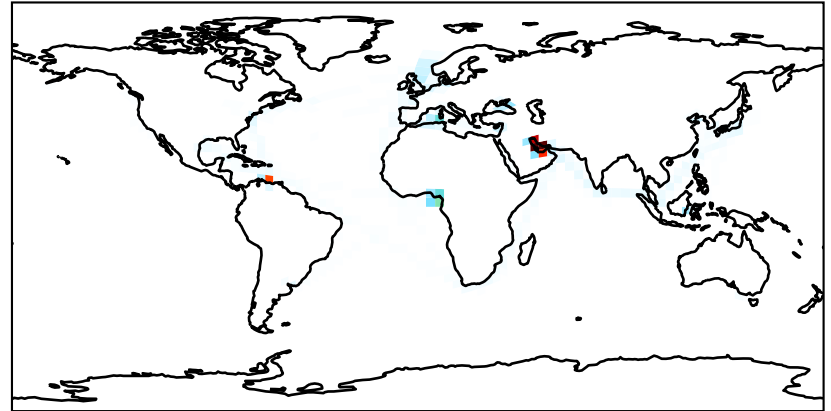
Undefined throughout domain  
unitless

# EmisALK4\_Ship (Apr2019)

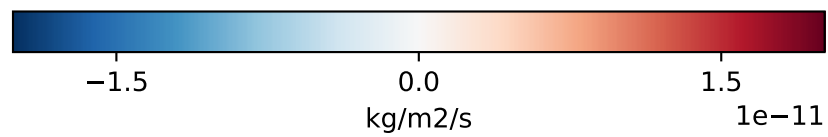
GCC\_14.0.0 (Ref)  
4.0x5.0



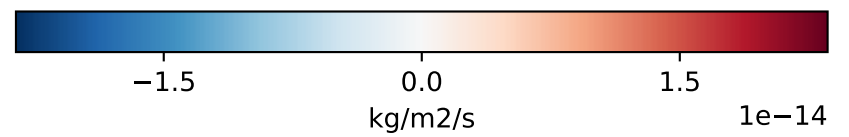
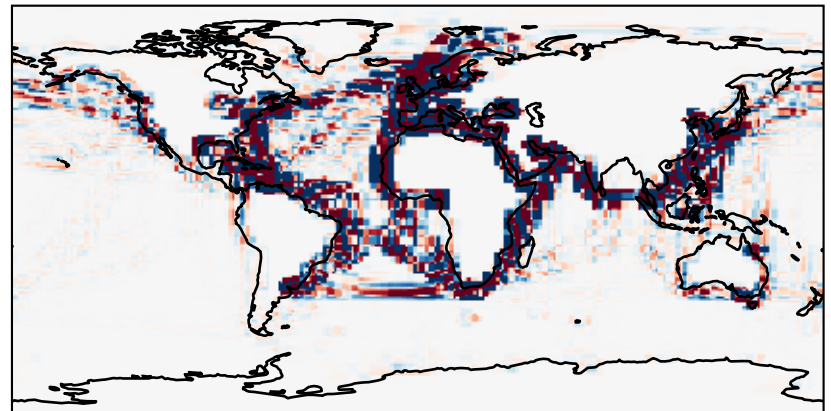
GCHP\_14.0.0 (Dev)  
c24



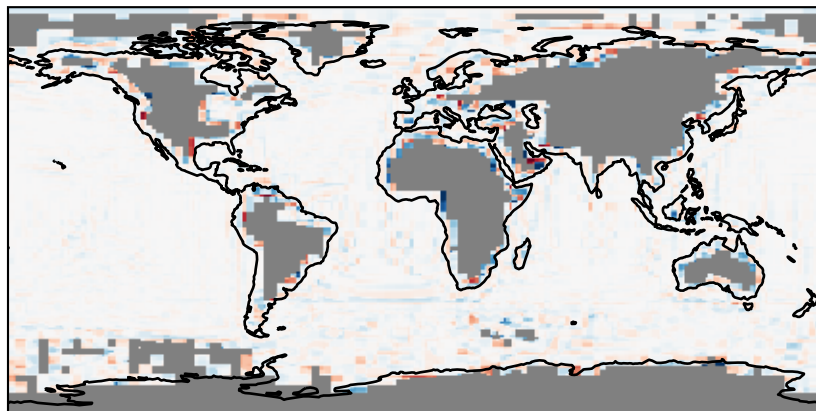
Difference (1x1.25)  
Dev - Ref, Dynamic Range



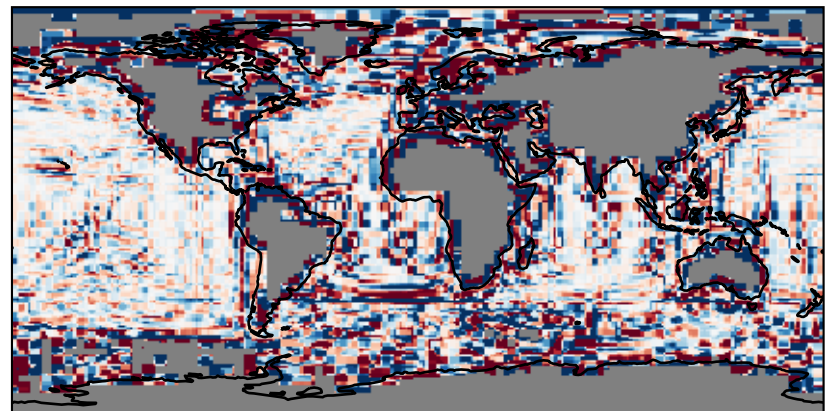
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

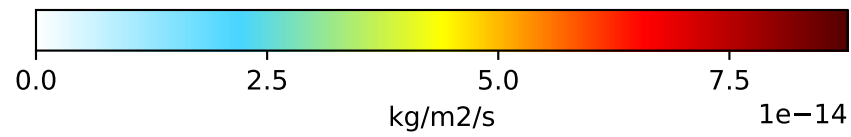
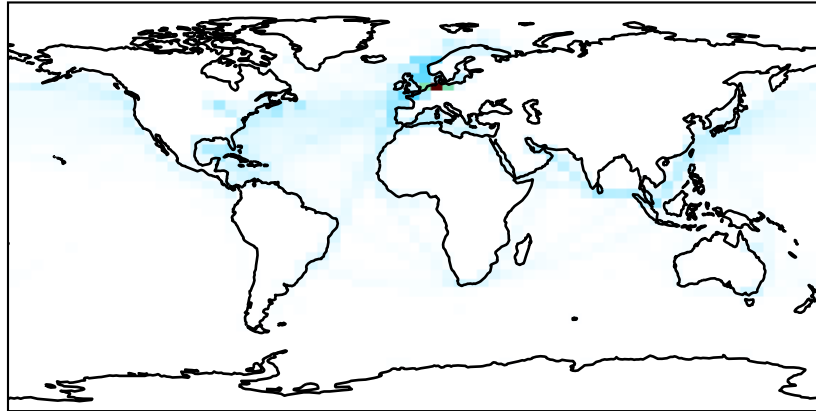


Ratio (1x1.25)  
Dev/Ref, Fixed Range

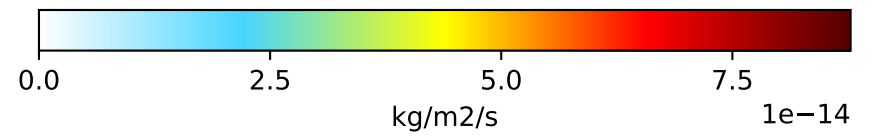
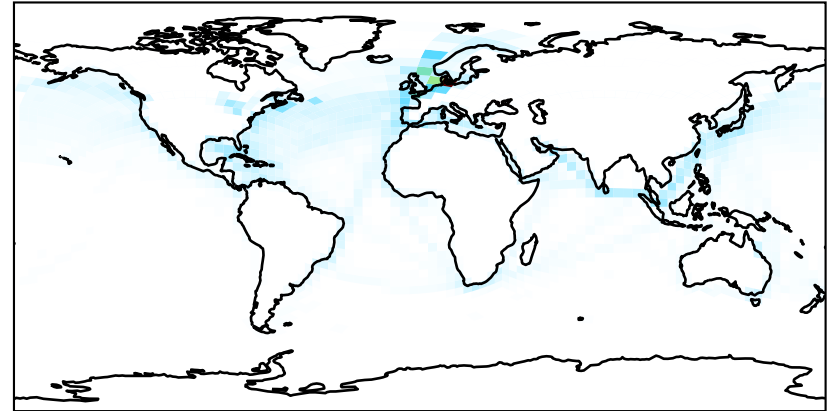


# EmisBCPI\_Ship (Apr2019)

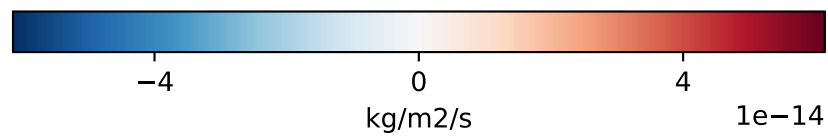
GCC\_14.0.0 (Ref)  
4.0x5.0



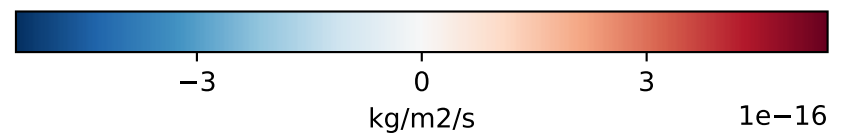
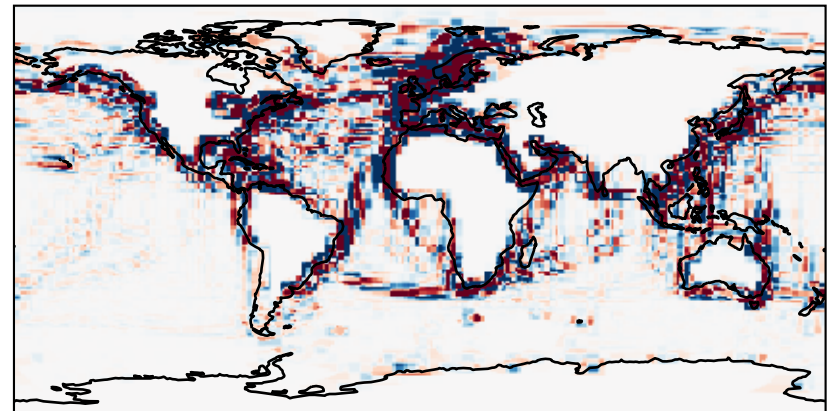
GCHP\_14.0.0 (Dev)  
c24



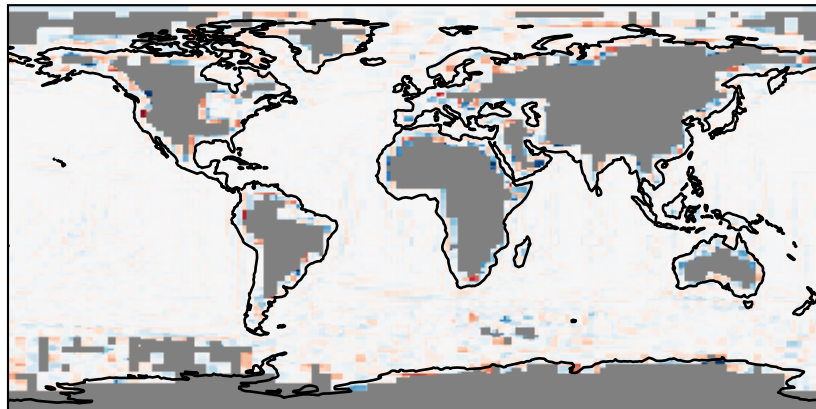
Difference (1x1.25)  
Dev - Ref, Dynamic Range



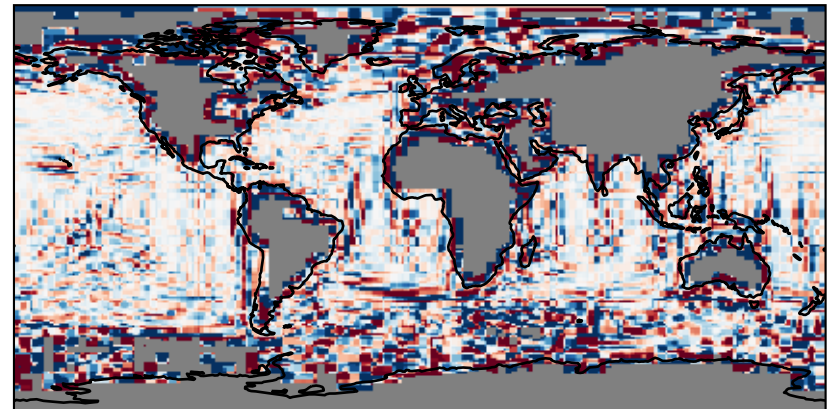
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



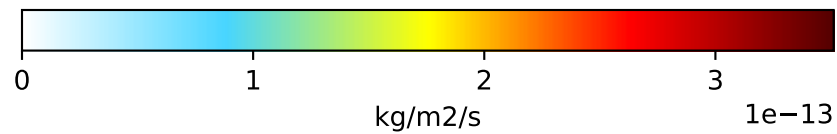
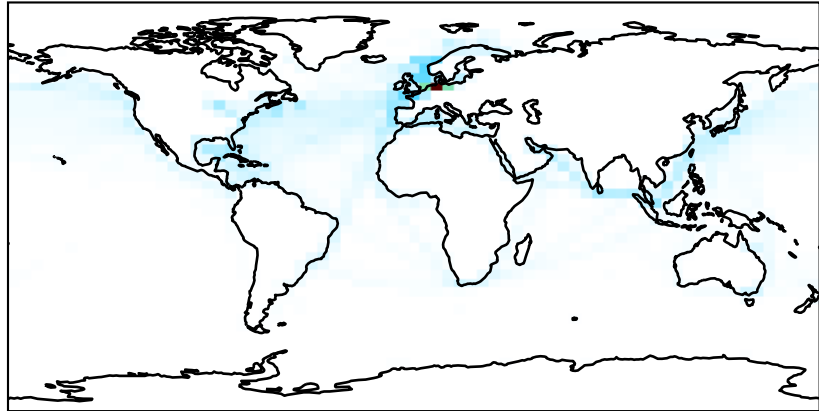
Ratio (1x1.25)  
Dev/Ref, Fixed Range



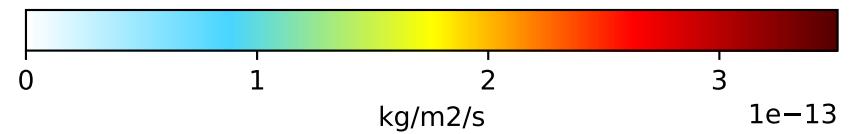
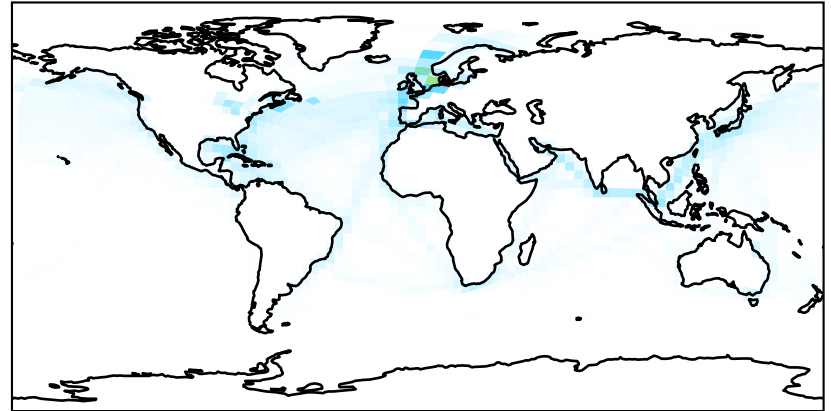


# EmisBCPO\_Ship (Apr2019)

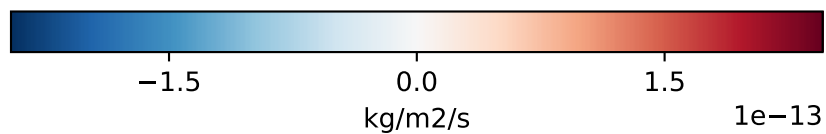
GCC\_14.0.0 (Ref)  
4.0x5.0



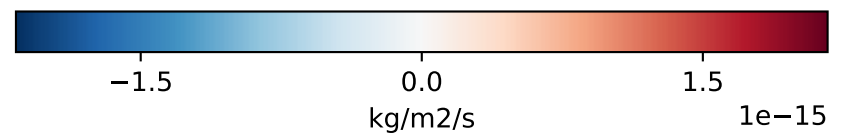
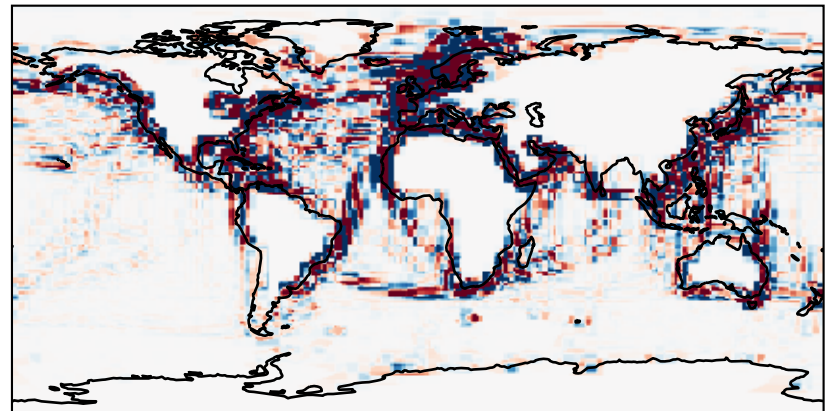
GCHP\_14.0.0 (Dev)  
c24



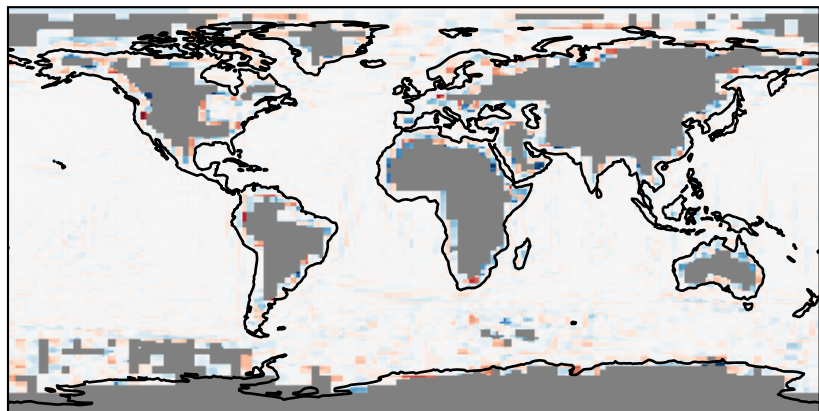
Difference (1x1.25)  
Dev - Ref, Dynamic Range



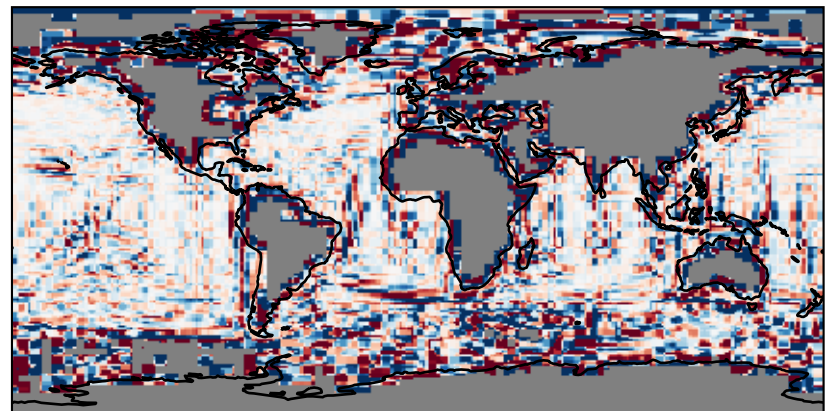
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



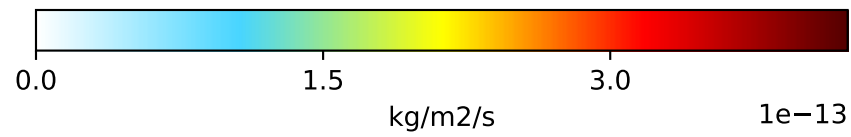
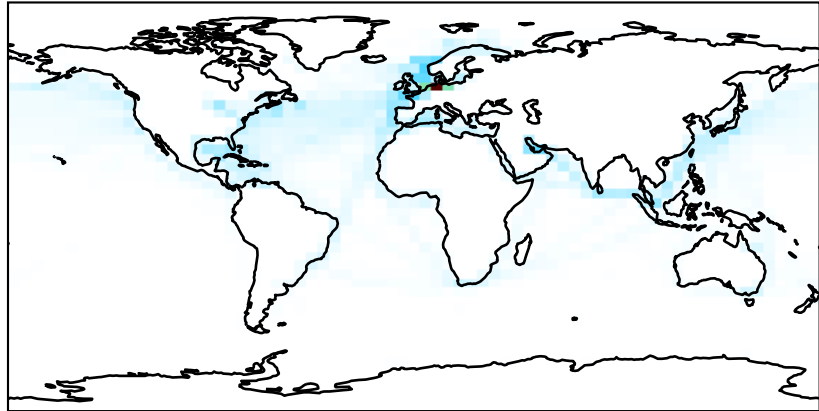
Ratio (1x1.25)  
Dev/Ref, Fixed Range



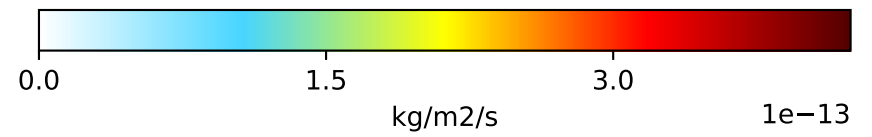
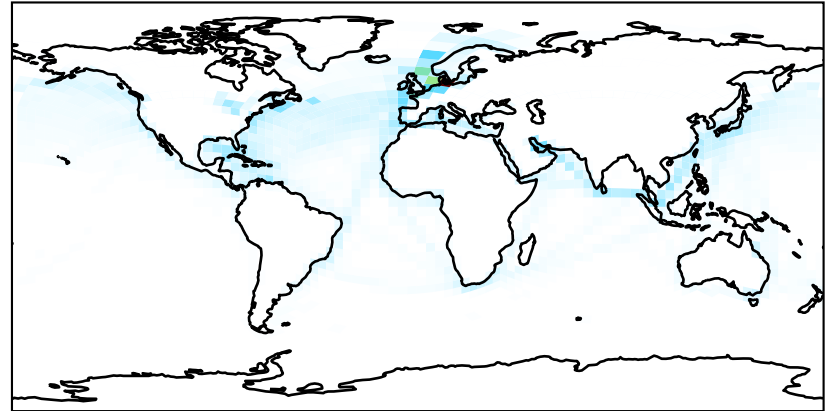


# EmisBENZ\_Ship (Apr2019)

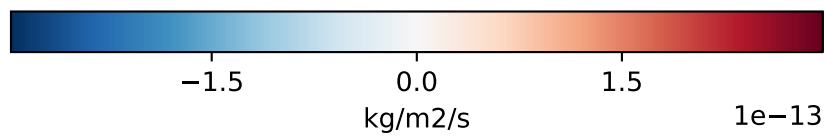
GCC\_14.0.0 (Ref)  
4.0x5.0



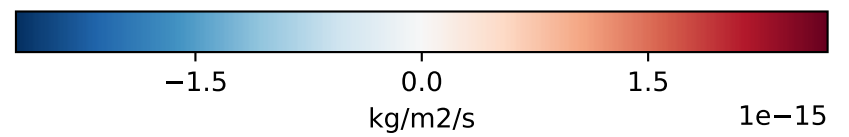
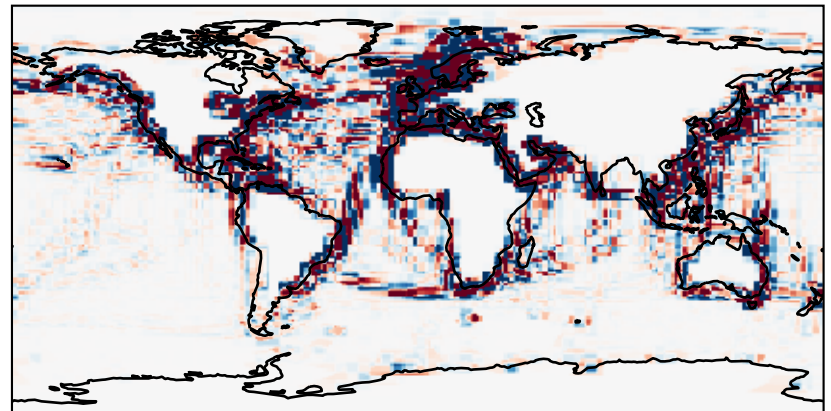
GCHP\_14.0.0 (Dev)  
c24



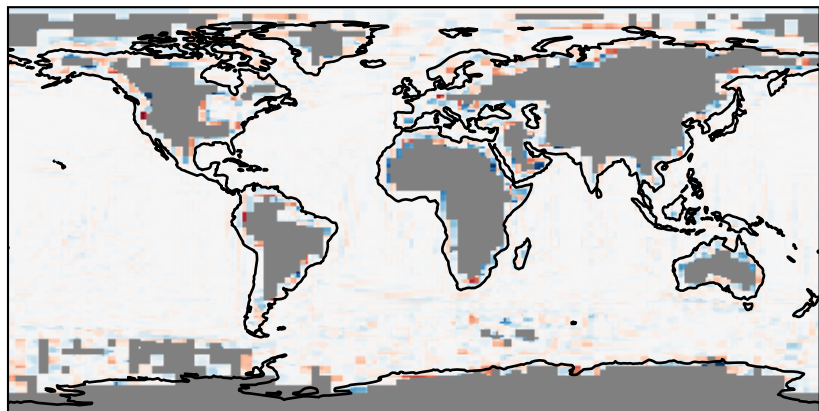
Difference (1x1.25)  
Dev - Ref, Dynamic Range



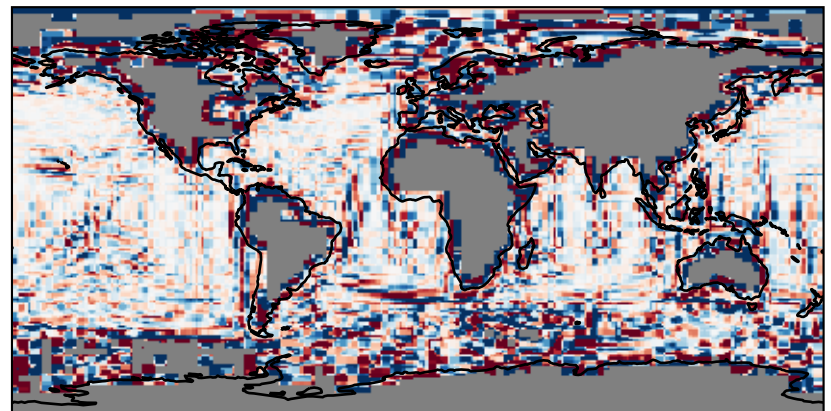
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

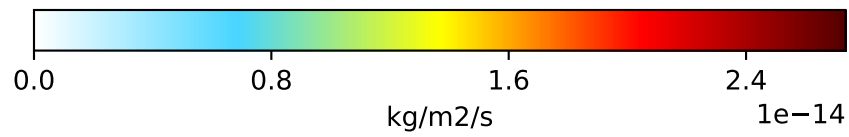
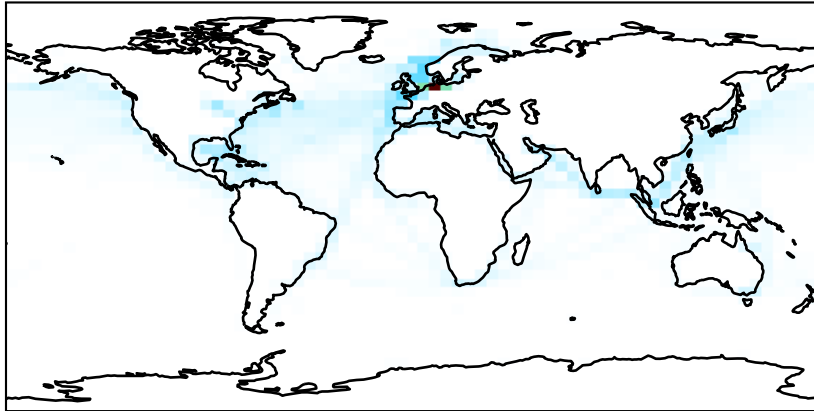


Ratio (1x1.25)  
Dev/Ref, Fixed Range

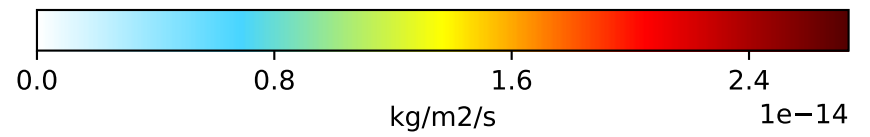
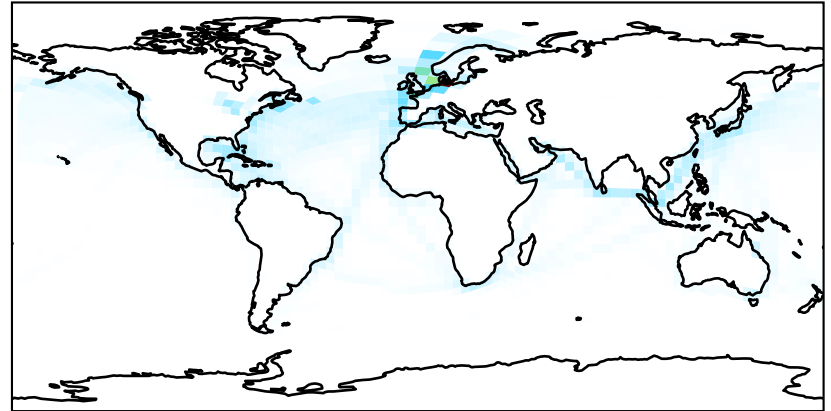


# EmisC2H2\_Ship (Apr2019)

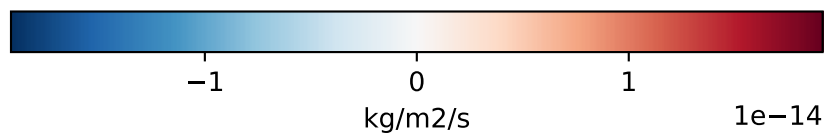
GCC\_14.0.0 (Ref)  
4.0x5.0



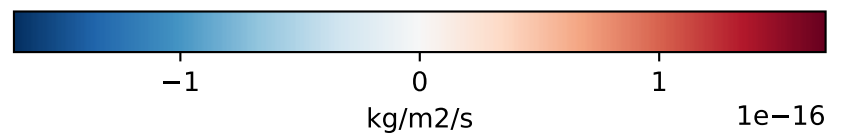
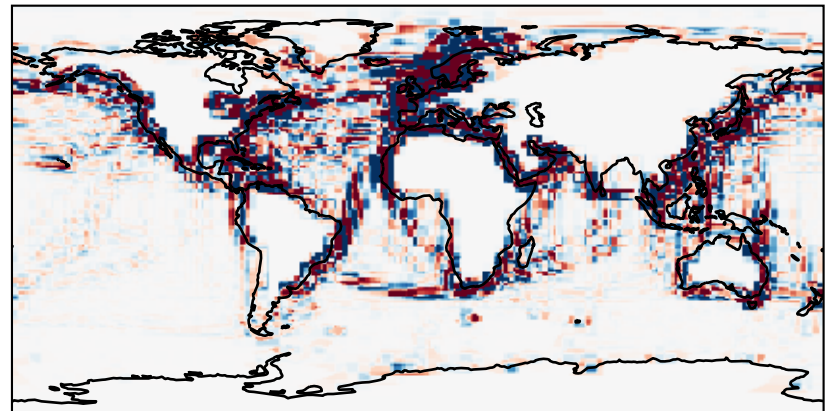
GCHP\_14.0.0 (Dev)  
c24



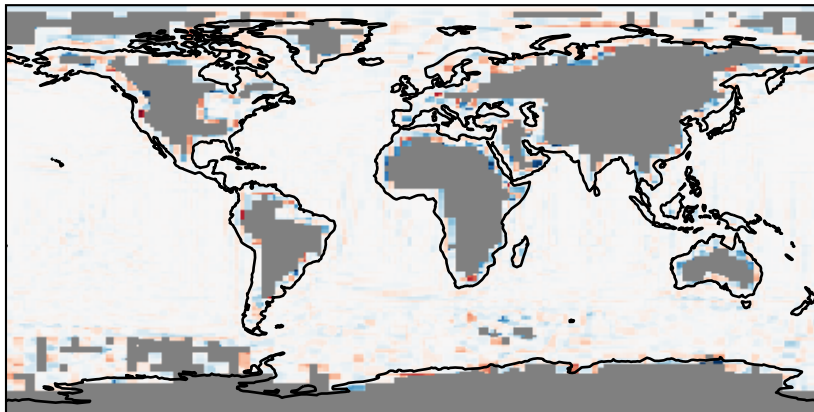
Difference (1x1.25)  
Dev - Ref, Dynamic Range



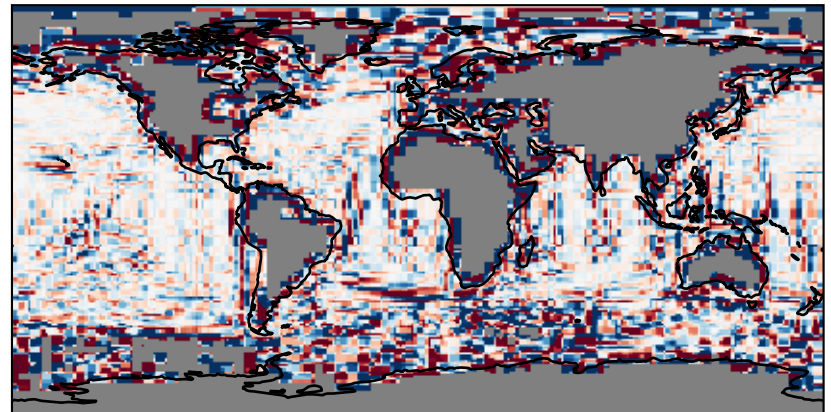
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



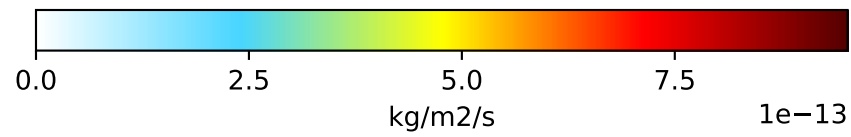
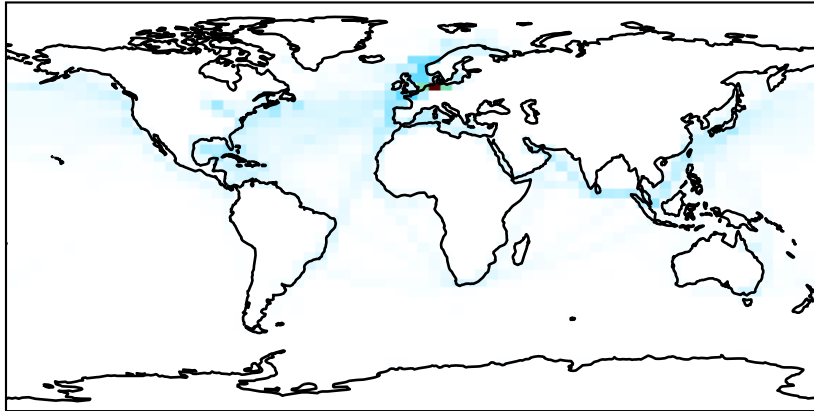
Ratio (1x1.25)  
Dev/Ref, Fixed Range



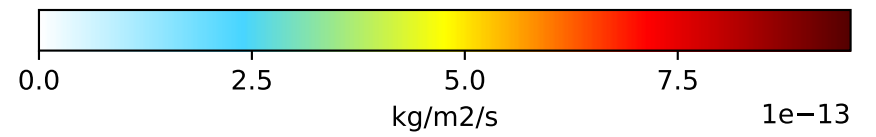
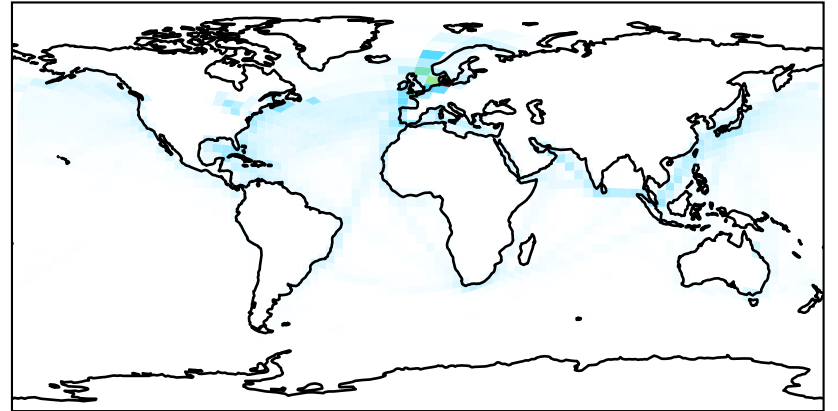


# EmisC2H4\_Ship (Apr2019)

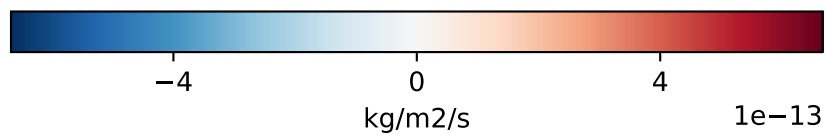
GCC\_14.0.0 (Ref)  
4.0x5.0



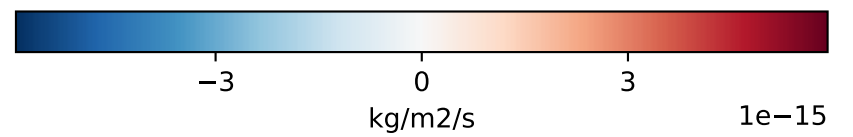
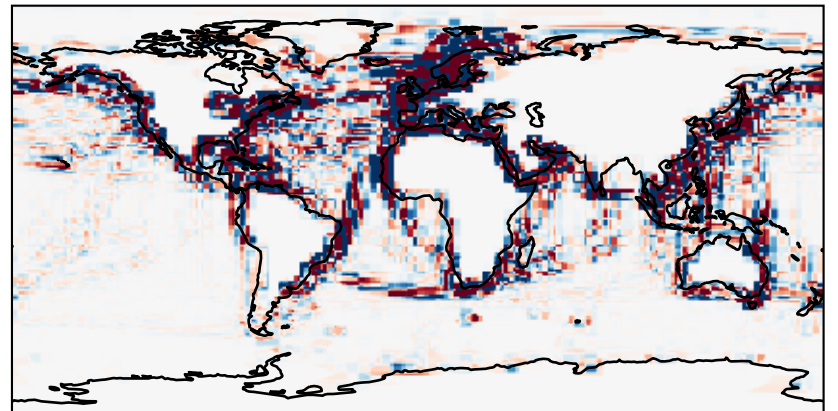
GCHP\_14.0.0 (Dev)  
c24



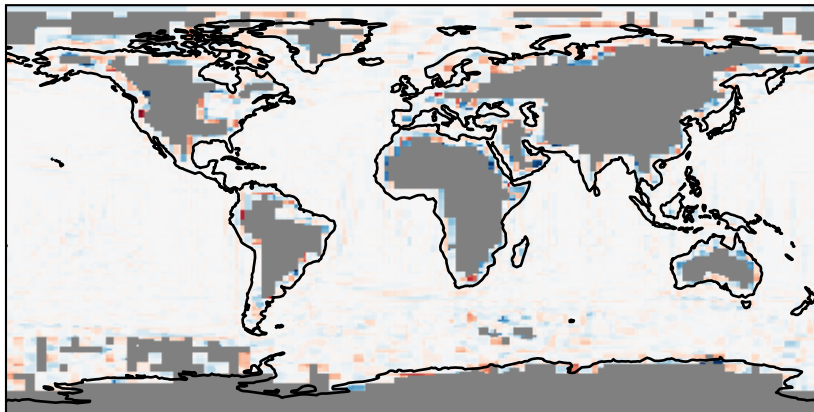
Difference (1x1.25)  
Dev - Ref, Dynamic Range



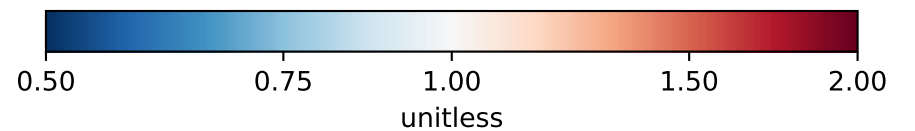
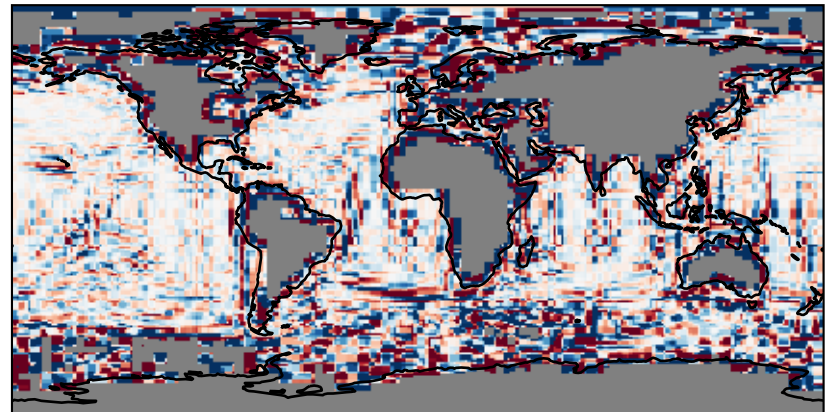
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



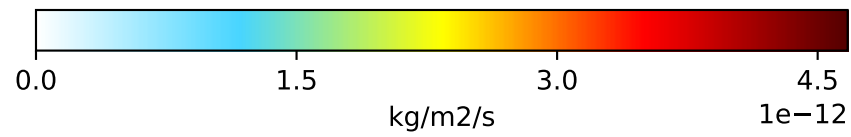
Ratio (1x1.25)  
Dev/Ref, Fixed Range



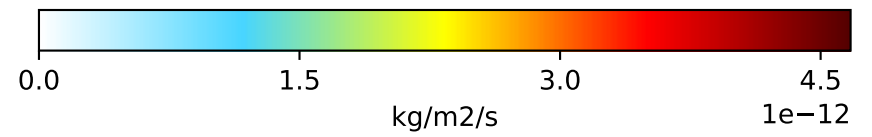
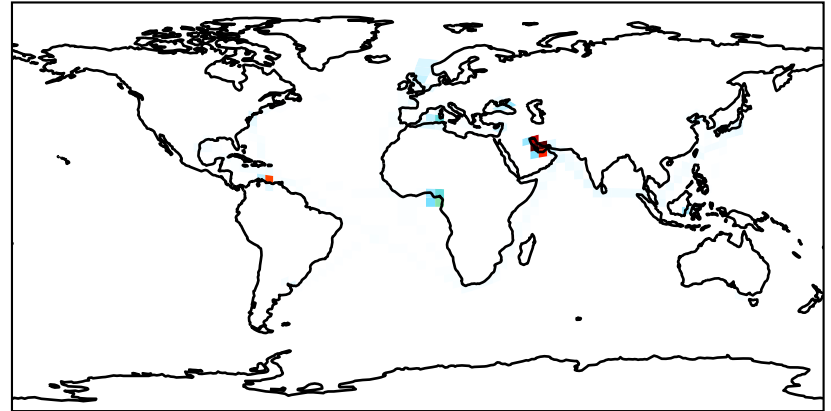


# EmisC2H6\_Ship (Apr2019)

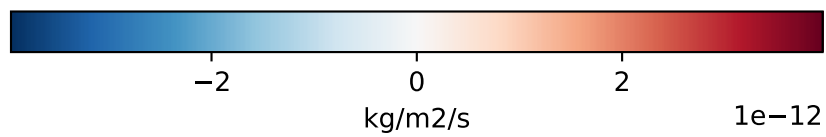
GCC\_14.0.0 (Ref)  
4.0x5.0



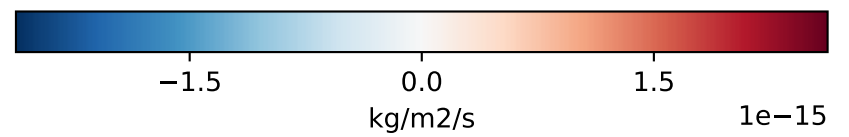
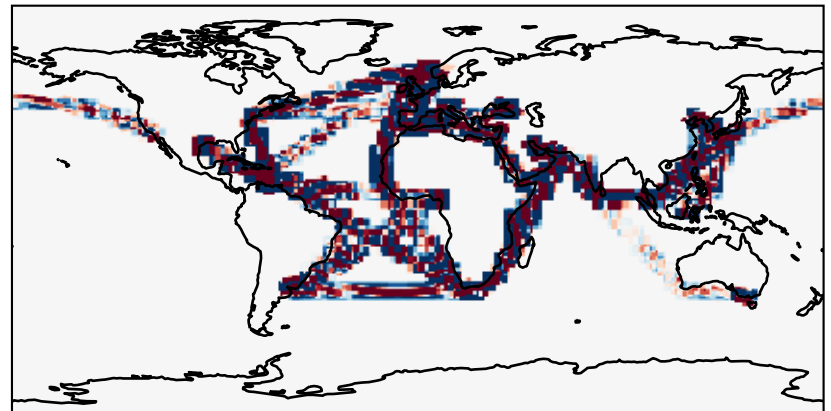
GCHP\_14.0.0 (Dev)  
c24



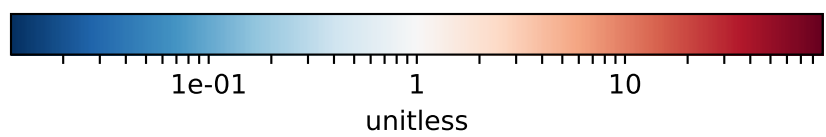
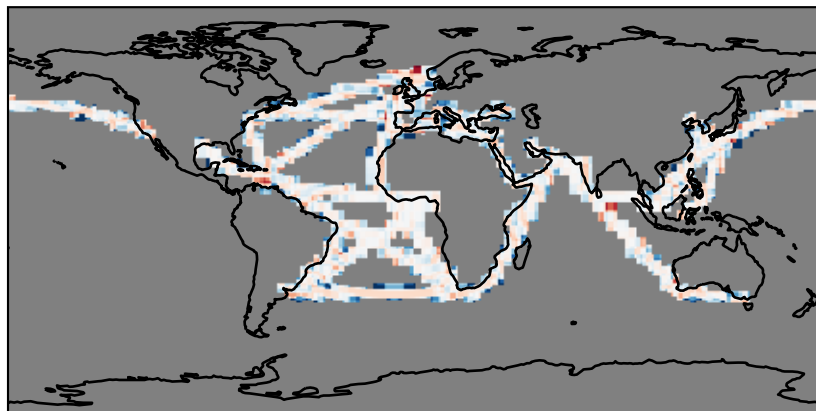
Difference (1x1.25)  
Dev - Ref, Dynamic Range



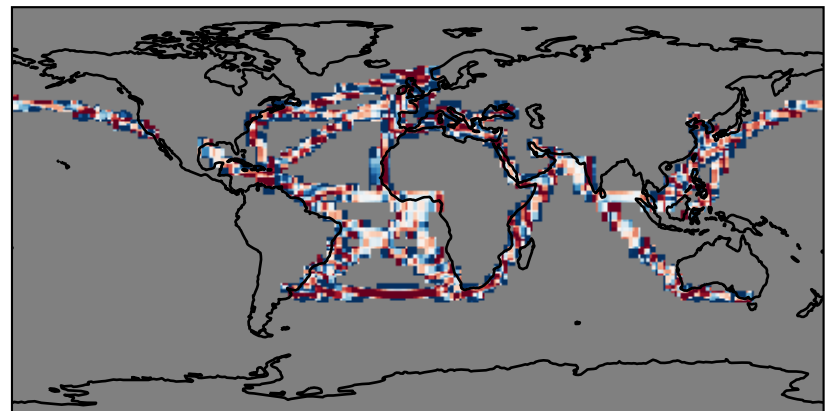
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

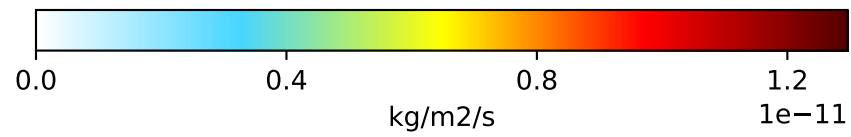


Ratio (1x1.25)  
Dev/Ref, Fixed Range

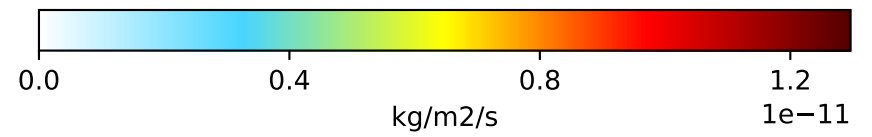
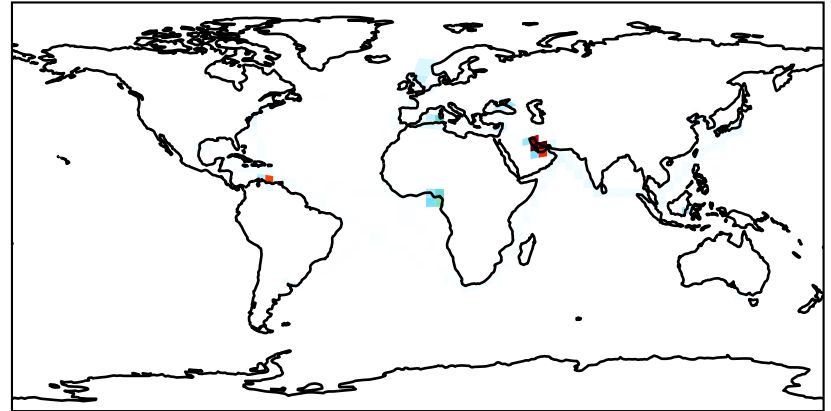


# EmisC3H8\_Ship (Apr2019)

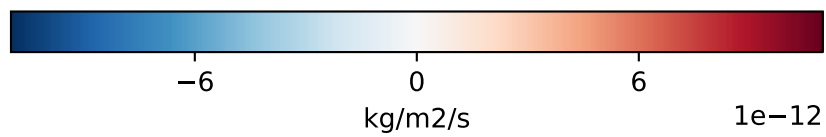
GCC\_14.0.0 (Ref)  
4.0x5.0



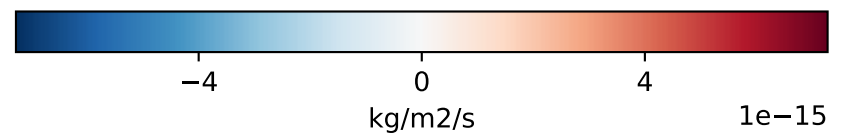
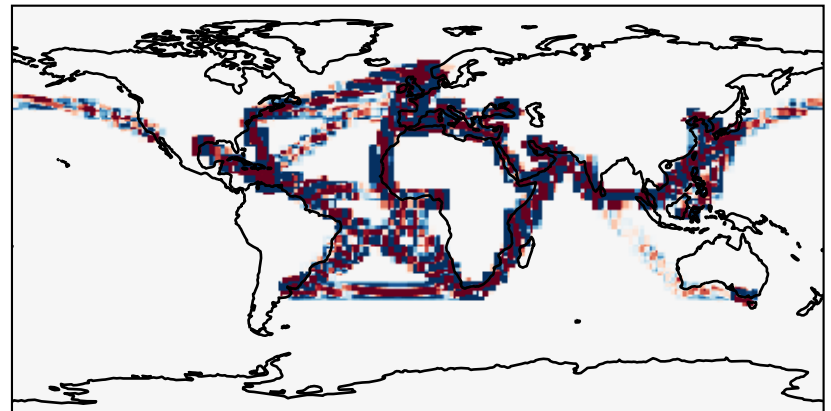
GCHP\_14.0.0 (Dev)  
c24



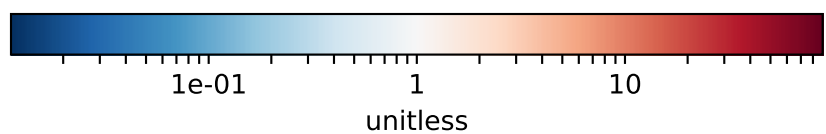
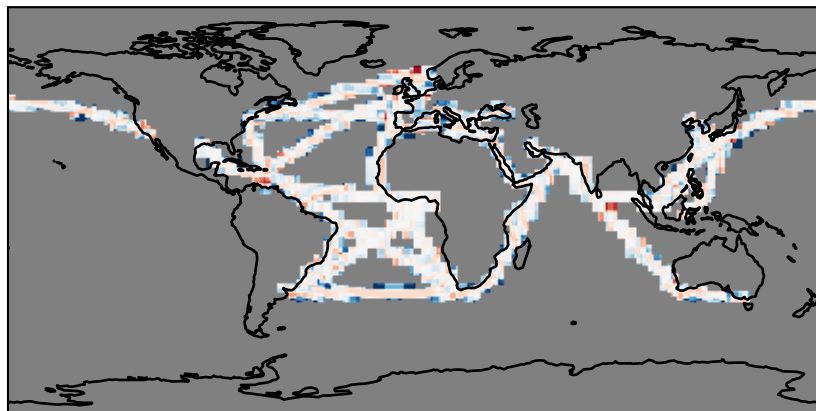
Difference (1x1.25)  
Dev - Ref, Dynamic Range



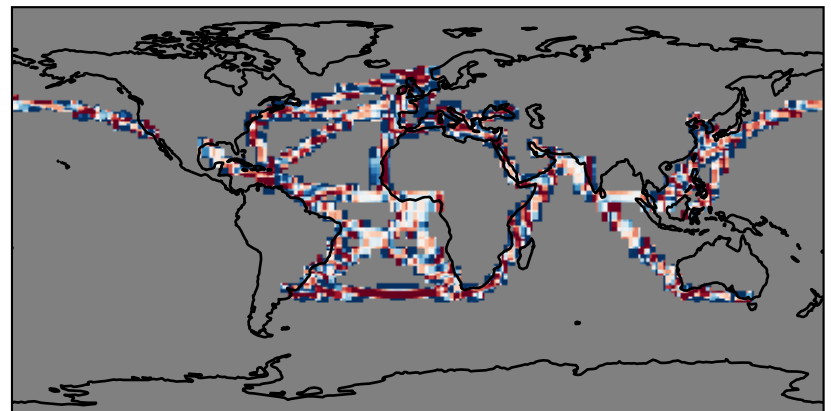
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Ratio (1x1.25)  
Dev/Ref, Fixed Range



# EmisCH2O\_Ship (Apr2019)

GCC\_14.0.0 (Ref)  
4.0x5.0



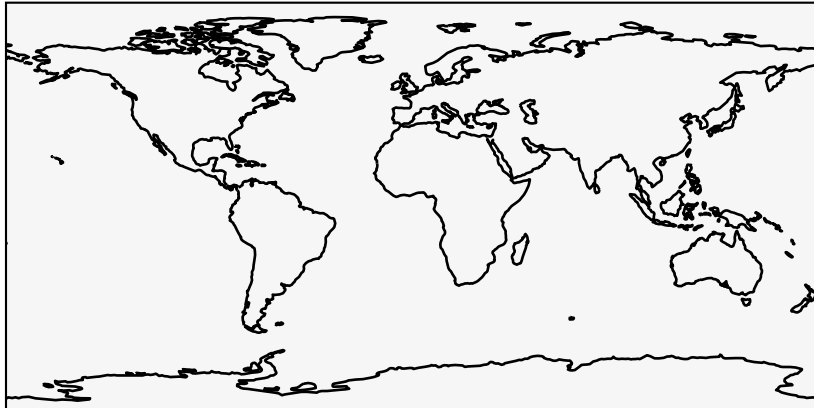
Zero throughout domain  
kg/m2/s

GCHP\_14.0.0 (Dev)  
c24



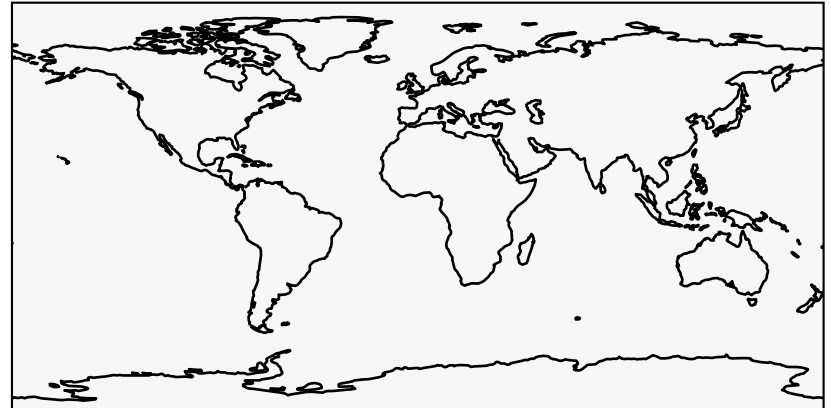
Zero throughout domain  
kg/m2/s

Difference (1x1.25)  
Dev - Ref, Dynamic Range



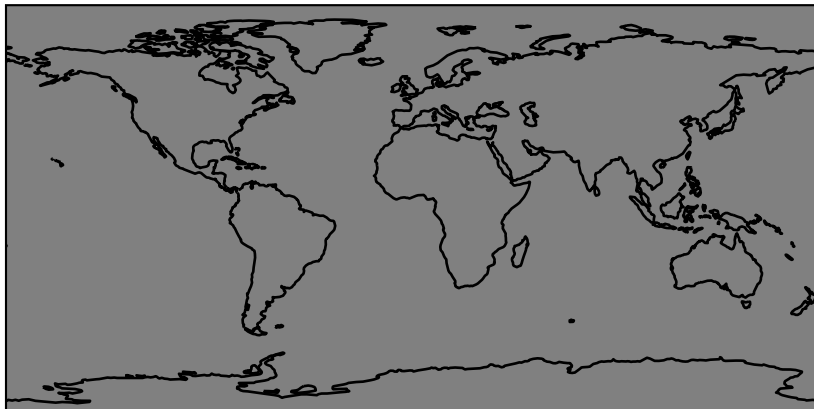
Zero throughout domain  
kg/m2/s

Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



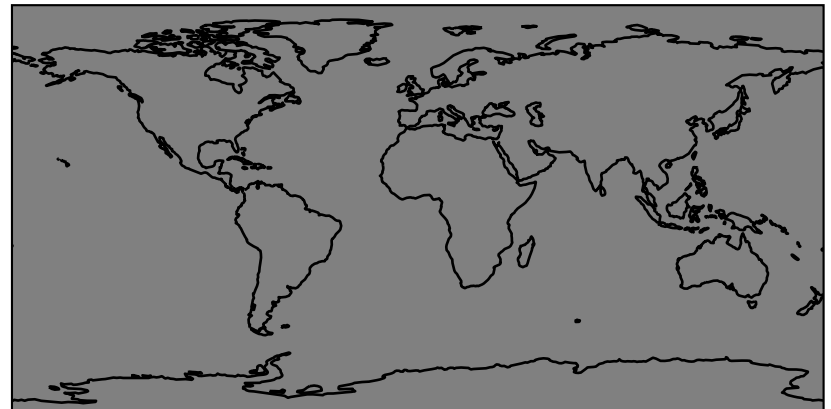
Zero throughout domain  
kg/m2/s

Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

Ratio (1x1.25)  
Dev/Ref, Fixed Range



Undefined throughout domain  
unitless



# EmisCO2\_Ship (Apr2019)

GCC\_14.0.0 (Ref)  
4.0x5.0



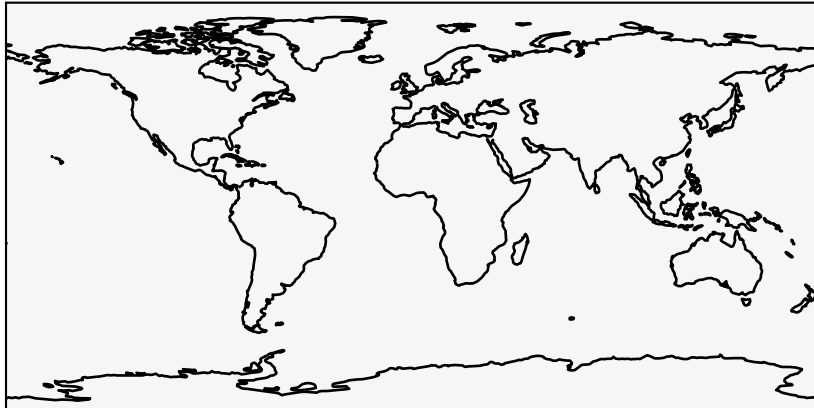
Zero throughout domain  
kg/m2/s

GCHP\_14.0.0 (Dev)  
c24



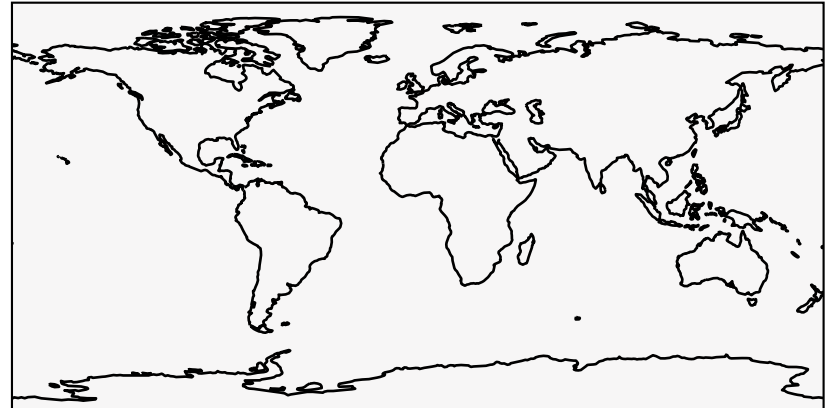
Zero throughout domain  
kg/m2/s

Difference (1x1.25)  
Dev - Ref, Dynamic Range



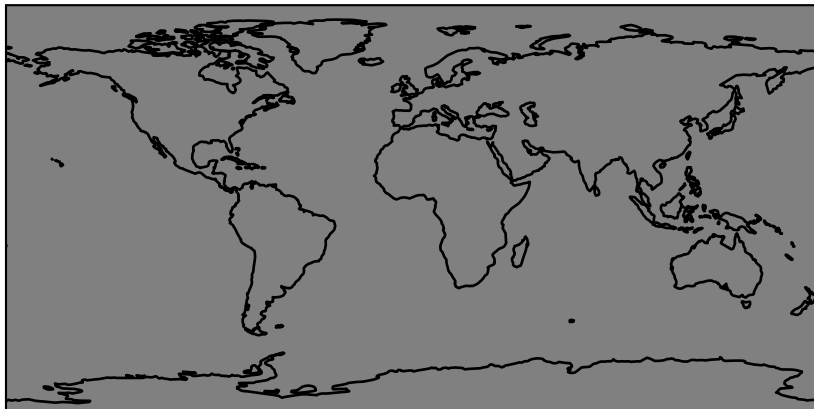
Zero throughout domain  
kg/m2/s

Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



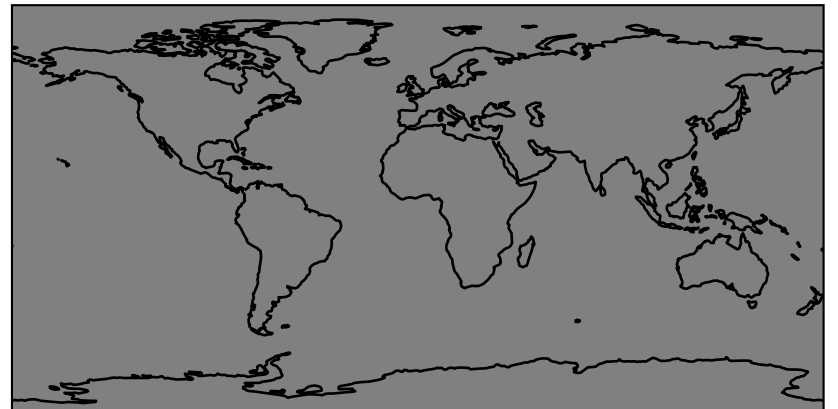
Zero throughout domain  
kg/m2/s

Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

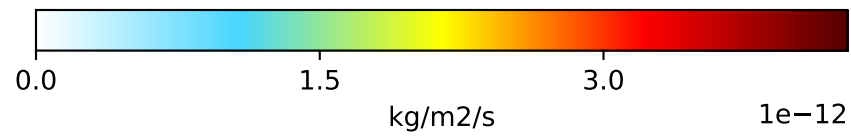
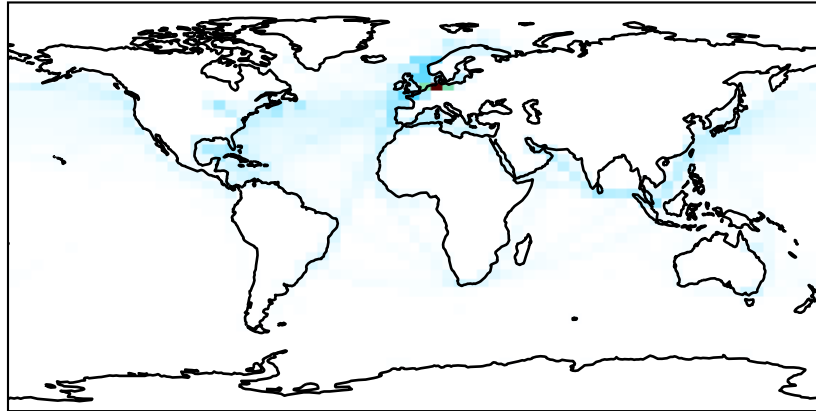
Ratio (1x1.25)  
Dev/Ref, Fixed Range



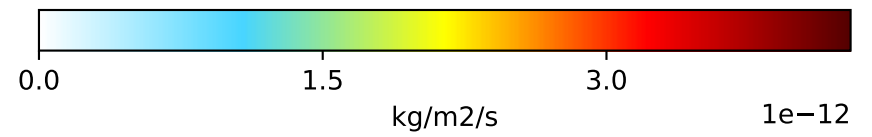
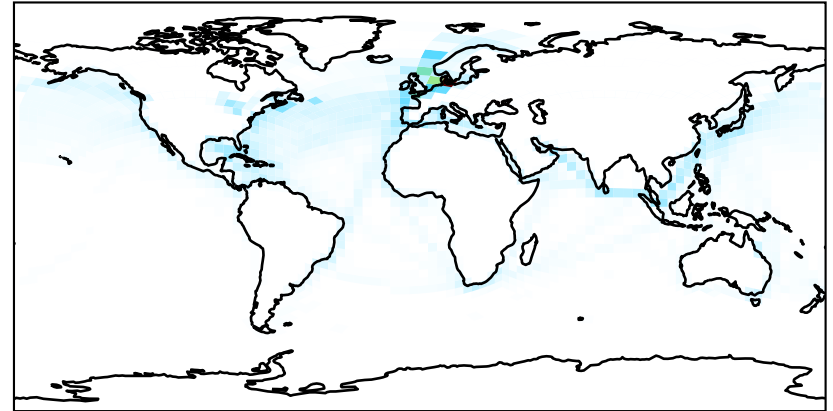
Undefined throughout domain  
unitless

# EmisCO\_Ship (Apr2019)

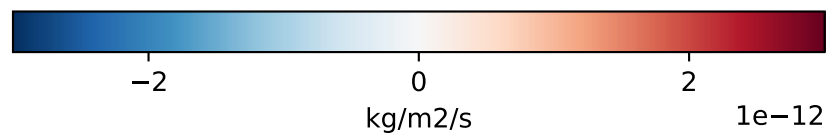
GCC\_14.0.0 (Ref)  
4.0x5.0



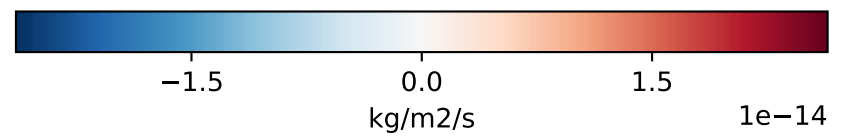
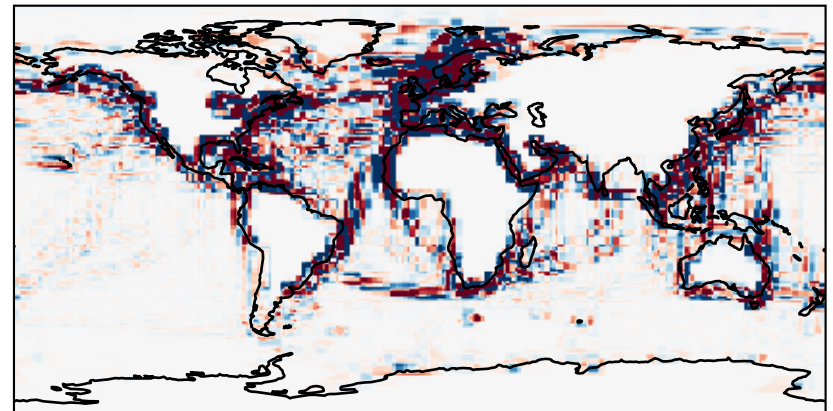
GCHP\_14.0.0 (Dev)  
c24



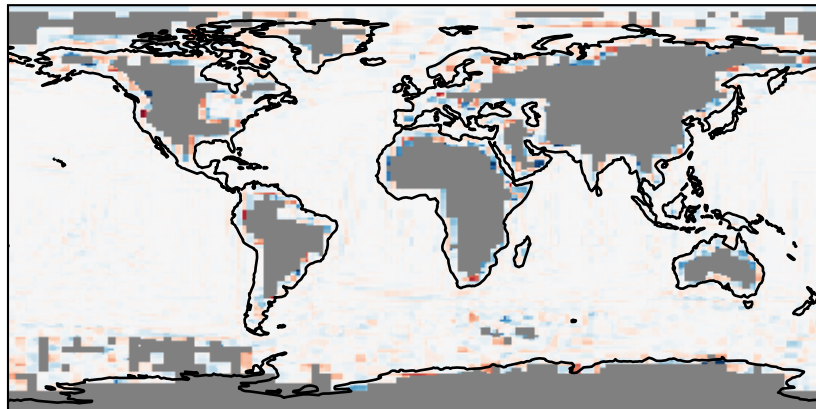
Difference (1x1.25)  
Dev - Ref, Dynamic Range



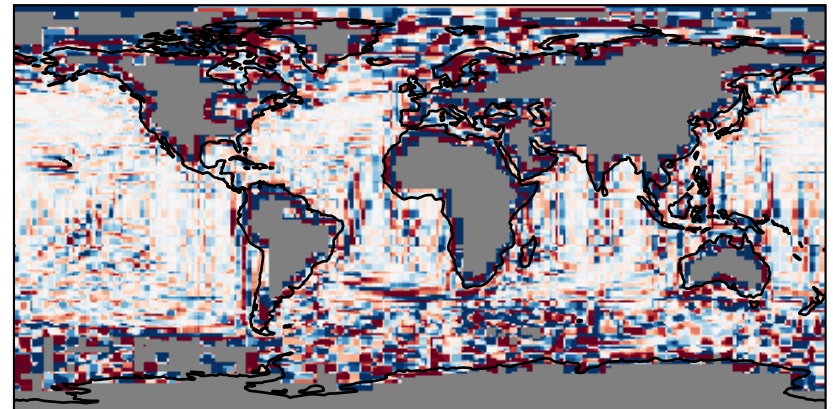
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

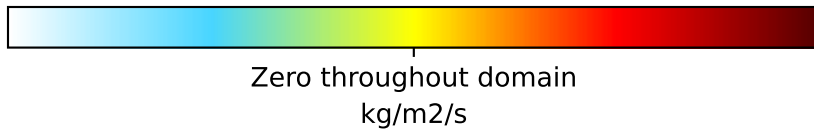
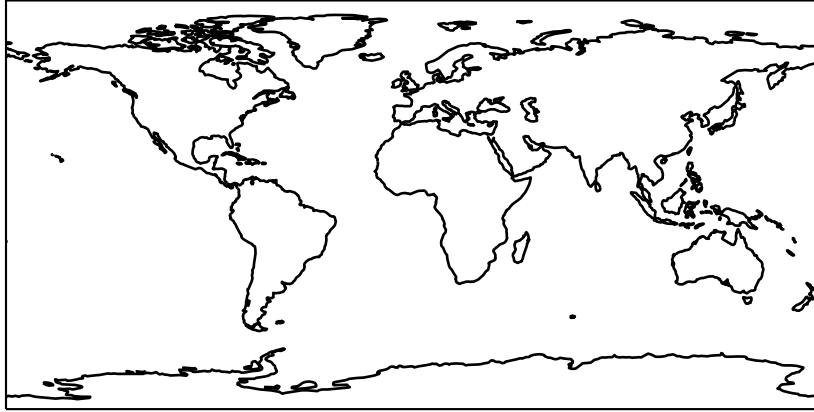


Ratio (1x1.25)  
Dev/Ref, Fixed Range

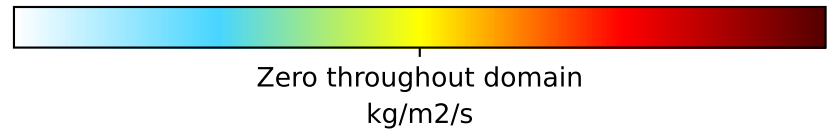
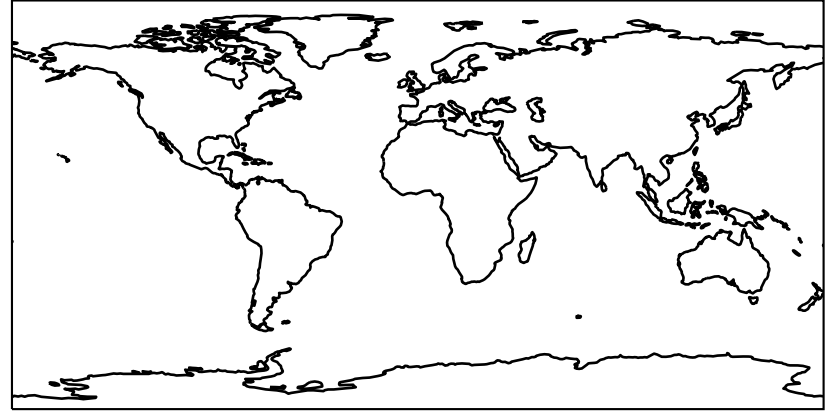


# EmisEOH\_Ship (Apr2019)

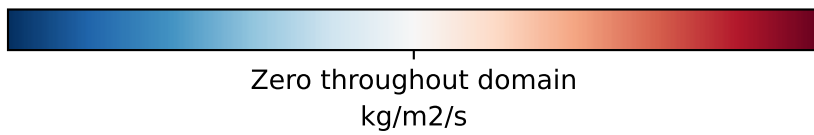
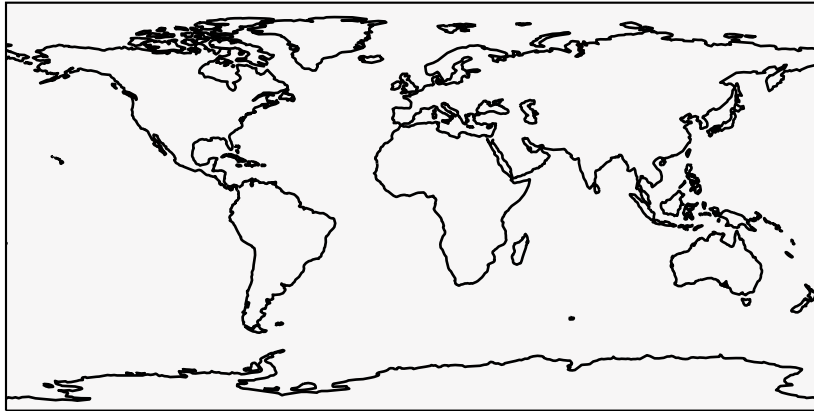
GCC\_14.0.0 (Ref)  
4.0x5.0



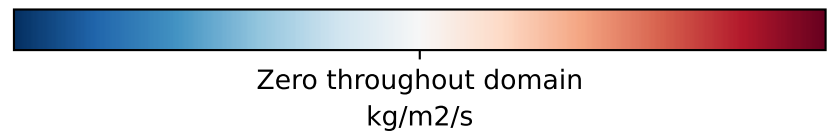
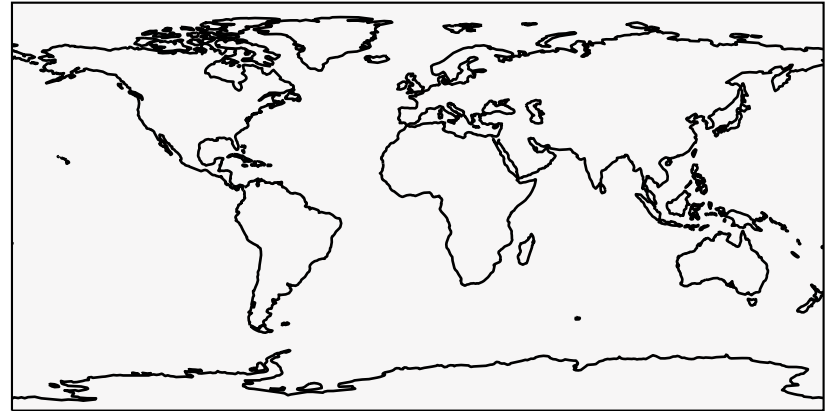
GCHP\_14.0.0 (Dev)  
c24



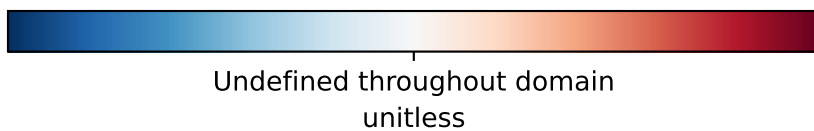
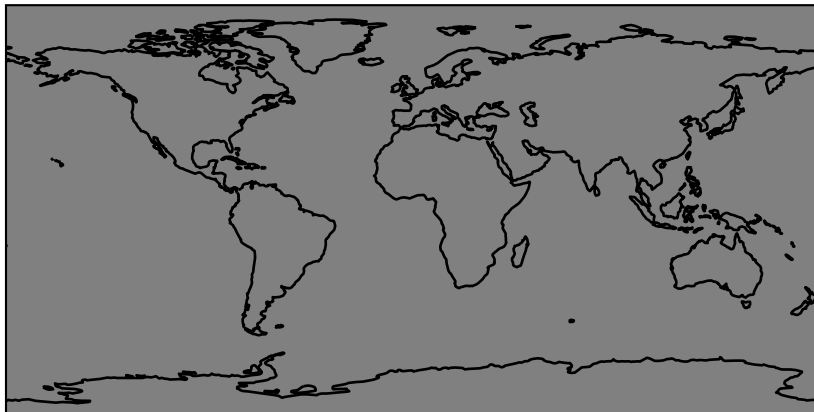
Difference (1x1.25)  
Dev - Ref, Dynamic Range



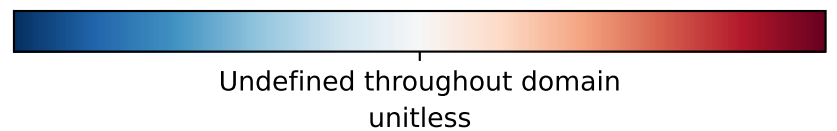
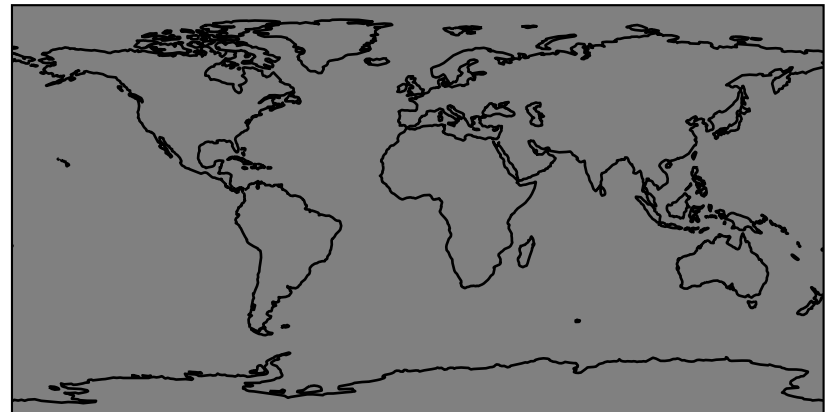
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Ratio (1x1.25)  
Dev/Ref, Fixed Range





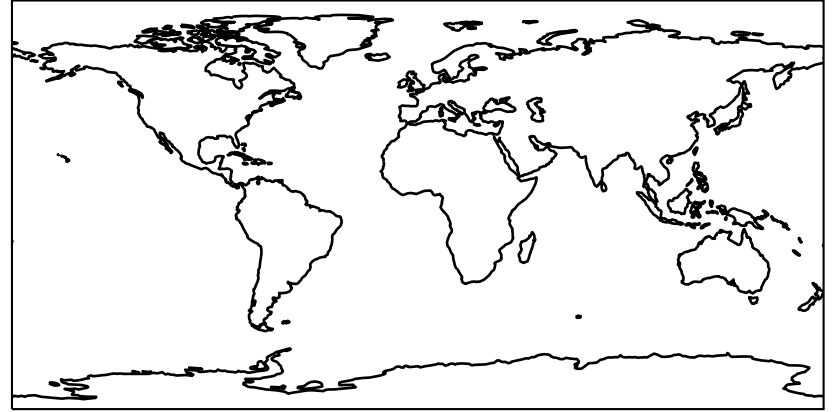
# EmisHCOOH\_Ship (Apr2019)

GCC\_14.0.0 (Ref)  
4.0x5.0



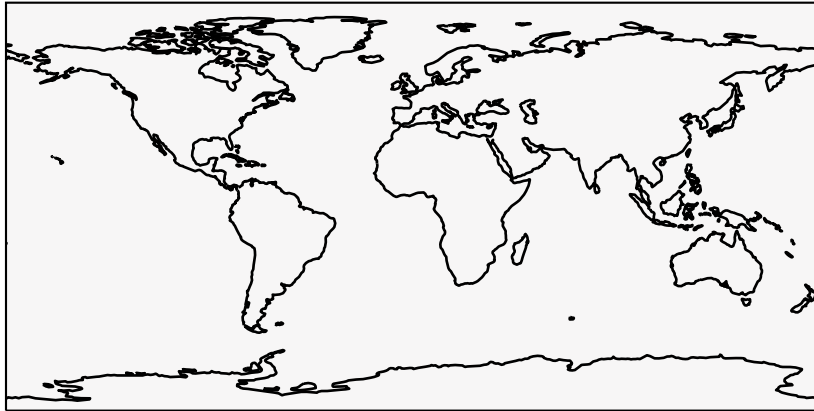
Zero throughout domain  
kg/m2/s

GCHP\_14.0.0 (Dev)  
c24



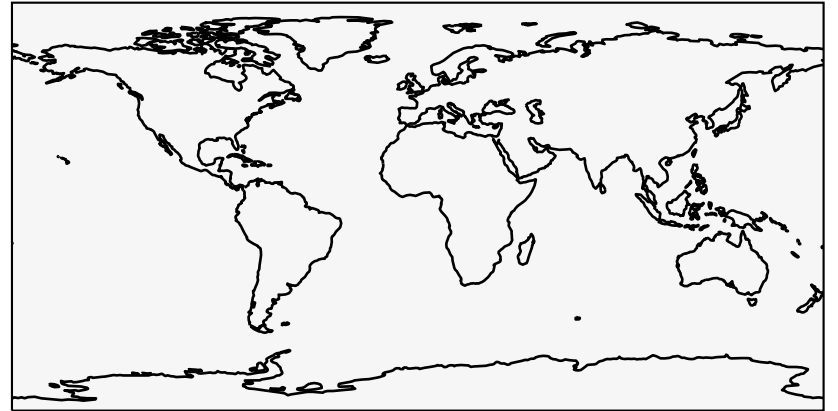
Zero throughout domain  
kg/m2/s

Difference (1x1.25)  
Dev - Ref, Dynamic Range



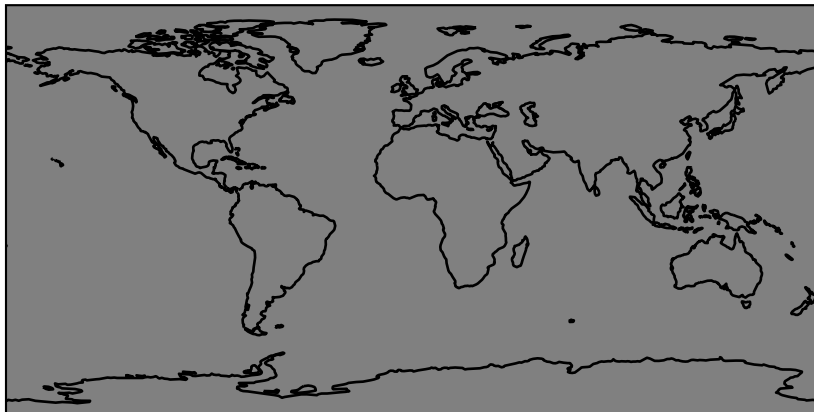
Zero throughout domain  
kg/m2/s

Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



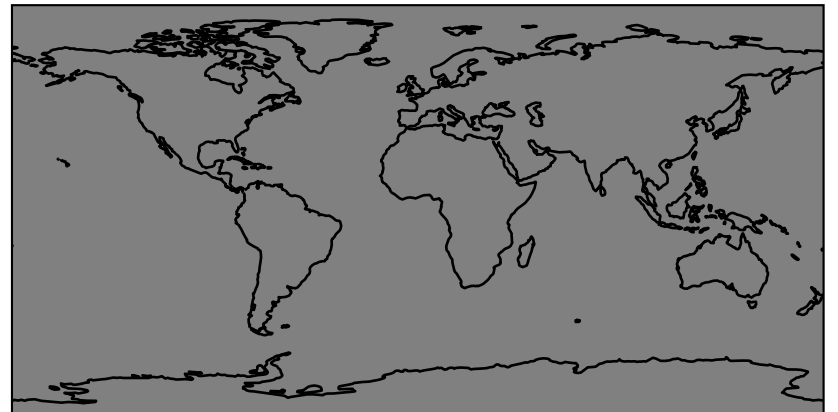
Zero throughout domain  
kg/m2/s

Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

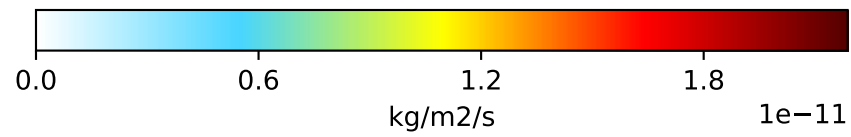
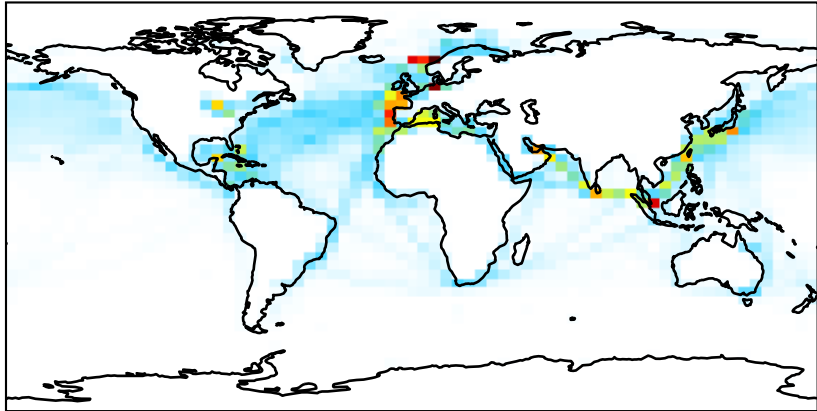
Ratio (1x1.25)  
Dev/Ref, Fixed Range



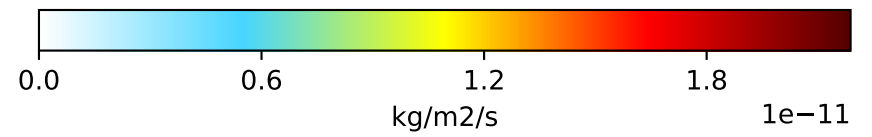
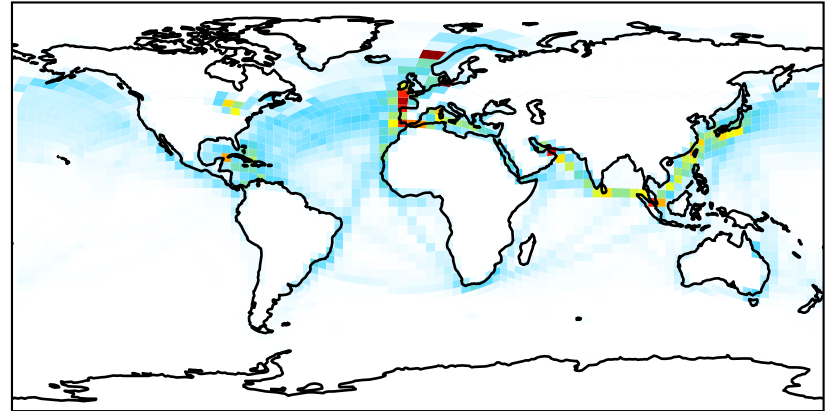
Undefined throughout domain  
unitless

# EmisHNO3\_Ship (Apr2019)

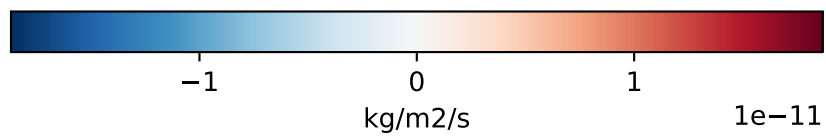
GCC\_14.0.0 (Ref)  
4.0x5.0



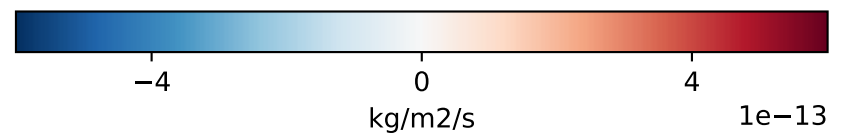
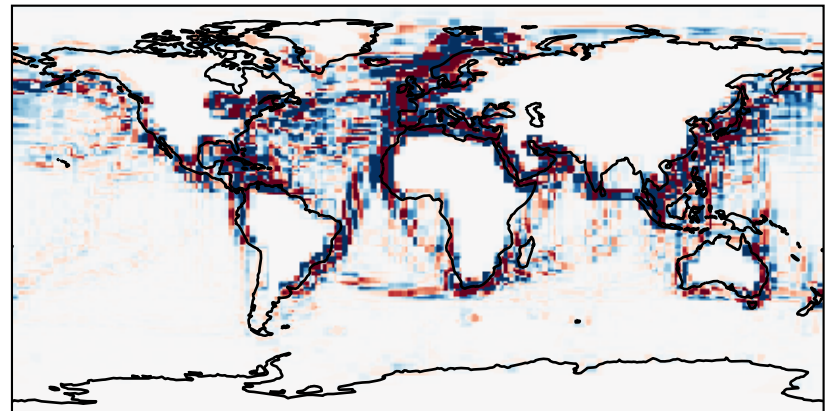
GCHP\_14.0.0 (Dev)  
c24



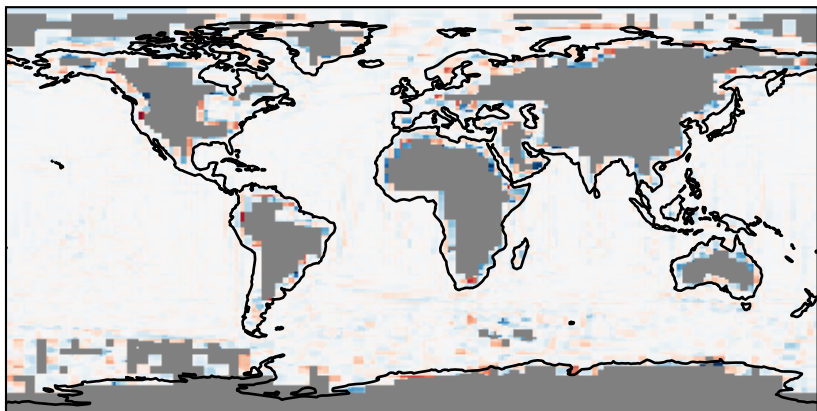
Difference (1x1.25)  
Dev - Ref, Dynamic Range



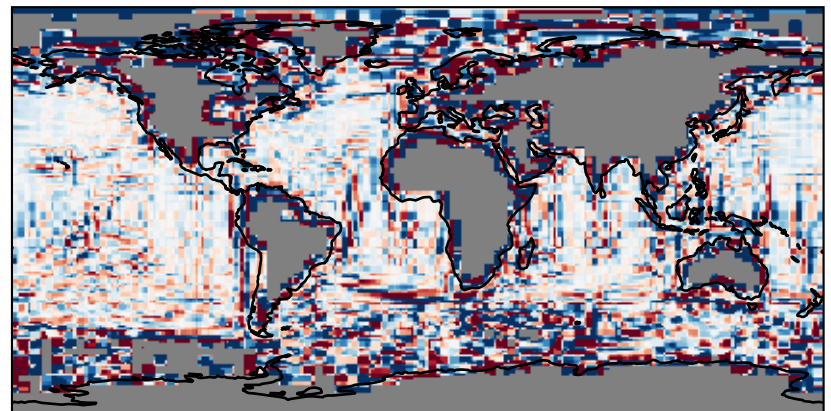
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Ratio (1x1.25)  
Dev/Ref, Fixed Range



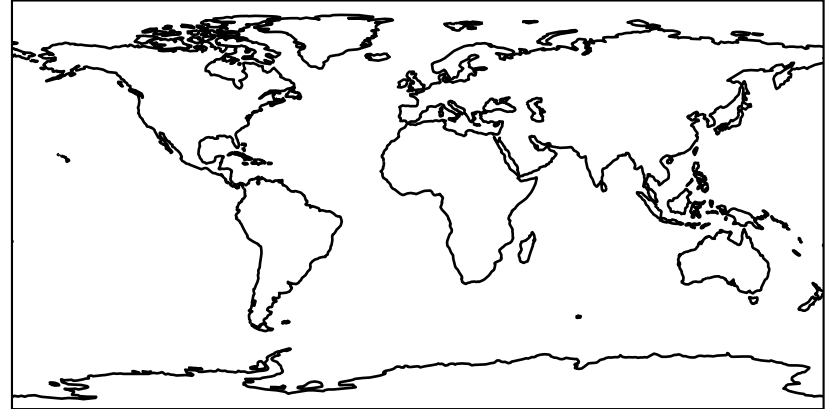
# EmisMEK\_Ship (Apr2019)

GCC\_14.0.0 (Ref)  
4.0x5.0



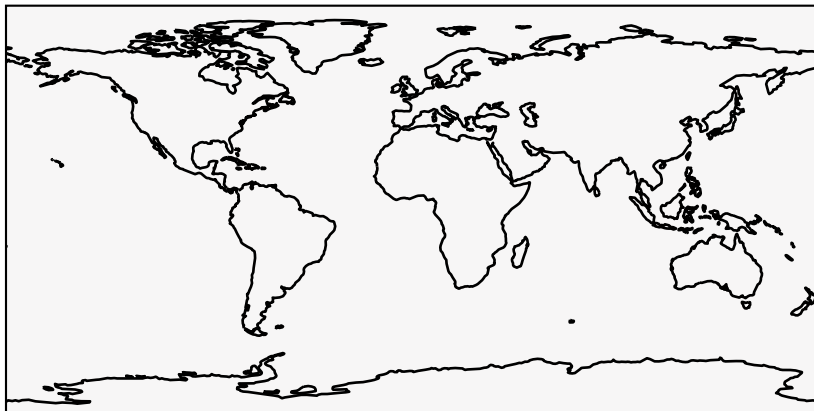
Zero throughout domain  
kg/m2/s

GCHP\_14.0.0 (Dev)  
c24



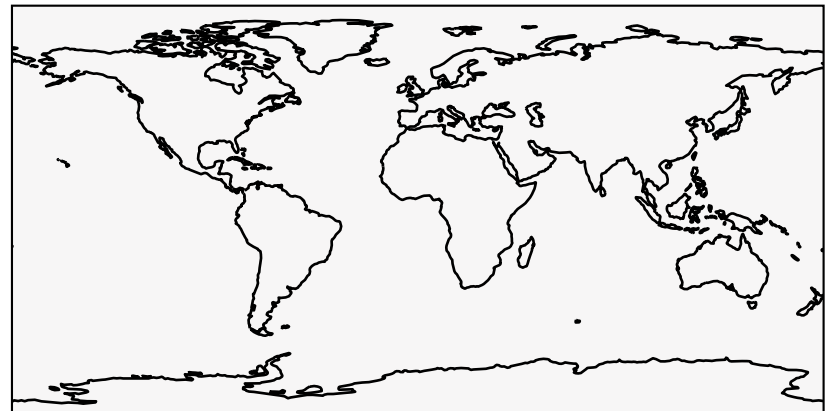
Zero throughout domain  
kg/m2/s

Difference (1x1.25)  
Dev - Ref, Dynamic Range



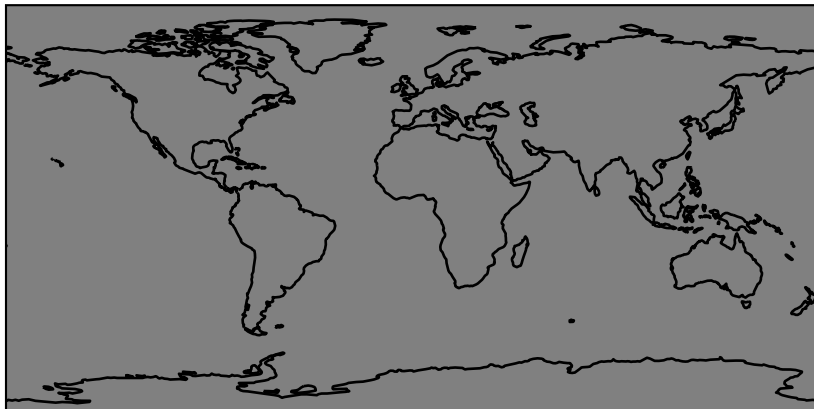
Zero throughout domain  
kg/m2/s

Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



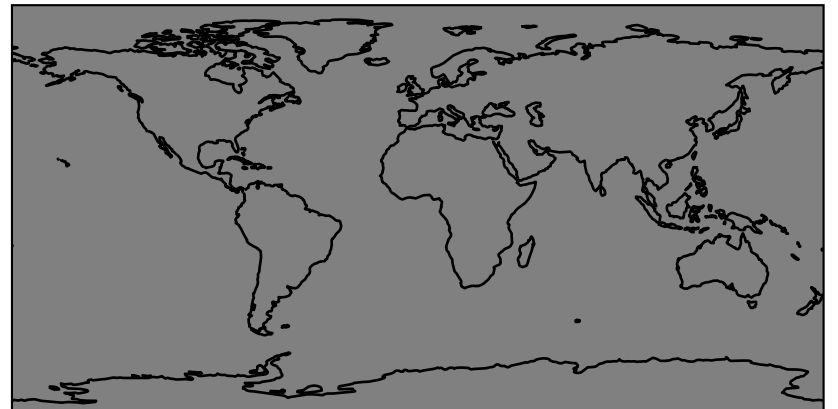
Zero throughout domain  
kg/m2/s

Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

Ratio (1x1.25)  
Dev/Ref, Fixed Range



Undefined throughout domain  
unitless



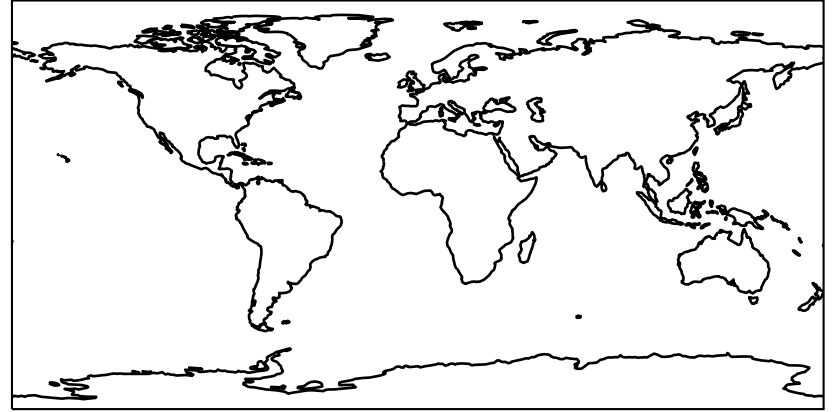
# EmisMOH\_Ship (Apr2019)

GCC\_14.0.0 (Ref)  
4.0x5.0



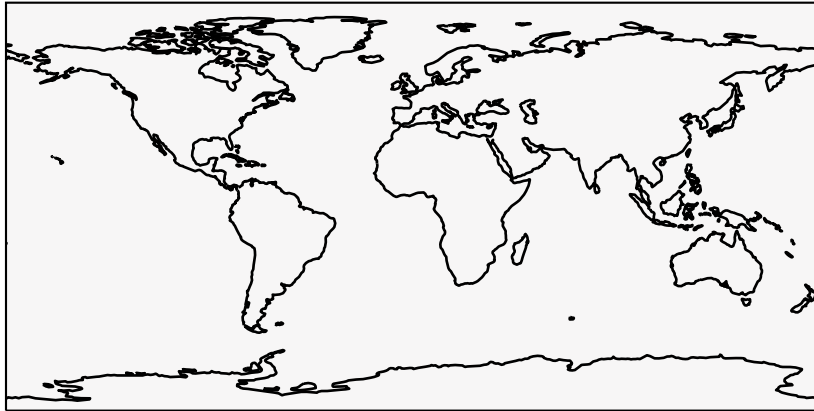
Zero throughout domain  
kg/m2/s

GCHP\_14.0.0 (Dev)  
c24



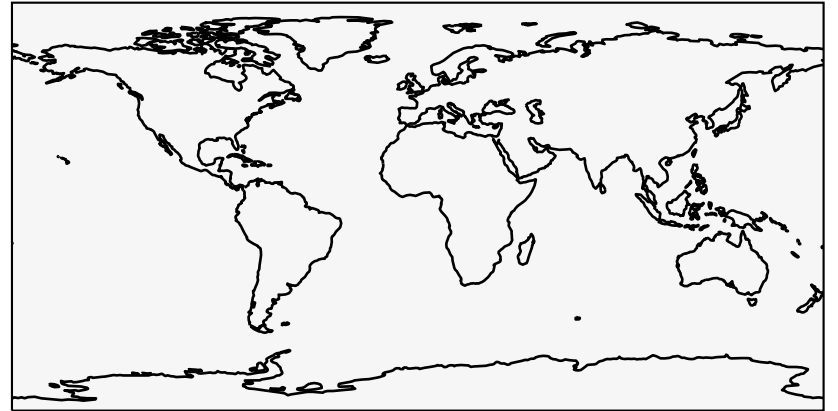
Zero throughout domain  
kg/m2/s

Difference (1x1.25)  
Dev - Ref, Dynamic Range



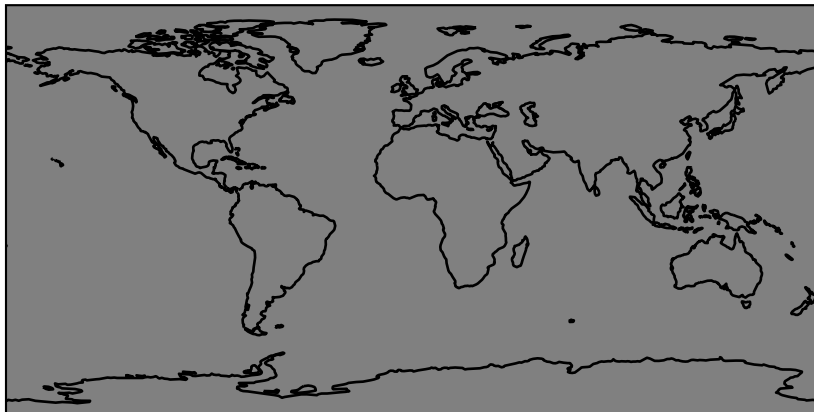
Zero throughout domain  
kg/m2/s

Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



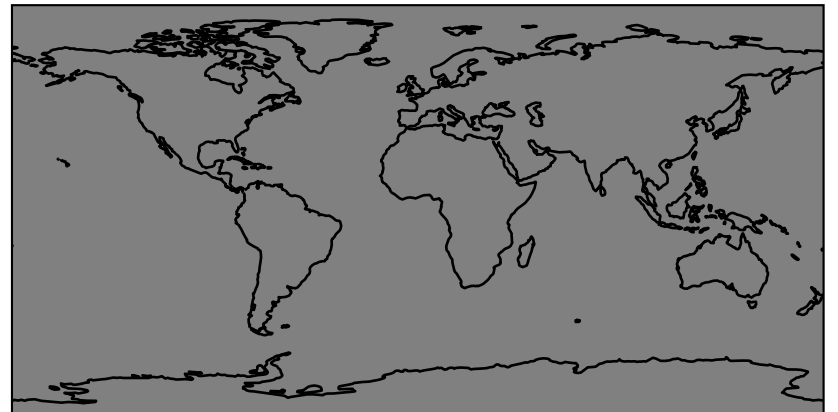
Zero throughout domain  
kg/m2/s

Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

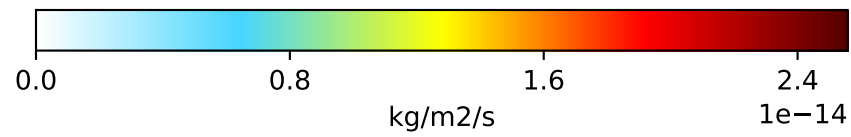
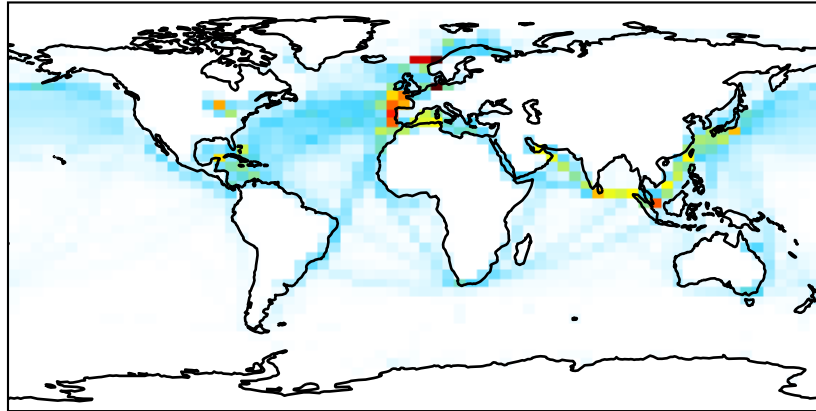
Ratio (1x1.25)  
Dev/Ref, Fixed Range



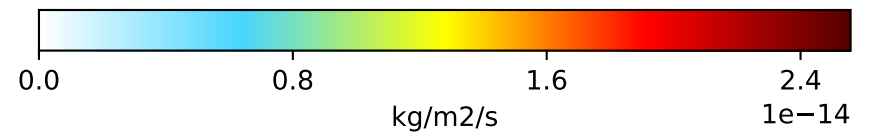
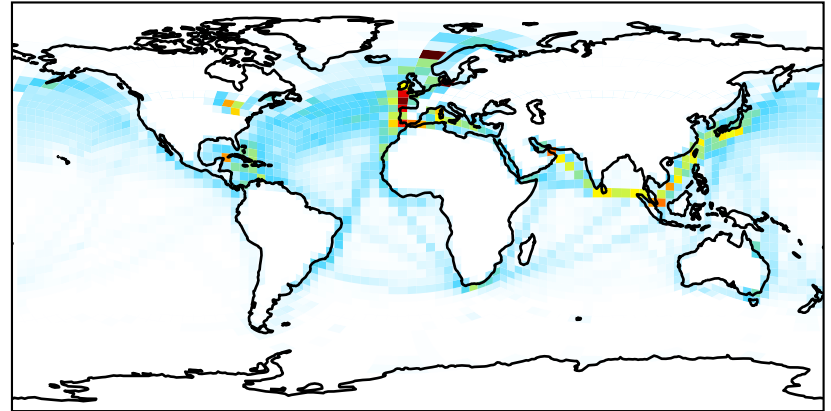
Undefined throughout domain  
unitless

# EmisNH3\_Ship (Apr2019)

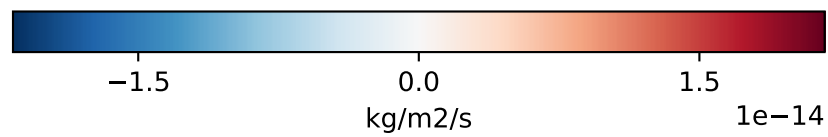
GCC\_14.0.0 (Ref)  
4.0x5.0



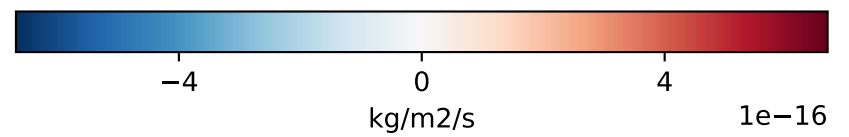
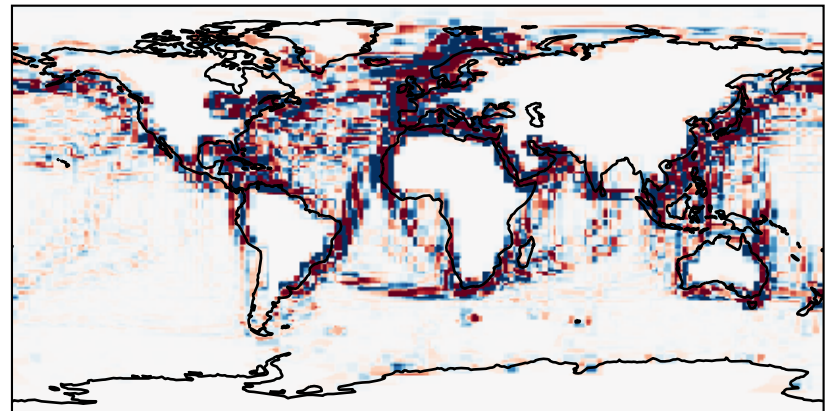
GCHP\_14.0.0 (Dev)  
c24



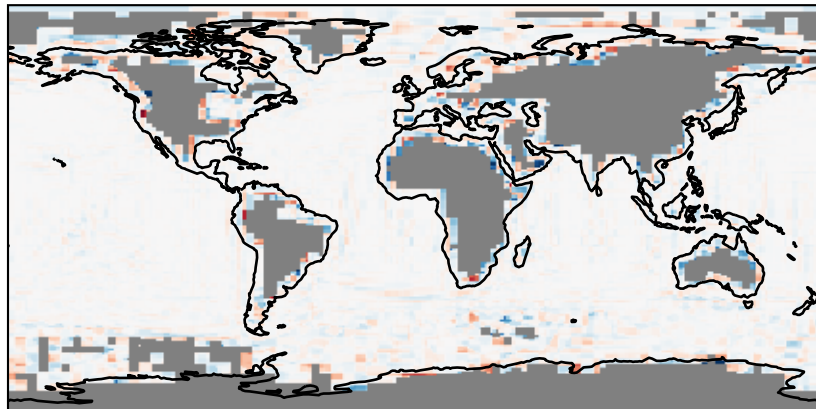
Difference (1x1.25)  
Dev - Ref, Dynamic Range



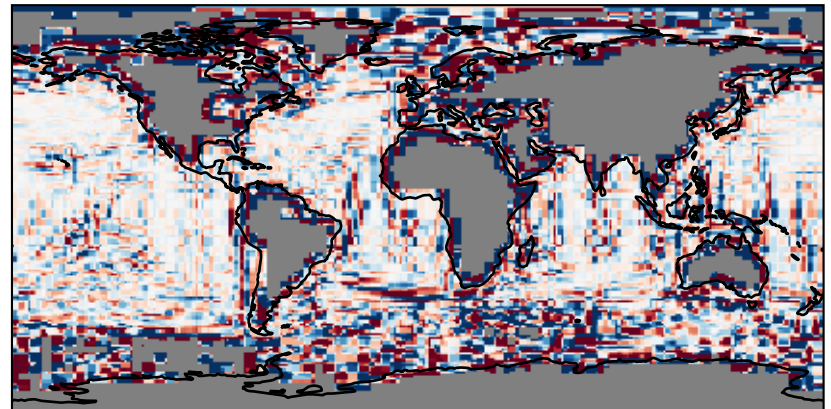
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

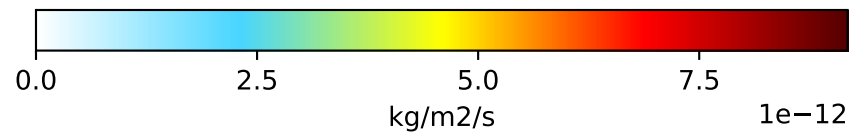
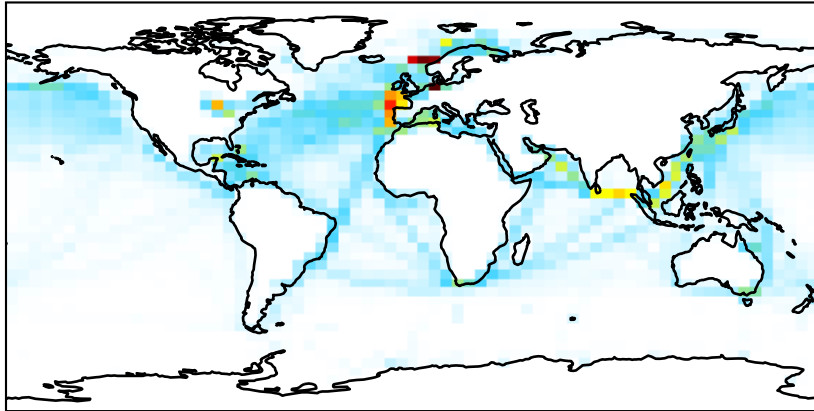


Ratio (1x1.25)  
Dev/Ref, Fixed Range

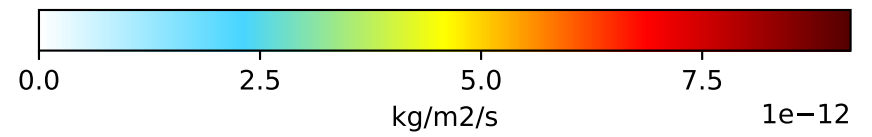
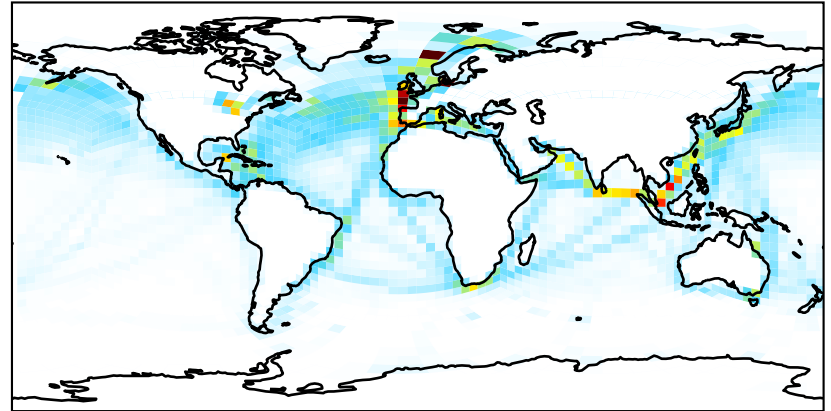


# EmisNO2\_Ship (Apr2019)

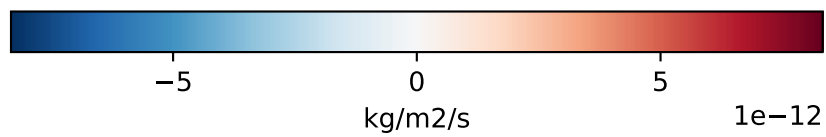
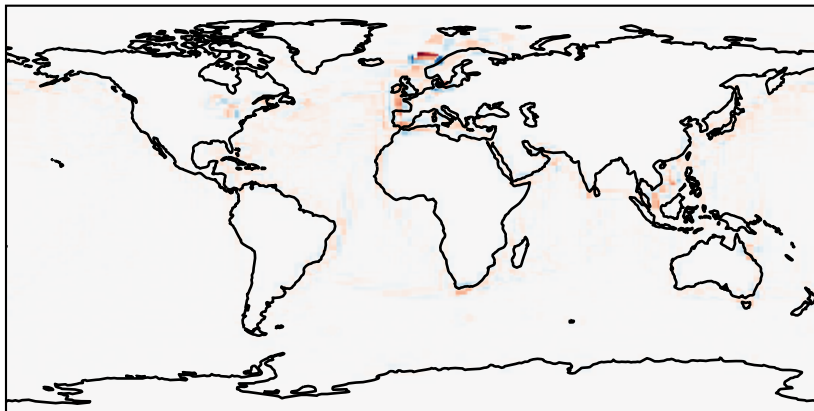
GCC\_14.0.0 (Ref)  
4.0x5.0



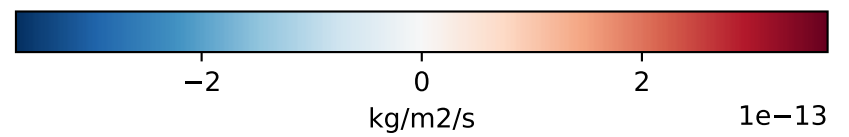
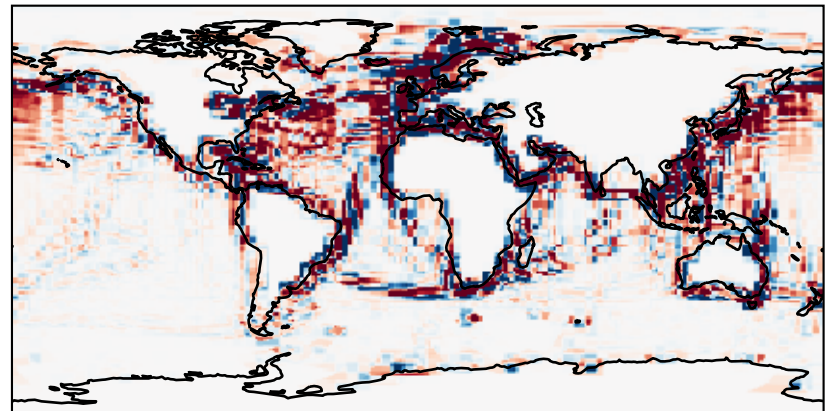
GCHP\_14.0.0 (Dev)  
c24



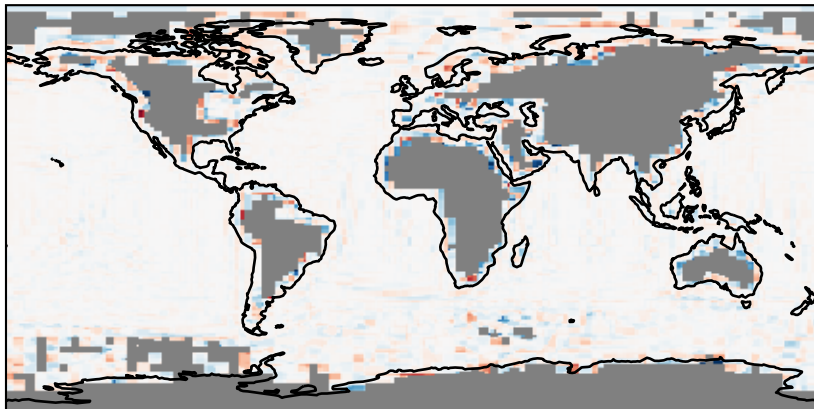
Difference (1x1.25)  
Dev - Ref, Dynamic Range



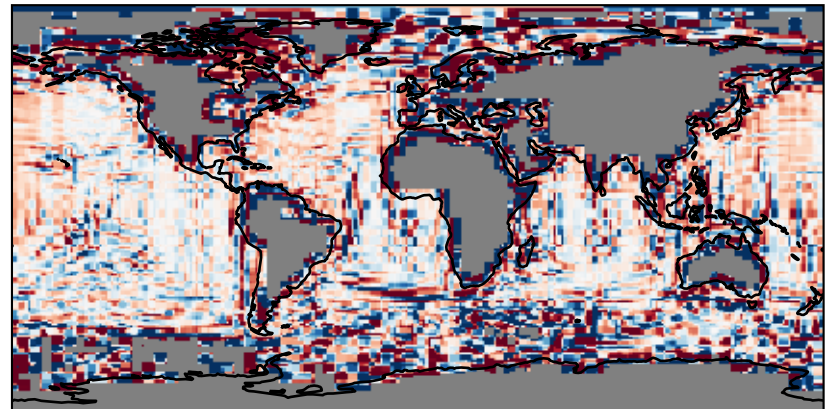
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



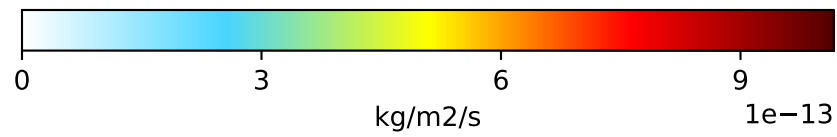
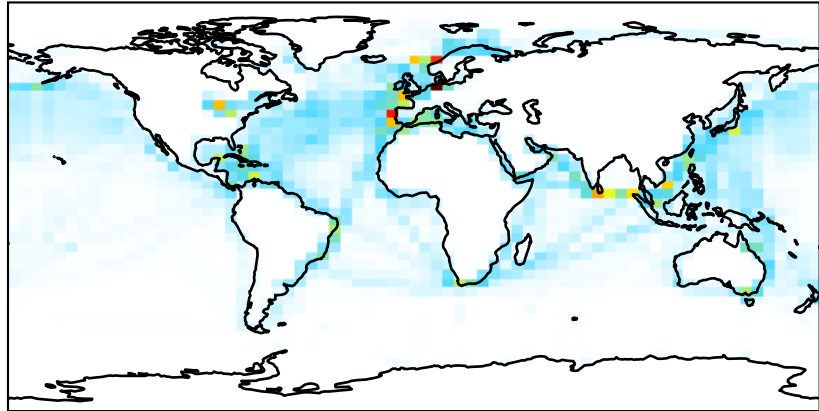
Ratio (1x1.25)  
Dev/Ref, Fixed Range



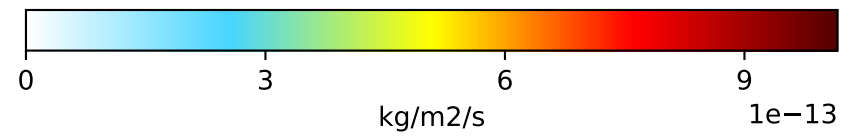
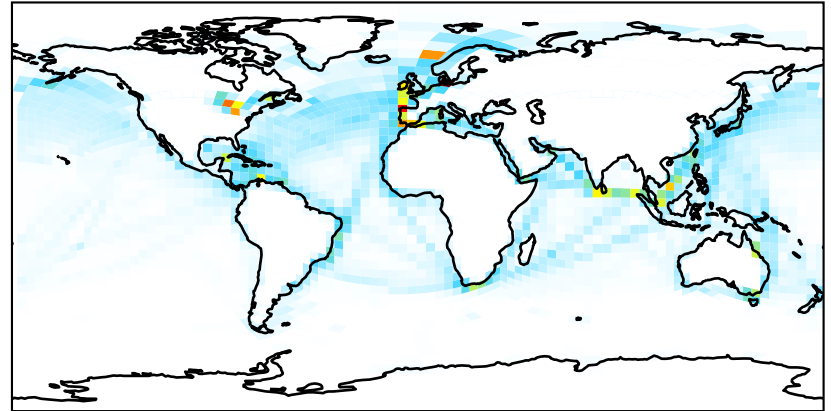


# EmisNO\_Ship (Apr2019)

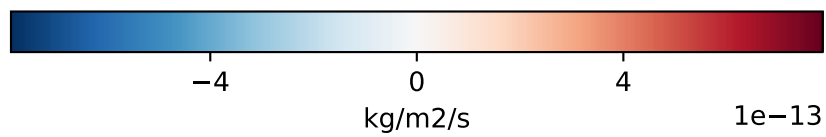
GCC\_14.0.0 (Ref)  
4.0x5.0



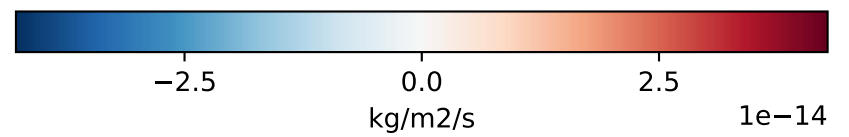
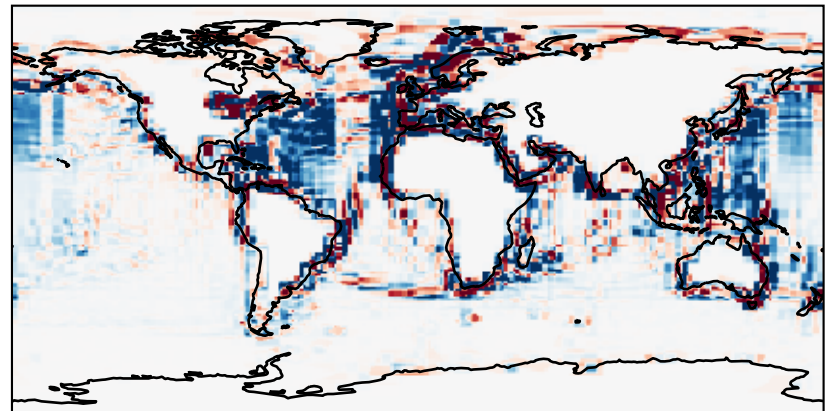
GCHP\_14.0.0 (Dev)  
c24



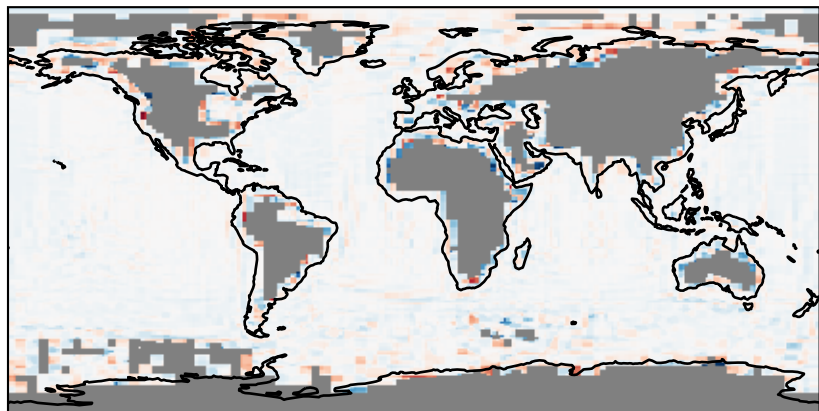
Difference (1x1.25)  
Dev - Ref, Dynamic Range



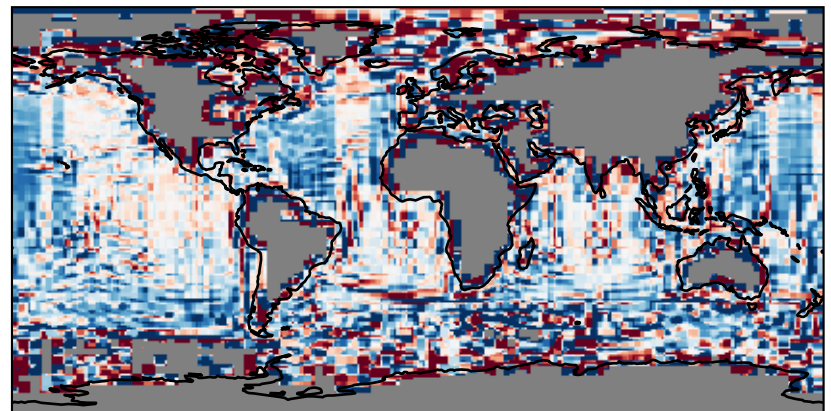
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



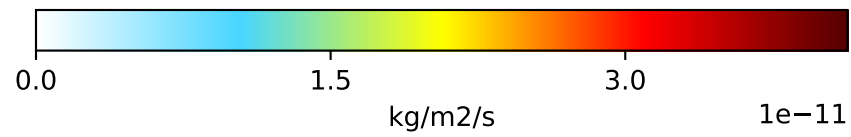
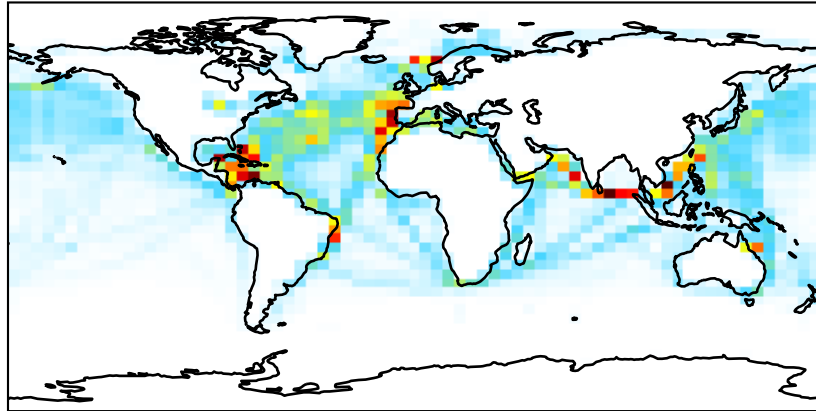
Ratio (1x1.25)  
Dev/Ref, Fixed Range



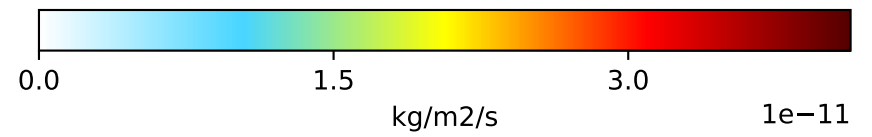
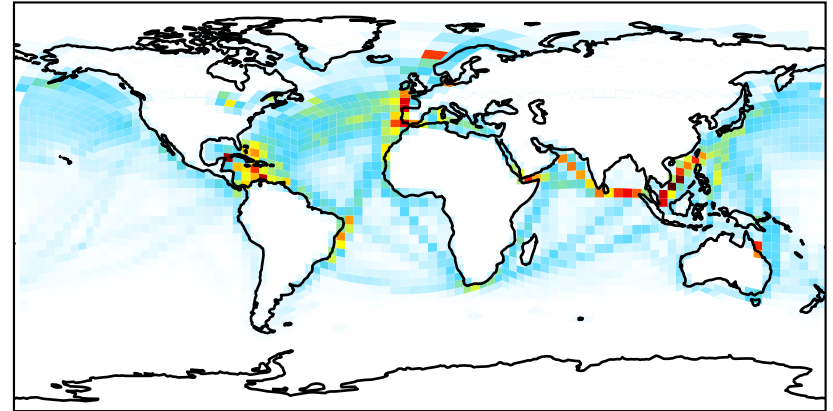


# EmisO3\_Ship (Apr2019)

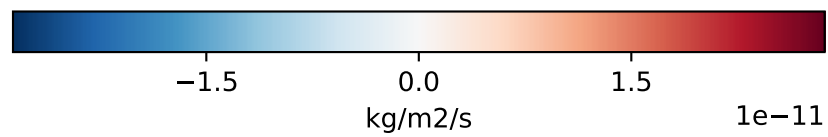
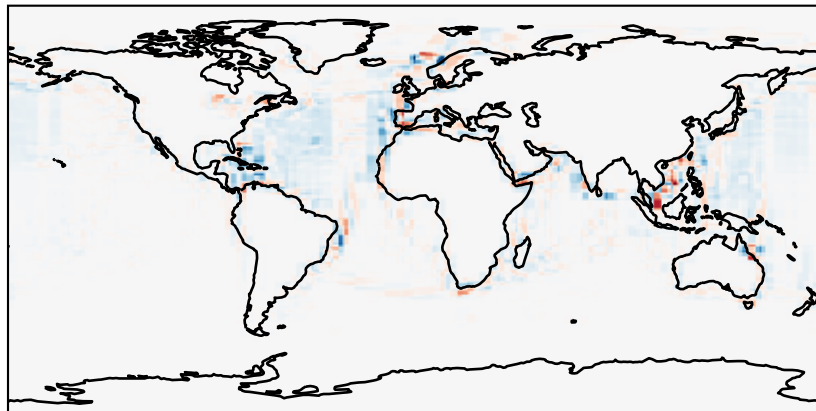
GCC\_14.0.0 (Ref)  
4.0x5.0



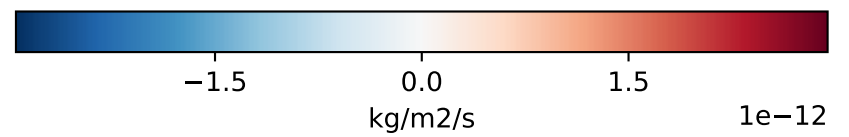
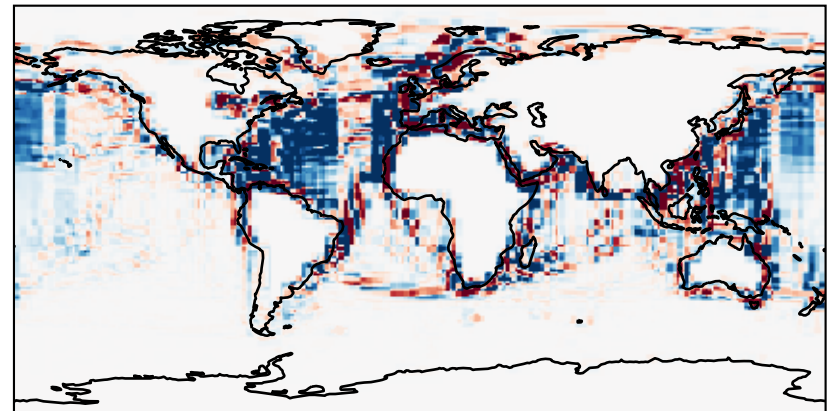
GCHP\_14.0.0 (Dev)  
c24



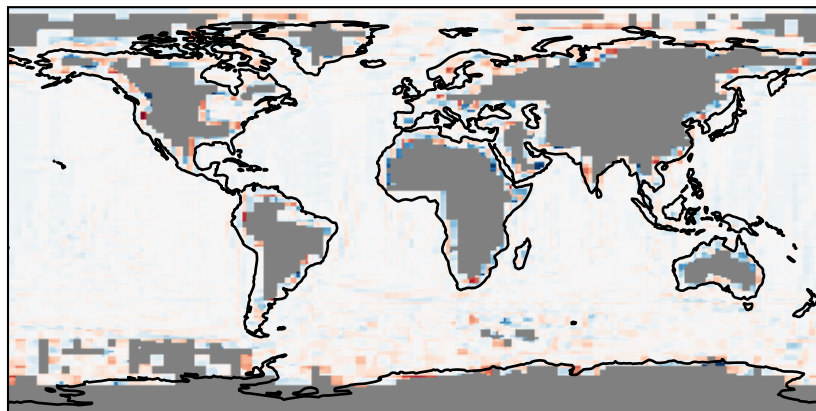
Difference (1x1.25)  
Dev - Ref, Dynamic Range



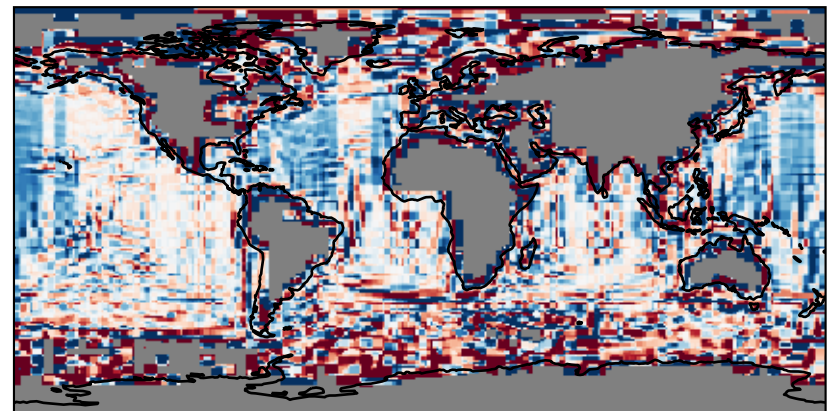
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

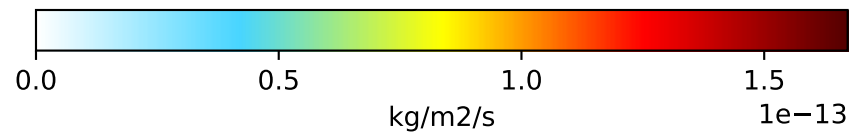
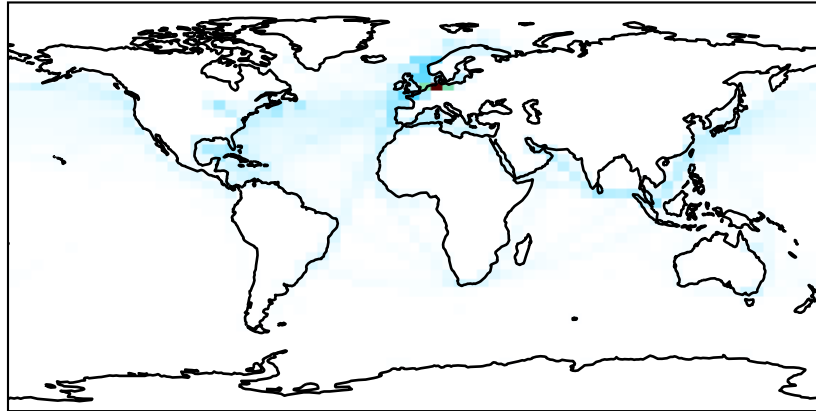


Ratio (1x1.25)  
Dev/Ref, Fixed Range

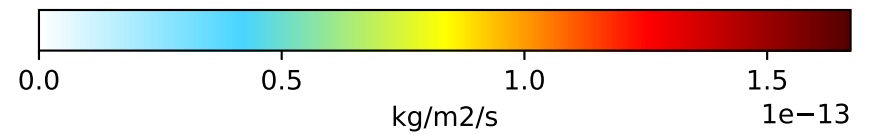
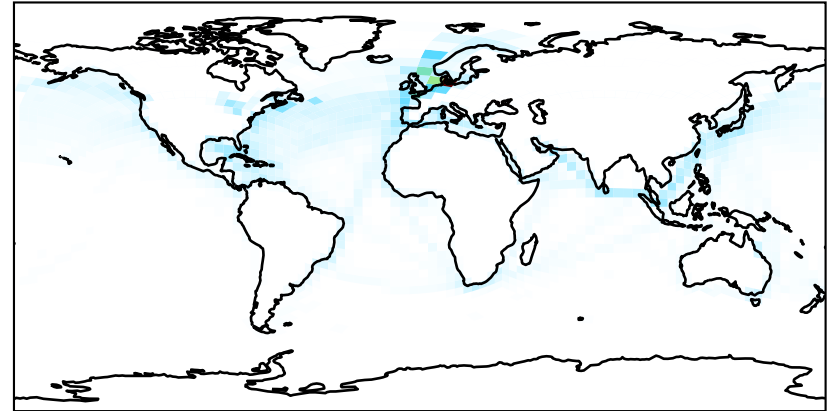


# EmisOCPI\_Ship (Apr2019)

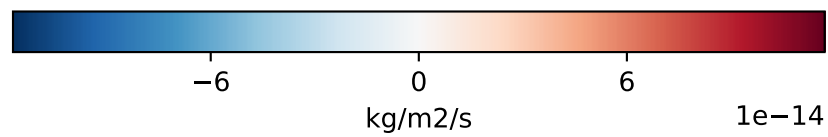
GCC\_14.0.0 (Ref)  
4.0x5.0



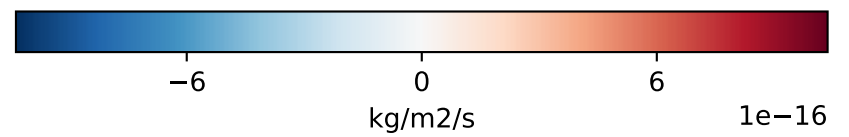
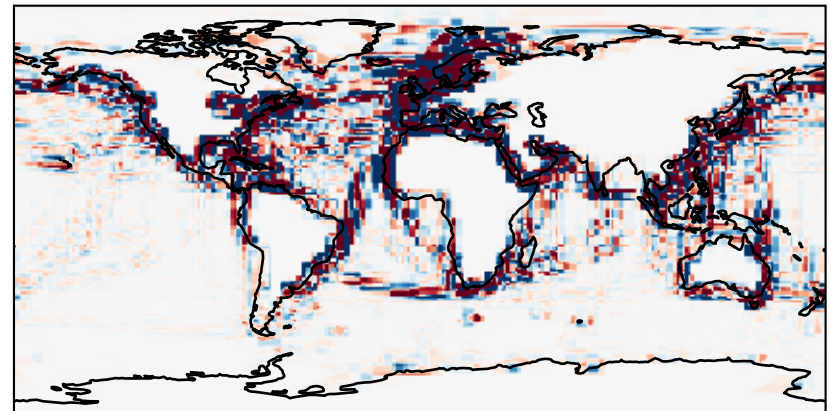
GCHP\_14.0.0 (Dev)  
c24



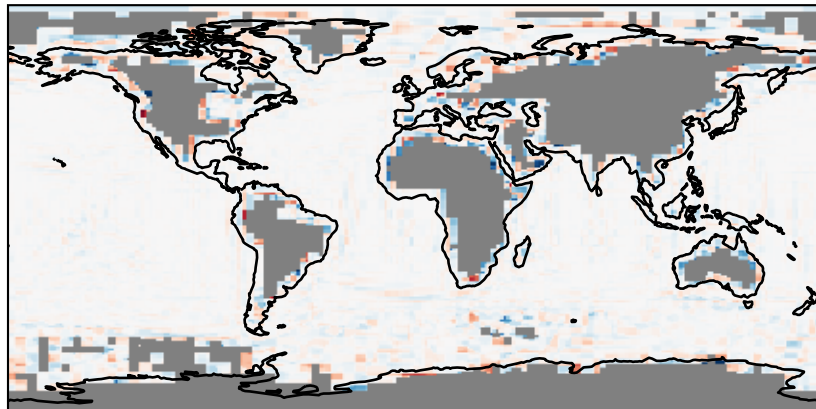
Difference (1x1.25)  
Dev - Ref, Dynamic Range



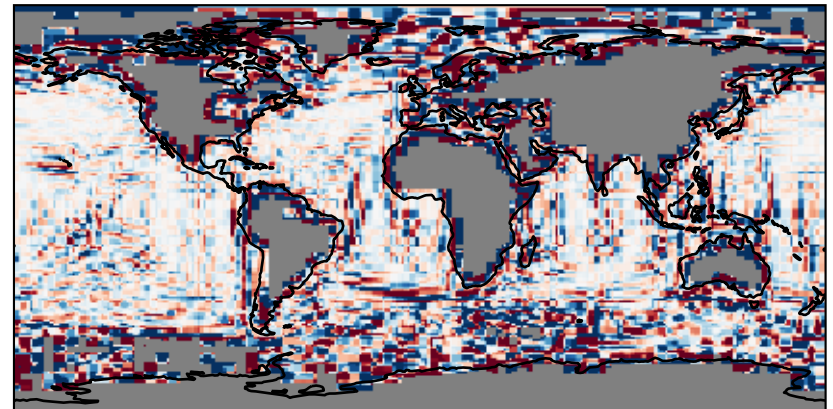
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



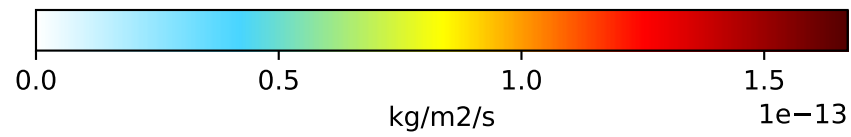
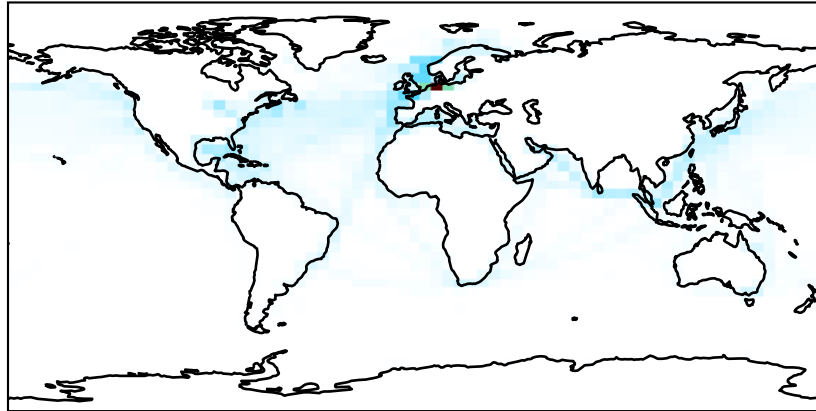
Ratio (1x1.25)  
Dev/Ref, Fixed Range



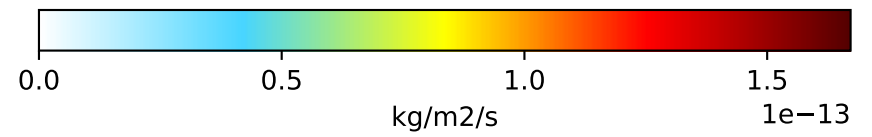
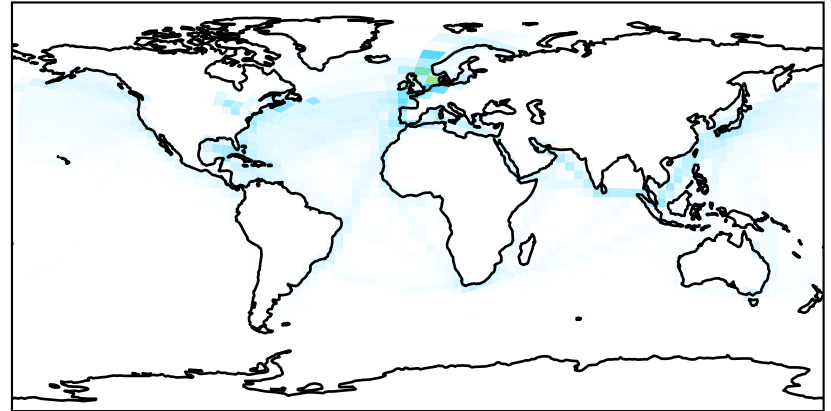


# EmisOCPO\_Ship (Apr2019)

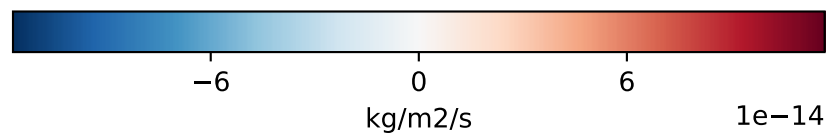
GCC\_14.0.0 (Ref)  
4.0x5.0



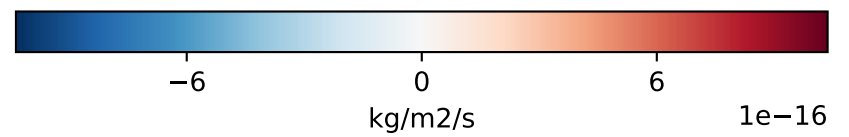
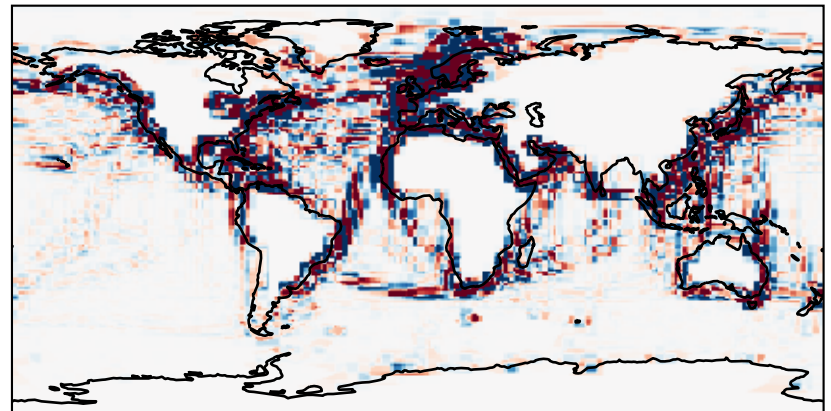
GCHP\_14.0.0 (Dev)  
c24



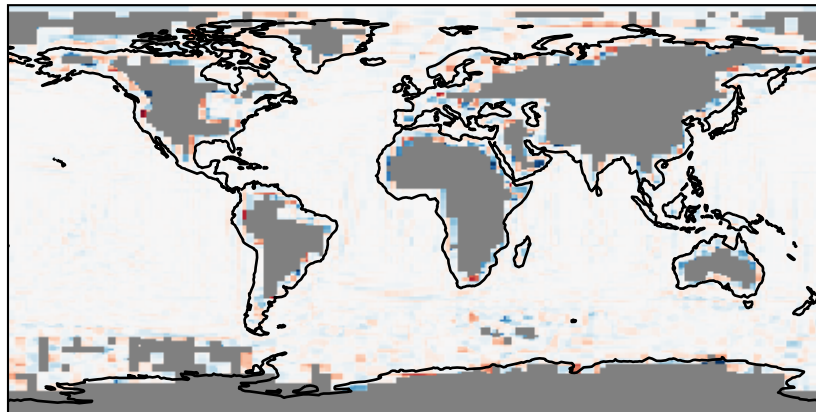
Difference (1x1.25)  
Dev - Ref, Dynamic Range



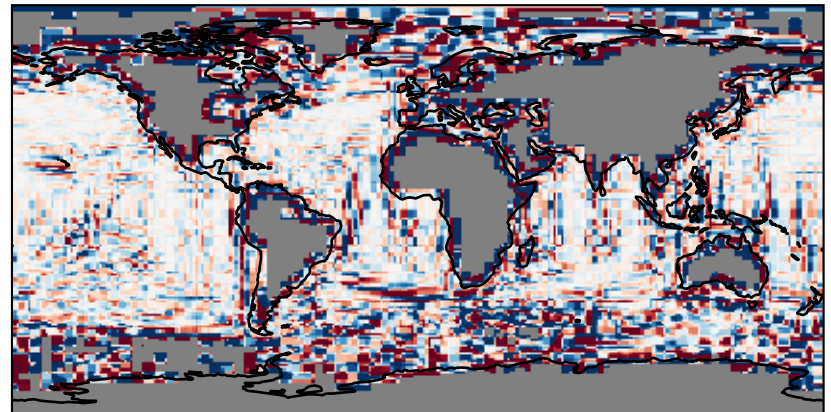
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

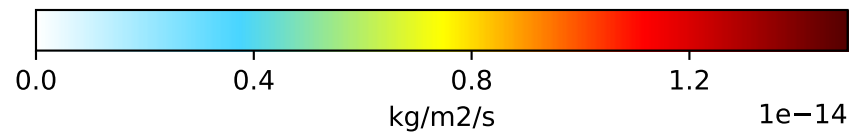
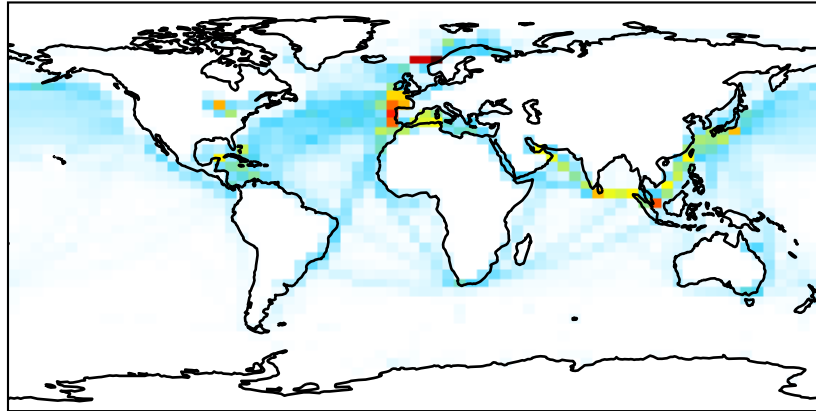


Ratio (1x1.25)  
Dev/Ref, Fixed Range

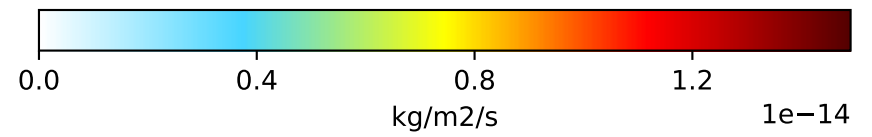
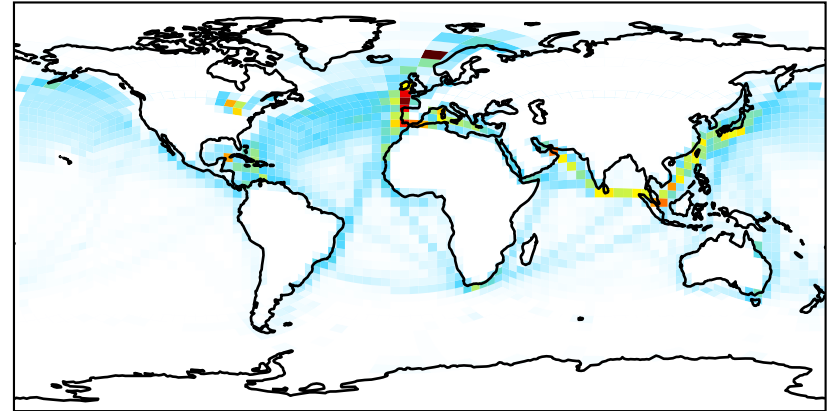


# EmispFe\_Ship (Apr2019)

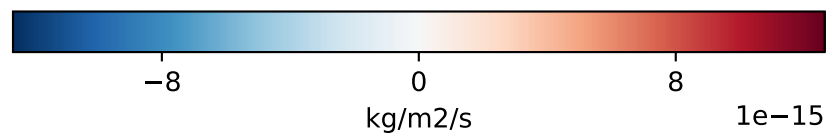
GCC\_14.0.0 (Ref)  
4.0x5.0



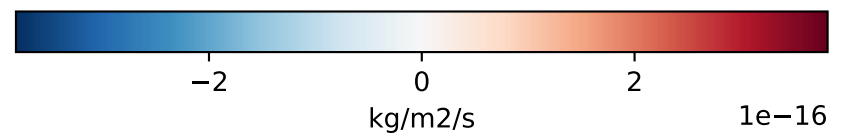
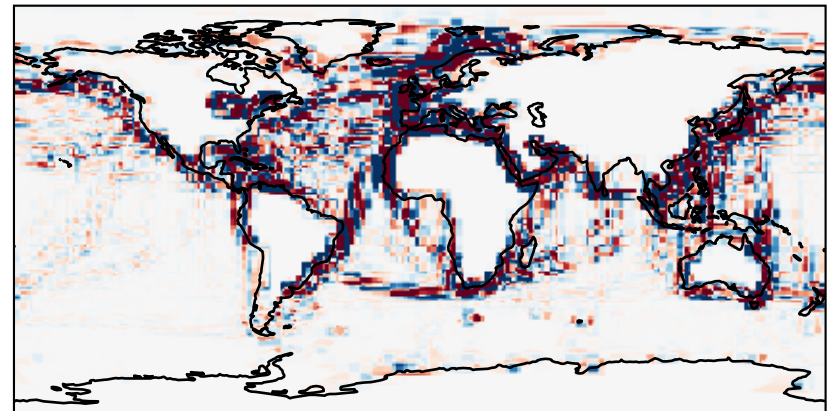
GCHP\_14.0.0 (Dev)  
c24



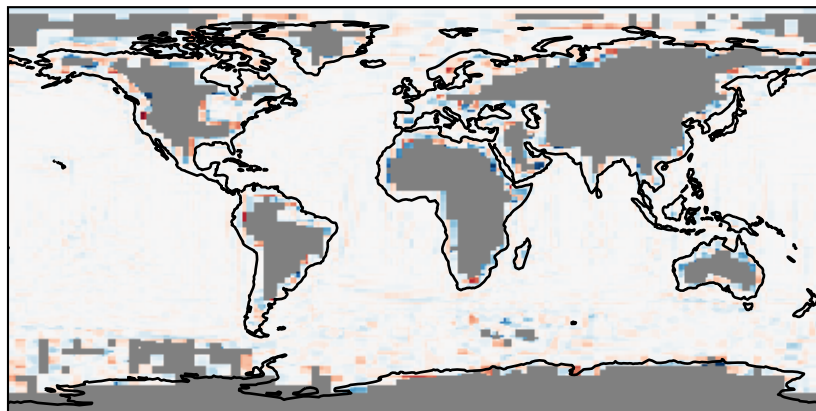
Difference (1x1.25)  
Dev - Ref, Dynamic Range



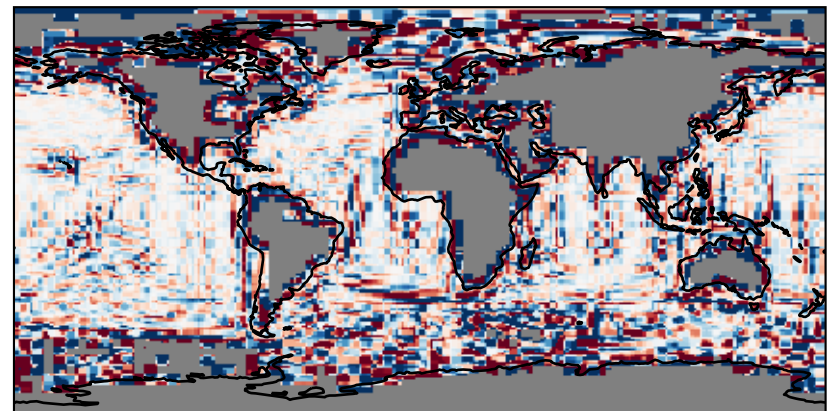
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



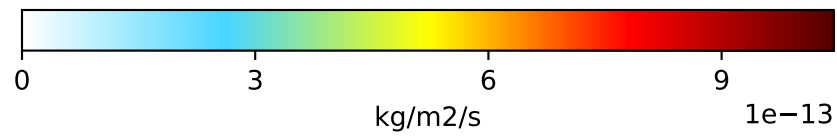
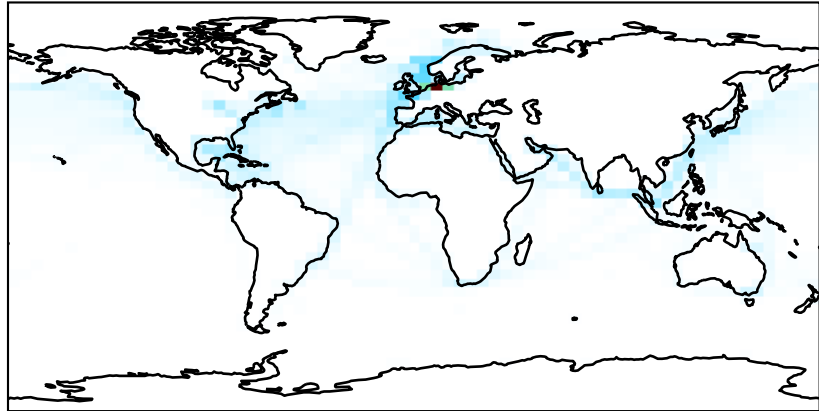
Ratio (1x1.25)  
Dev/Ref, Fixed Range



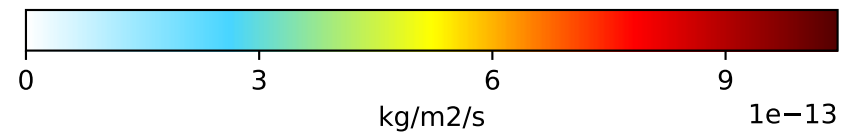
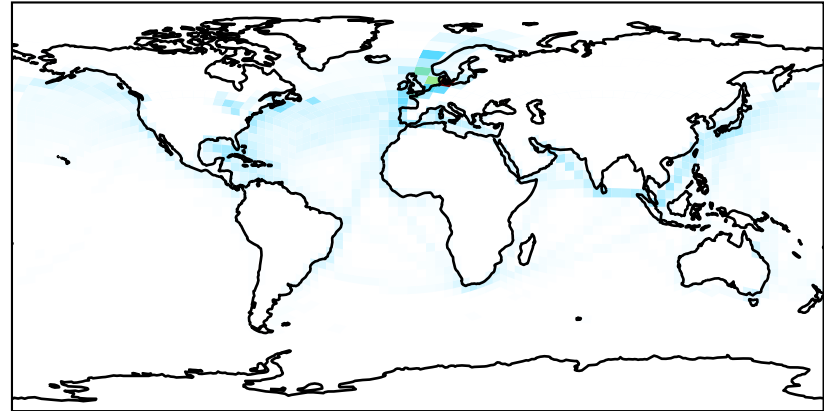


# EmisPRPE\_Ship (Apr2019)

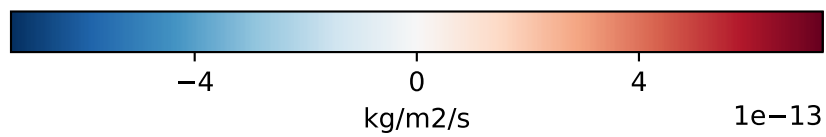
GCC\_14.0.0 (Ref)  
4.0x5.0



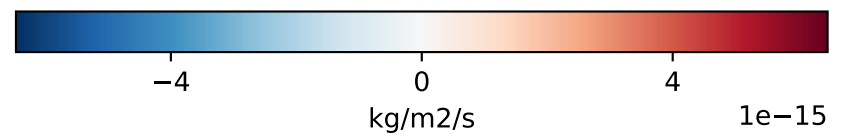
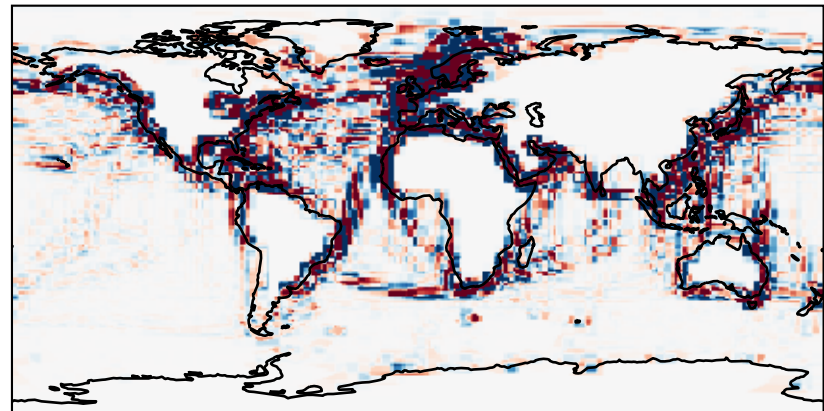
GCHP\_14.0.0 (Dev)  
c24



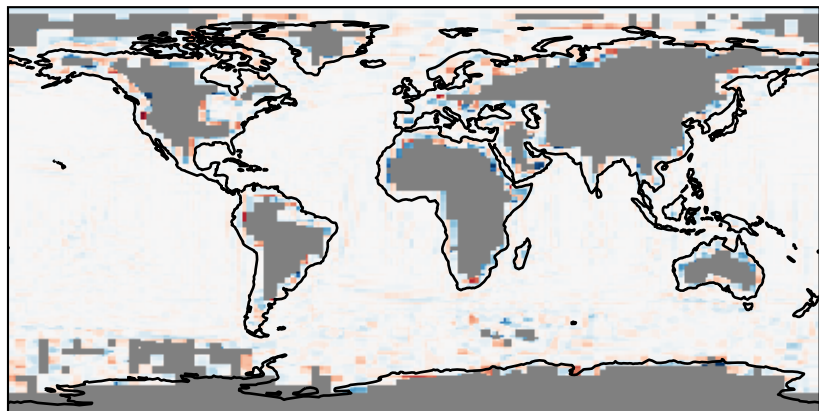
Difference (1x1.25)  
Dev - Ref, Dynamic Range



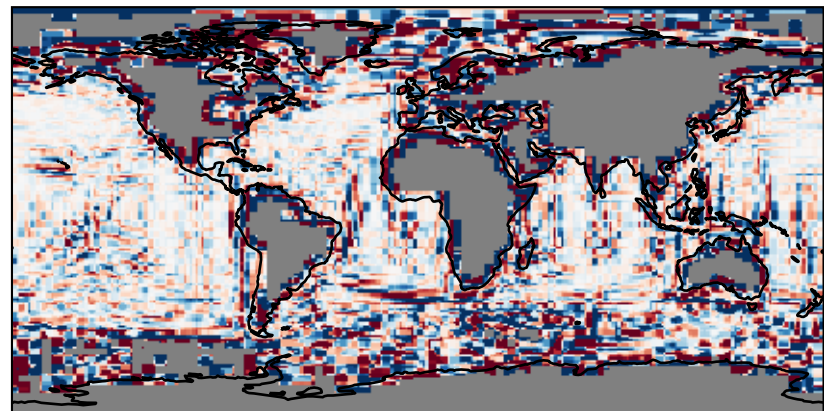
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

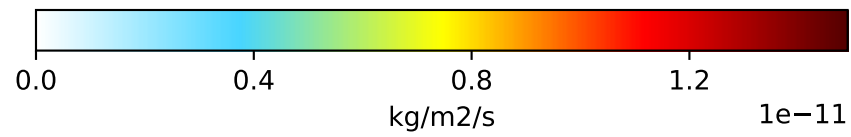
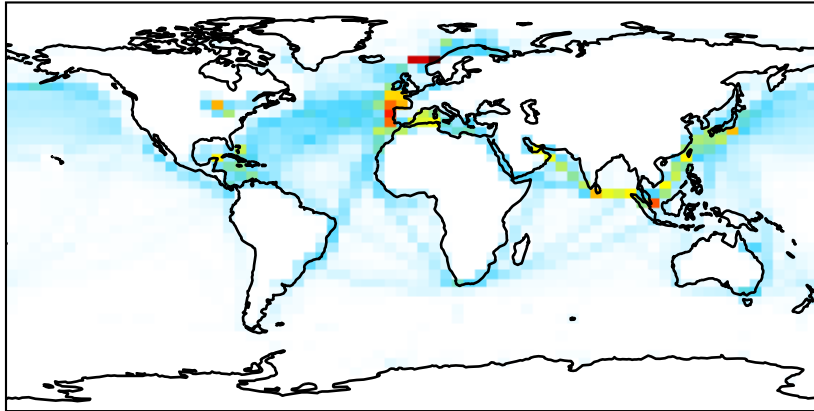


Ratio (1x1.25)  
Dev/Ref, Fixed Range

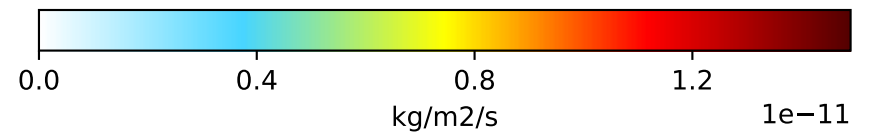
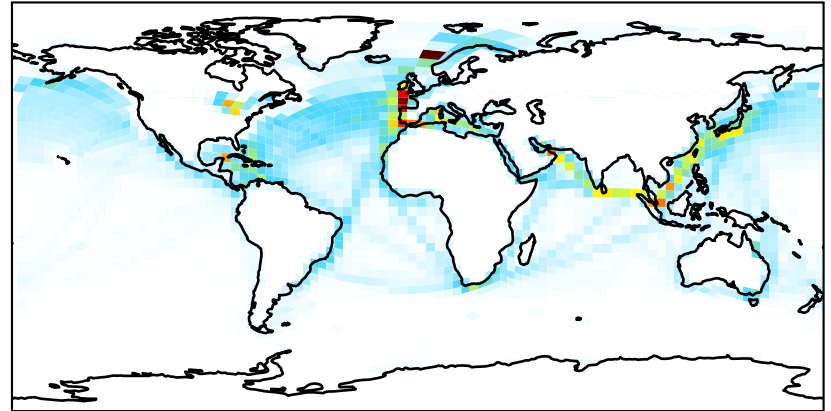


# EmisSO2\_Ship (Apr2019)

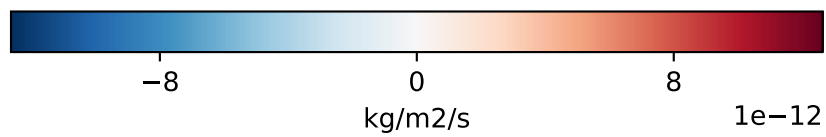
GCC\_14.0.0 (Ref)  
4.0x5.0



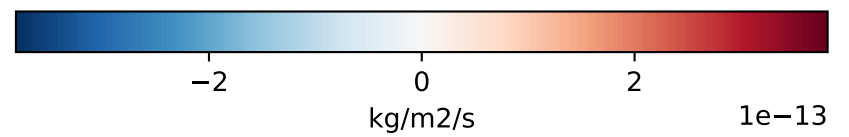
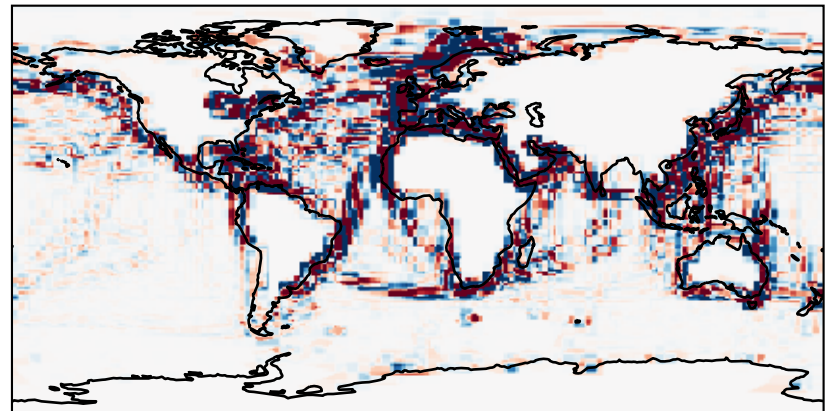
GCHP\_14.0.0 (Dev)  
c24



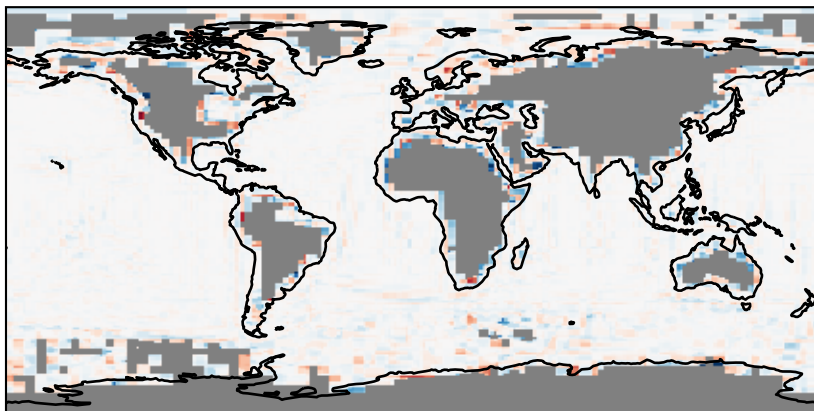
Difference (1x1.25)  
Dev - Ref, Dynamic Range



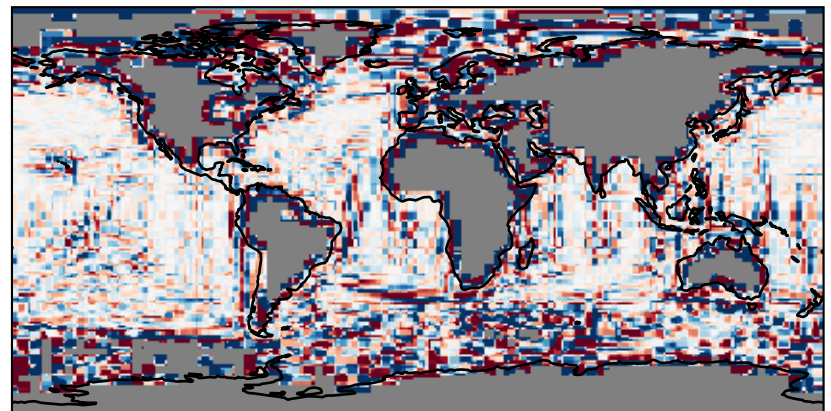
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



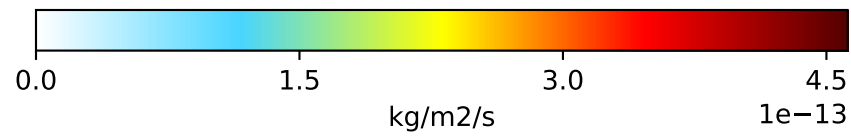
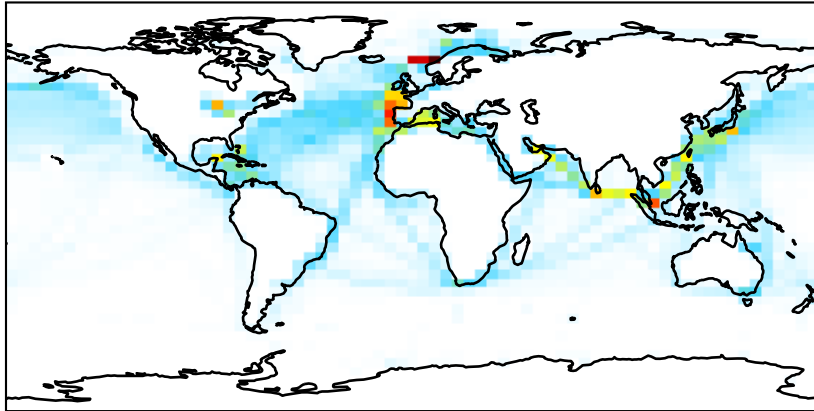
Ratio (1x1.25)  
Dev/Ref, Fixed Range



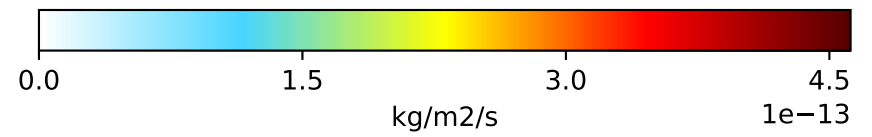
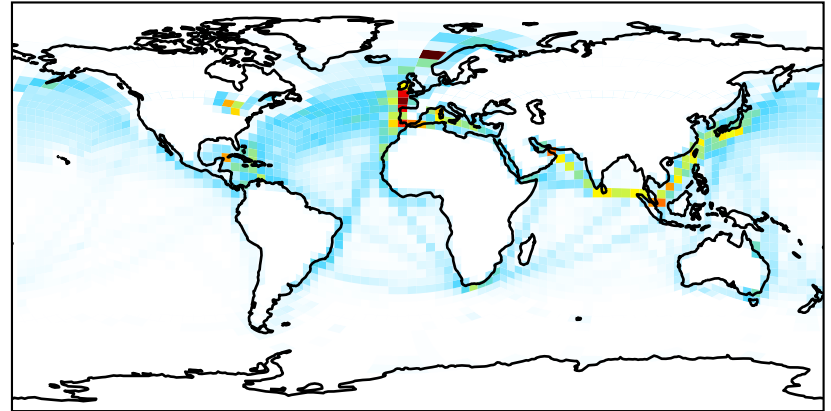


# EmisSO4\_Ship (Apr2019)

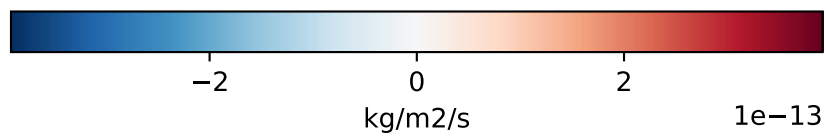
GCC\_14.0.0 (Ref)  
4.0x5.0



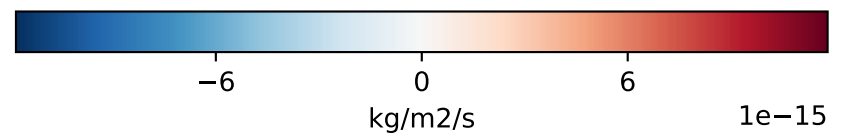
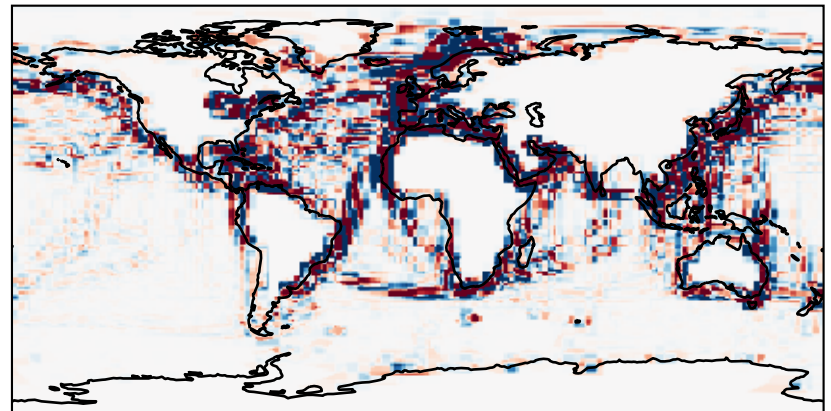
GCHP\_14.0.0 (Dev)  
c24



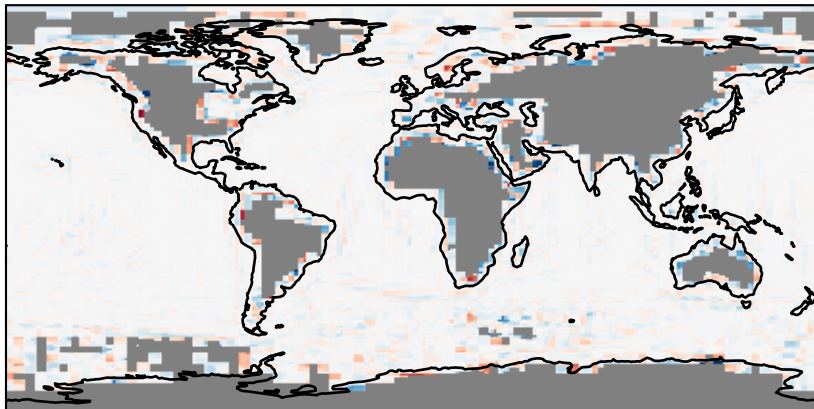
Difference (1x1.25)  
Dev - Ref, Dynamic Range



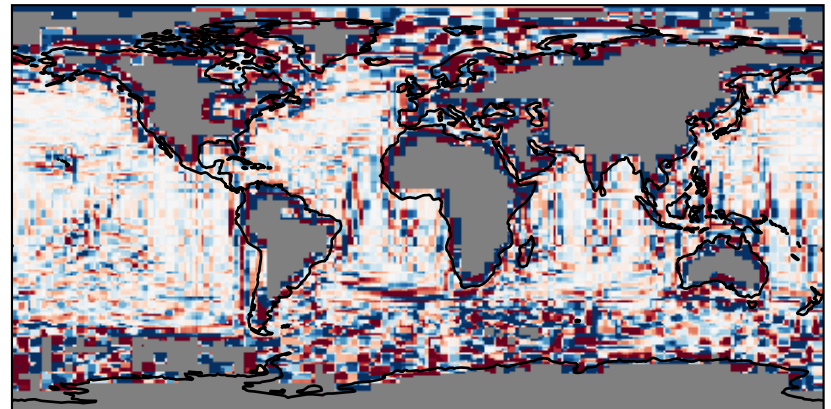
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



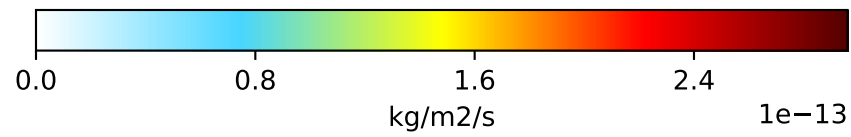
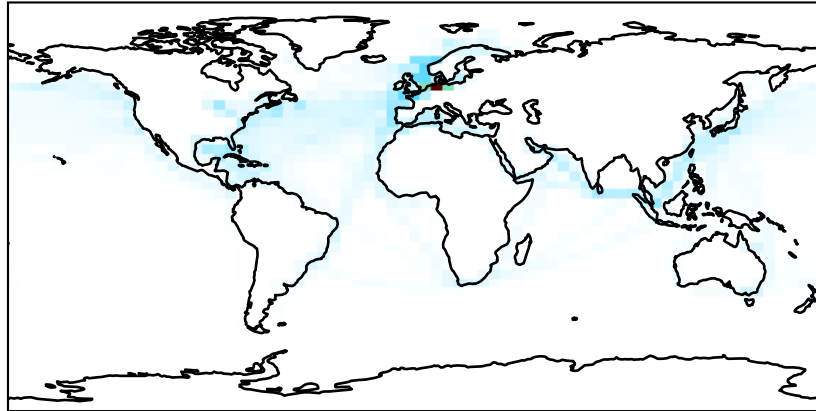
Ratio (1x1.25)  
Dev/Ref, Fixed Range



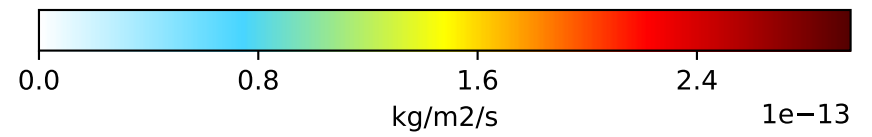
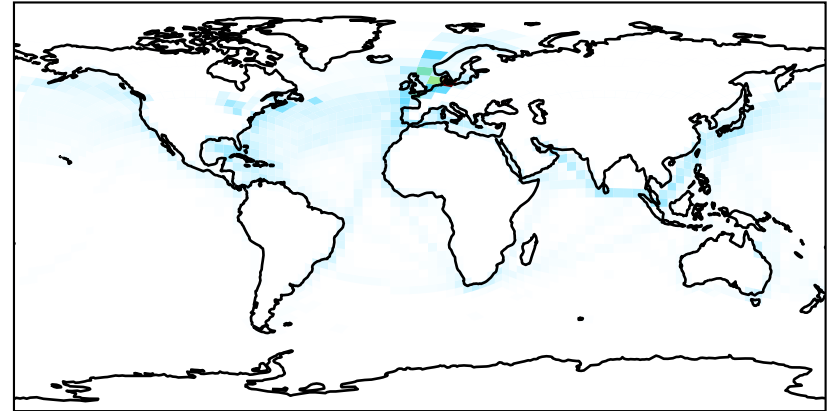


# EmisSOAP\_Ship (Apr2019)

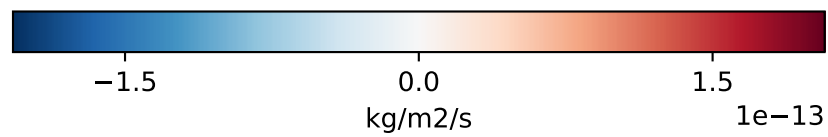
GCC\_14.0.0 (Ref)  
4.0x5.0



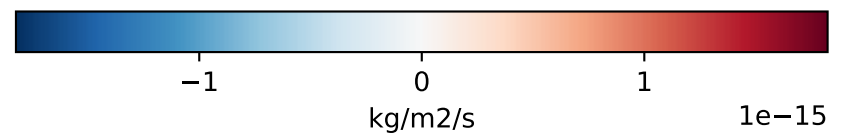
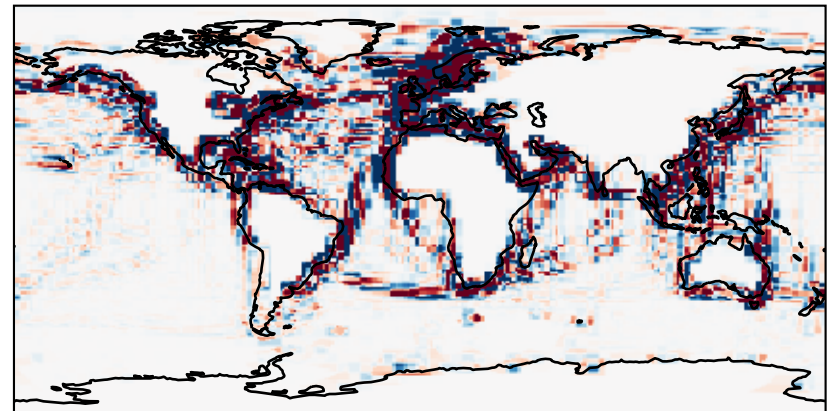
GCHP\_14.0.0 (Dev)  
c24



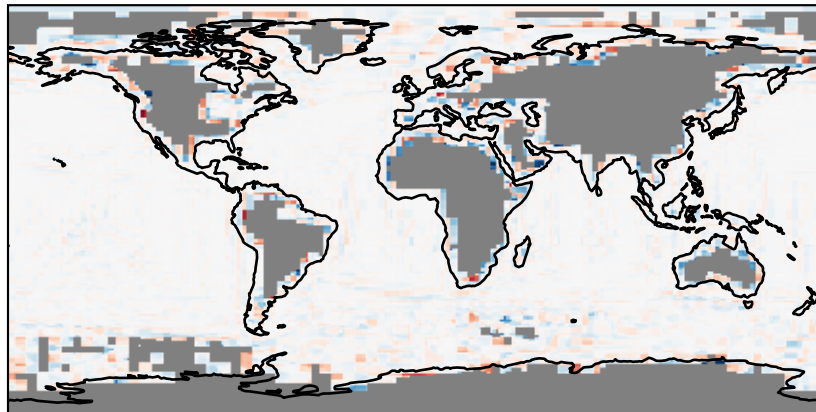
Difference (1x1.25)  
Dev - Ref, Dynamic Range



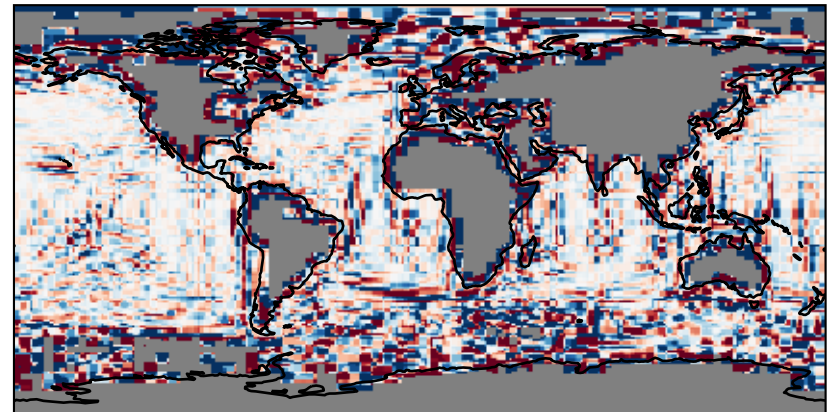
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

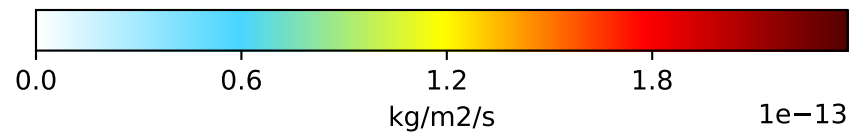
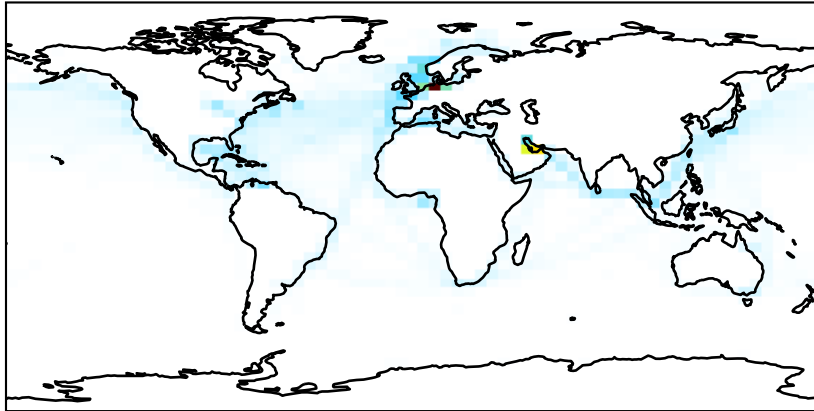


Ratio (1x1.25)  
Dev/Ref, Fixed Range

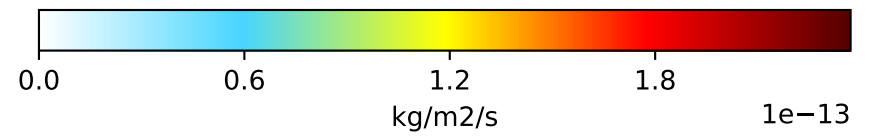
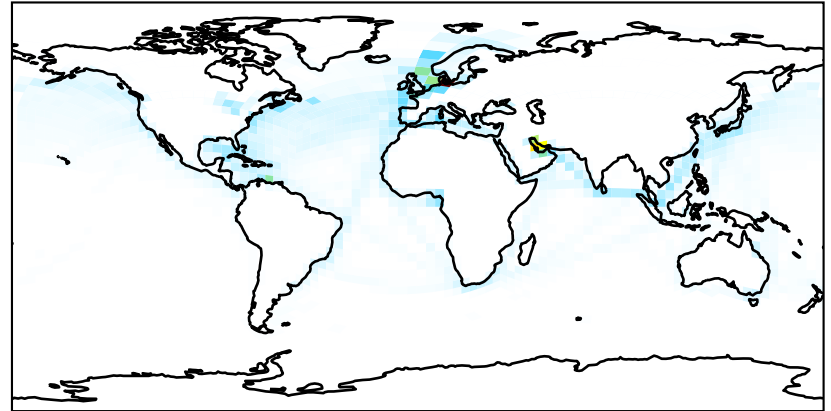


# EmisTOLU\_Ship (Apr2019)

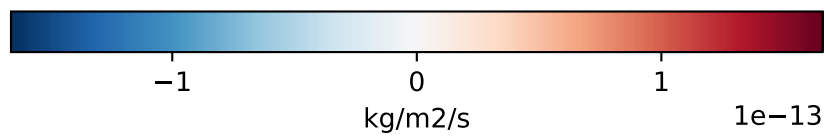
GCC\_14.0.0 (Ref)  
4.0x5.0



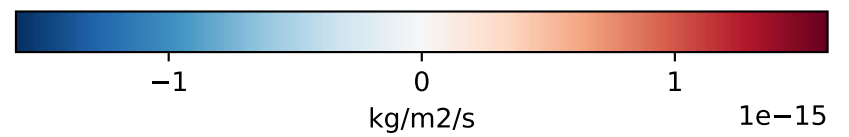
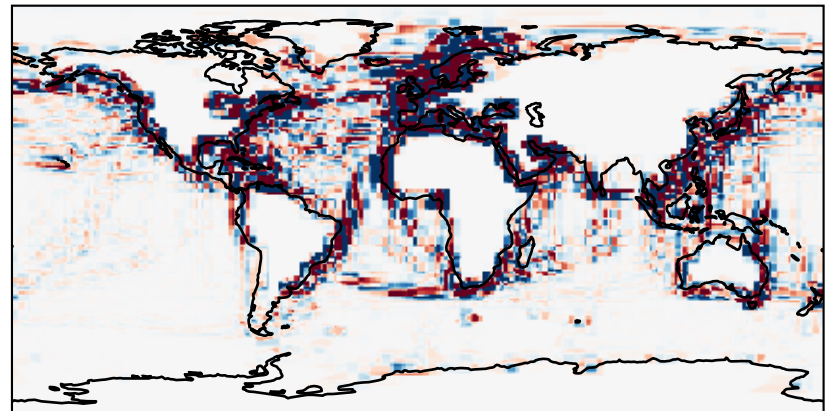
GCHP\_14.0.0 (Dev)  
c24



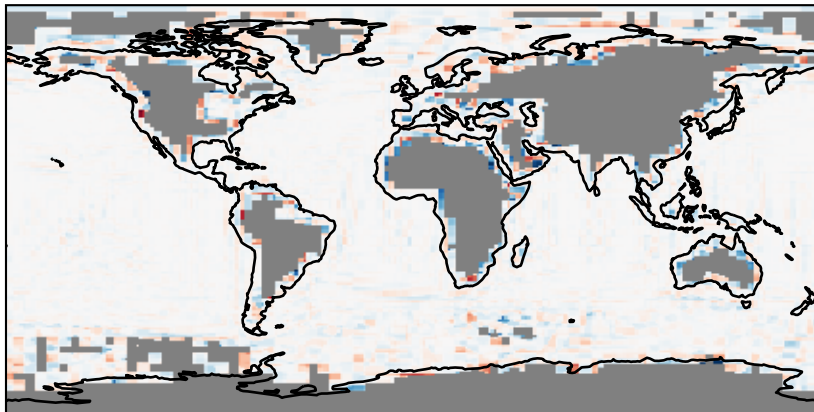
Difference (1x1.25)  
Dev - Ref, Dynamic Range



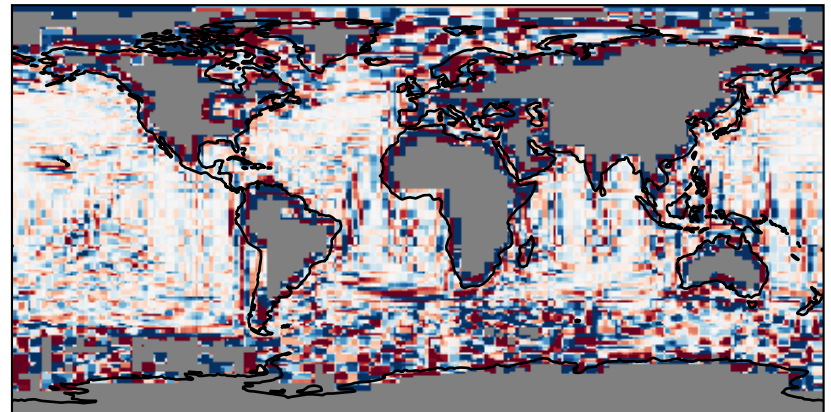
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



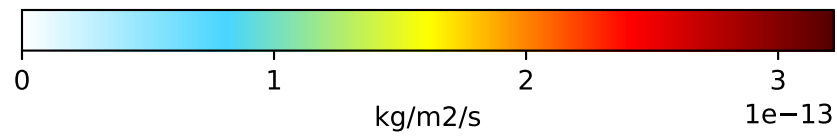
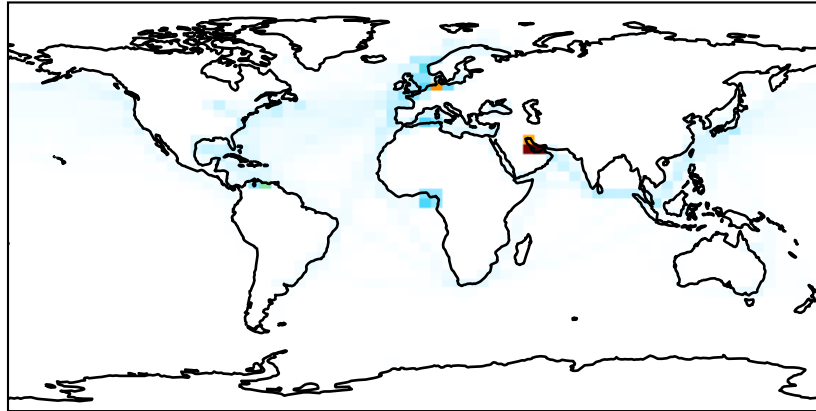
Ratio (1x1.25)  
Dev/Ref, Fixed Range



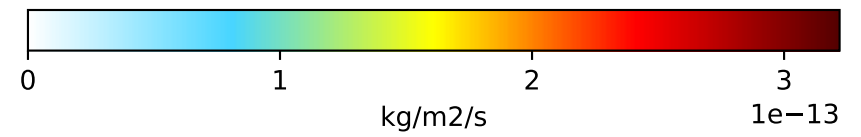
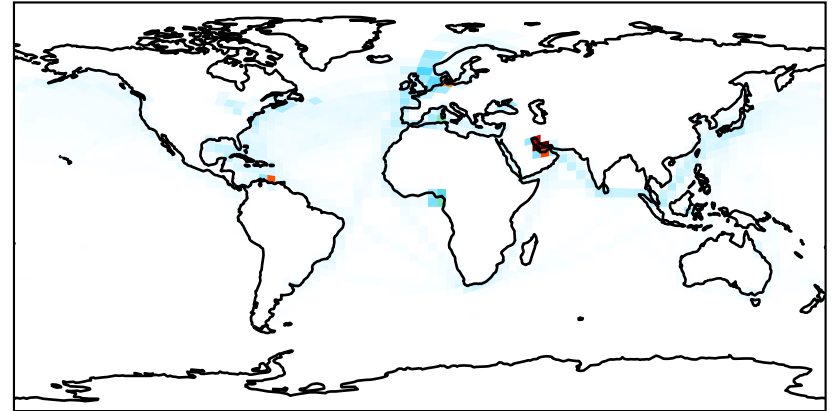


# EmisXYLE\_Ship (Apr2019)

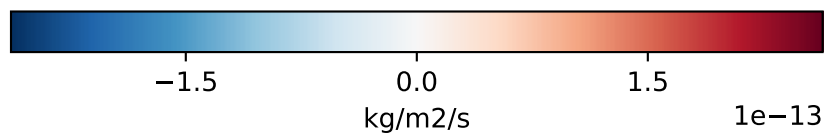
GCC\_14.0.0 (Ref)  
4.0x5.0



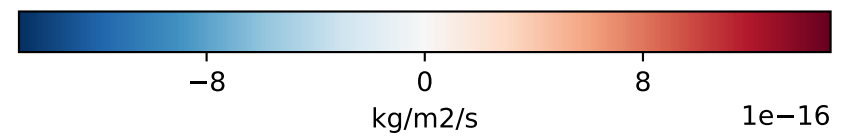
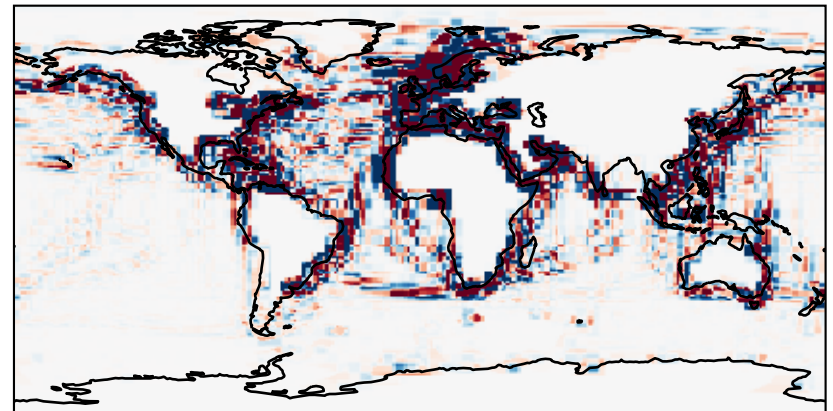
GCHP\_14.0.0 (Dev)  
c24



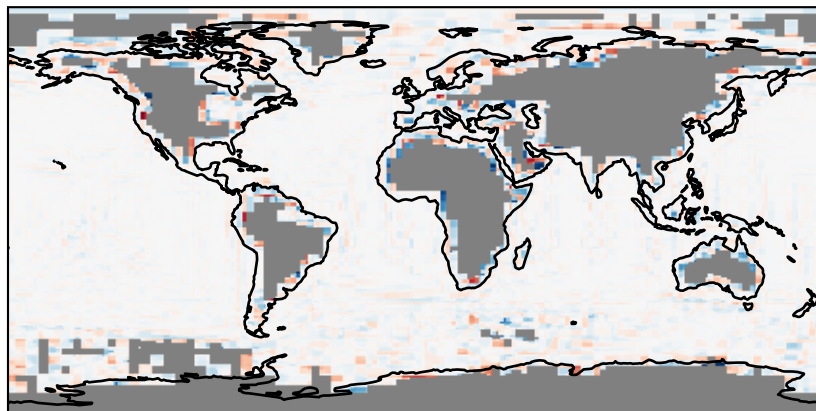
Difference (1x1.25)  
Dev - Ref, Dynamic Range



Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Ratio (1x1.25)  
Dev/Ref, Fixed Range

