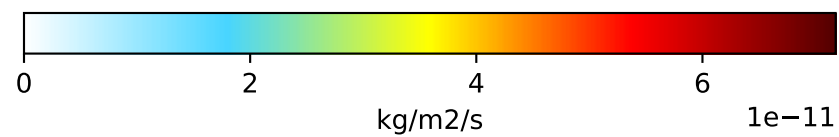
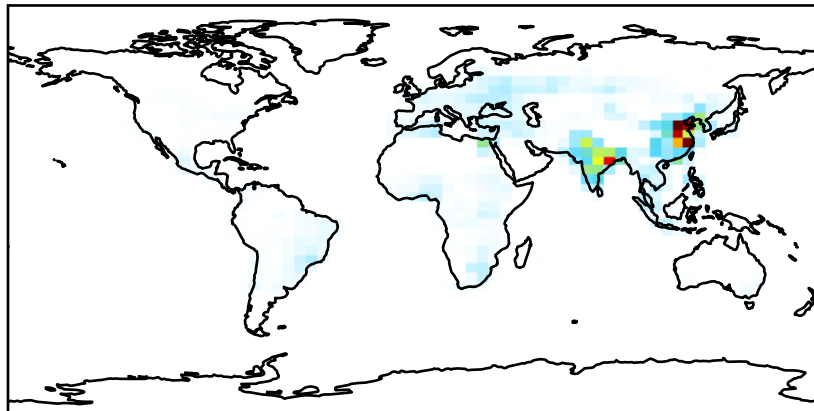
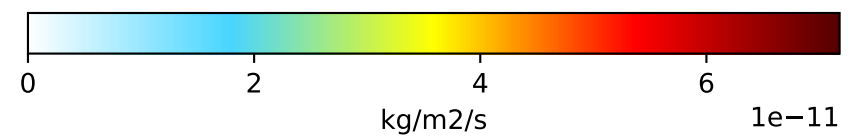
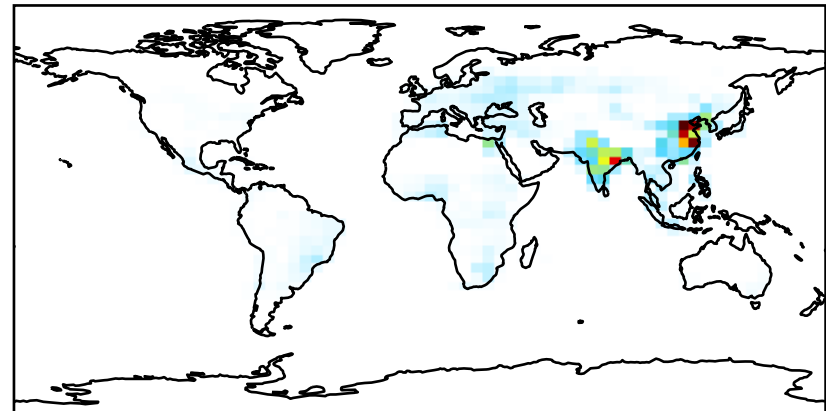


EmisDST1_Anthro

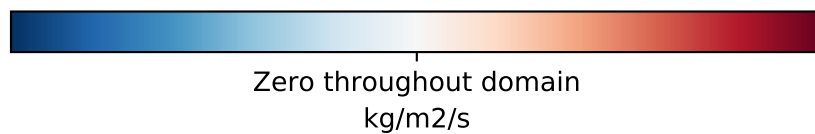
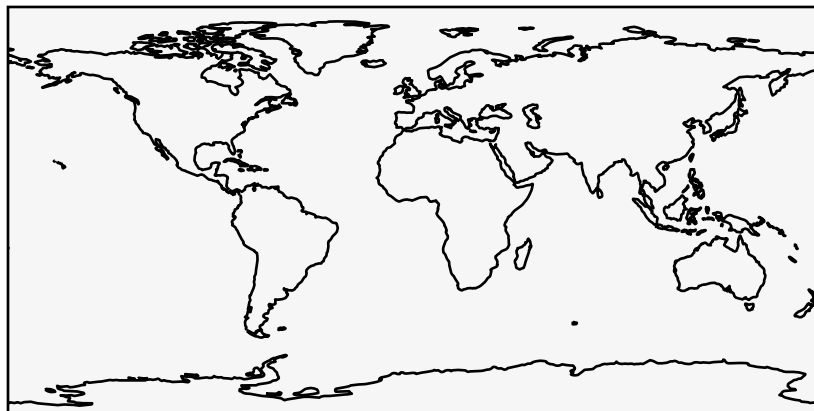
GCC_12.9.0 (Ref)
4.0x5.0



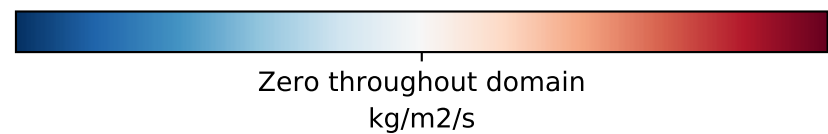
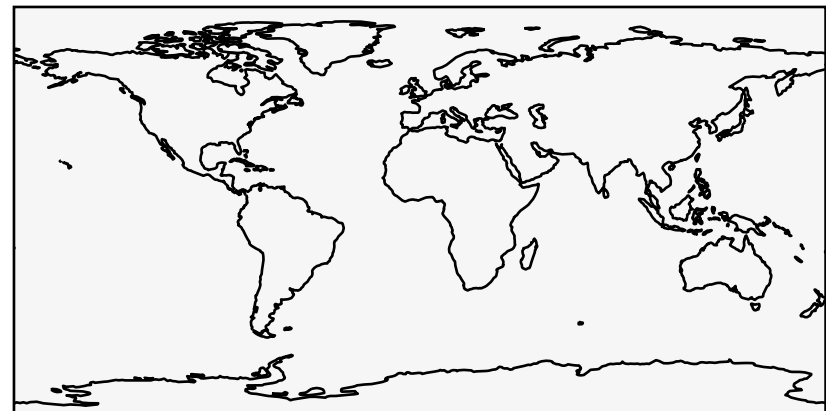
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



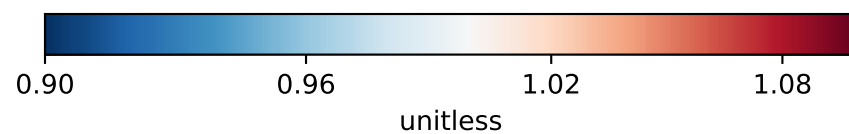
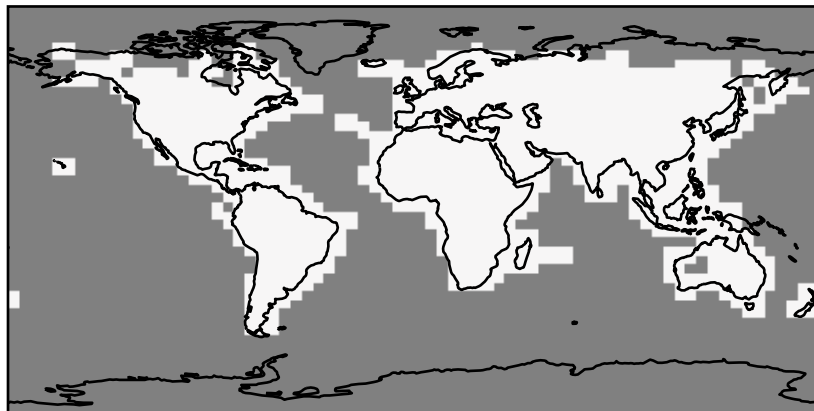
Difference
Dev - Ref, Dynamic Range



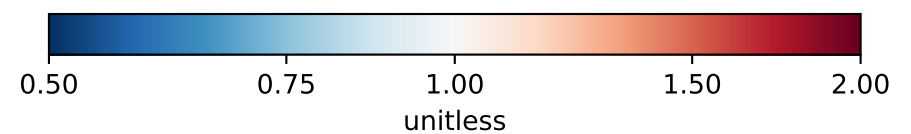
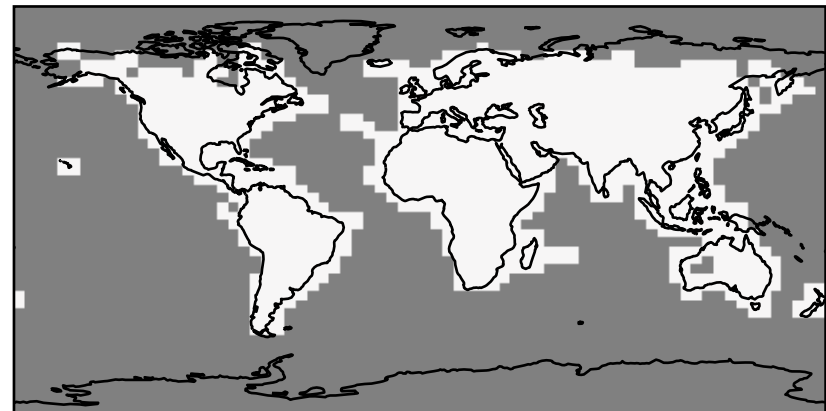
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

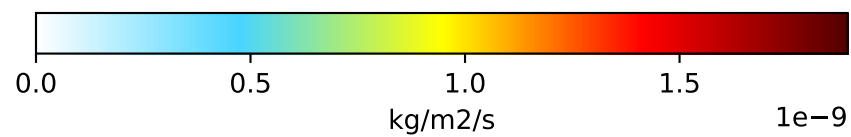
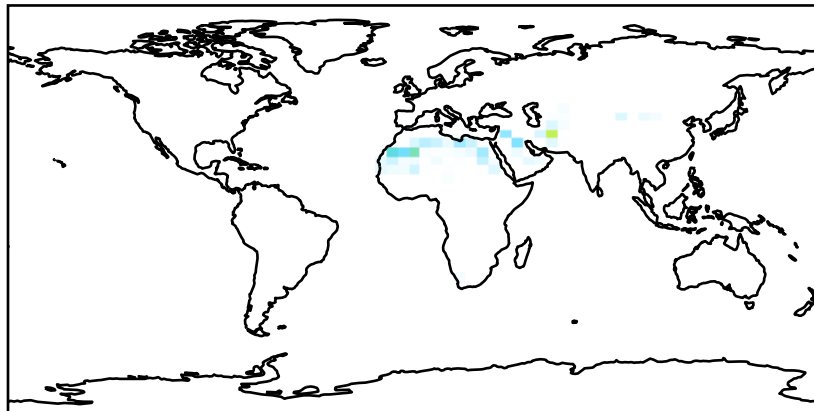


Ratio
Dev/Ref, Fixed Range

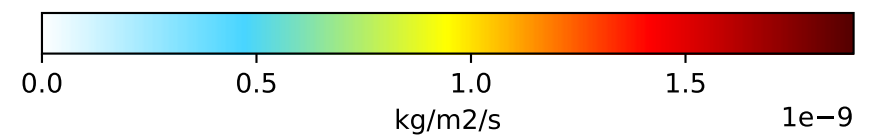
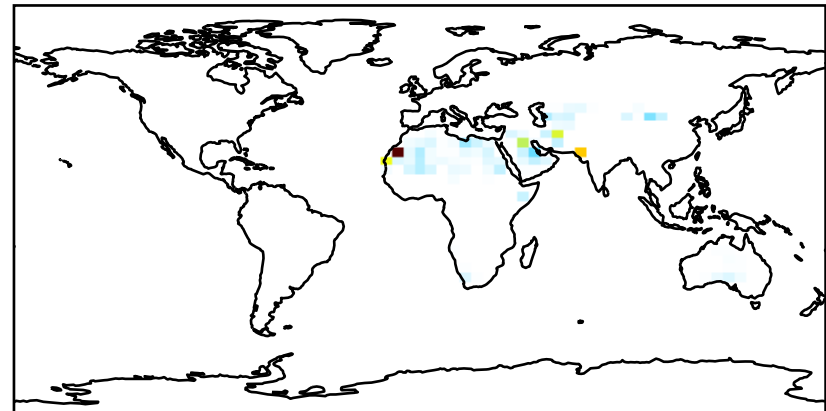


EmisDST1_Natural

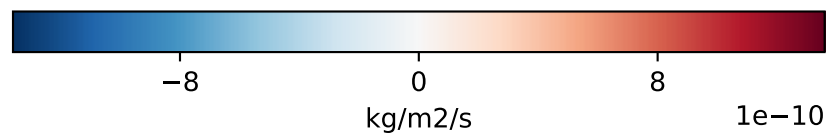
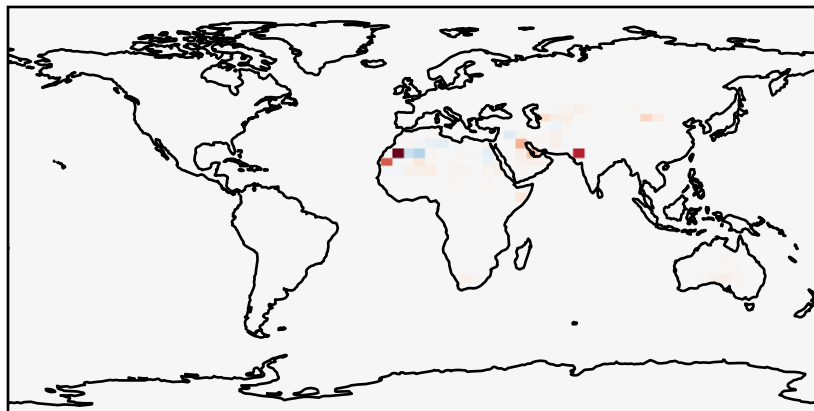
GCC_12.9.0 (Ref)
4.0x5.0



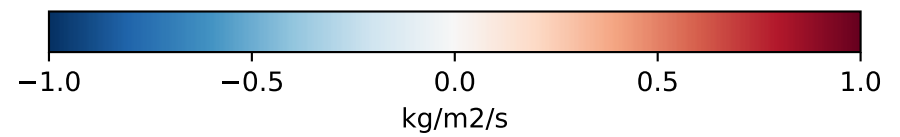
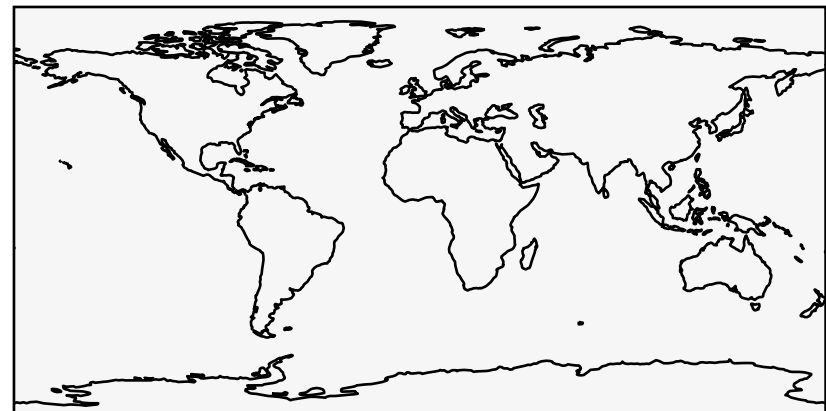
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



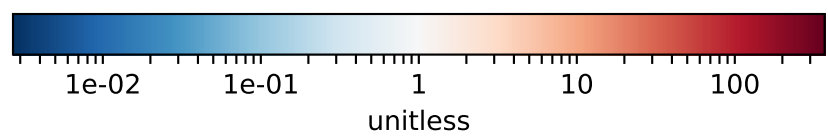
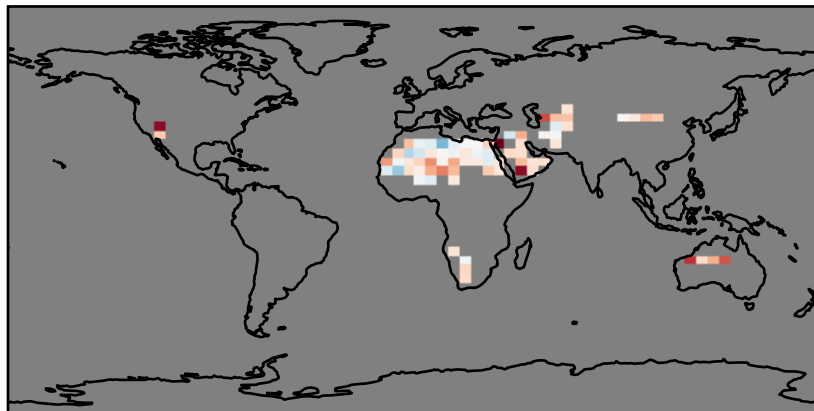
Difference
Dev - Ref, Dynamic Range



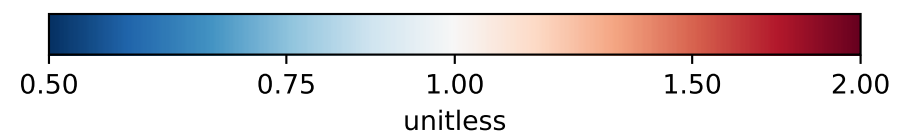
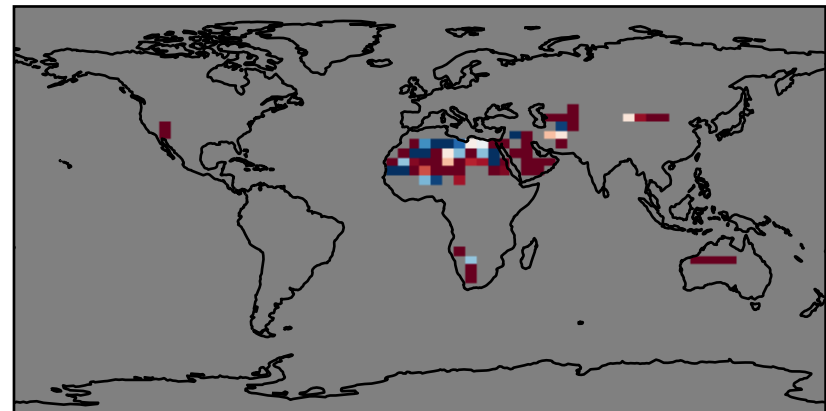
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

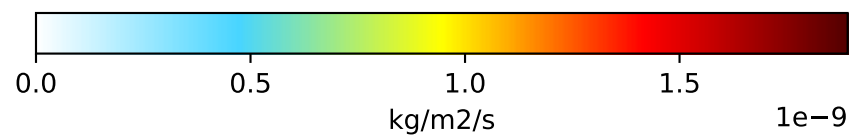
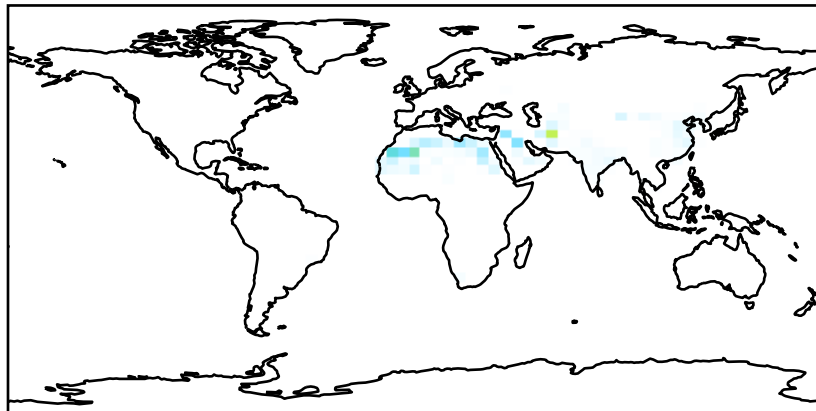


Ratio
Dev/Ref, Fixed Range

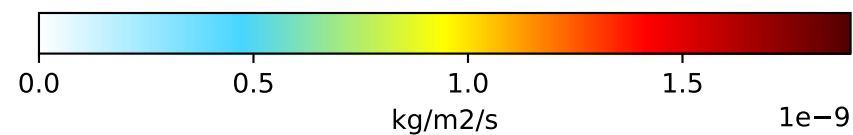
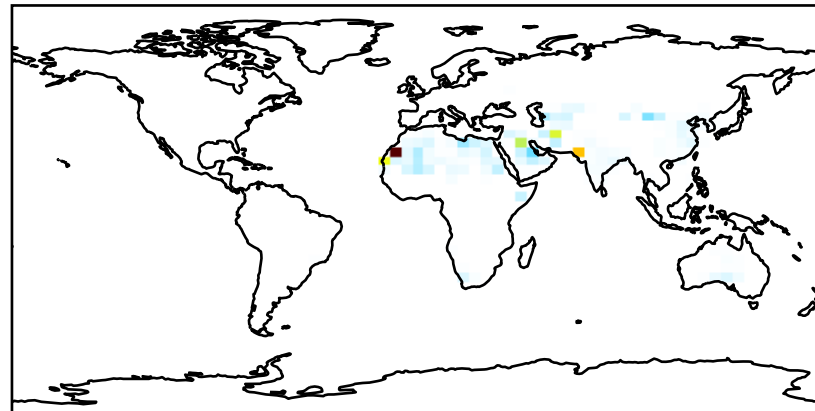


EmisDST1_Total

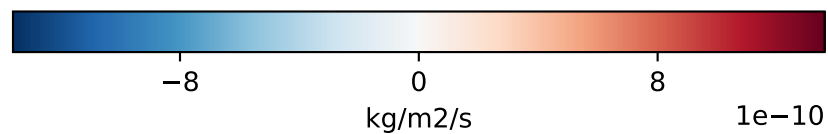
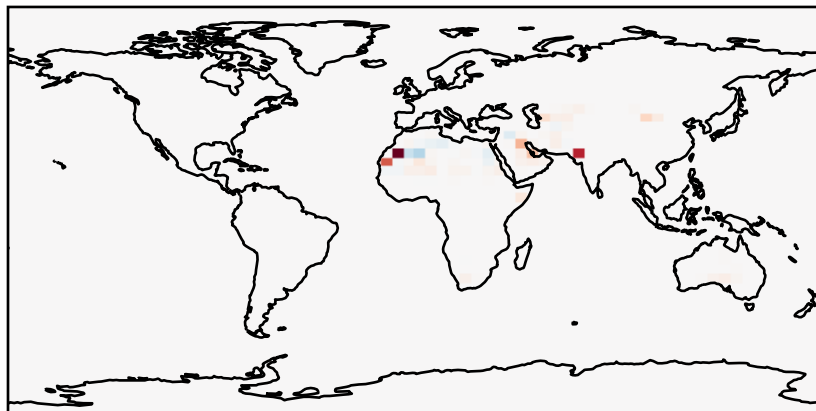
GCC_12.9.0 (Ref)
4.0x5.0



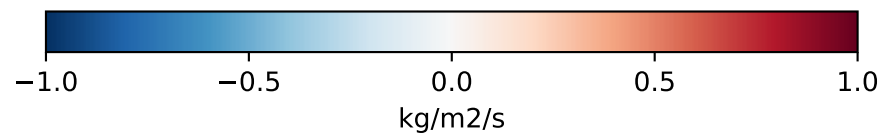
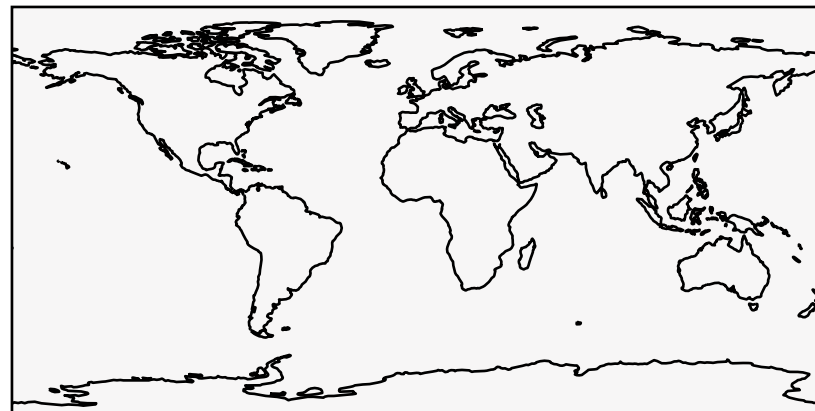
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



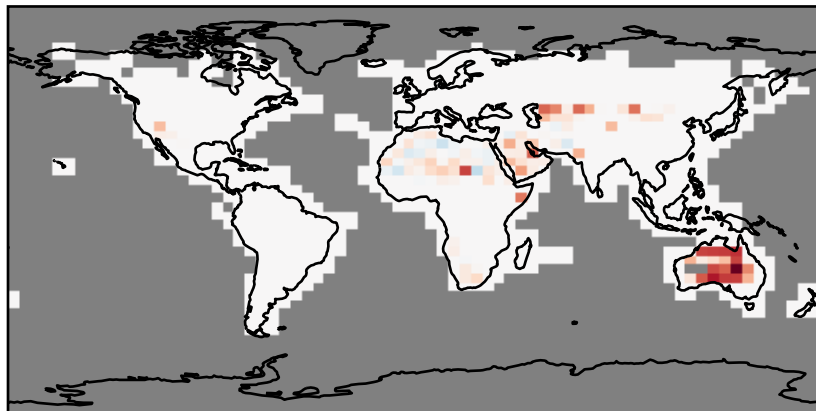
Difference
Dev - Ref, Dynamic Range



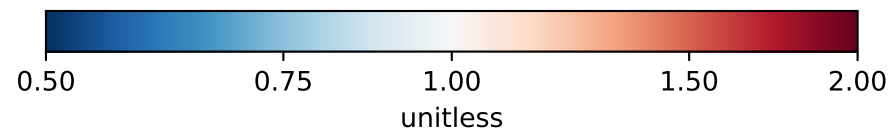
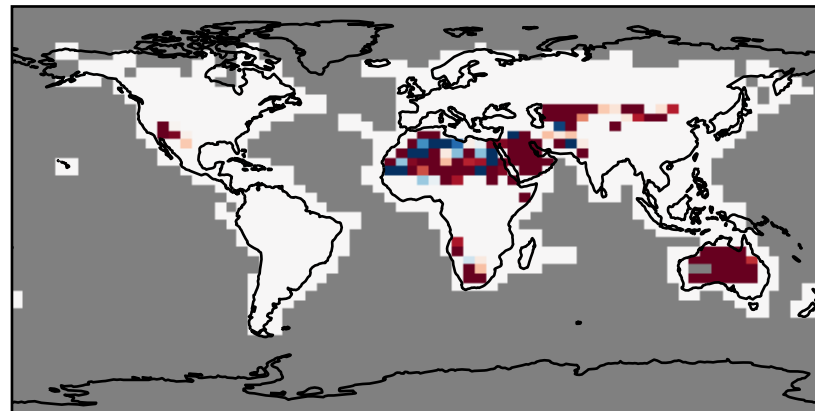
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

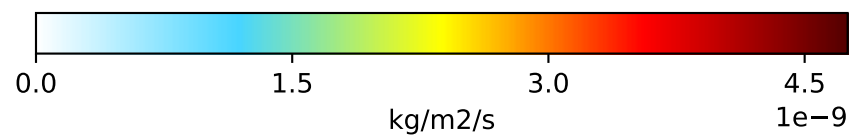
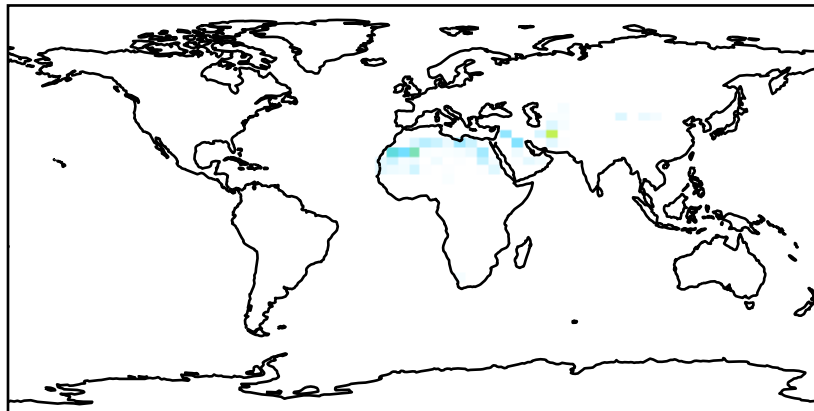


Ratio
Dev/Ref, Fixed Range

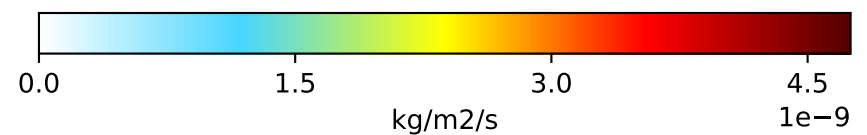
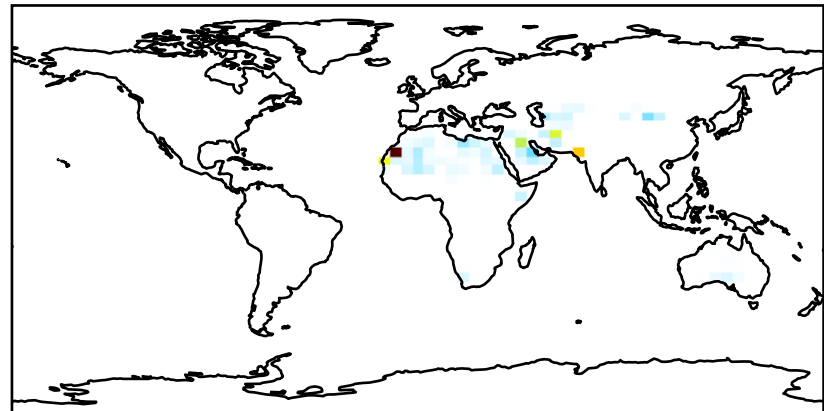


EmisDST2_Natural

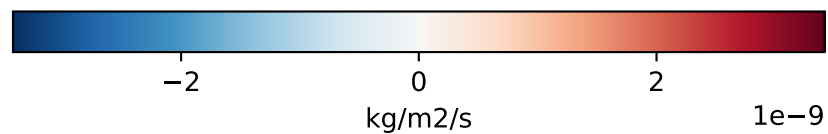
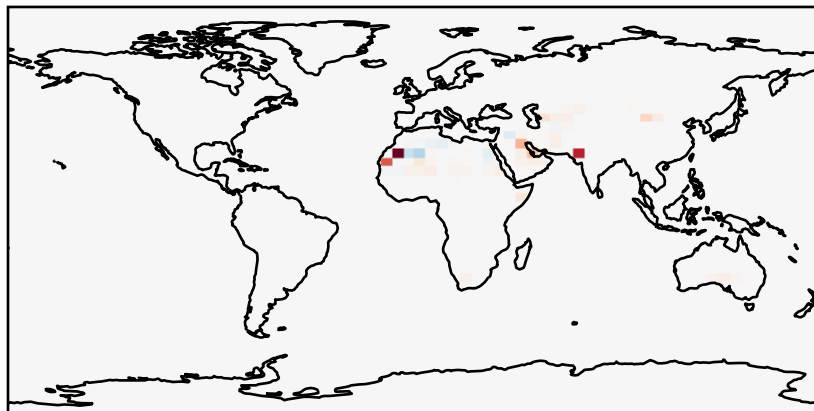
GCC_12.9.0 (Ref)
4.0x5.0



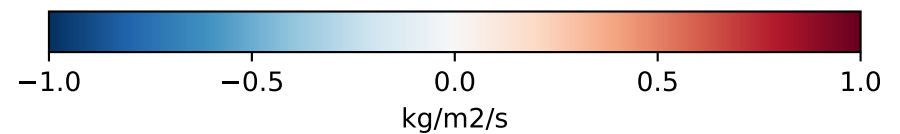
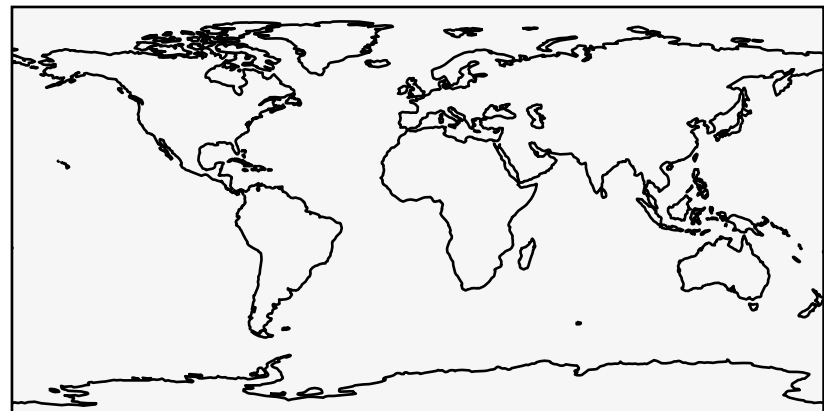
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



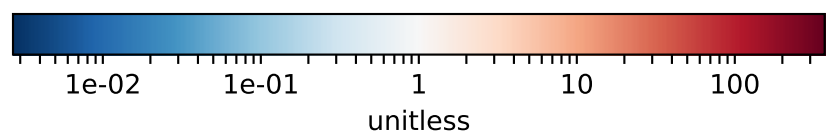
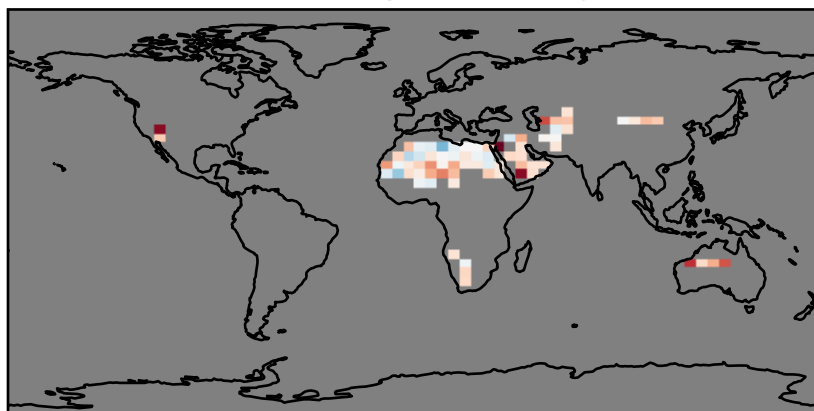
Difference
Dev - Ref, Dynamic Range



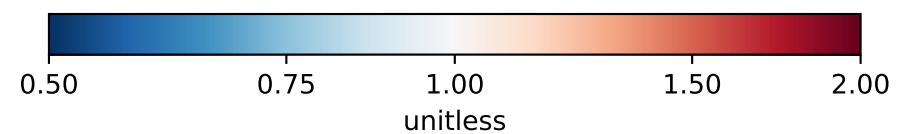
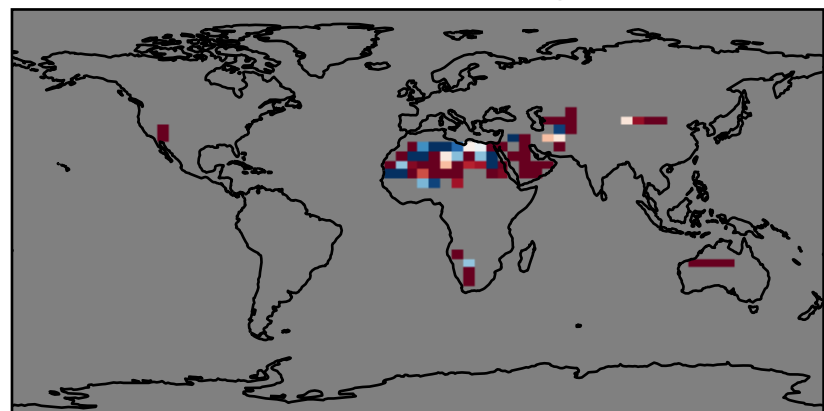
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

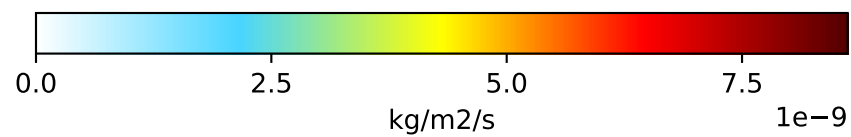
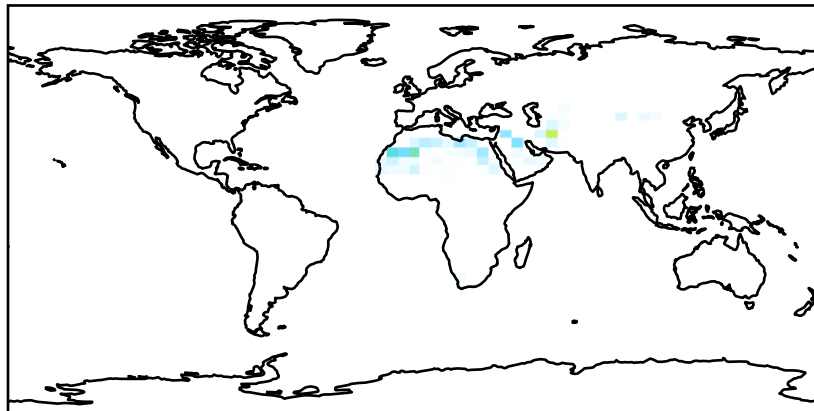


Ratio
Dev/Ref, Fixed Range

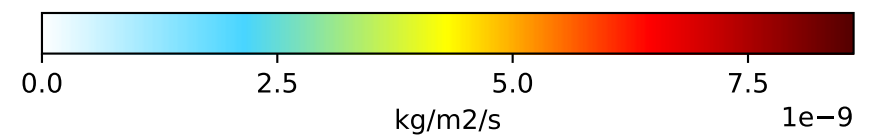
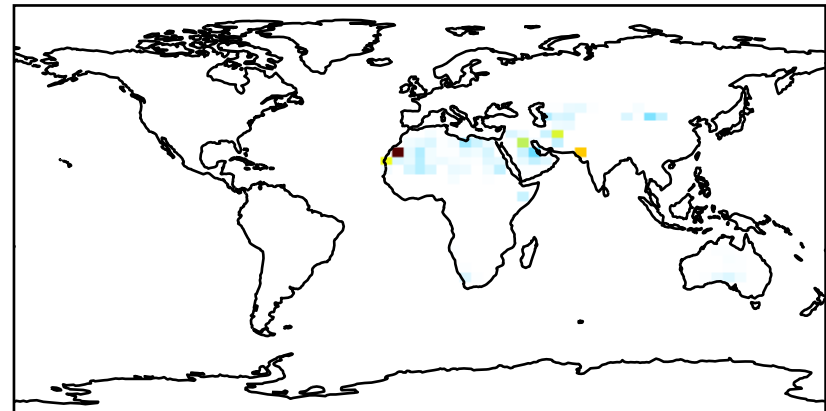


EmisDST3_Natural

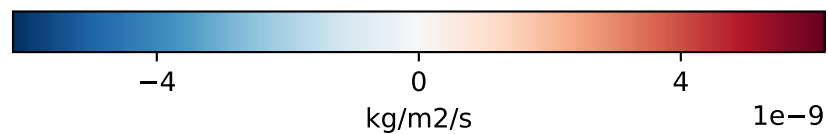
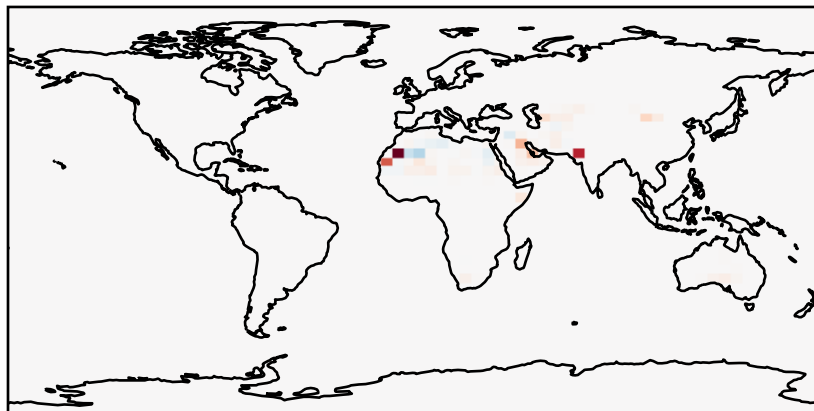
GCC_12.9.0 (Ref)
4.0x5.0



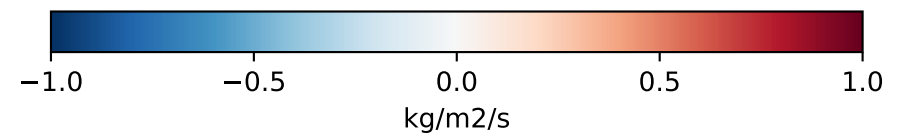
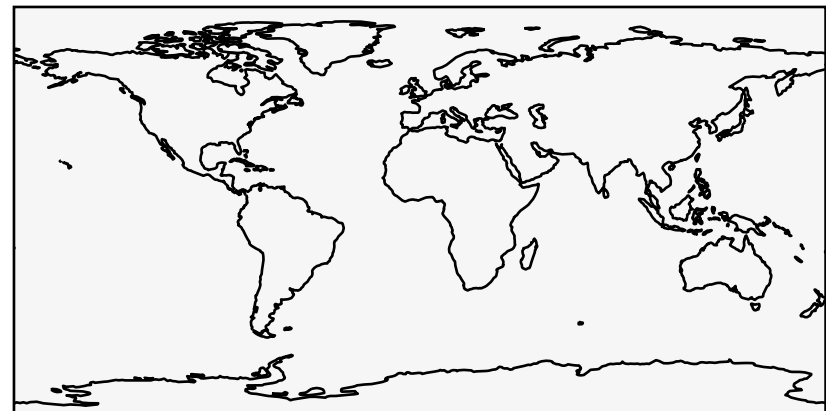
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



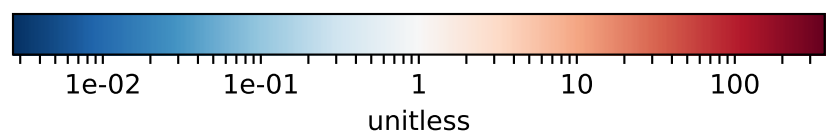
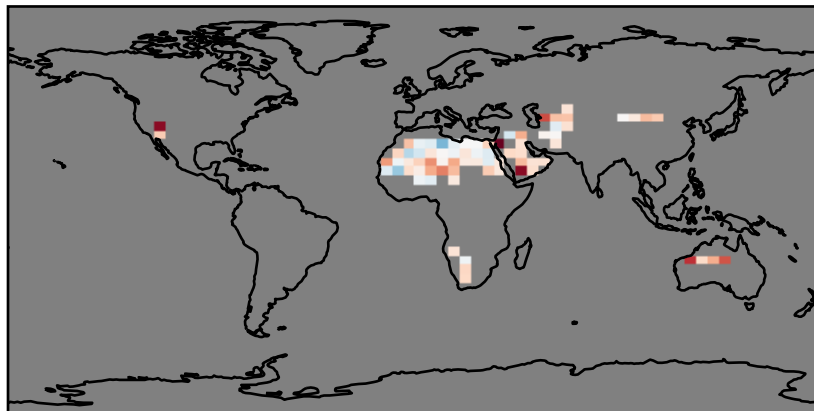
Difference
Dev - Ref, Dynamic Range



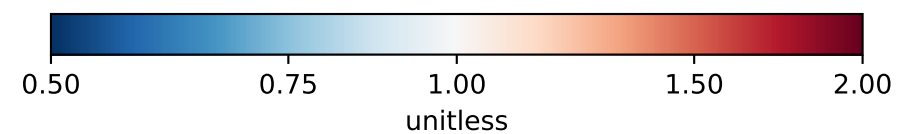
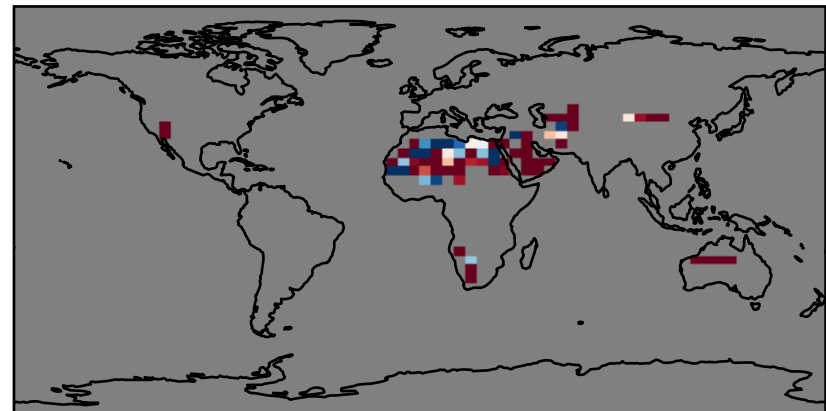
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

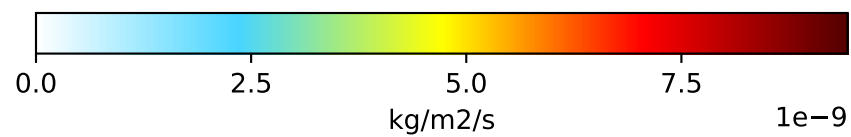
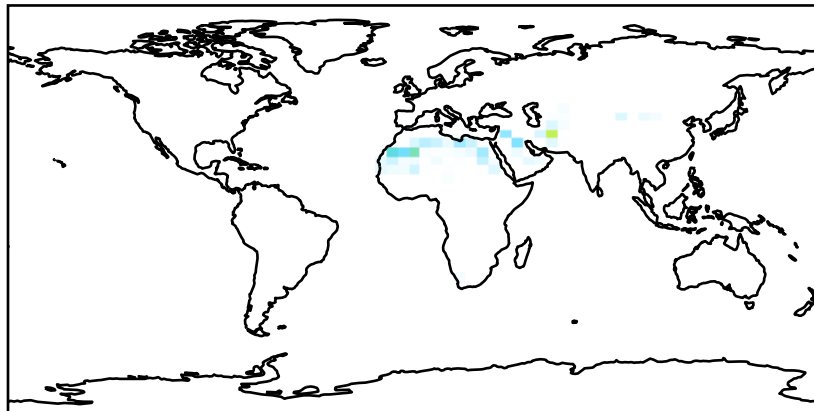


Ratio
Dev/Ref, Fixed Range

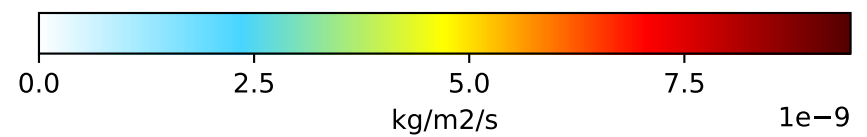
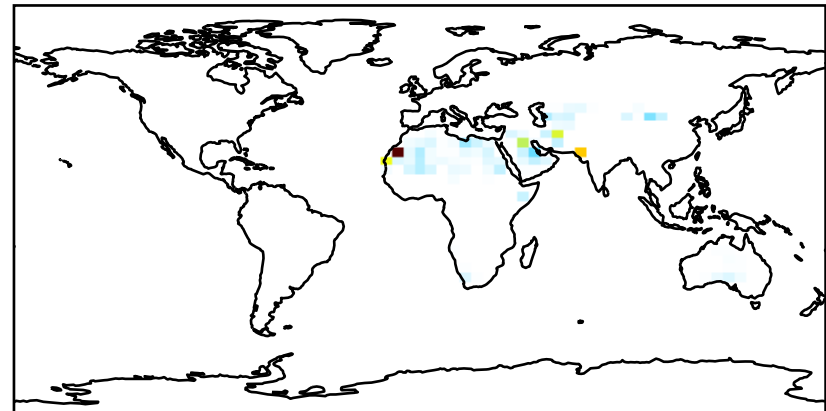


EmisDST4_Natural

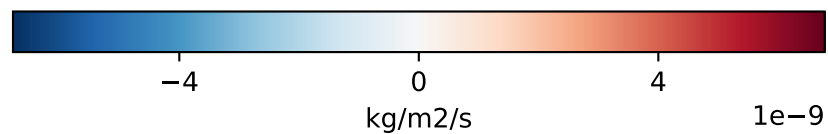
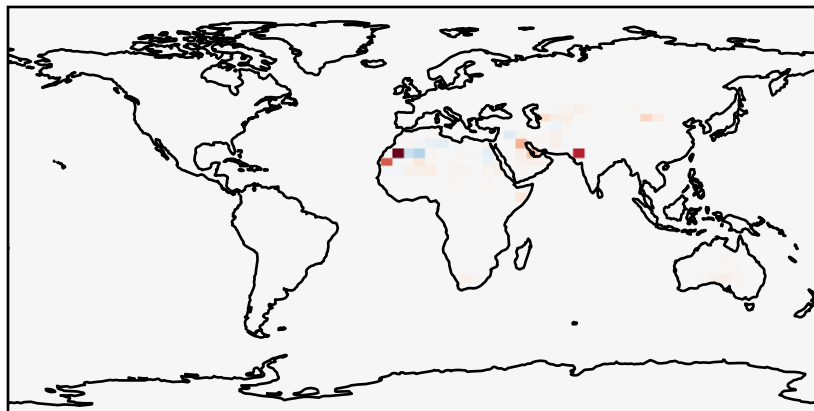
GCC_12.9.0 (Ref)
4.0x5.0



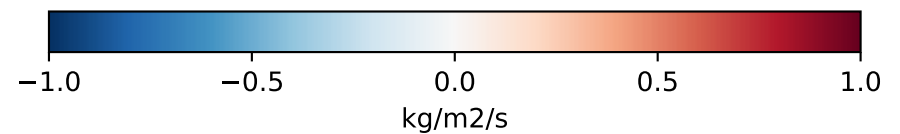
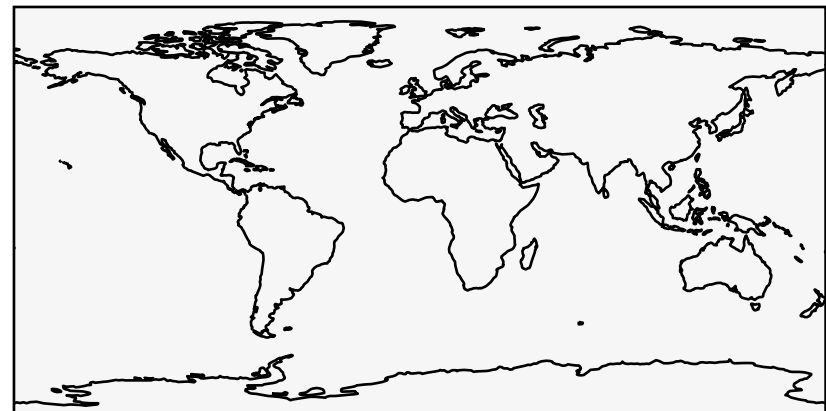
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



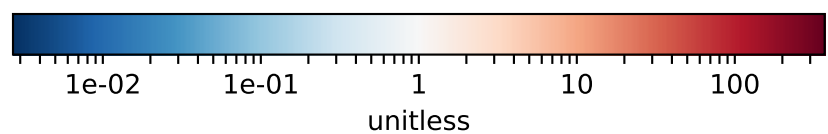
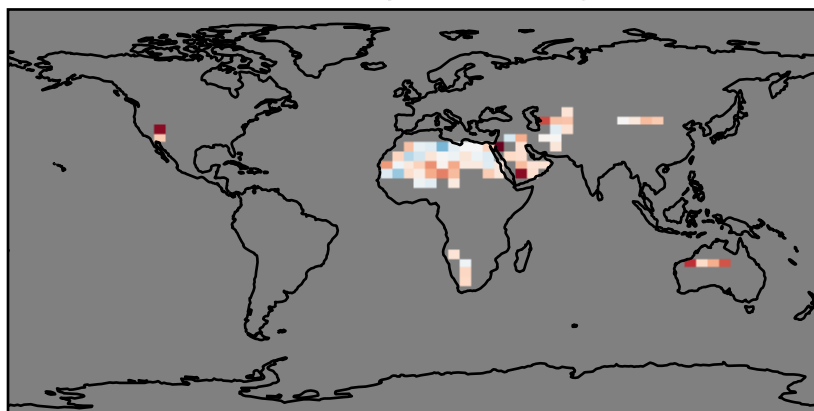
Difference
Dev - Ref, Dynamic Range



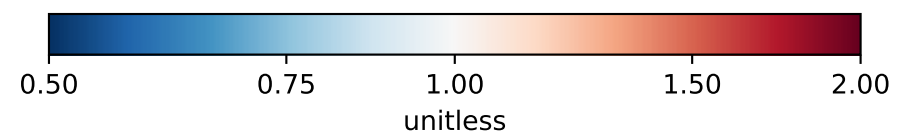
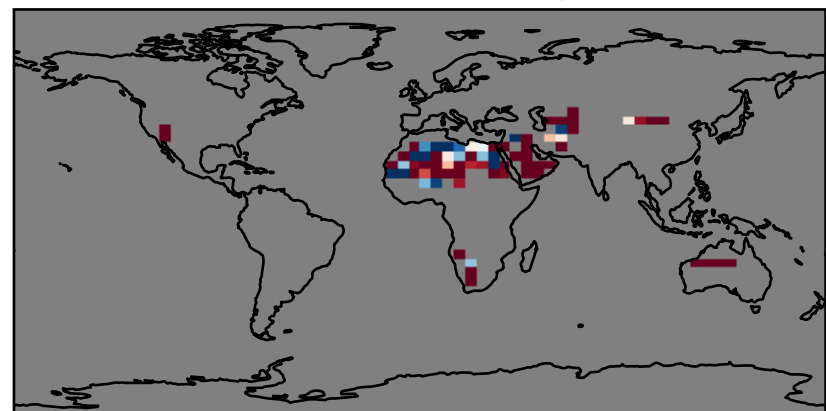
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

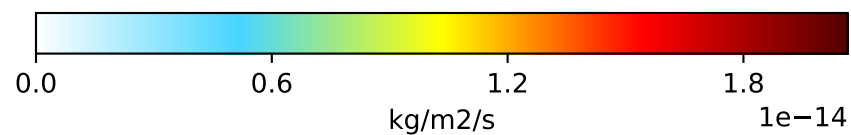
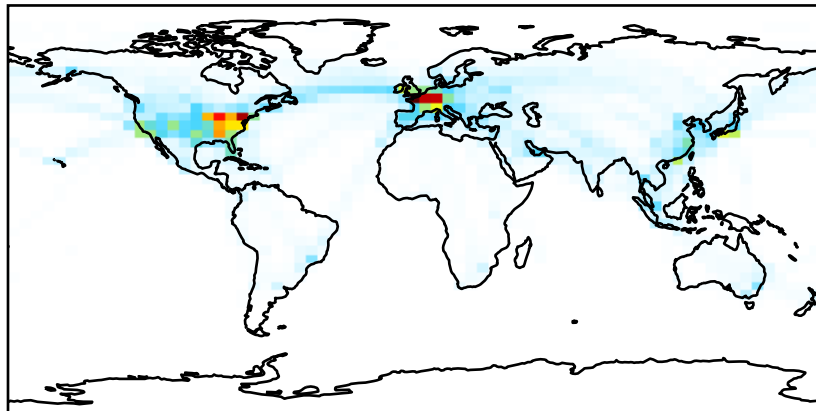


Ratio
Dev/Ref, Fixed Range

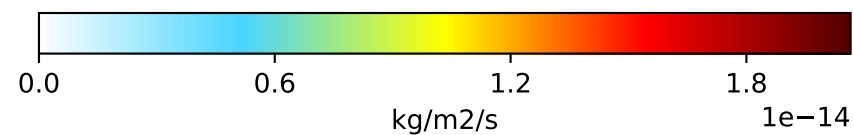
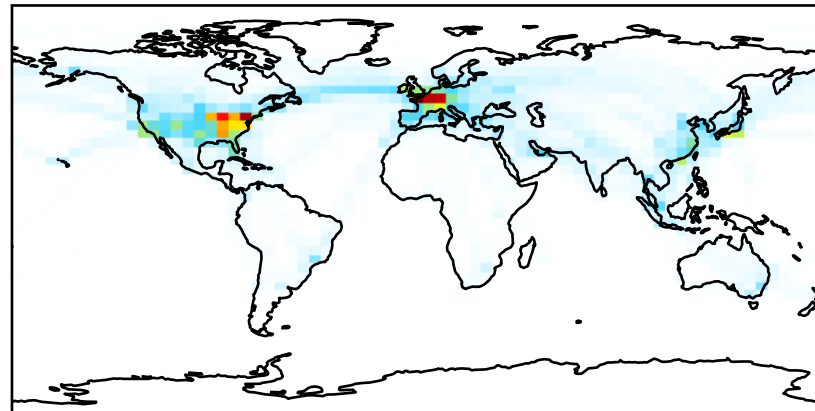


EmisSO4_Aircraft

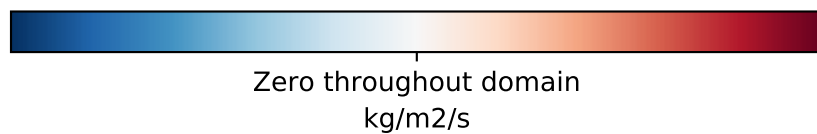
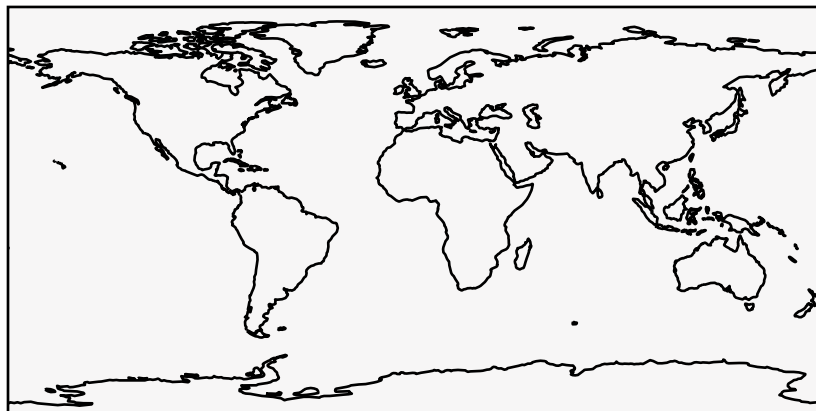
GCC_12.9.0 (Ref)
4.0x5.0



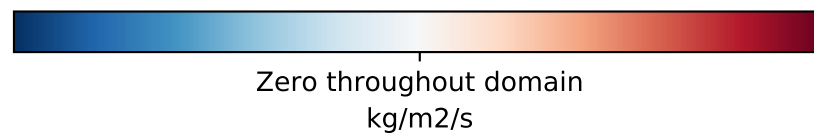
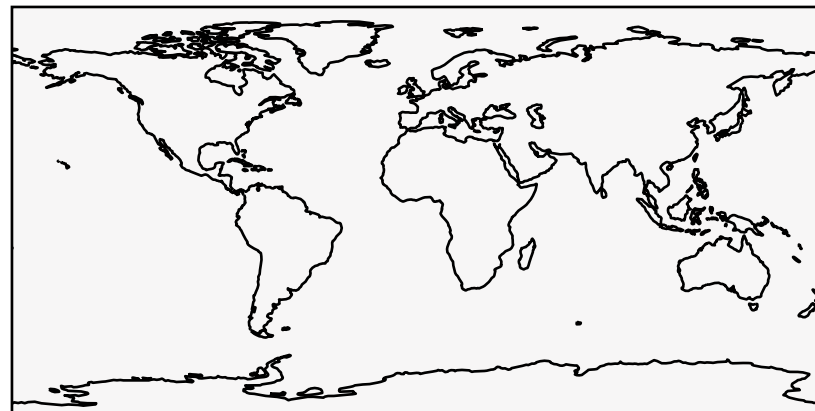
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



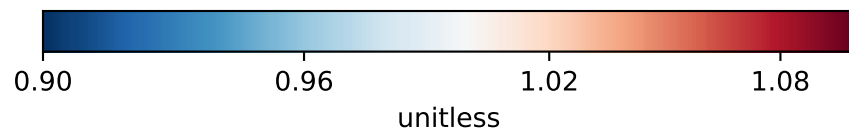
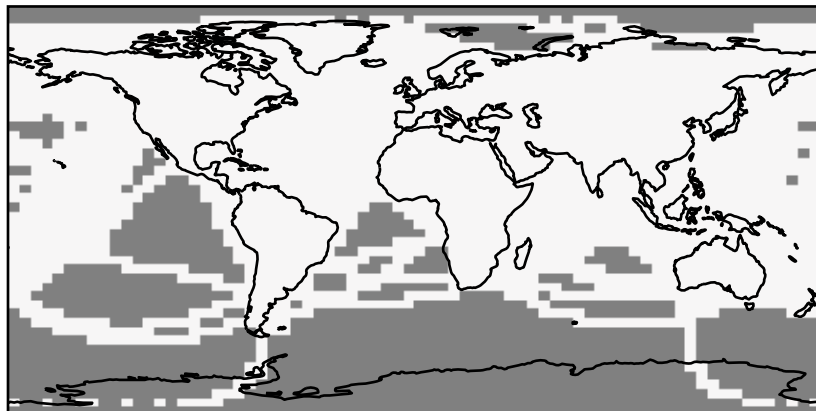
Difference
Dev - Ref, Dynamic Range



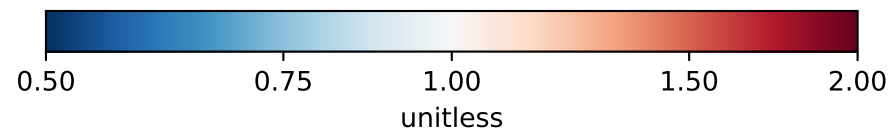
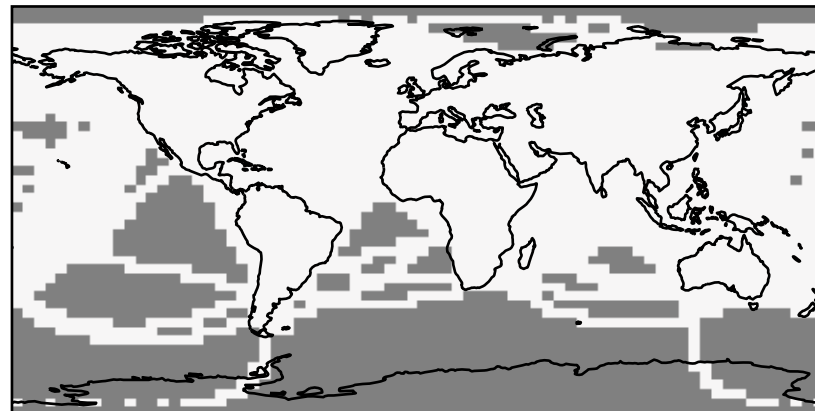
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

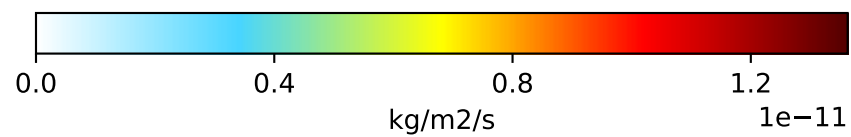
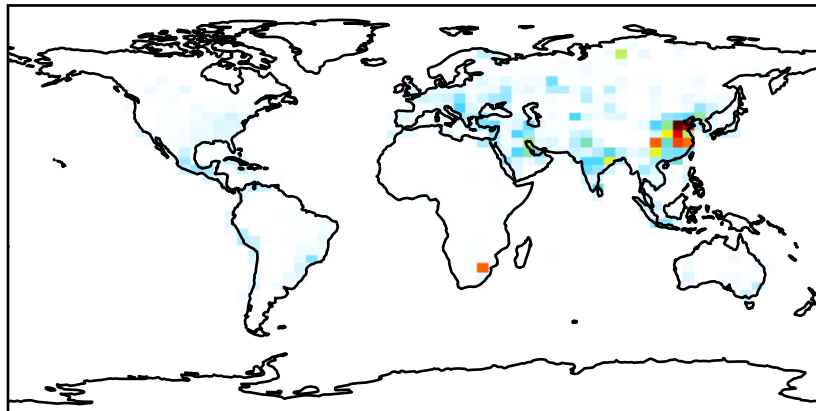


Ratio
Dev/Ref, Fixed Range

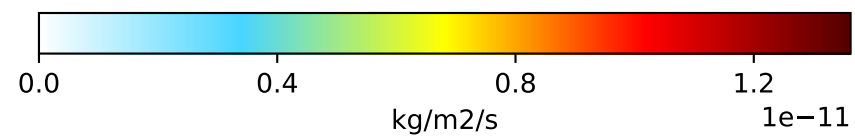
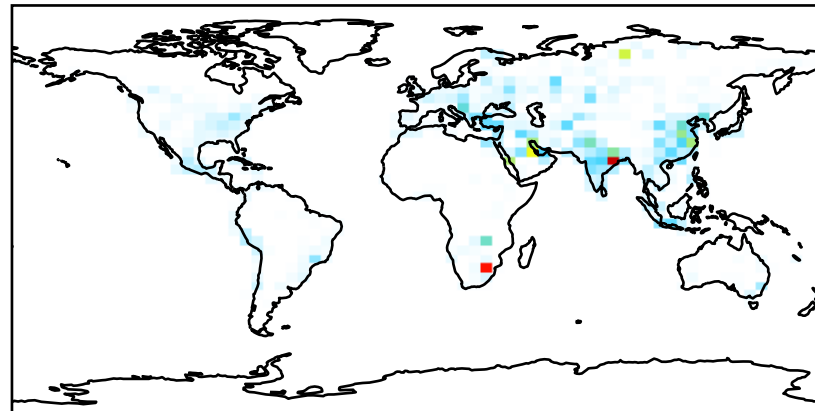


EmisSO4_Anthro

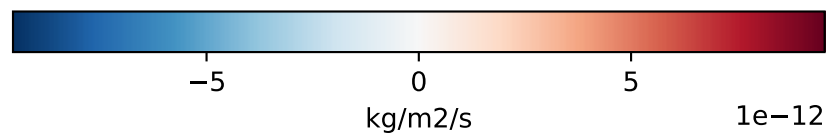
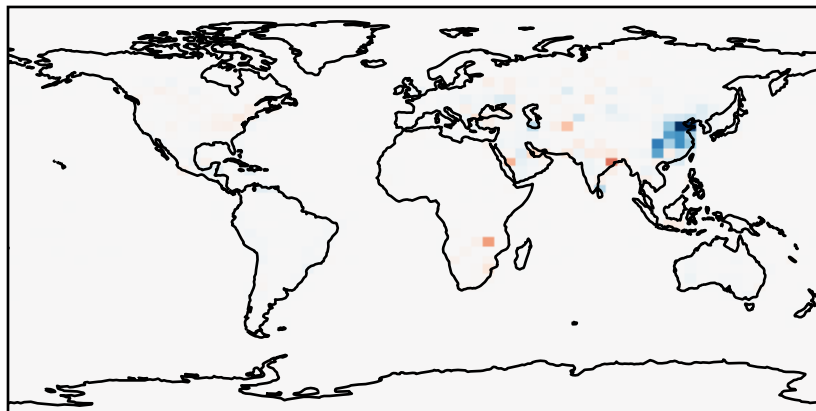
GCC_12.9.0 (Ref)
4.0x5.0



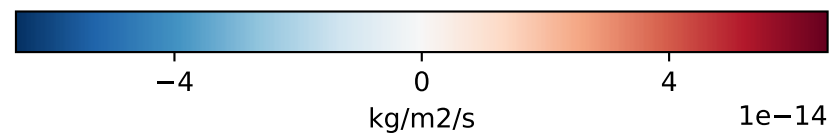
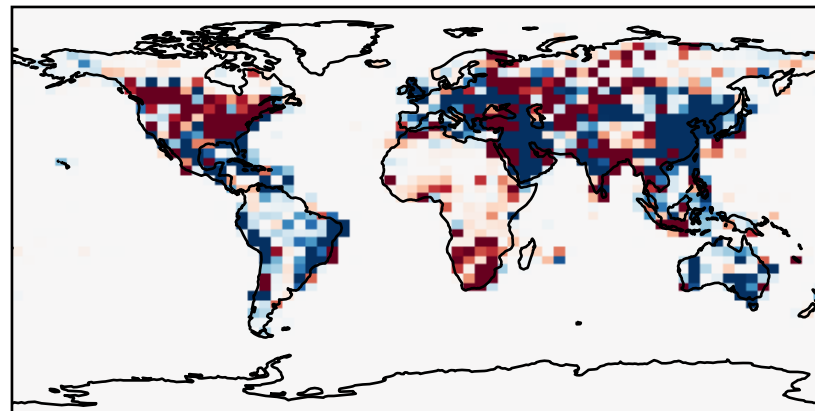
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



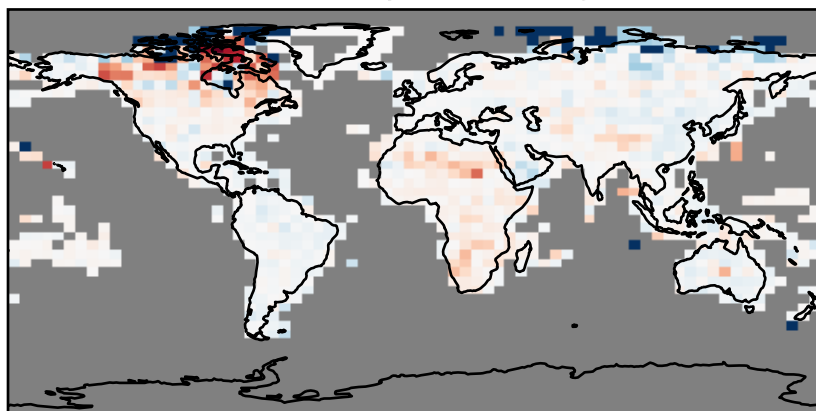
Difference
Dev - Ref, Dynamic Range



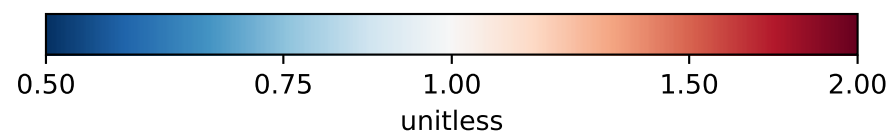
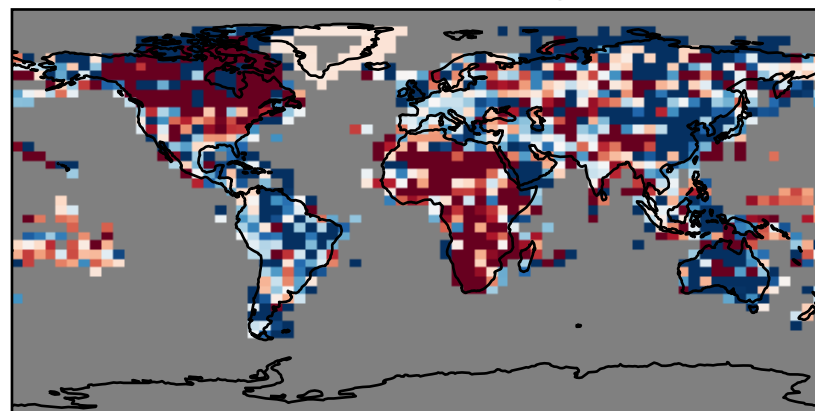
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

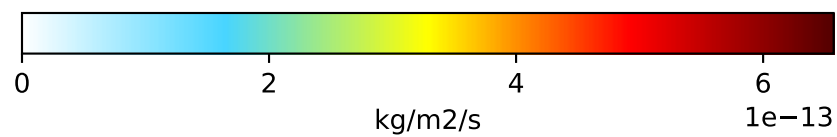
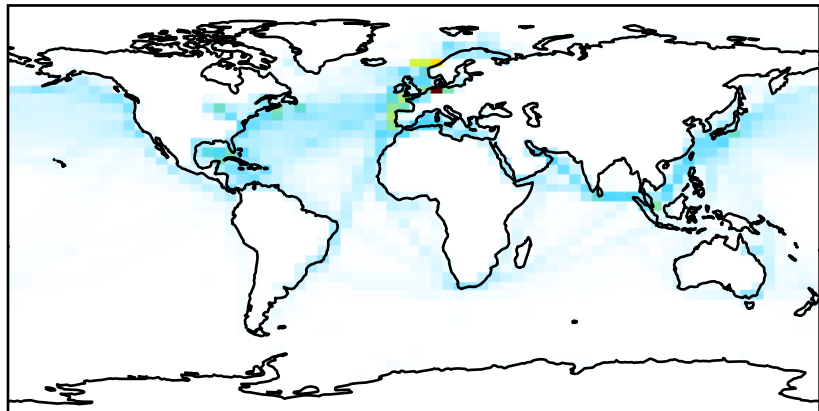


Ratio
Dev/Ref, Fixed Range

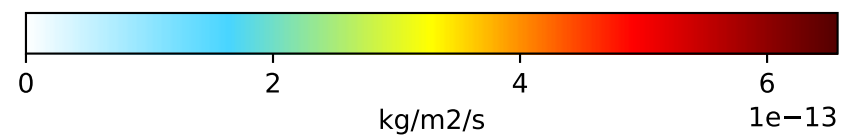
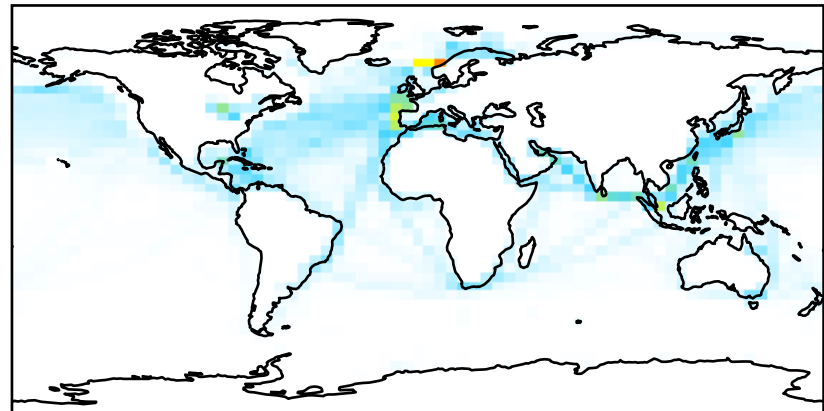


EmisSO4_Ship

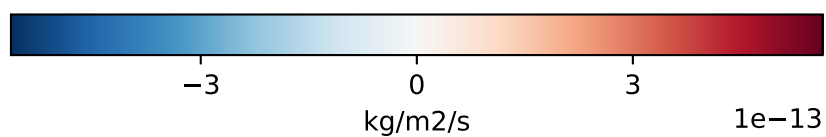
GCC_12.9.0 (Ref)
4.0x5.0



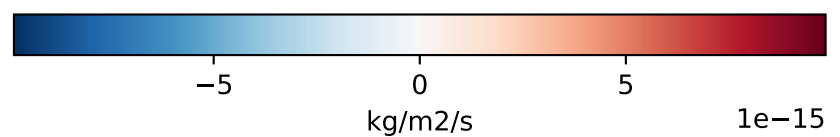
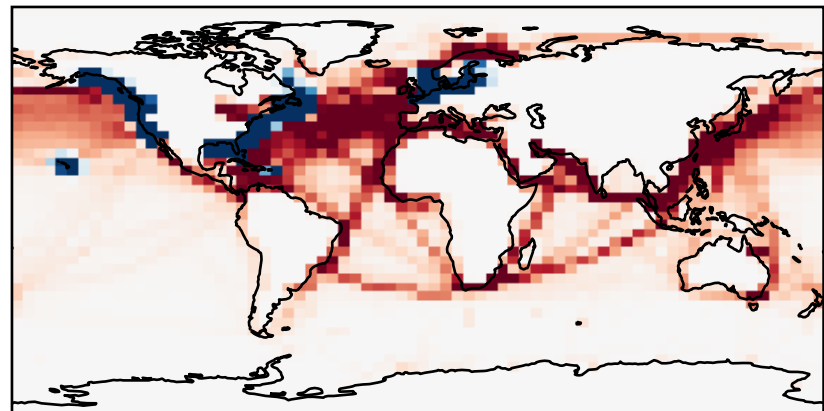
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



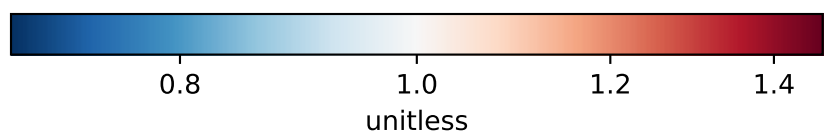
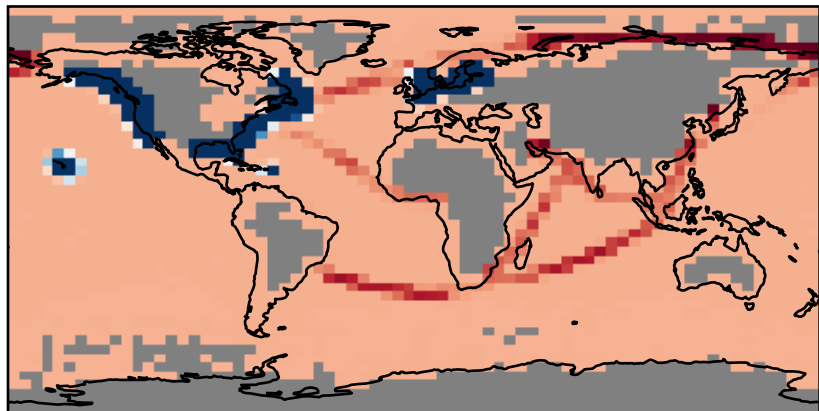
Difference
Dev - Ref, Dynamic Range



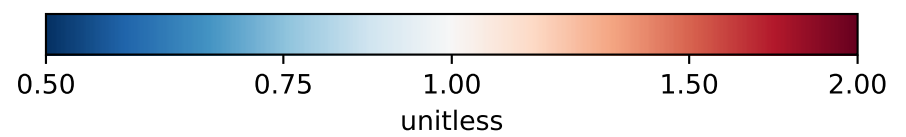
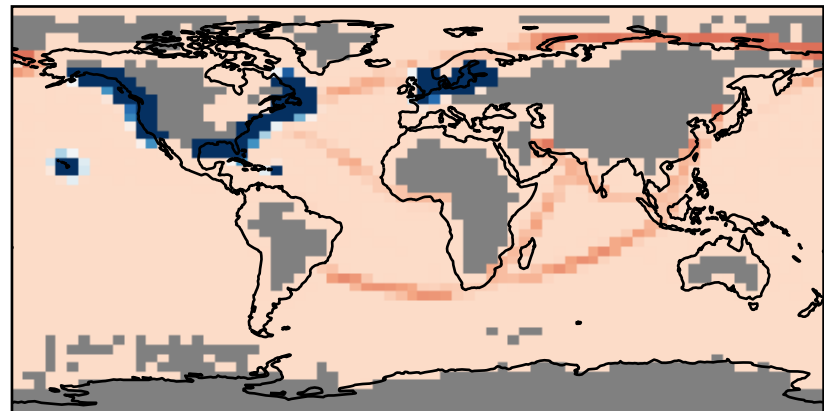
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

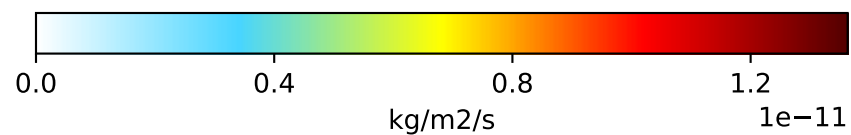
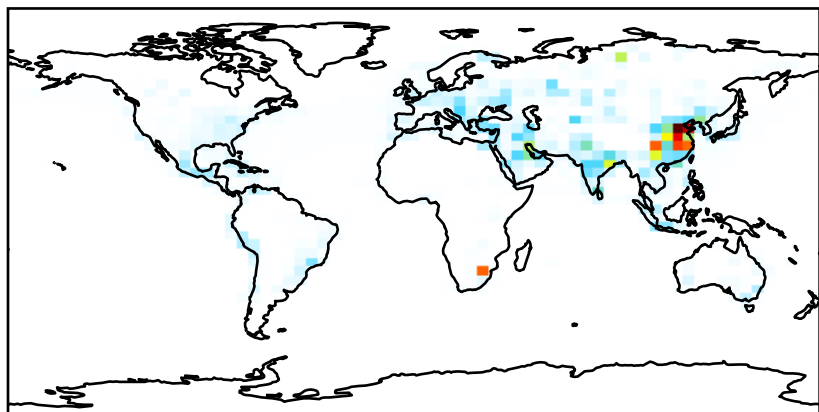


Ratio
Dev/Ref, Fixed Range

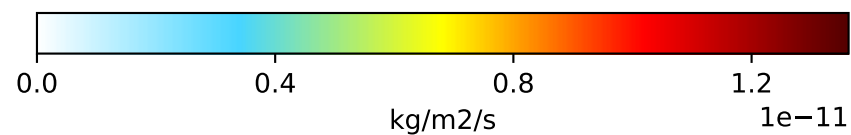
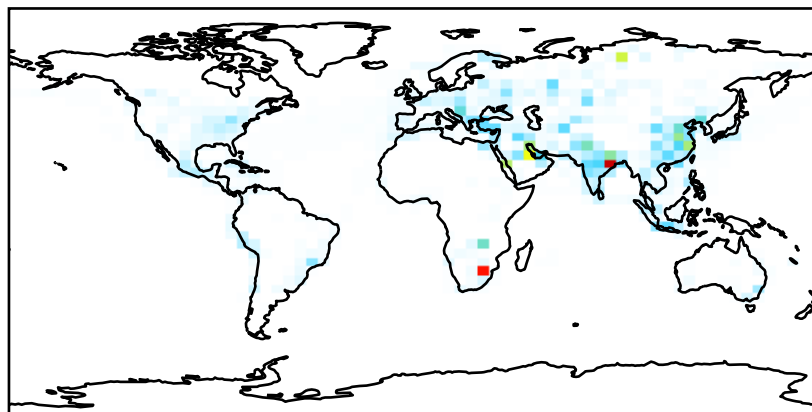


EmisSO4_Total

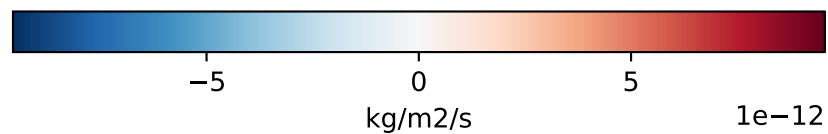
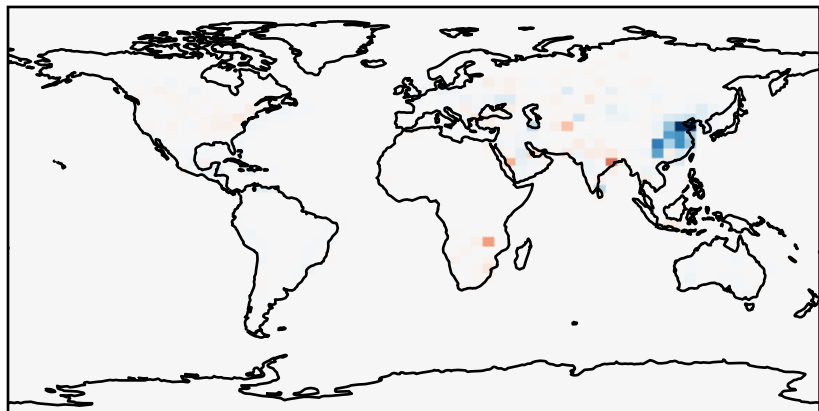
GCC_12.9.0 (Ref)
4.0x5.0



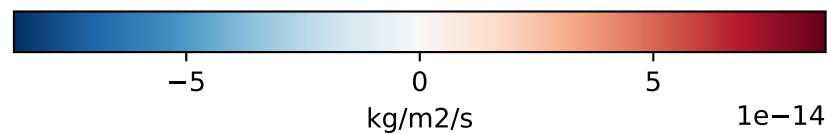
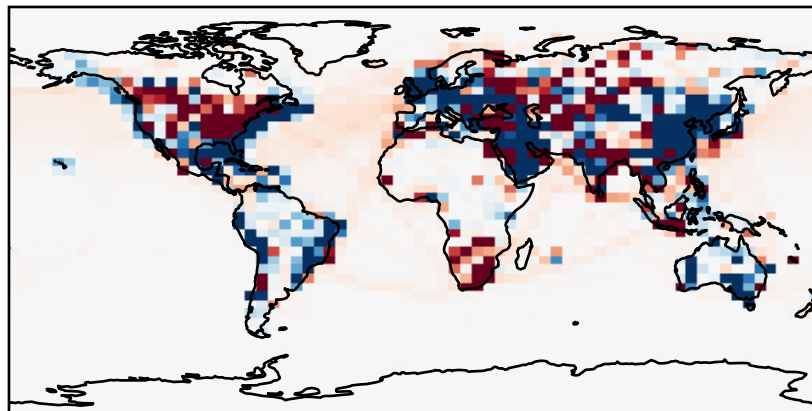
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



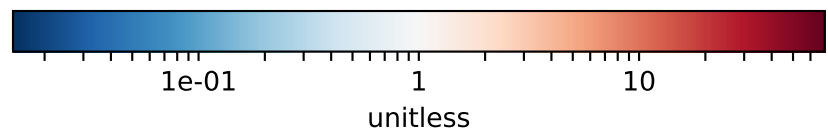
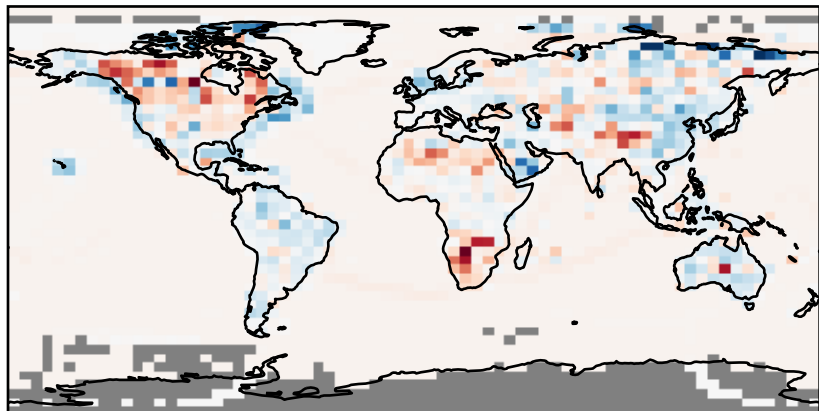
Difference
Dev - Ref, Dynamic Range



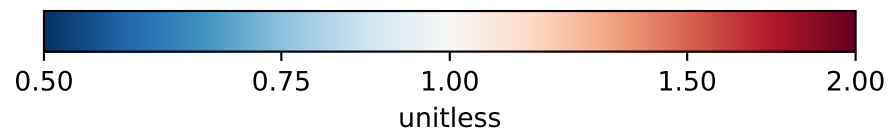
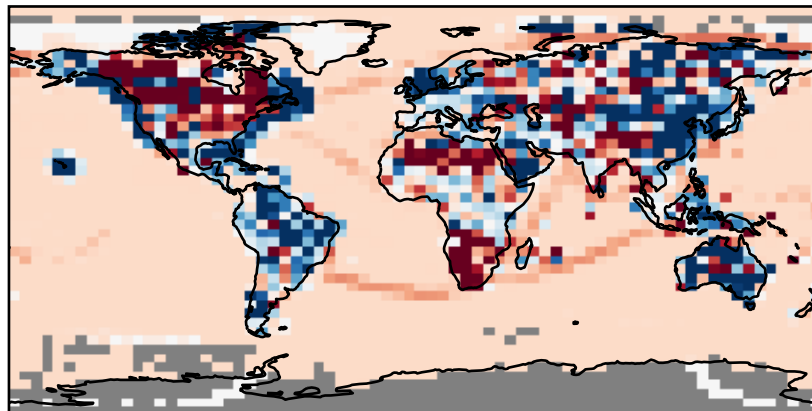
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

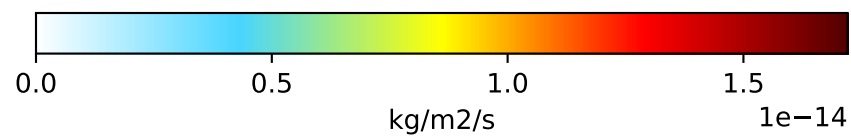
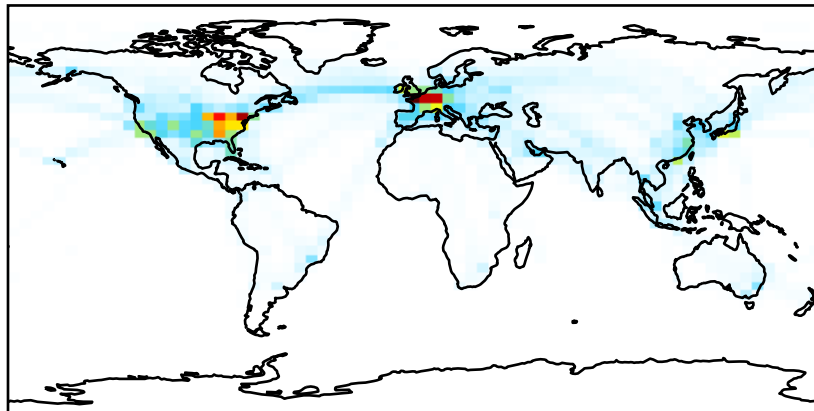


Ratio
Dev/Ref, Fixed Range

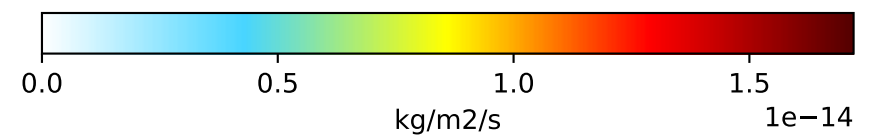
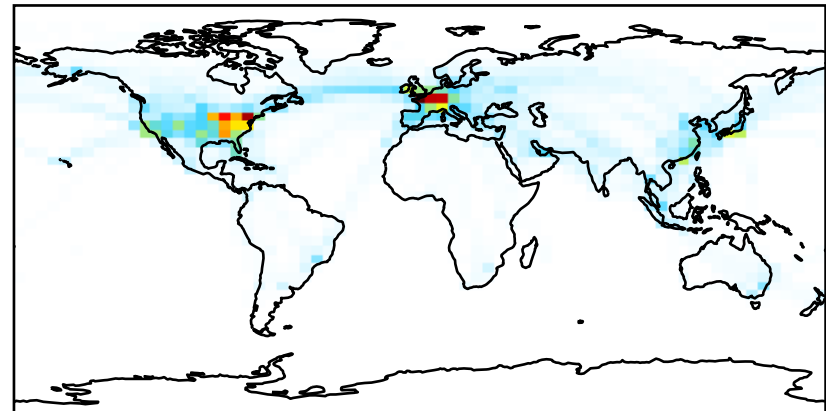


EmisBCPI_Aircraft

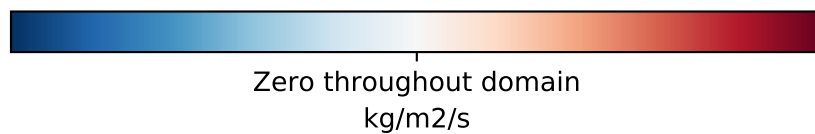
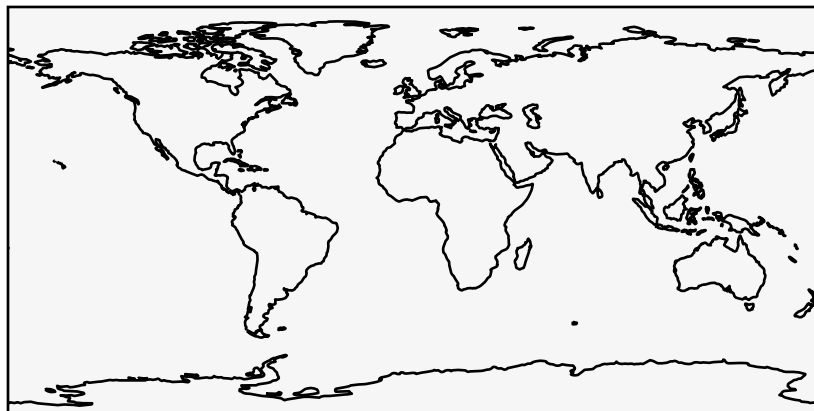
GCC_12.9.0 (Ref)
4.0x5.0



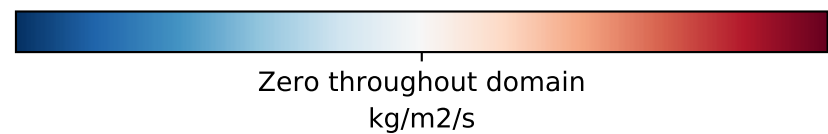
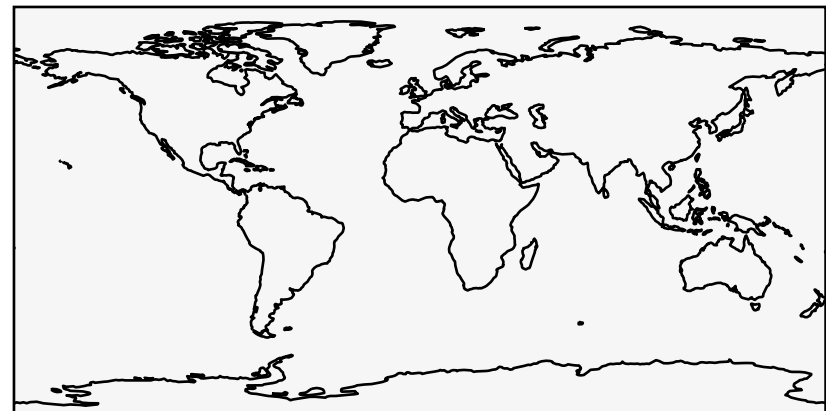
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



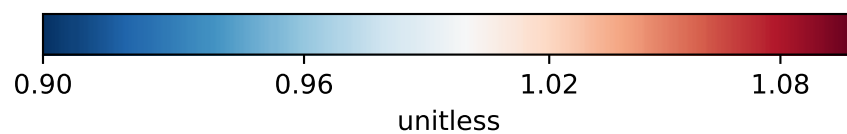
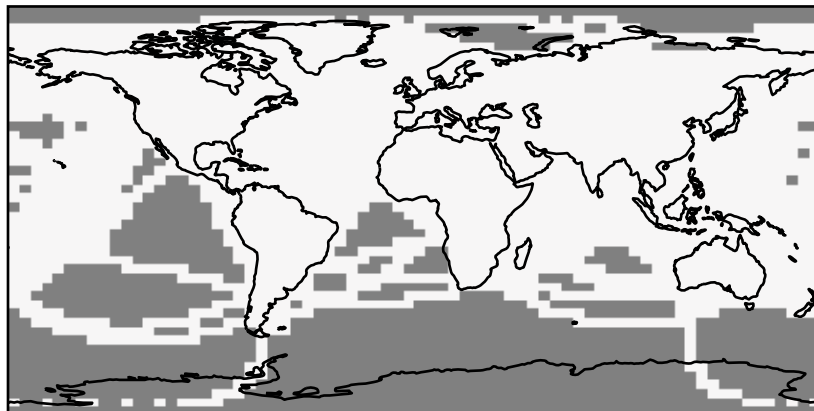
Difference
Dev - Ref, Dynamic Range



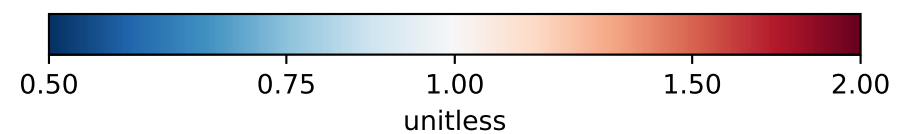
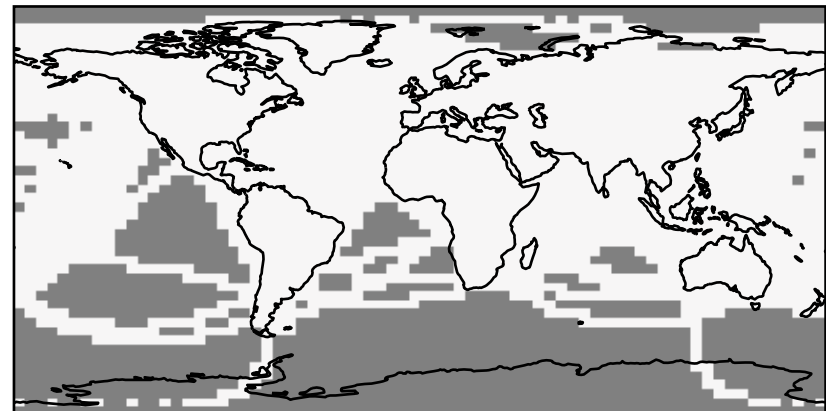
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

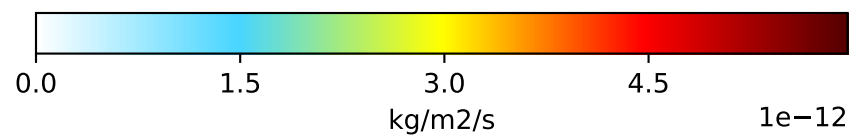
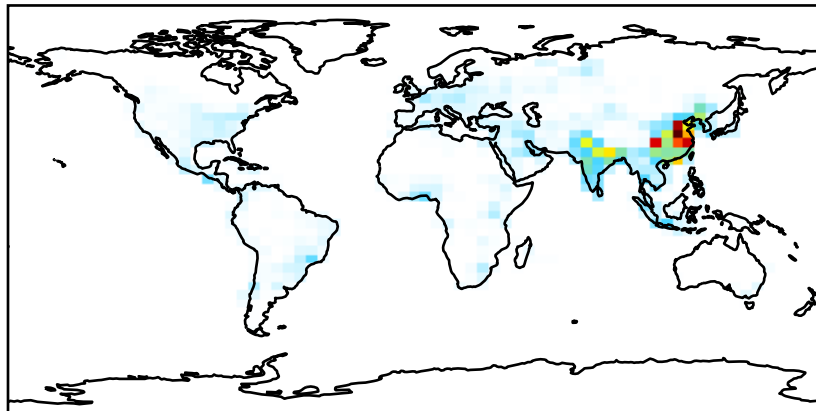


Ratio
Dev/Ref, Fixed Range

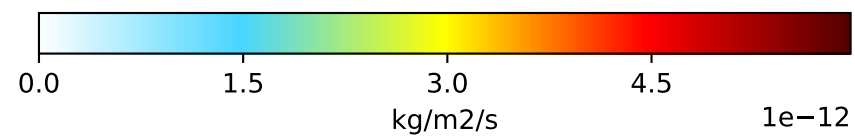
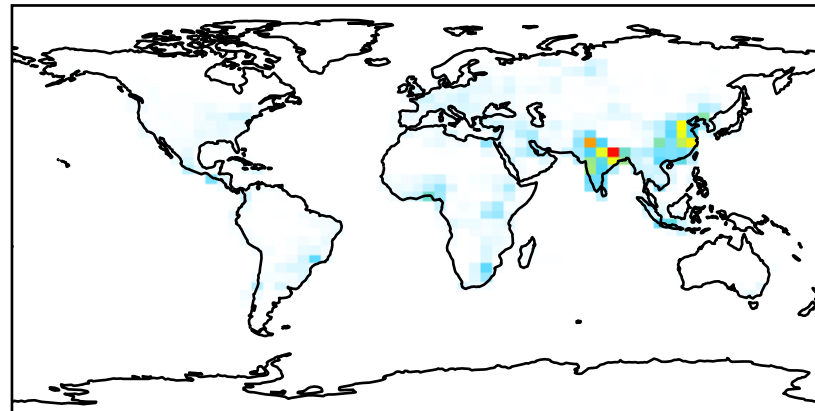


EmisBCPI_Anthro

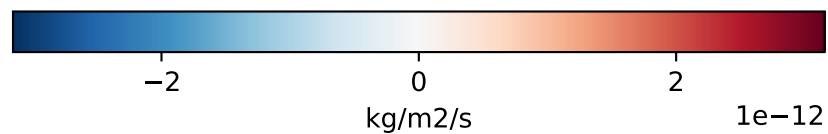
GCC_12.9.0 (Ref)
4.0x5.0



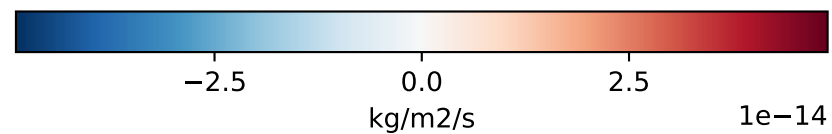
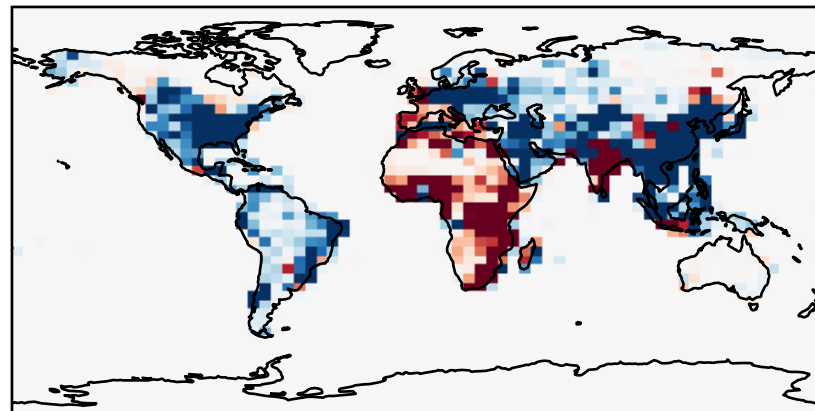
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



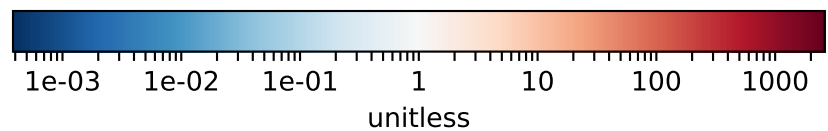
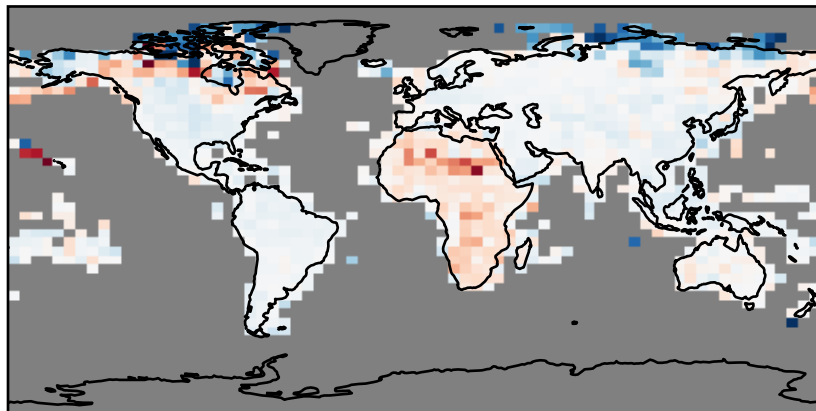
Difference
Dev - Ref, Dynamic Range



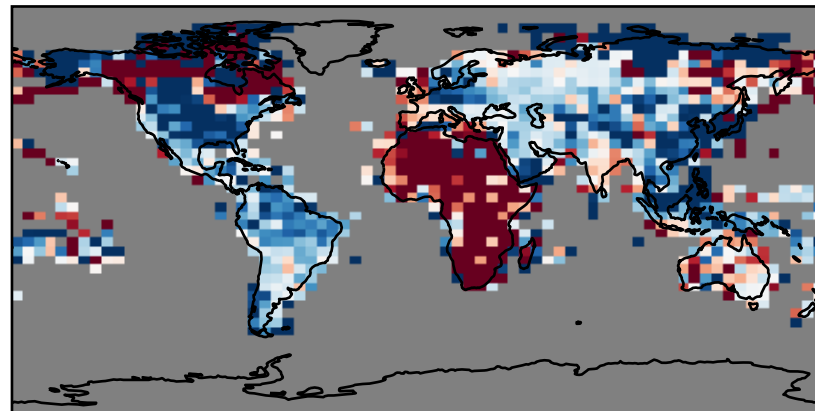
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

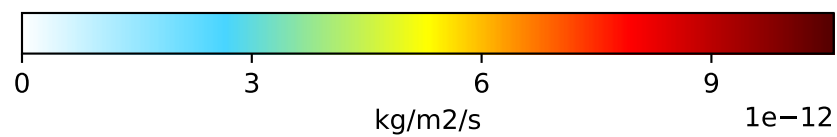
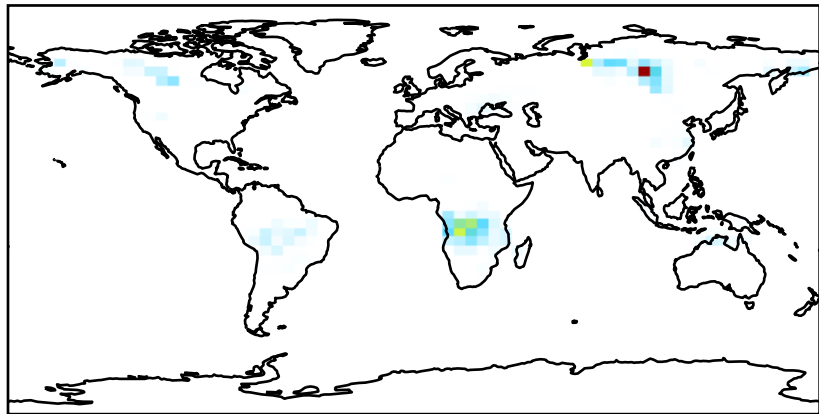


Ratio
Dev/Ref, Fixed Range

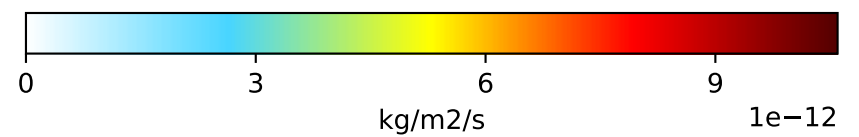
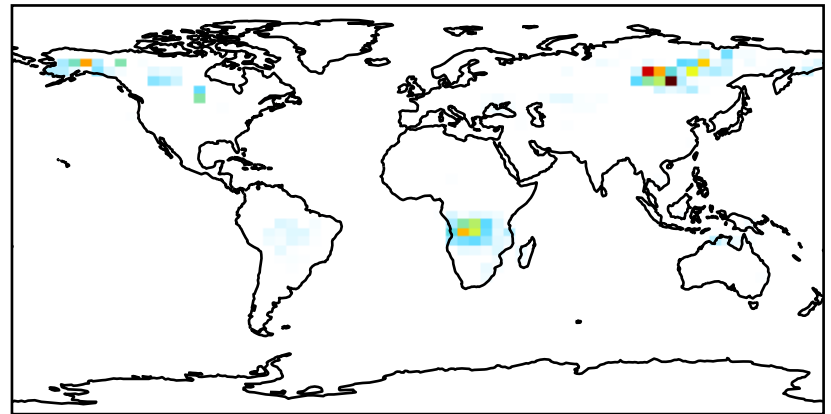


EmisBCPI_BioBurn

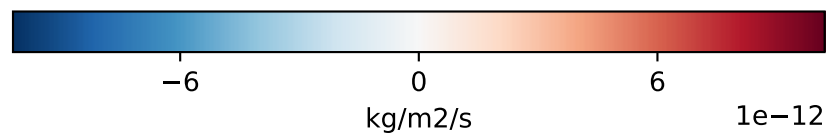
GCC_12.9.0 (Ref)
4.0x5.0



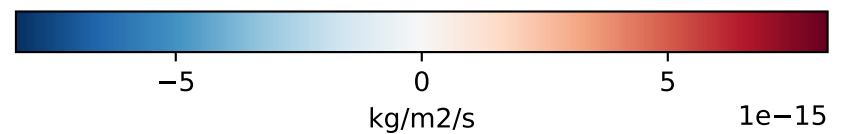
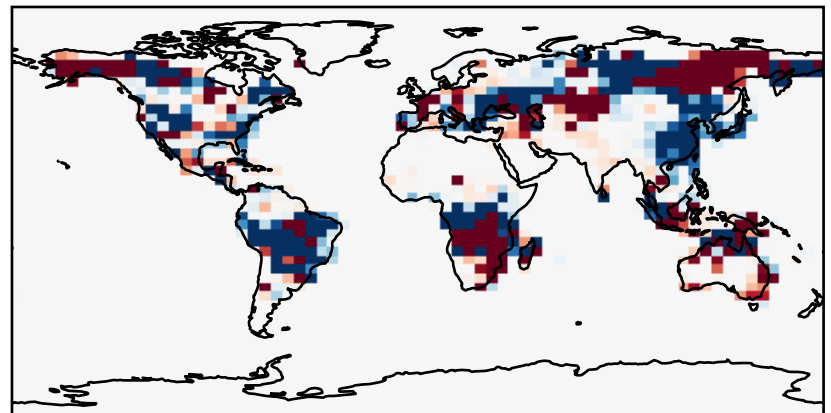
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



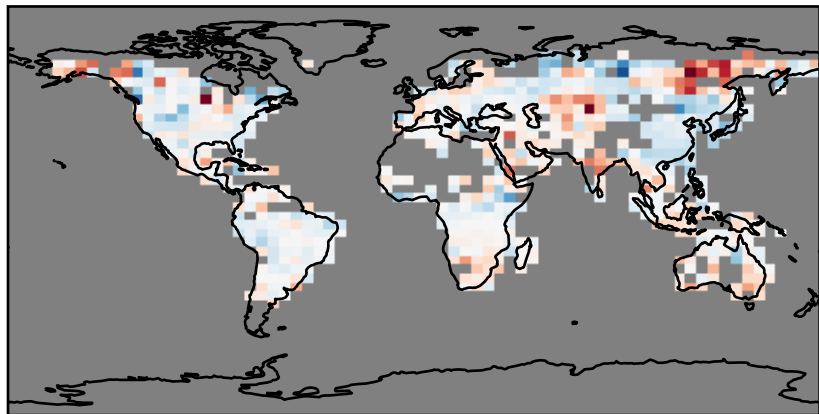
Difference
Dev - Ref, Dynamic Range



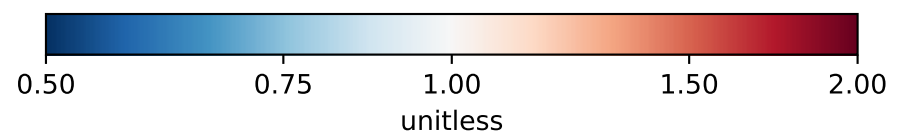
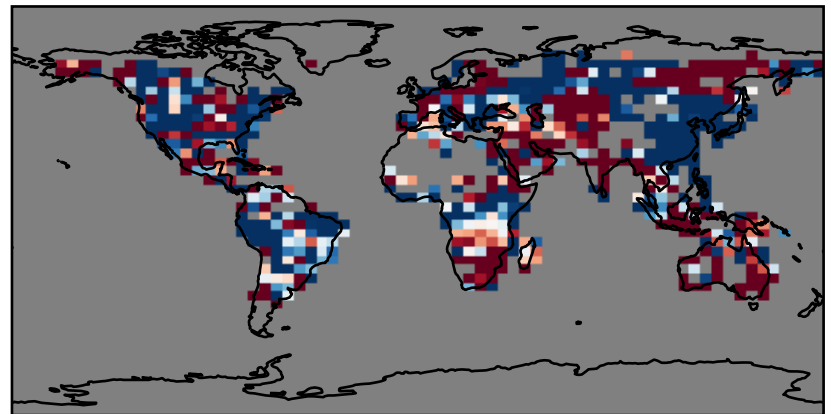
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

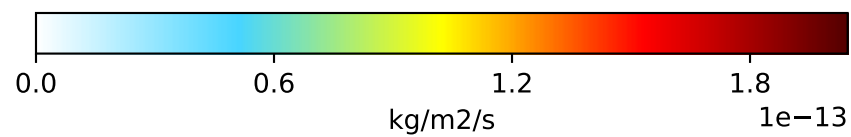
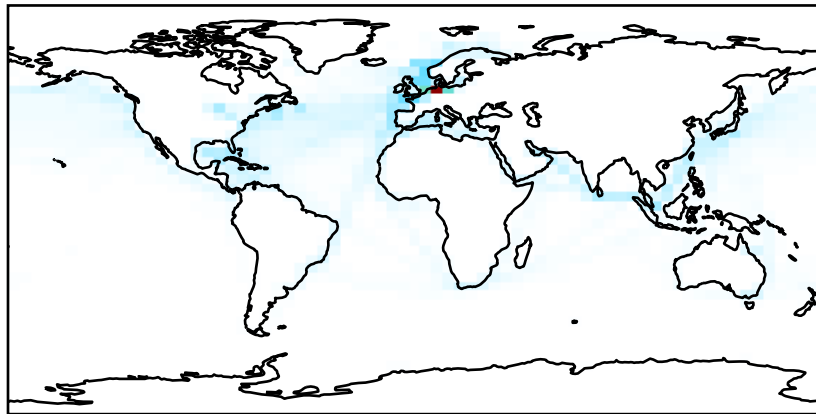


Ratio
Dev/Ref, Fixed Range

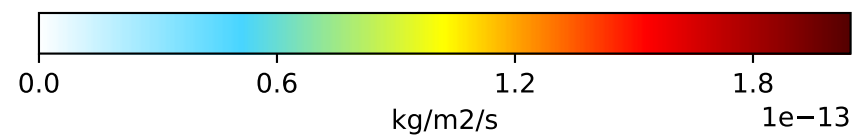
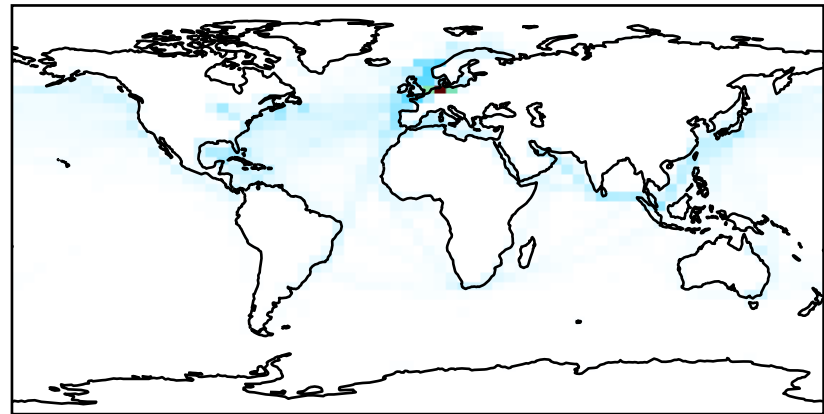


EmisBCPI_Ship

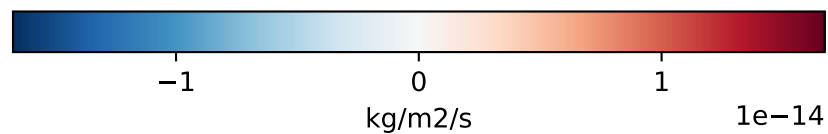
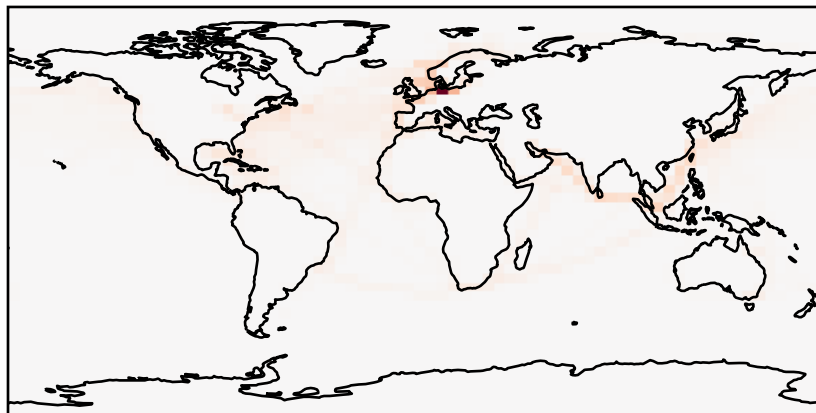
GCC_12.9.0 (Ref)
4.0x5.0



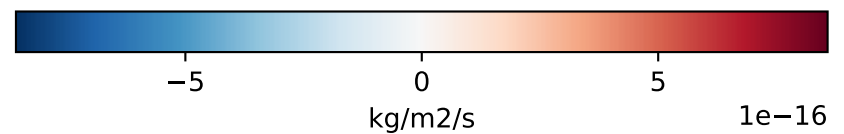
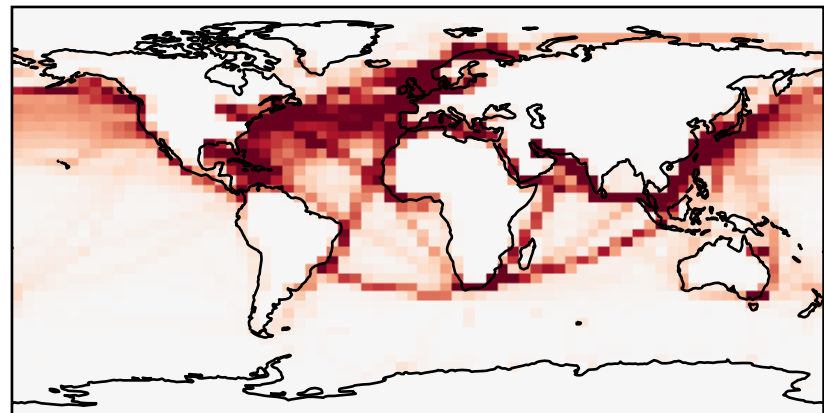
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



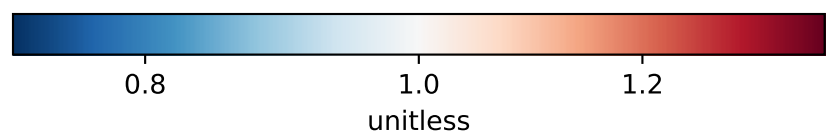
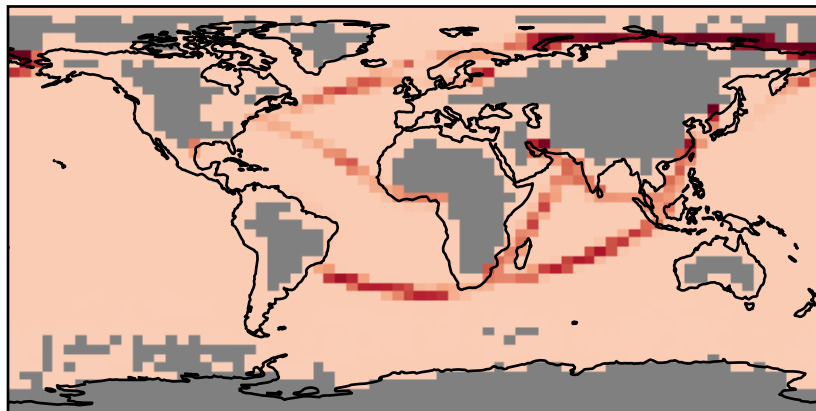
Difference
Dev - Ref, Dynamic Range



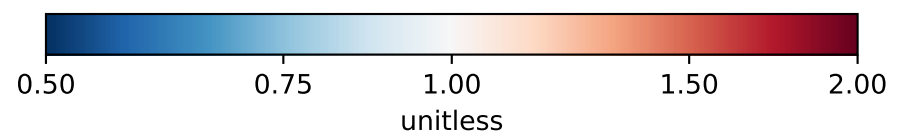
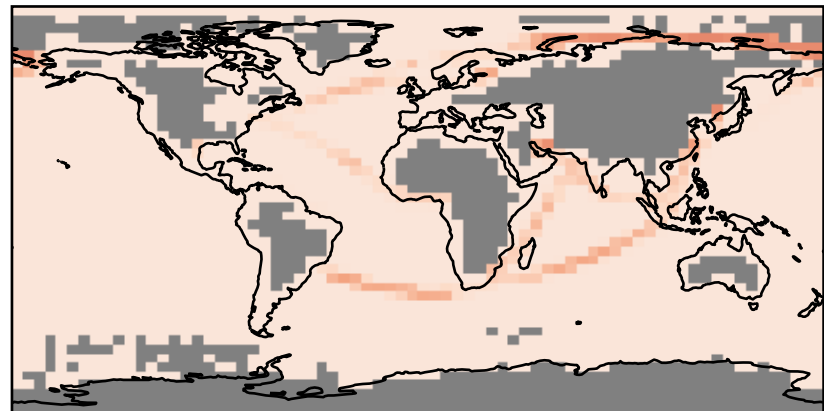
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

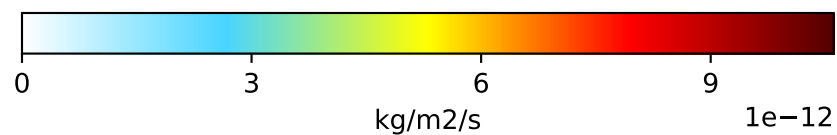
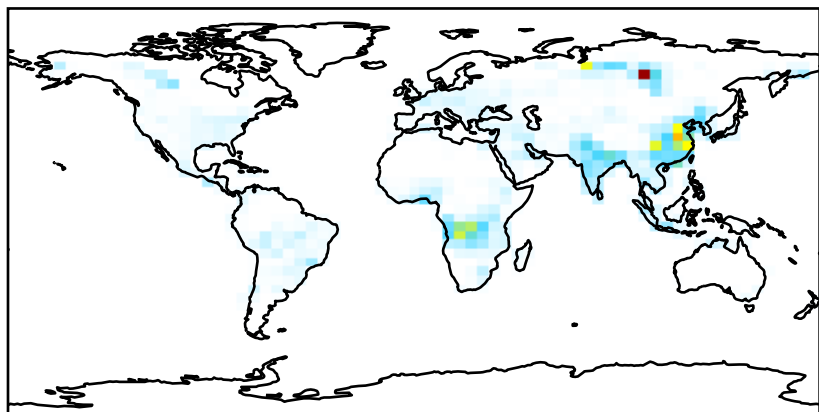


Ratio
Dev/Ref, Fixed Range

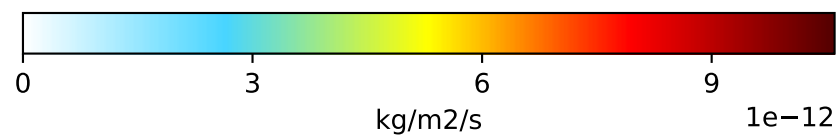
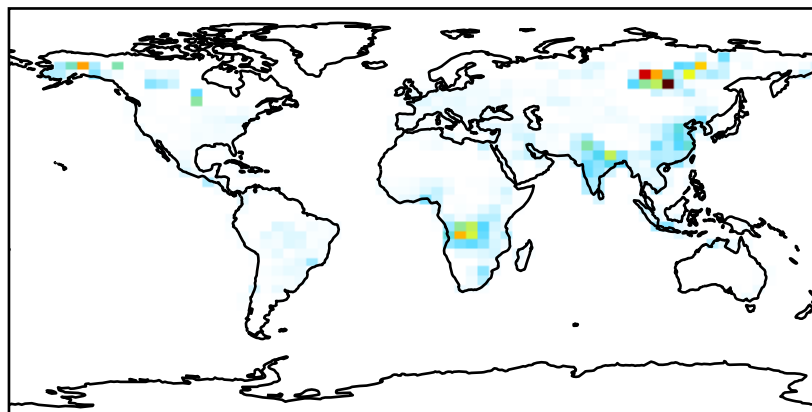


EmisBCPI_Total

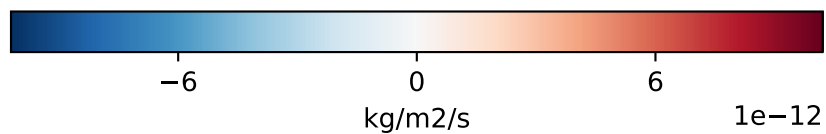
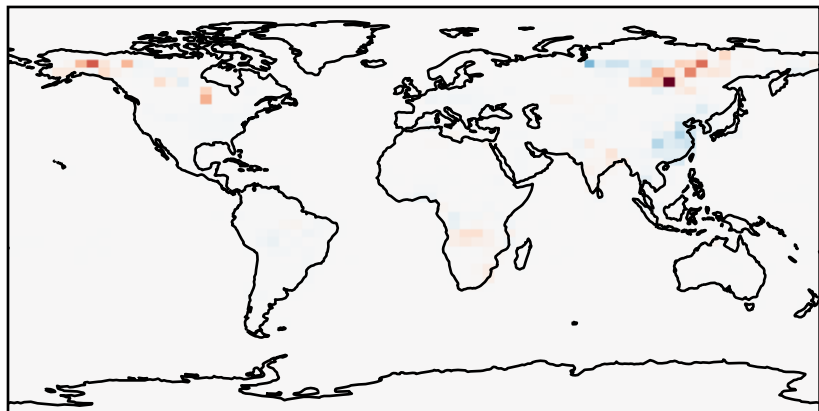
GCC_12.9.0 (Ref)
4.0x5.0



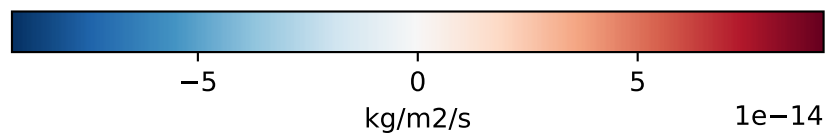
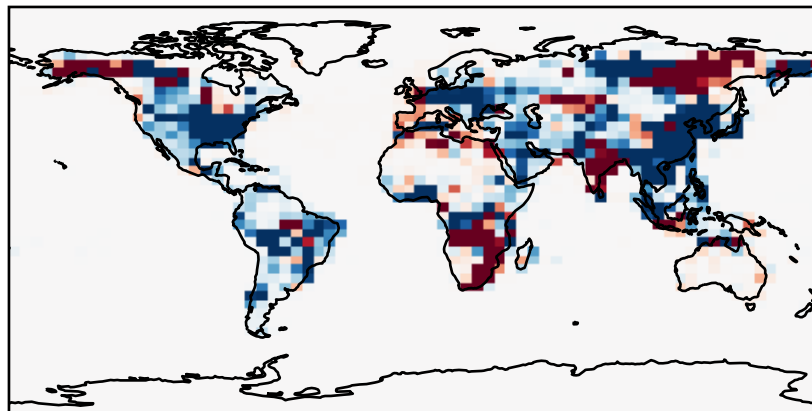
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



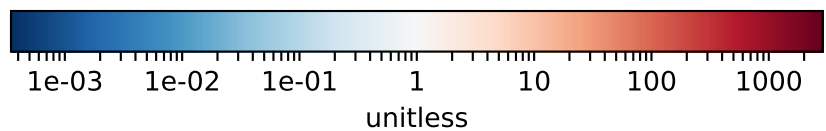
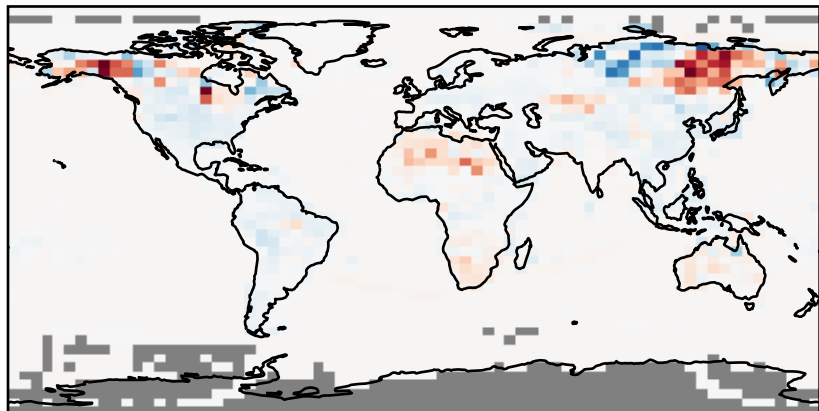
Difference
Dev - Ref, Dynamic Range



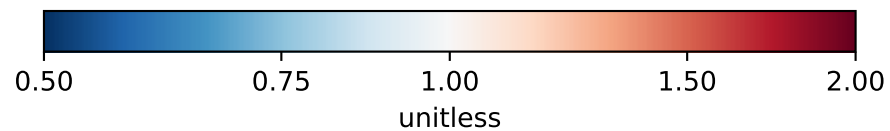
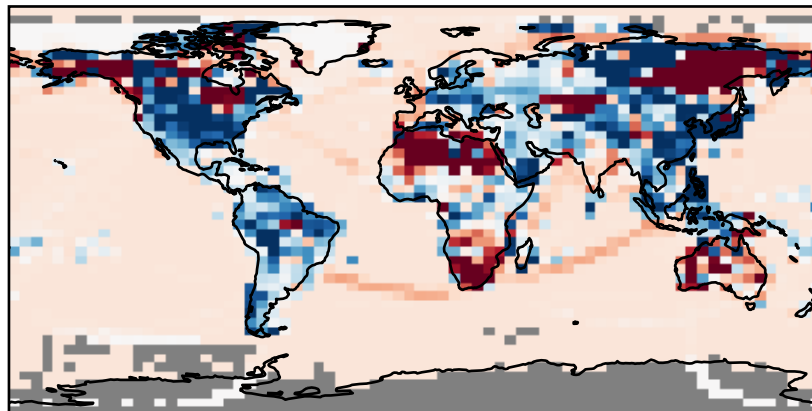
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

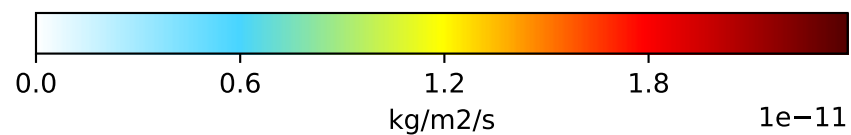
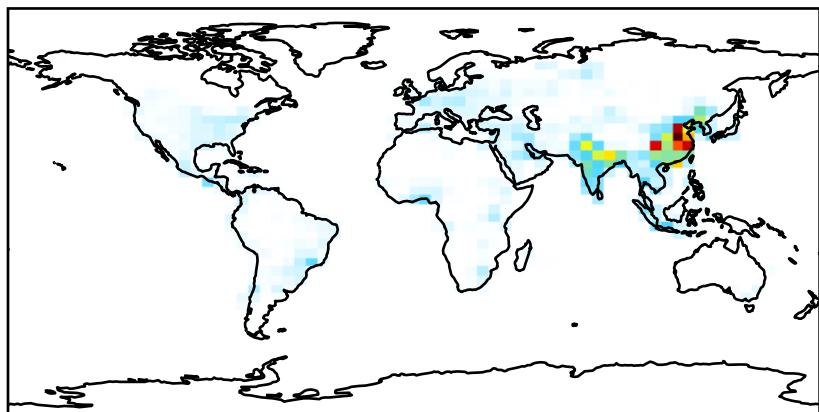


Ratio
Dev/Ref, Fixed Range

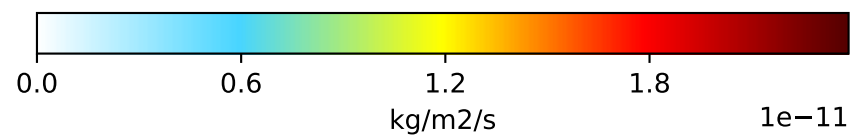
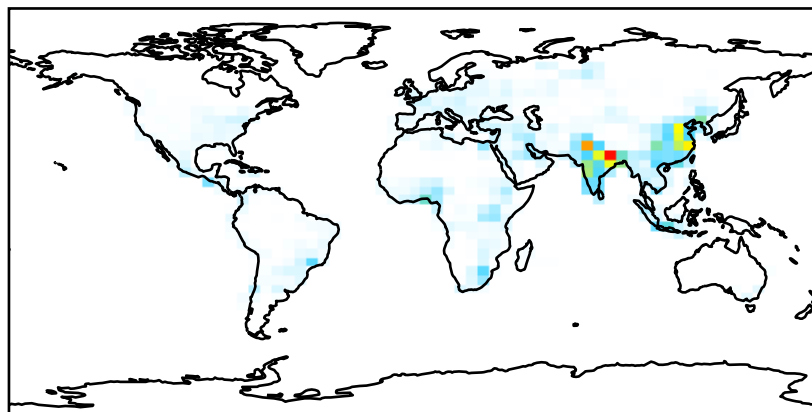


EmisBCPO_Anthro

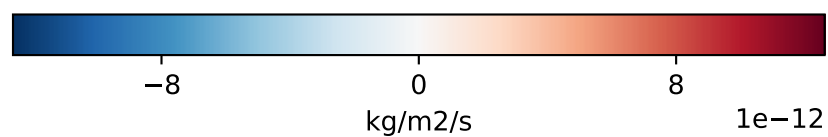
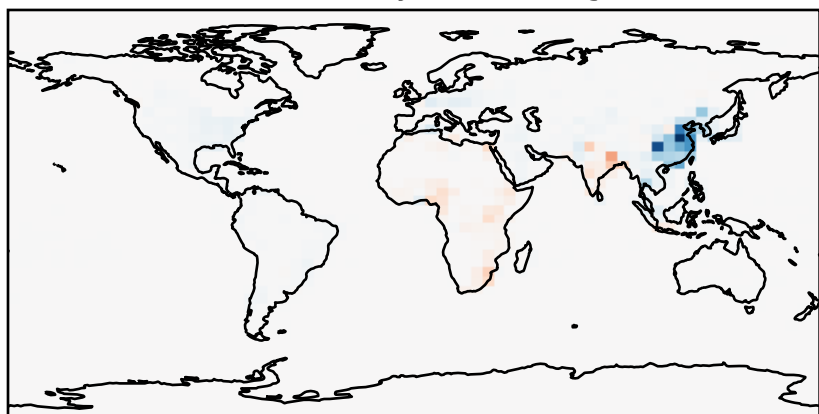
GCC_12.9.0 (Ref)
4.0x5.0



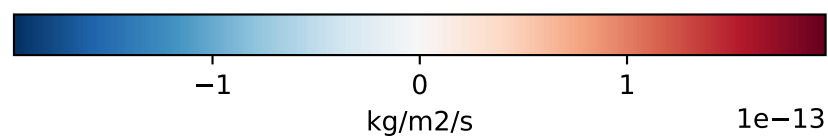
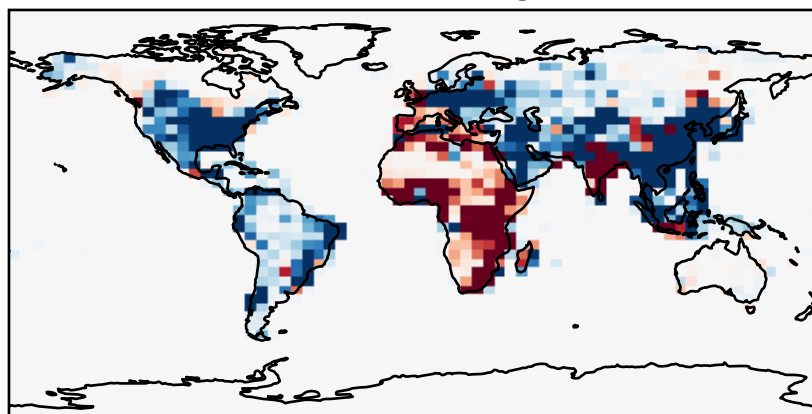
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



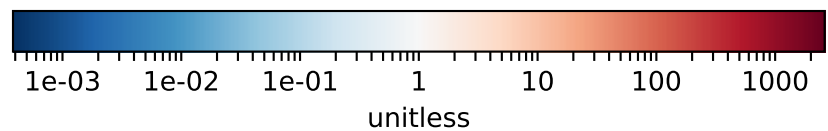
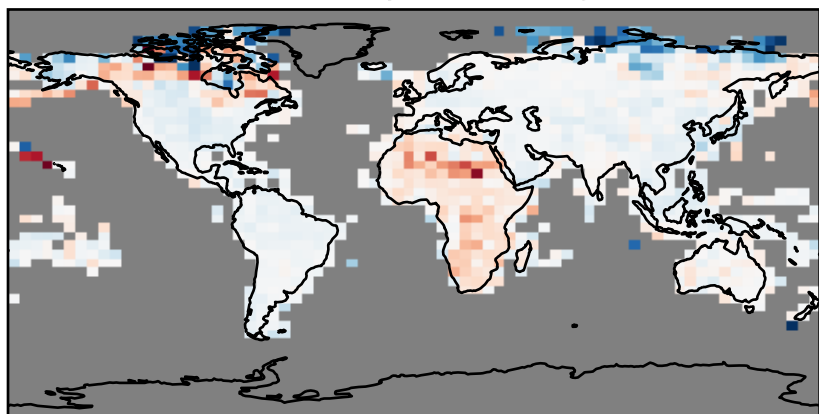
Difference
Dev - Ref, Dynamic Range



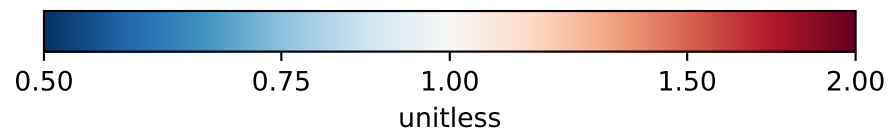
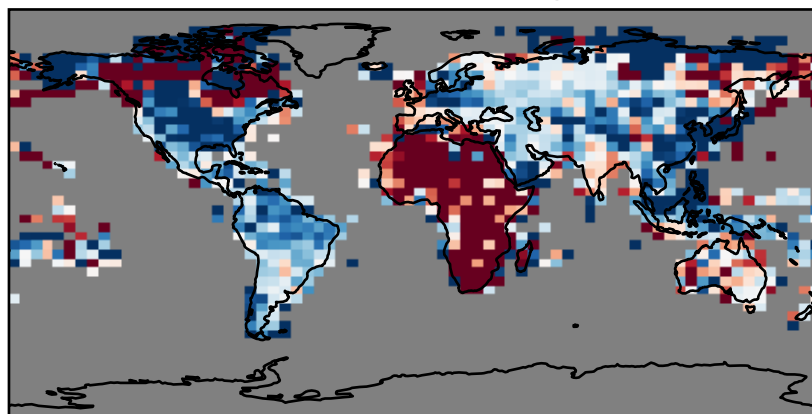
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

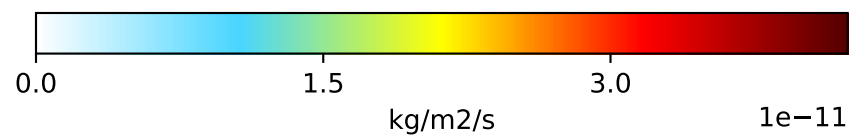
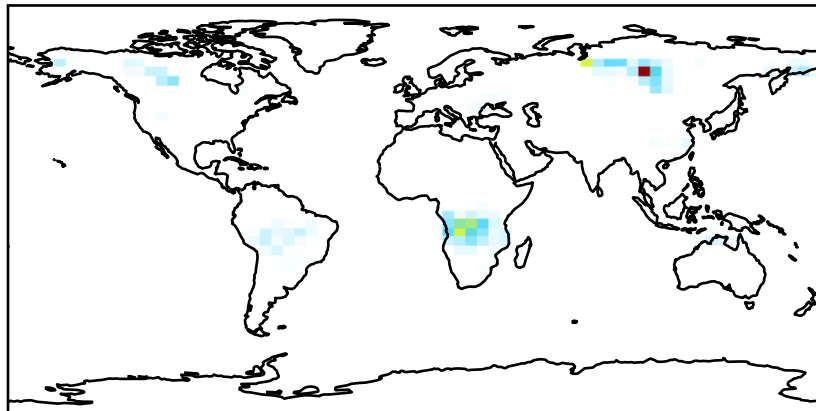


Ratio
Dev/Ref, Fixed Range

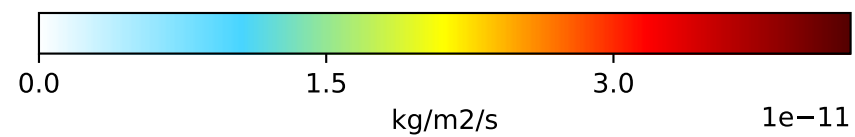
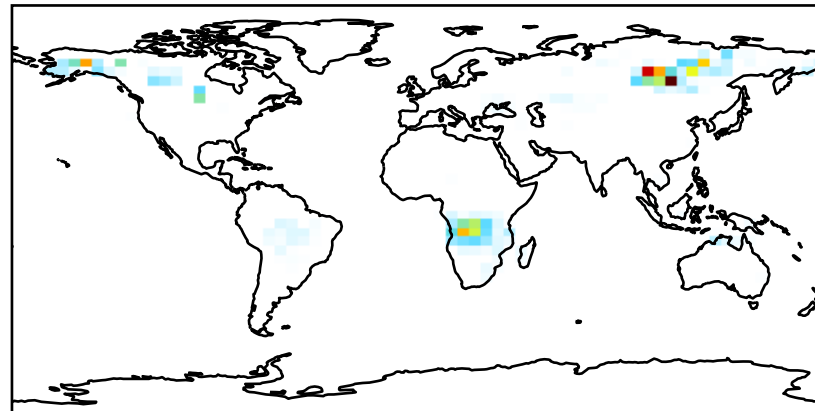


EmisBCPO_BioBurn

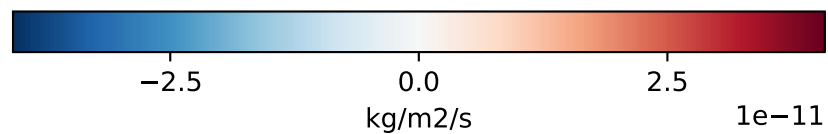
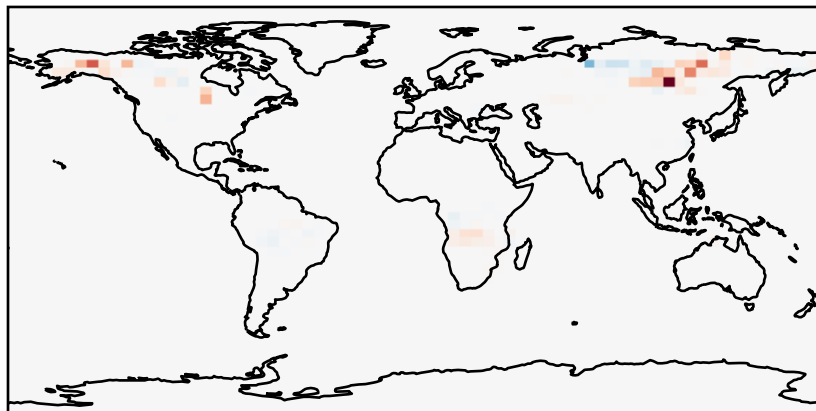
GCC_12.9.0 (Ref)
4.0x5.0



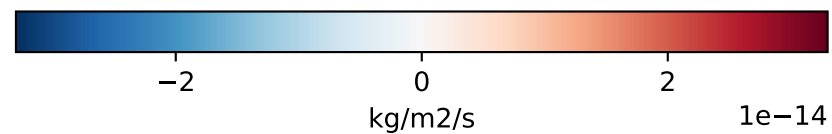
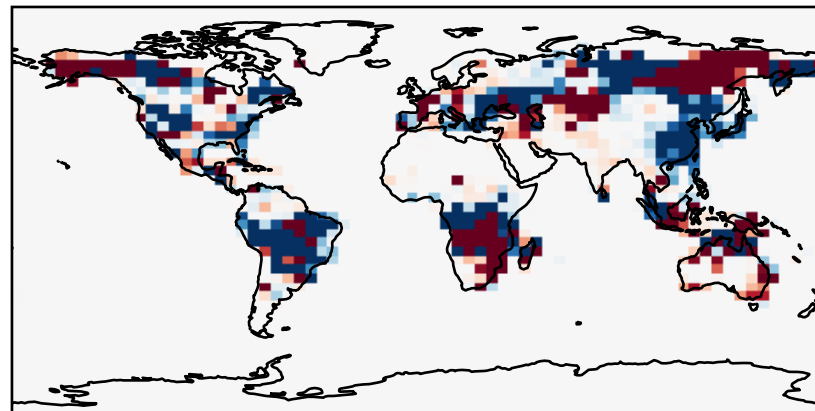
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



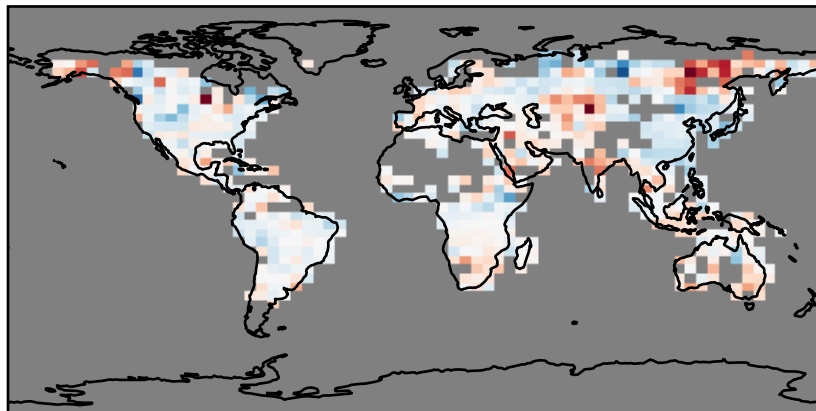
Difference
Dev - Ref, Dynamic Range



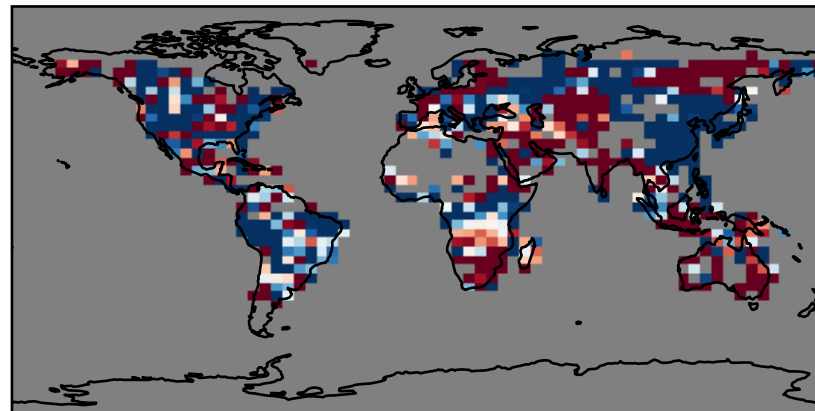
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

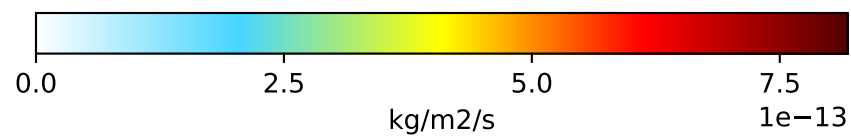
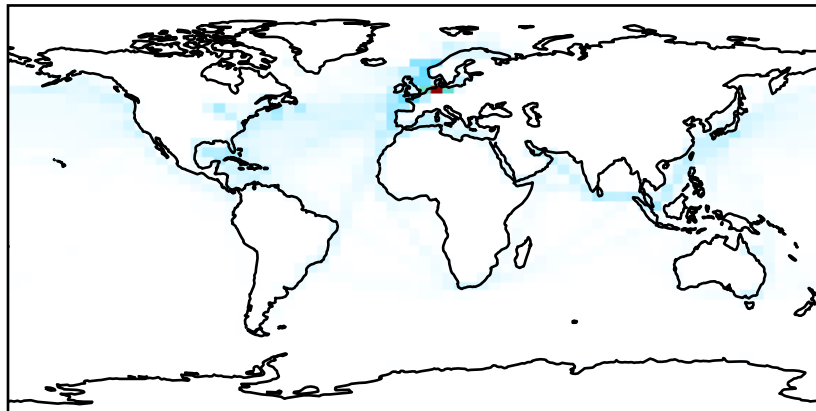


Ratio
Dev/Ref, Fixed Range

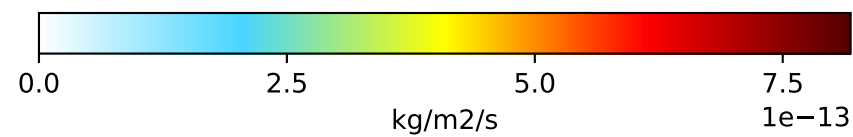
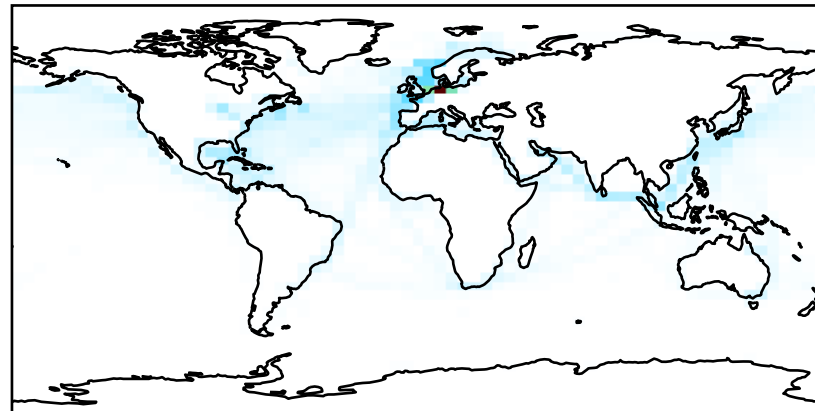


EmisBCPO_Ship

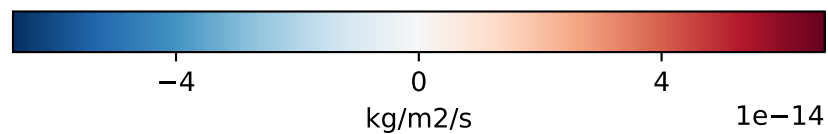
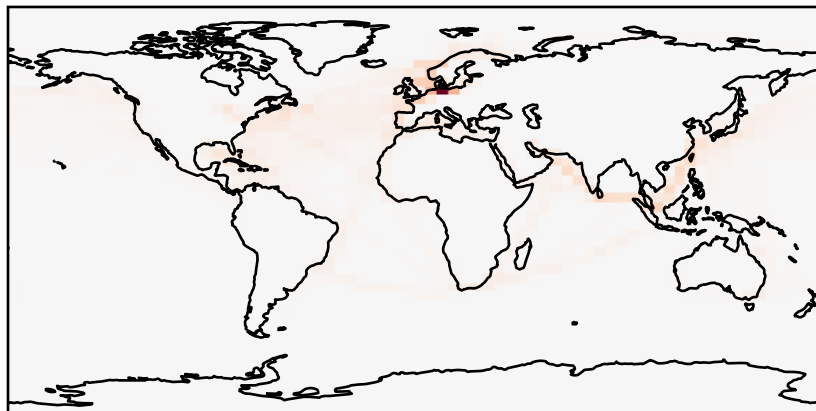
GCC_12.9.0 (Ref)
4.0x5.0



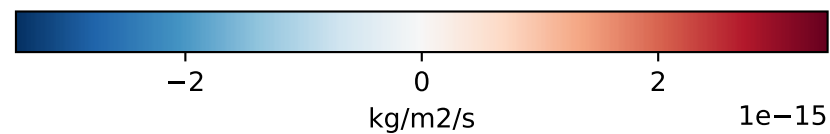
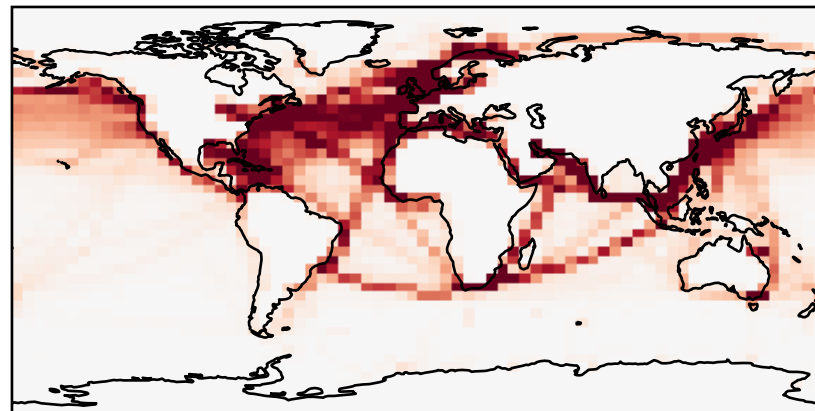
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



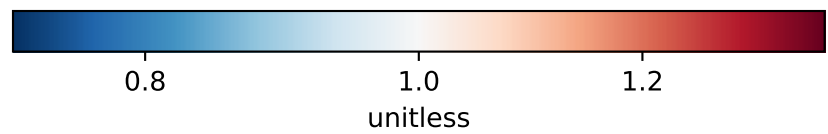
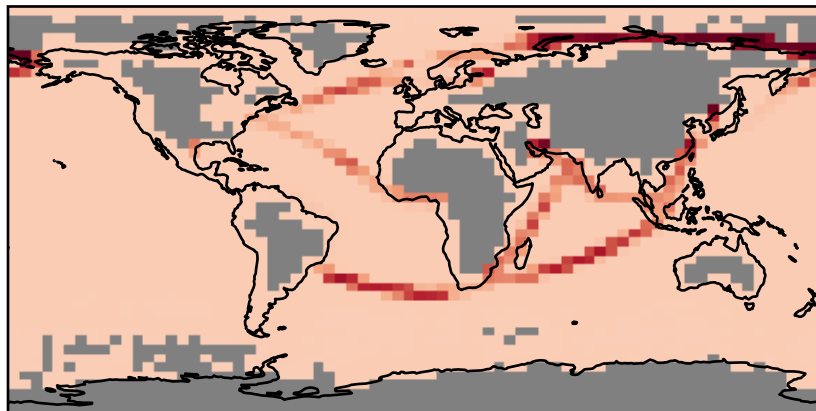
Difference
Dev - Ref, Dynamic Range



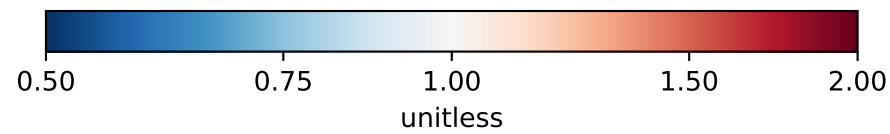
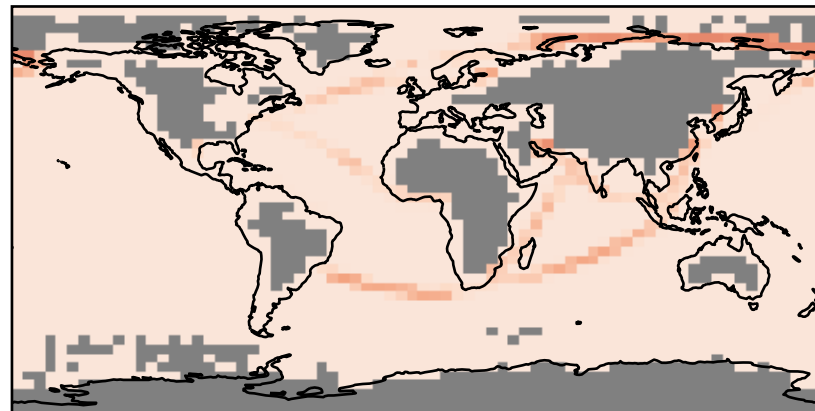
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

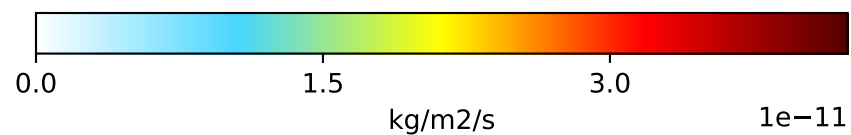
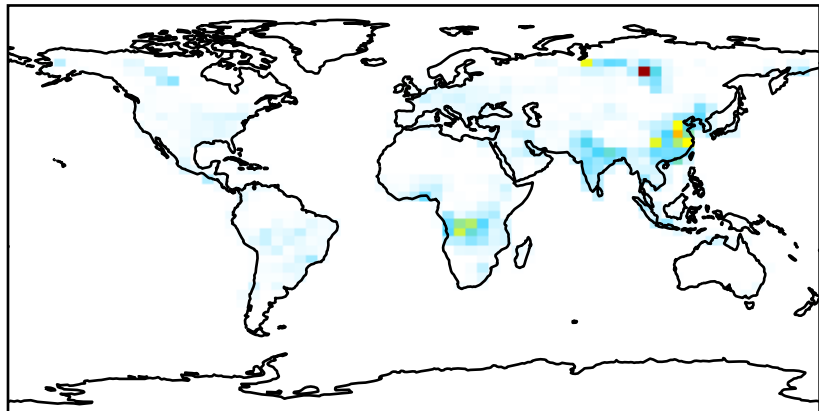


Ratio
Dev/Ref, Fixed Range

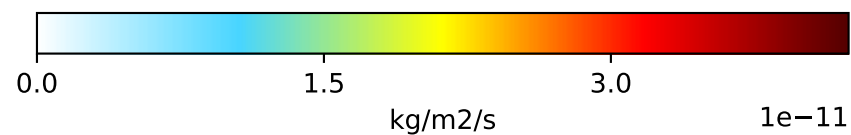
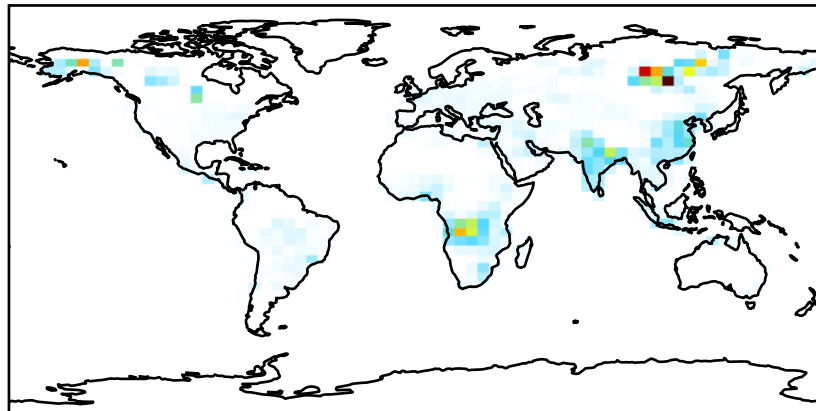


EmisBCPO_Total

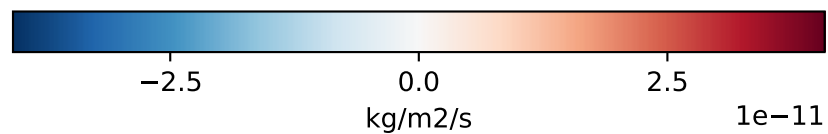
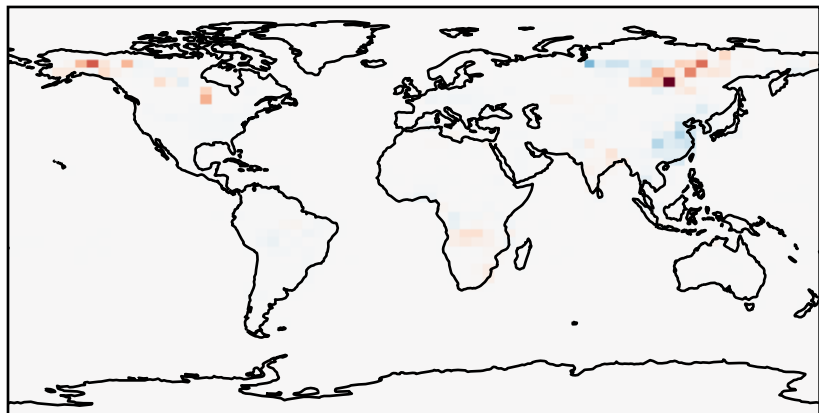
GCC_12.9.0 (Ref)
4.0x5.0



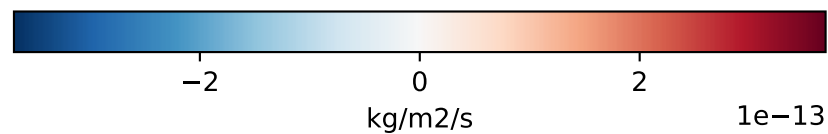
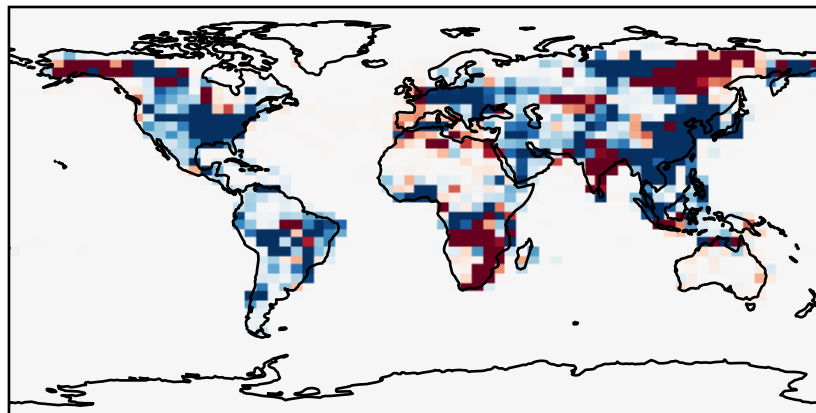
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



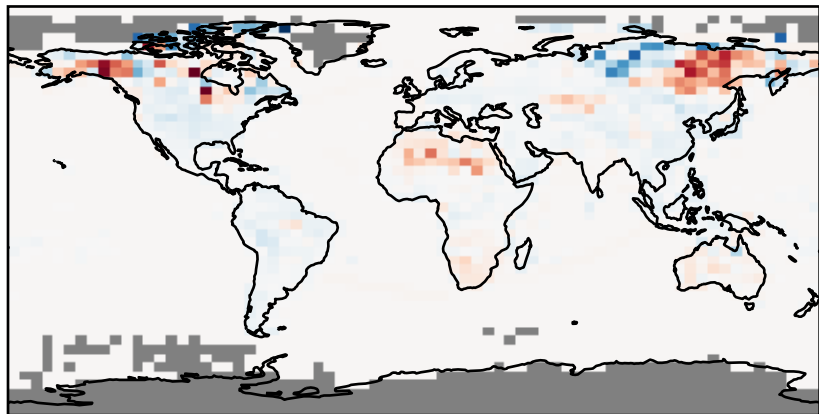
Difference
Dev - Ref, Dynamic Range



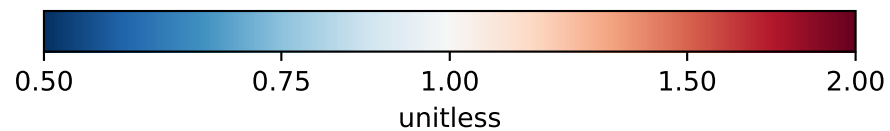
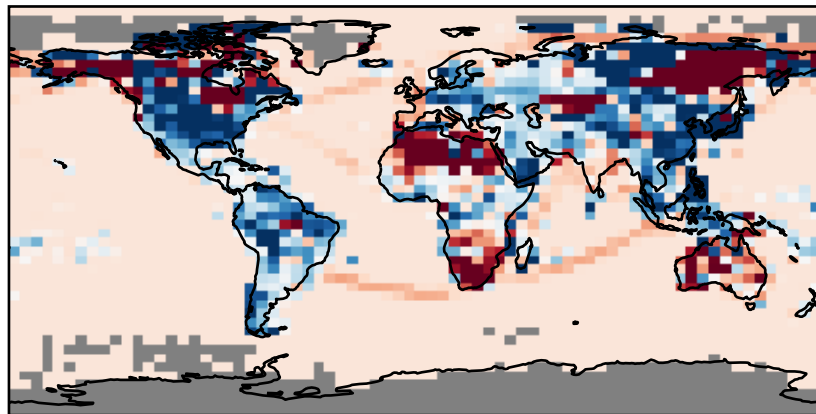
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

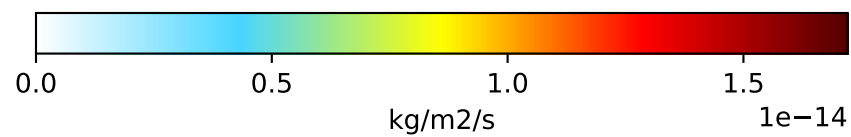
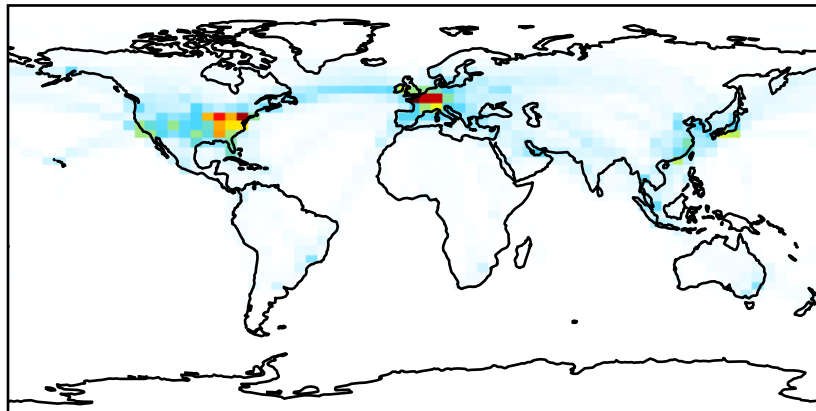


Ratio
Dev/Ref, Fixed Range

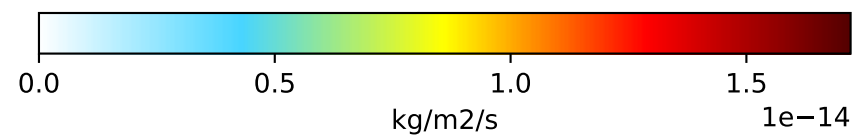
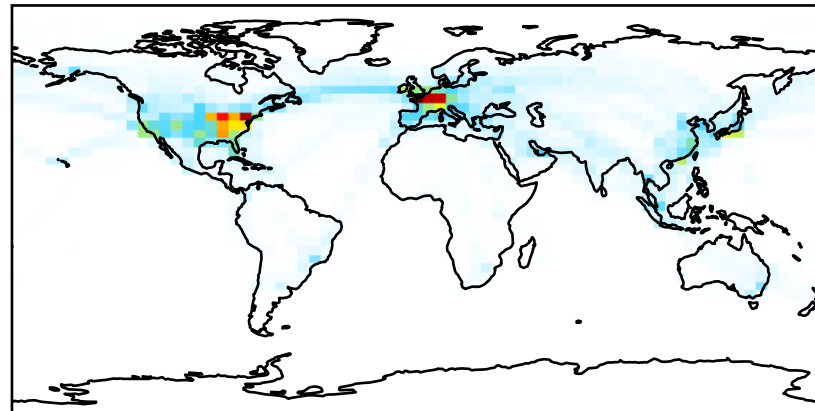


EmisOCPI_Aircraft

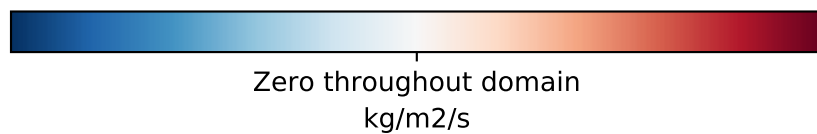
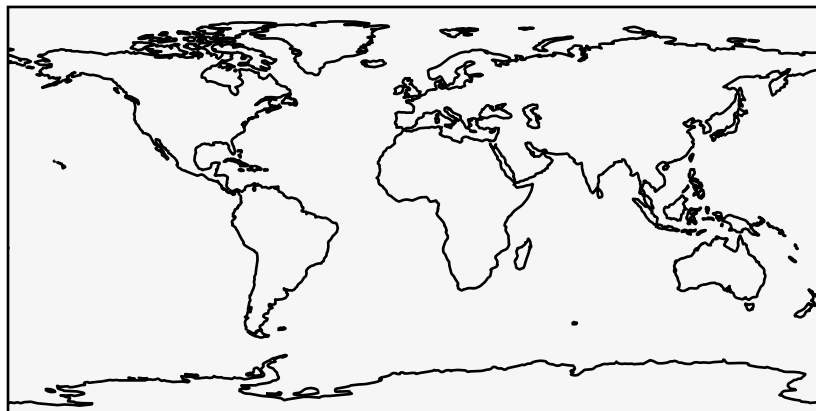
GCC_12.9.0 (Ref)
4.0x5.0



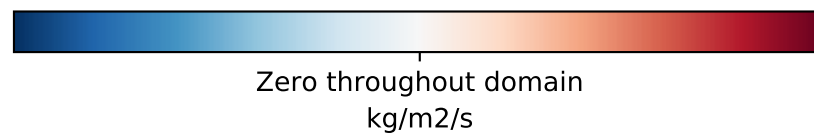
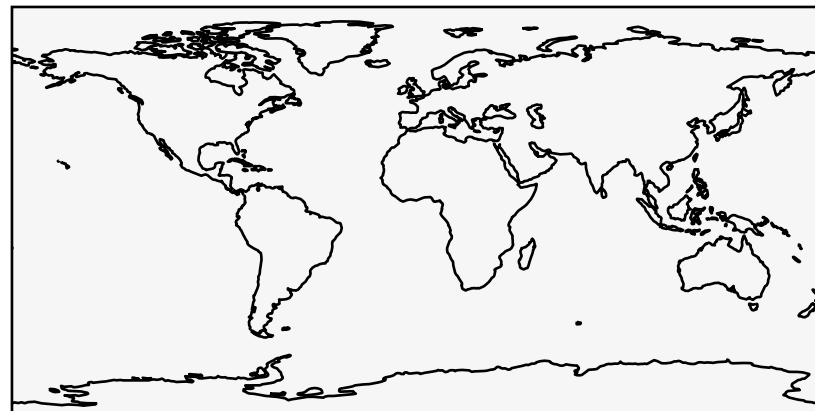
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



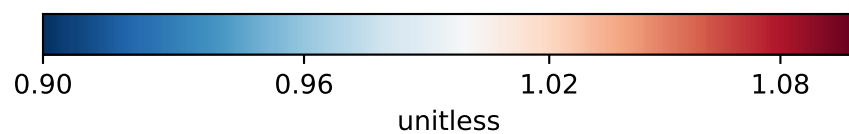
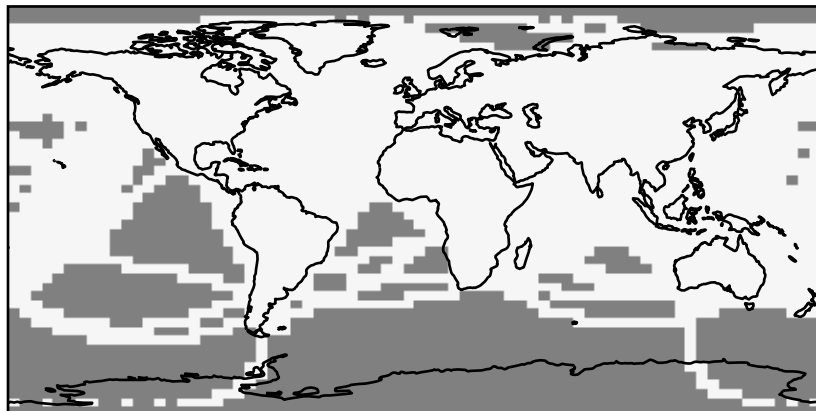
Difference
Dev - Ref, Dynamic Range



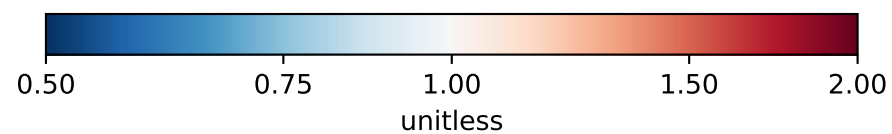
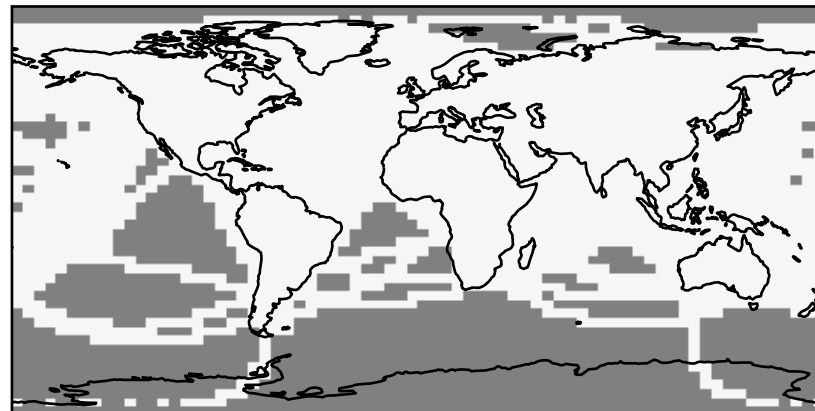
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

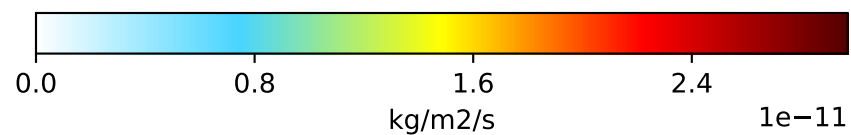
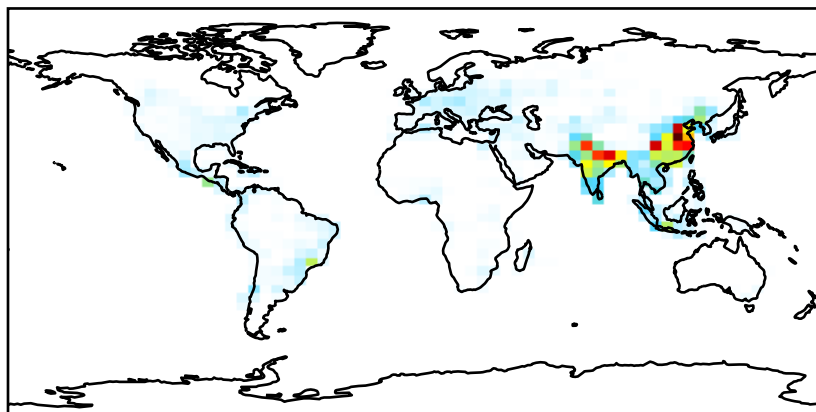


Ratio
Dev/Ref, Fixed Range

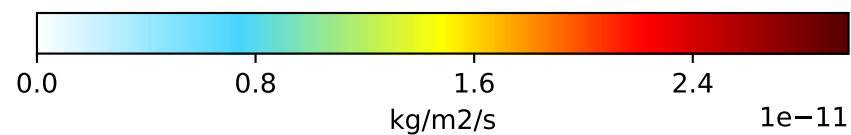
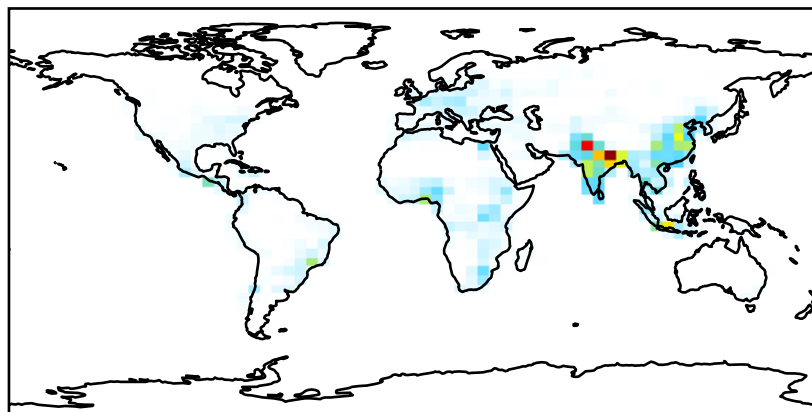


EmisOCPI_Anthro

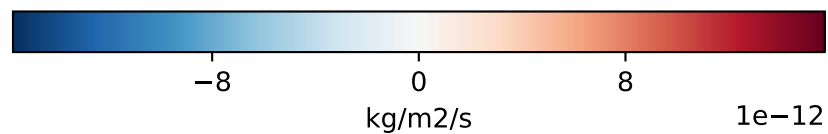
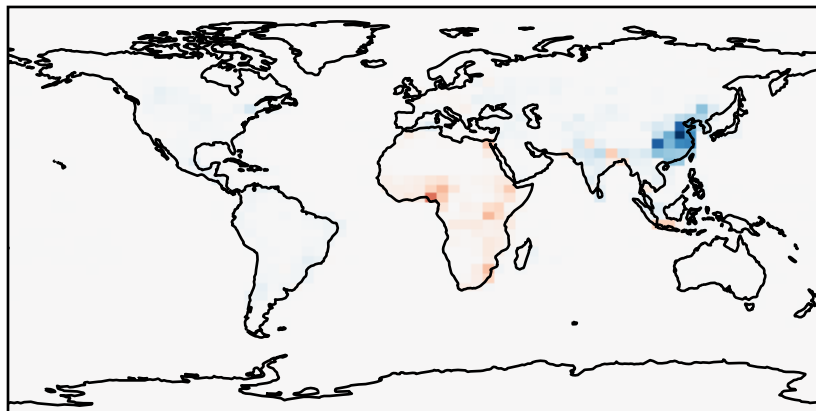
GCC_12.9.0 (Ref)
4.0x5.0



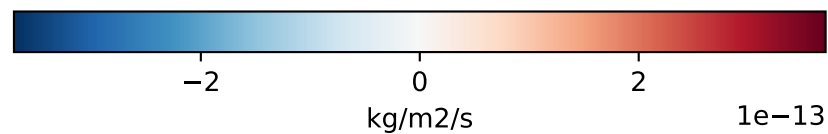
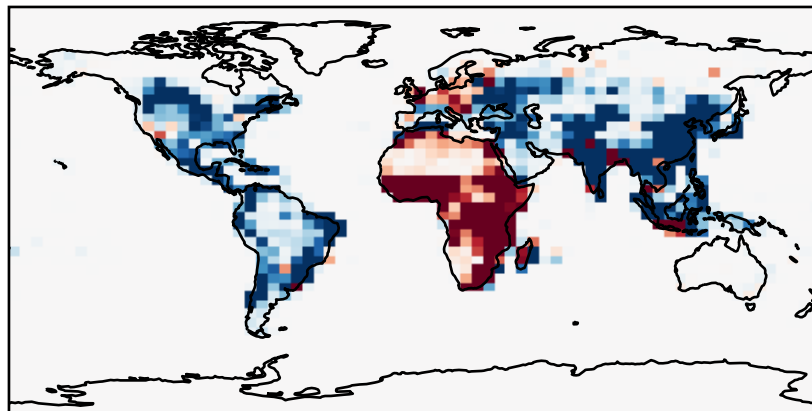
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



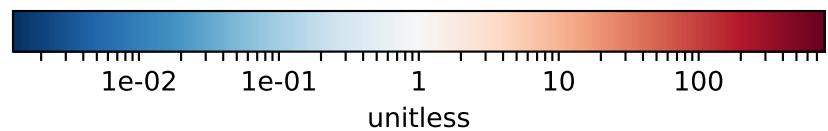
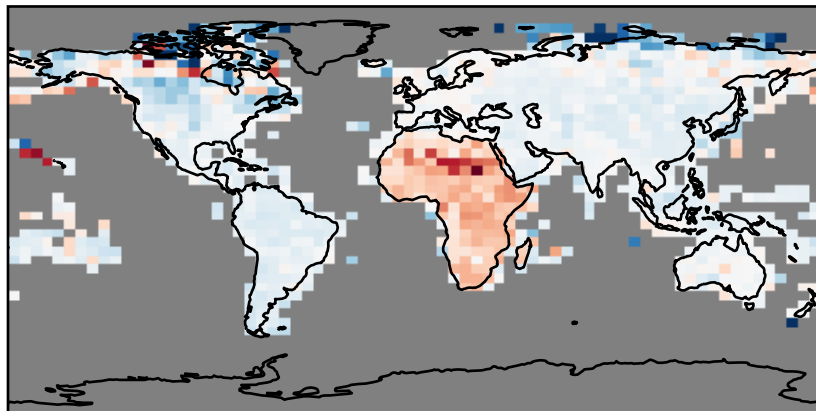
Difference
Dev - Ref, Dynamic Range



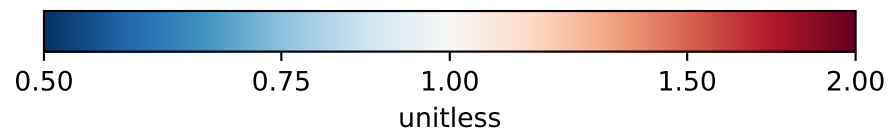
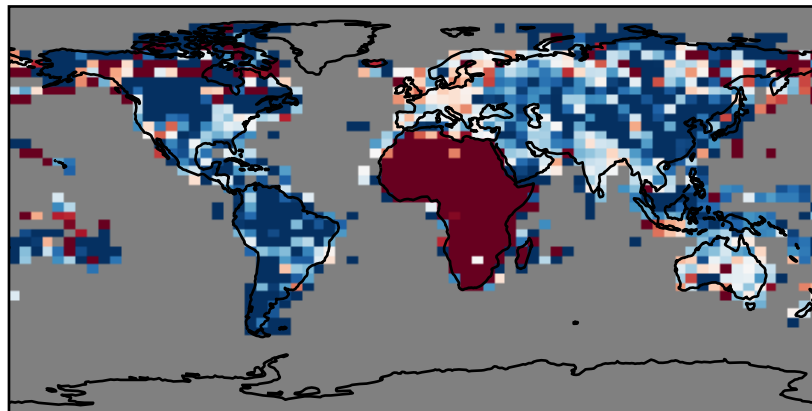
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

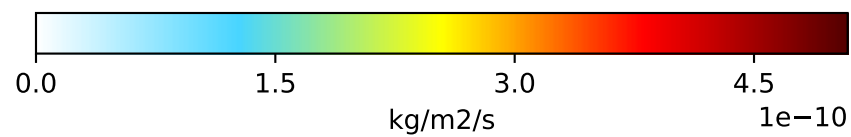
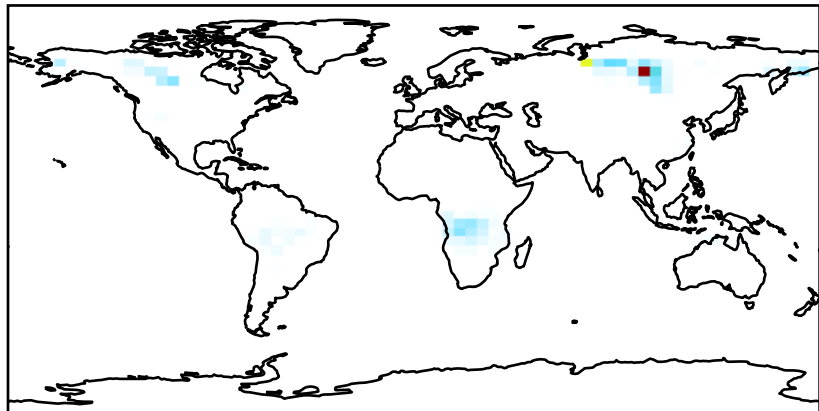


Ratio
Dev/Ref, Fixed Range

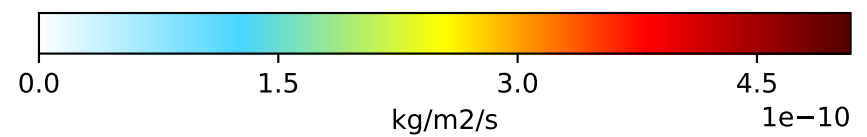
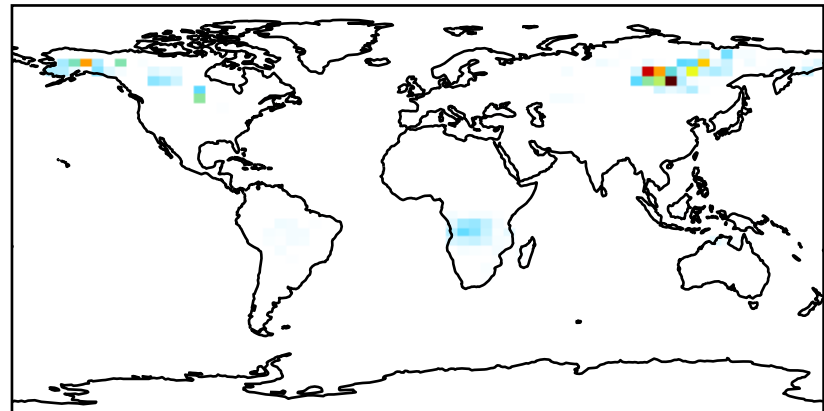


EmisOCPI_BioBurn

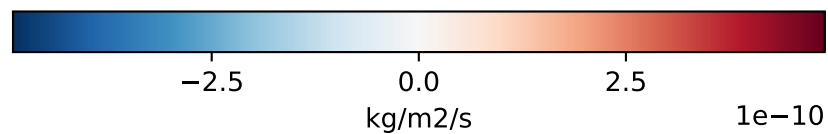
GCC_12.9.0 (Ref)
4.0x5.0



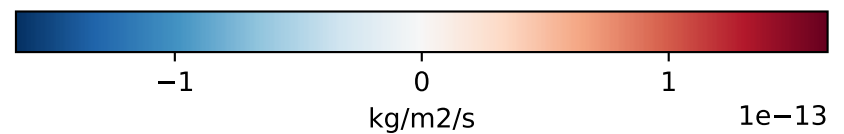
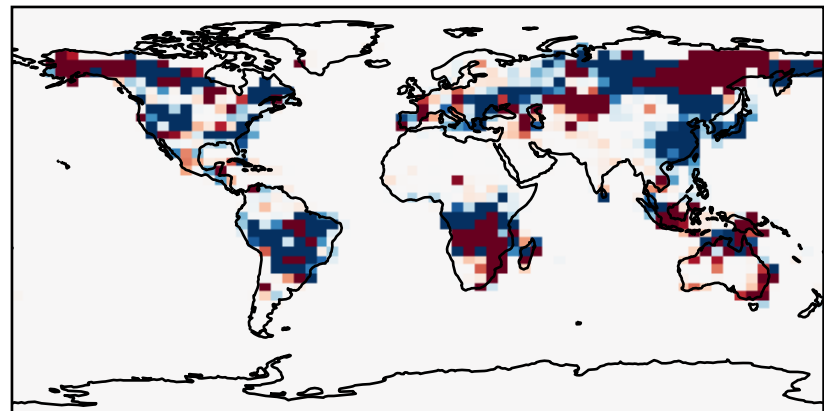
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



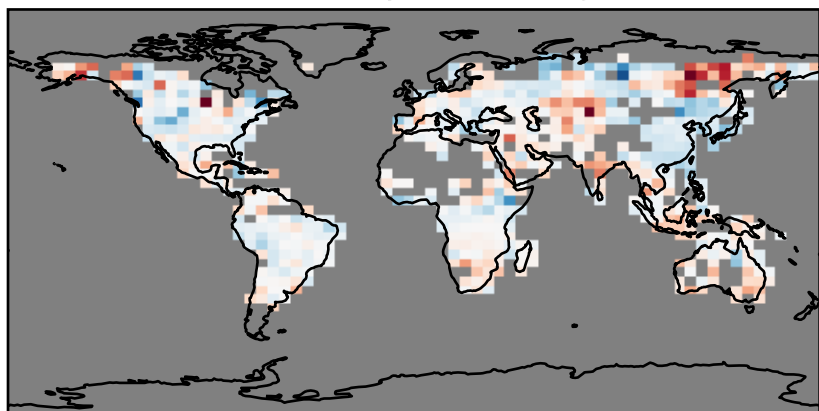
Difference
Dev - Ref, Dynamic Range



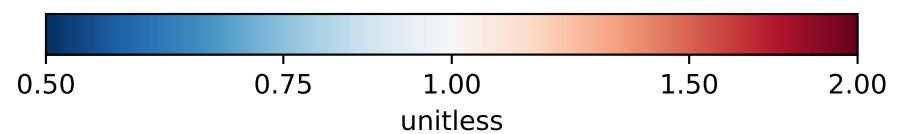
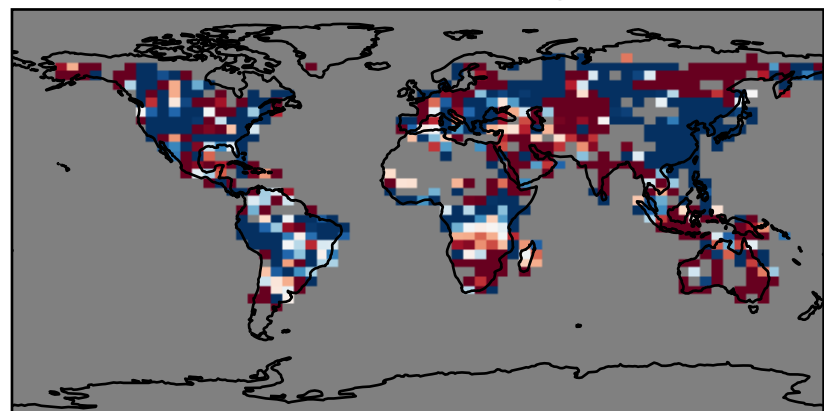
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

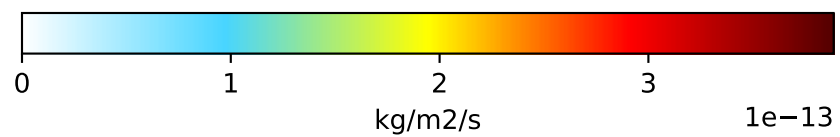
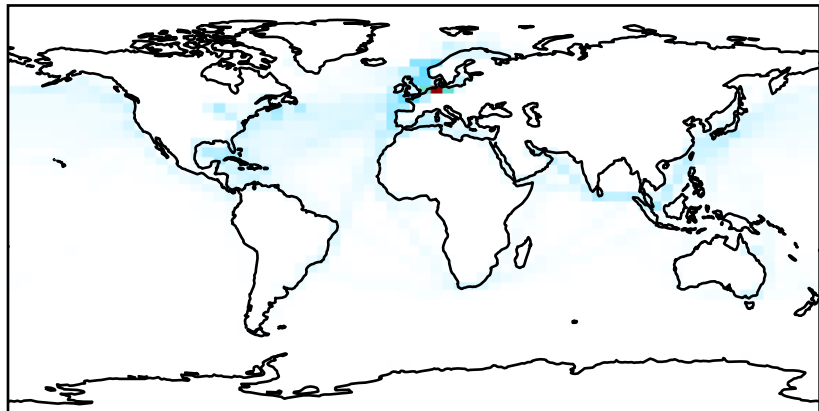


Ratio
Dev/Ref, Fixed Range

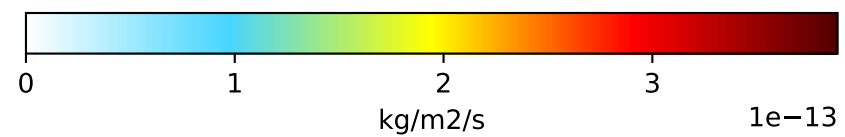
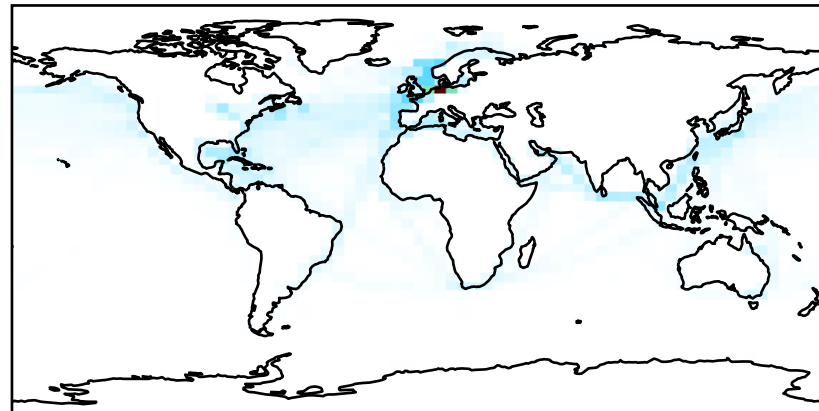


EmisOCPI_Ship

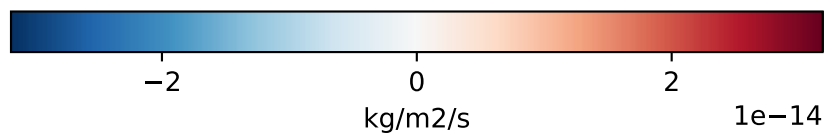
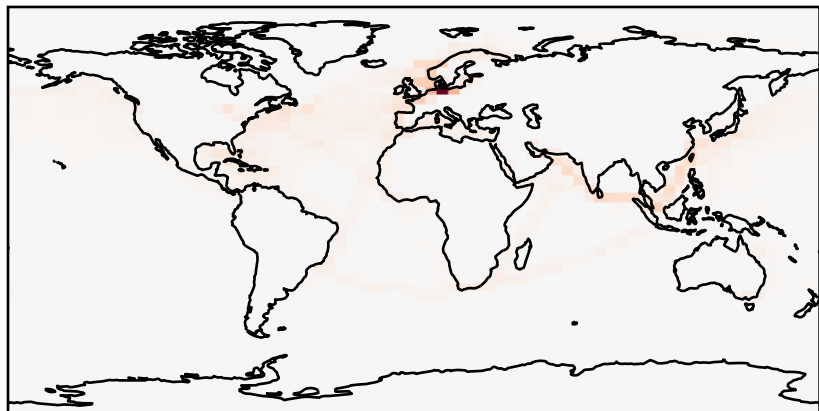
GCC_12.9.0 (Ref)
4.0x5.0



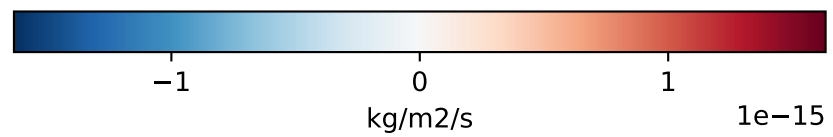
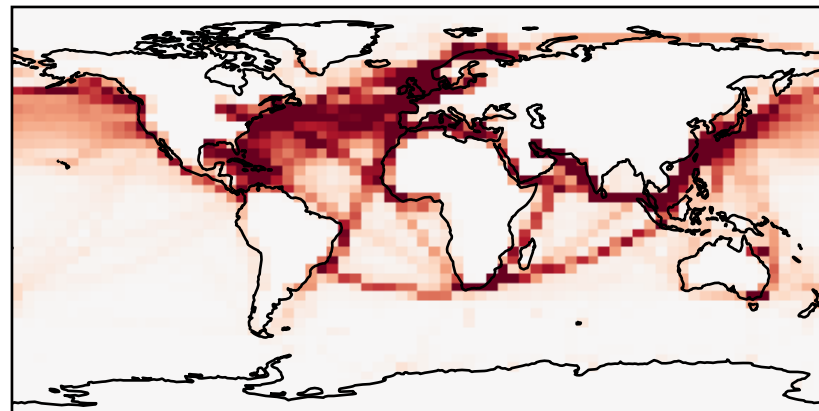
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



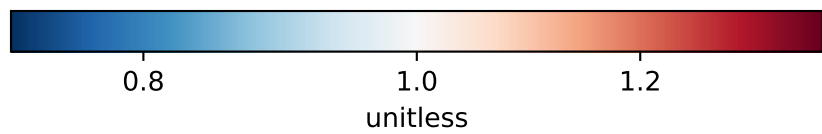
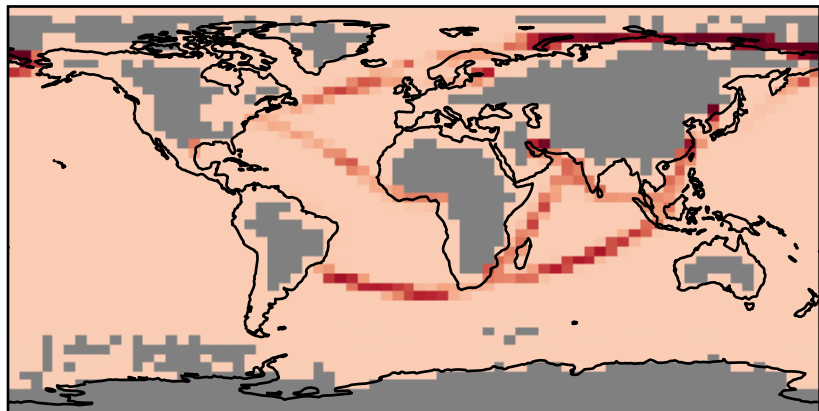
Difference
Dev - Ref, Dynamic Range



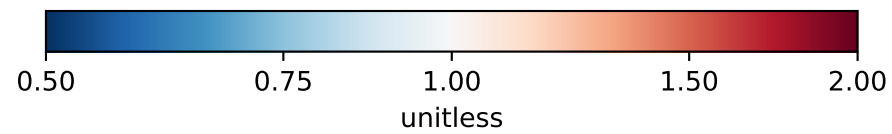
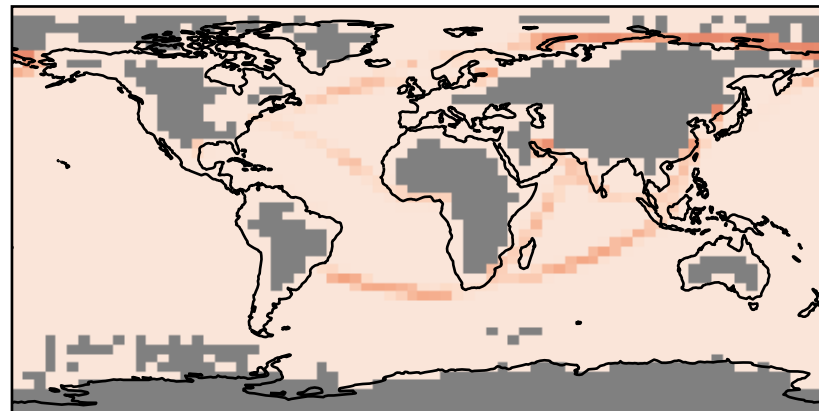
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

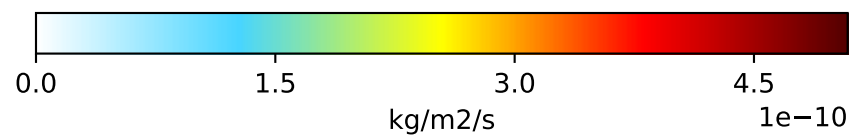
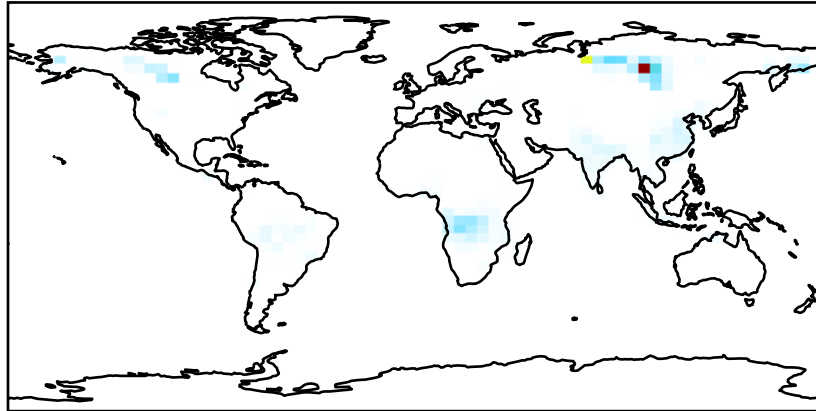


Ratio
Dev/Ref, Fixed Range

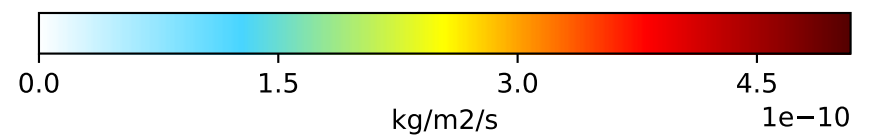
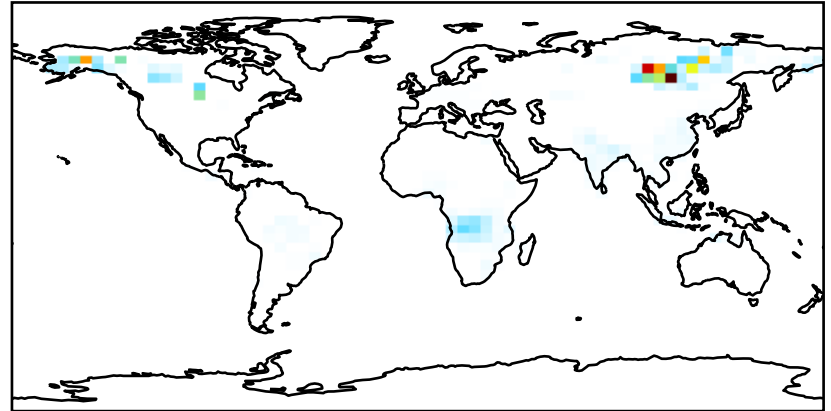


EmisOCPI_Total

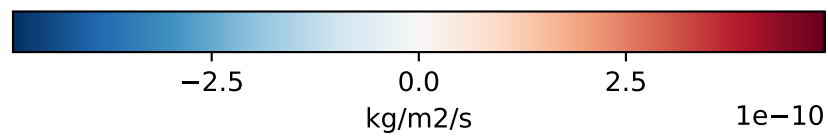
GCC_12.9.0 (Ref)
4.0x5.0



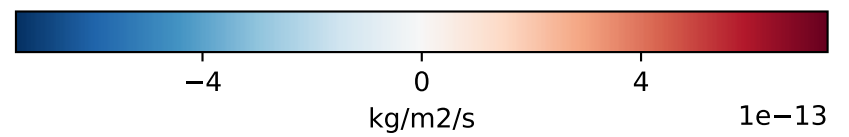
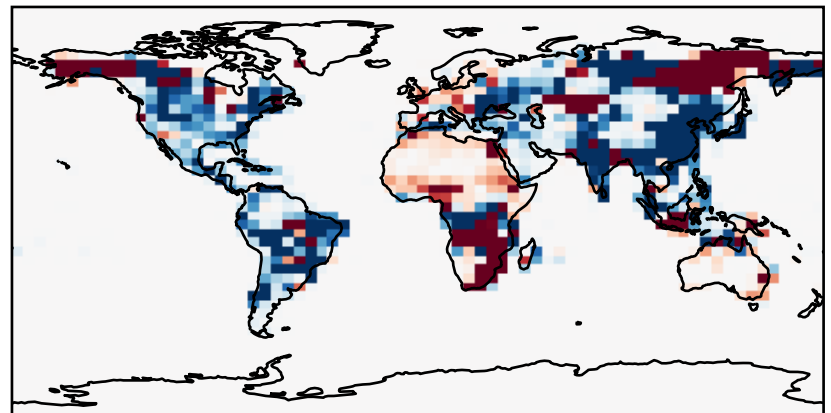
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



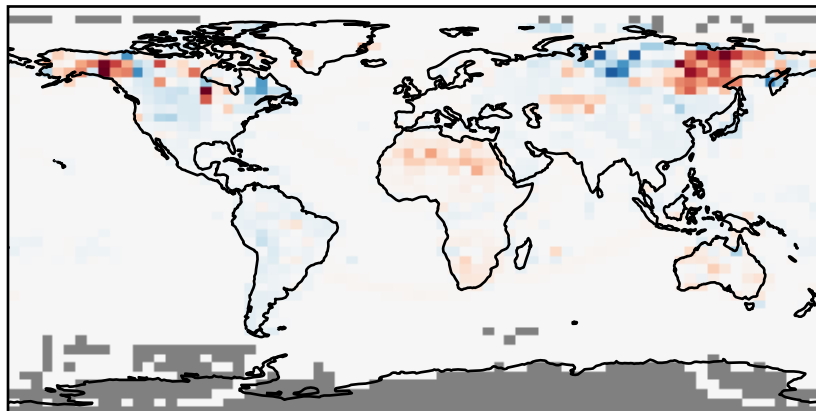
Difference
Dev - Ref, Dynamic Range



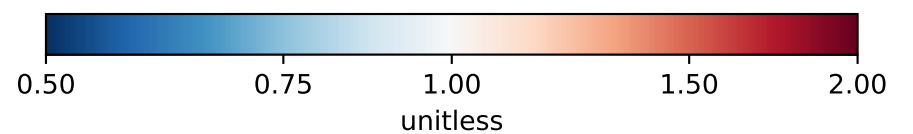
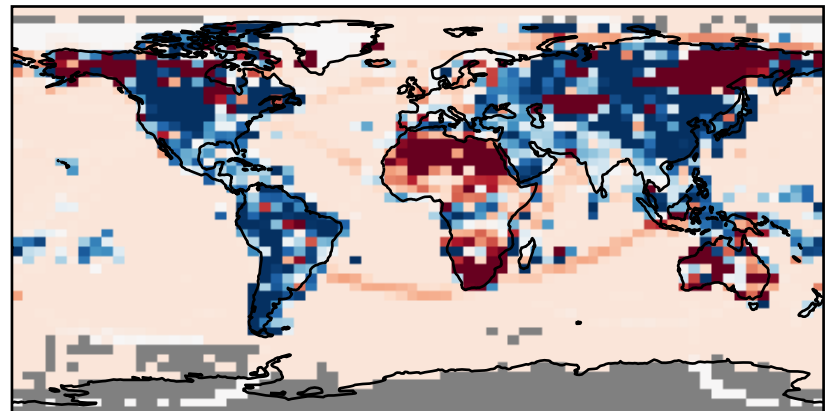
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

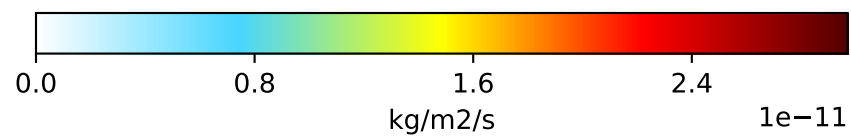
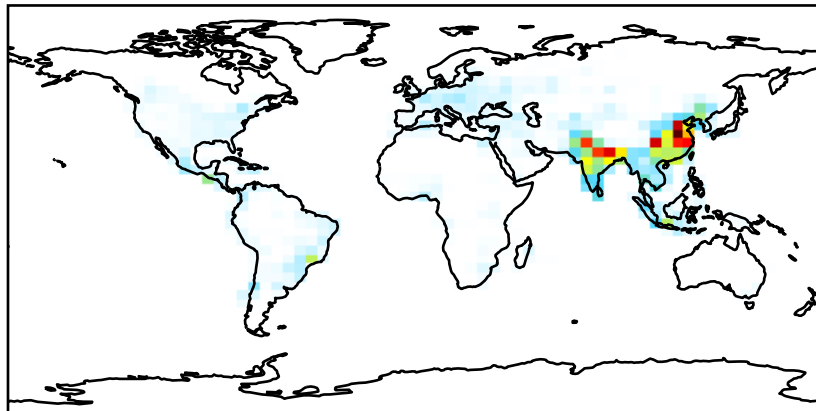


Ratio
Dev/Ref, Fixed Range

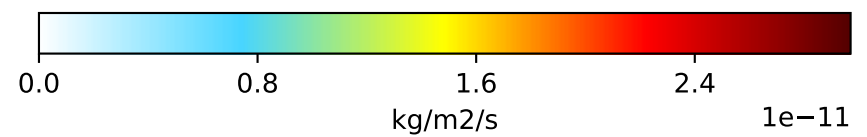
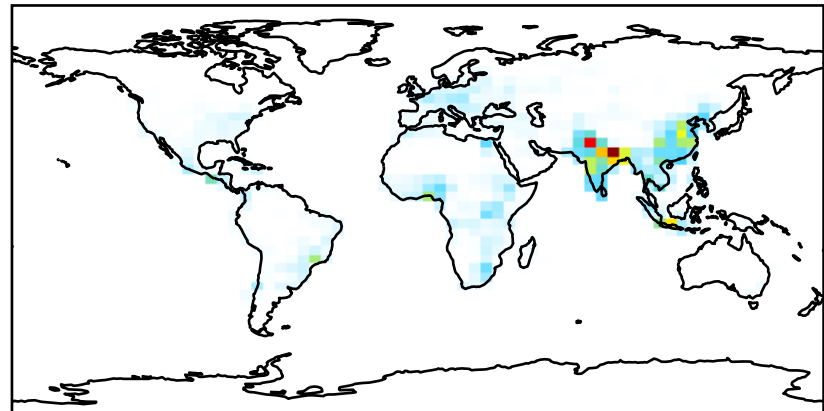


EmisOCPO_Anthro

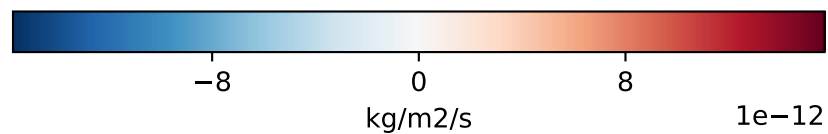
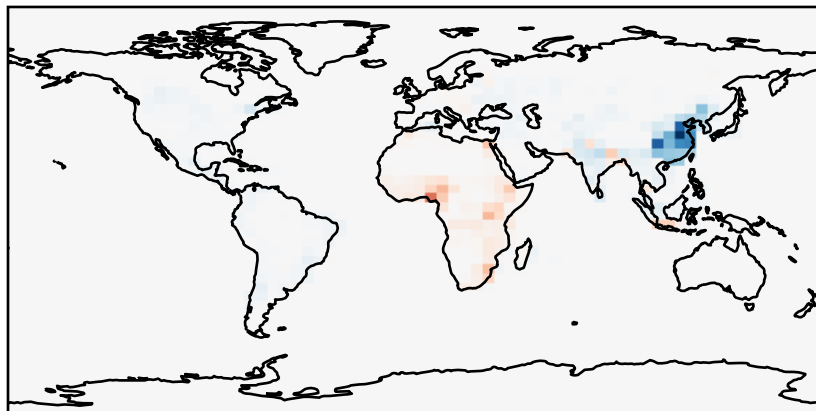
GCC_12.9.0 (Ref)
4.0x5.0



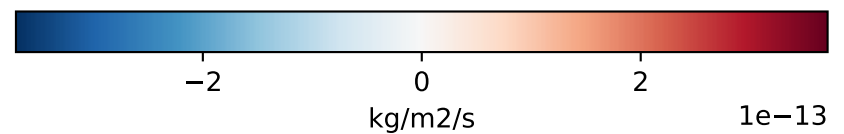
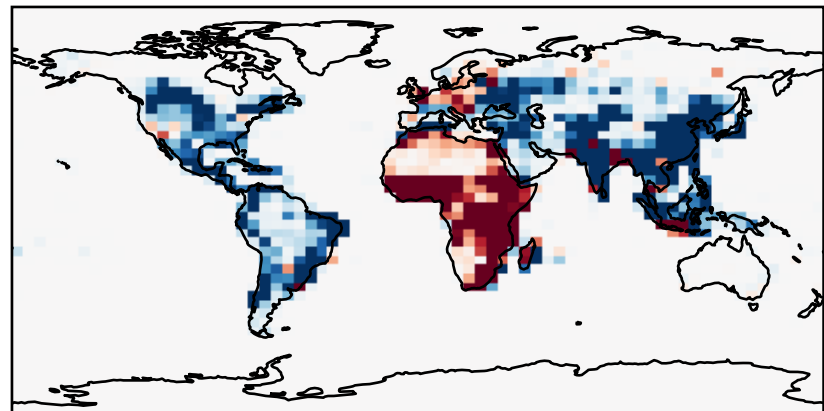
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



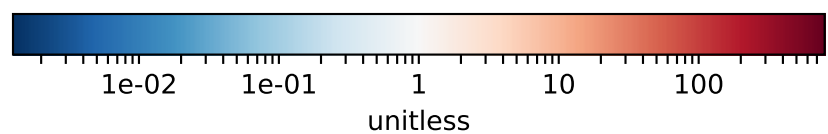
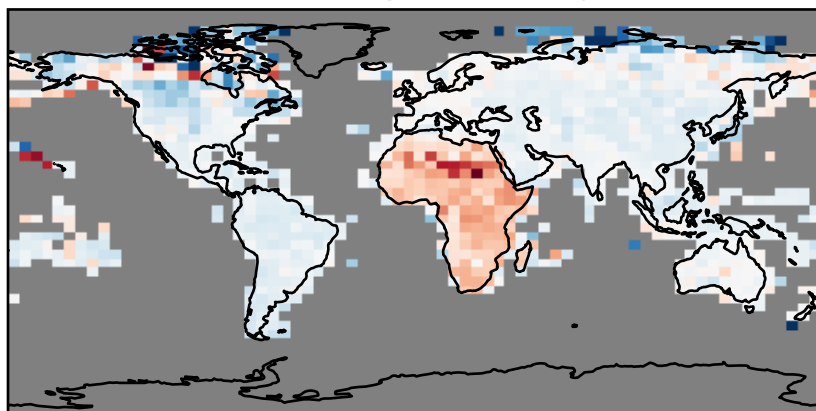
Difference
Dev - Ref, Dynamic Range



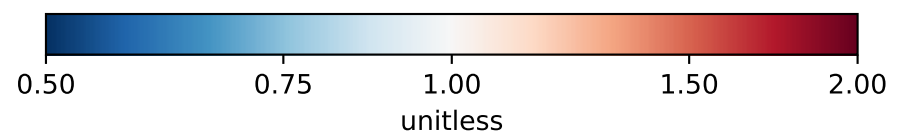
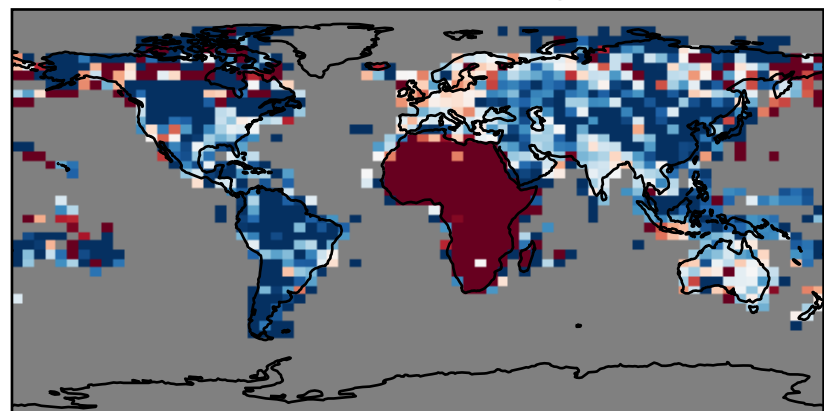
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

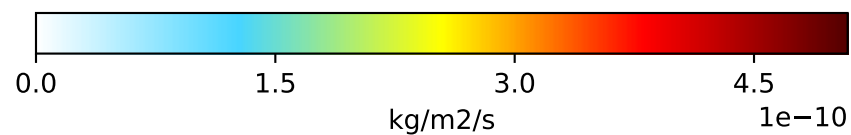
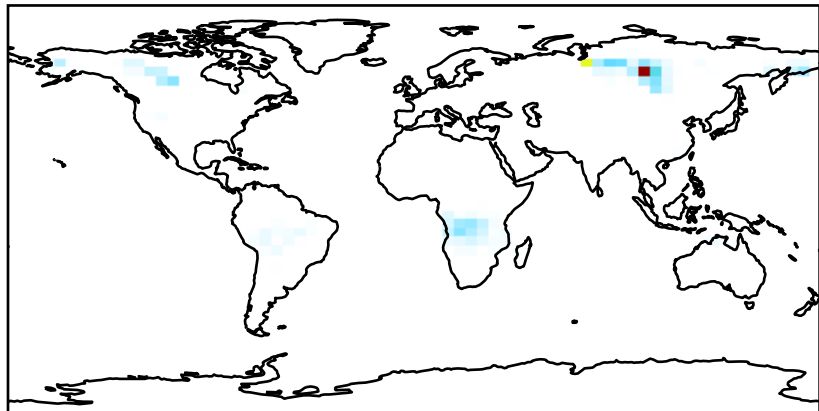


Ratio
Dev/Ref, Fixed Range

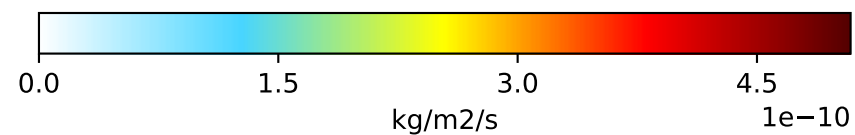
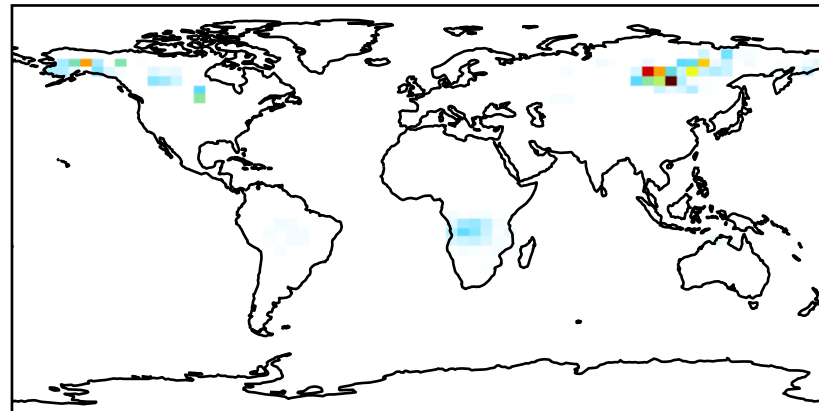


EmisOCPO_BioBurn

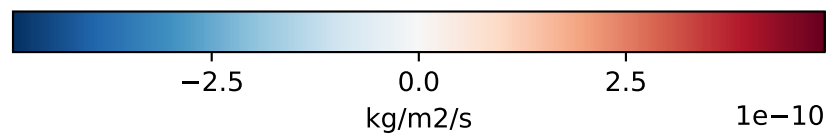
GCC_12.9.0 (Ref)
4.0x5.0



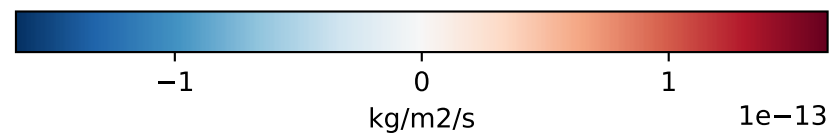
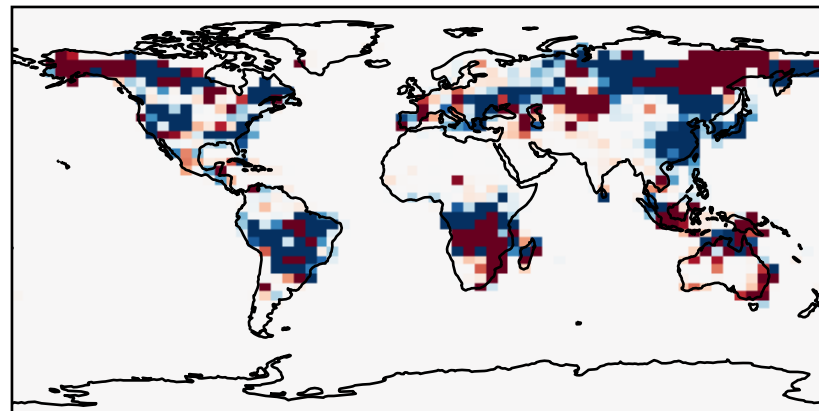
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



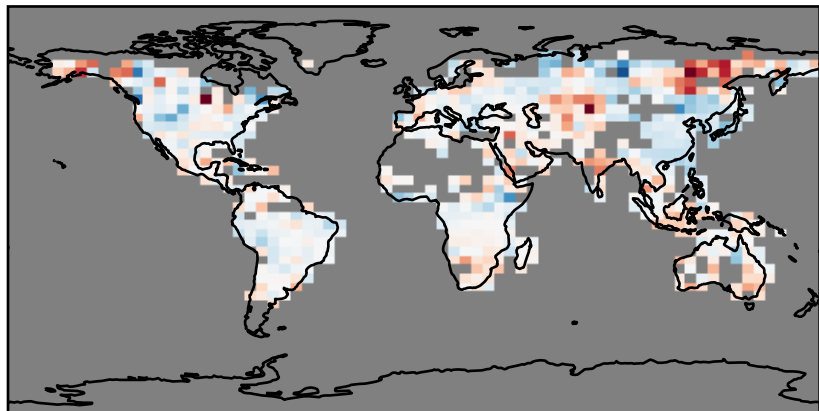
Difference
Dev - Ref, Dynamic Range



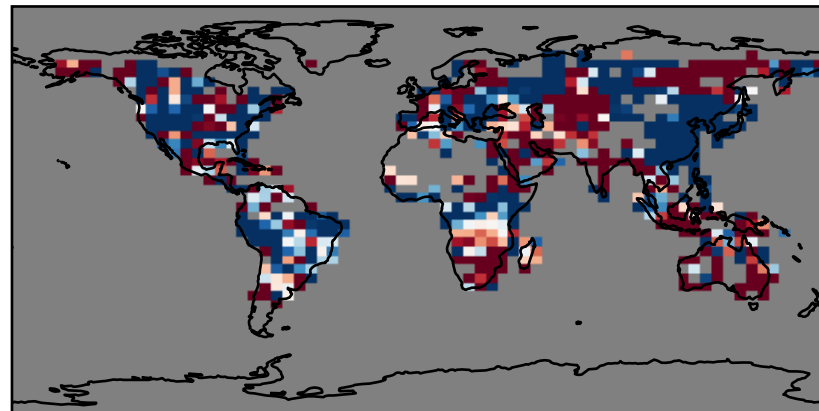
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

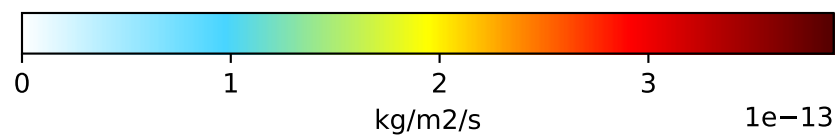
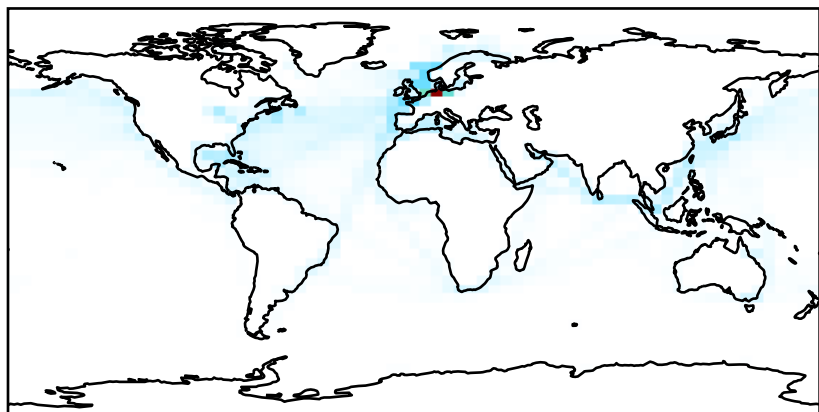


Ratio
Dev/Ref, Fixed Range

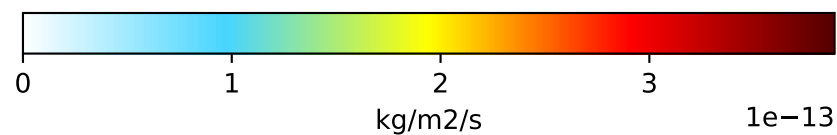
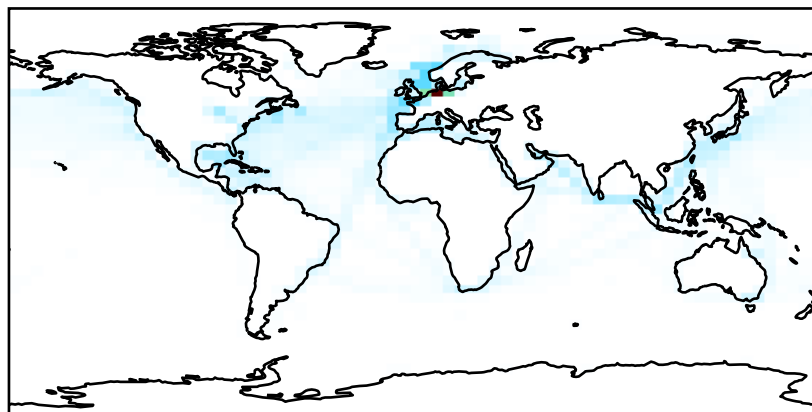


EmisOCPO_Ship

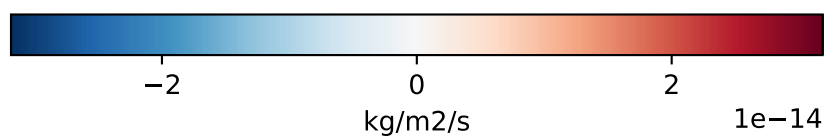
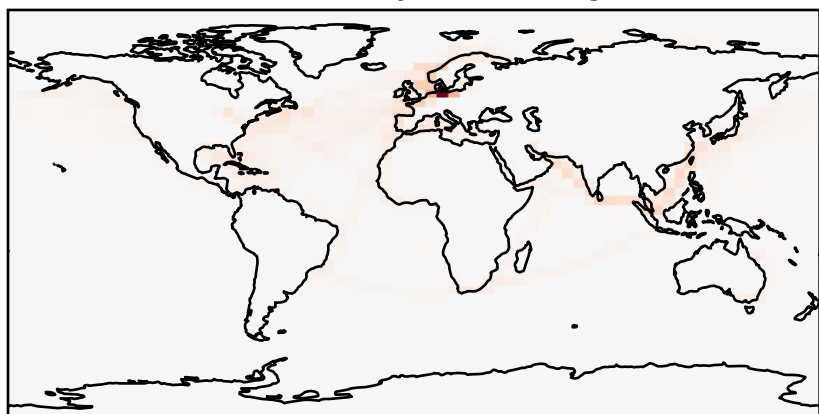
GCC_12.9.0 (Ref)
4.0x5.0



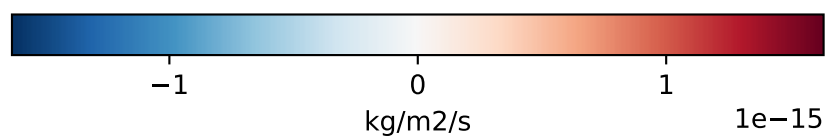
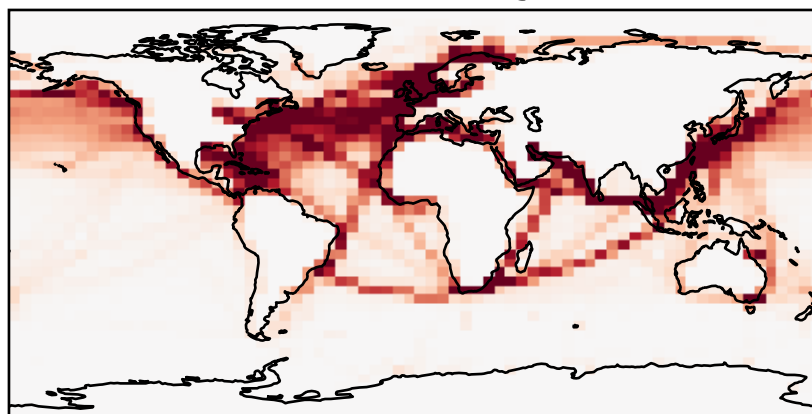
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



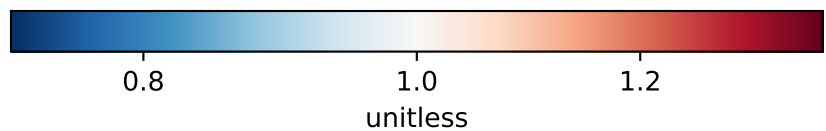
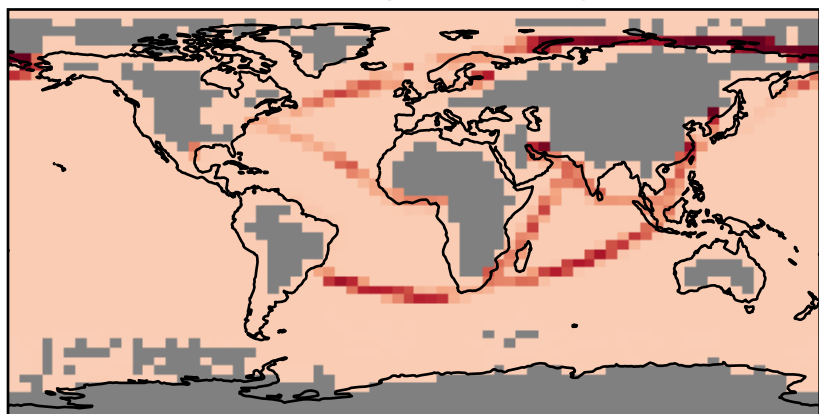
Difference
Dev - Ref, Dynamic Range



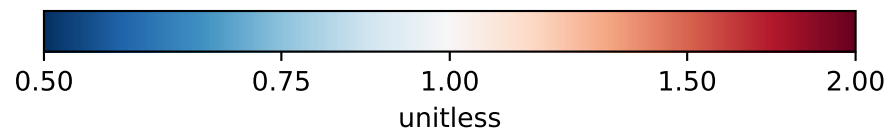
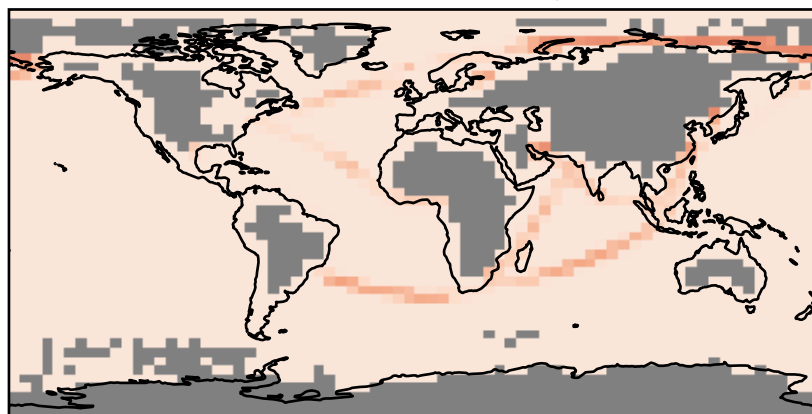
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

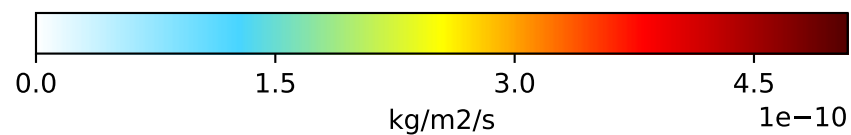
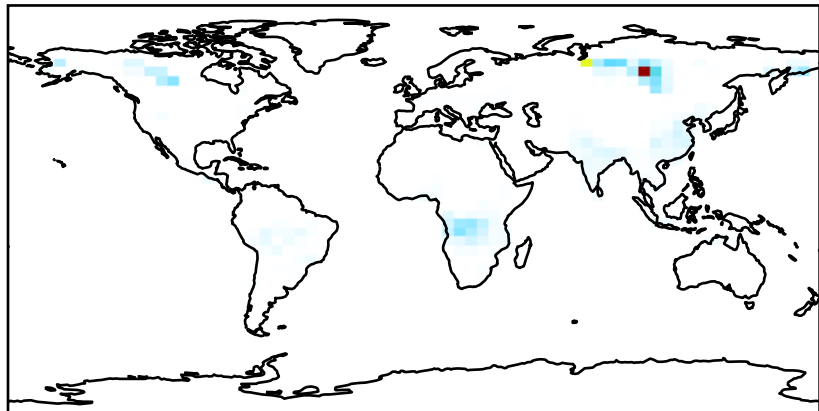


Ratio
Dev/Ref, Fixed Range

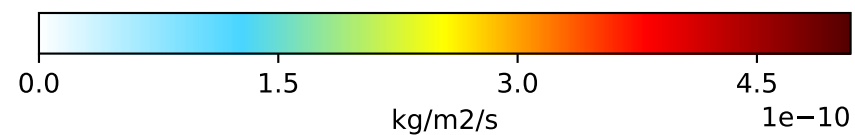
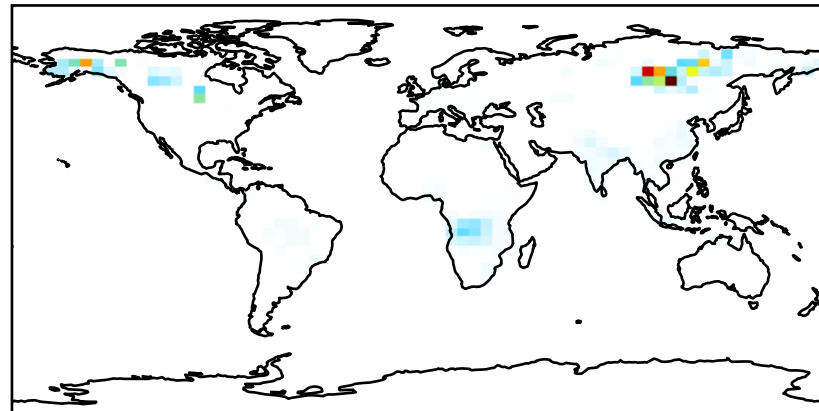


EmisOCPO_Total

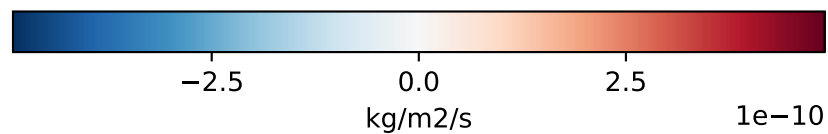
GCC_12.9.0 (Ref)
4.0x5.0



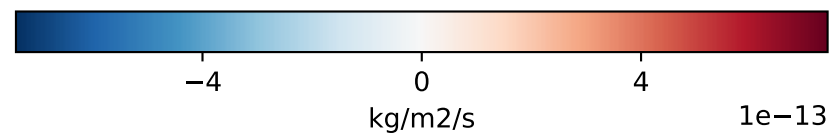
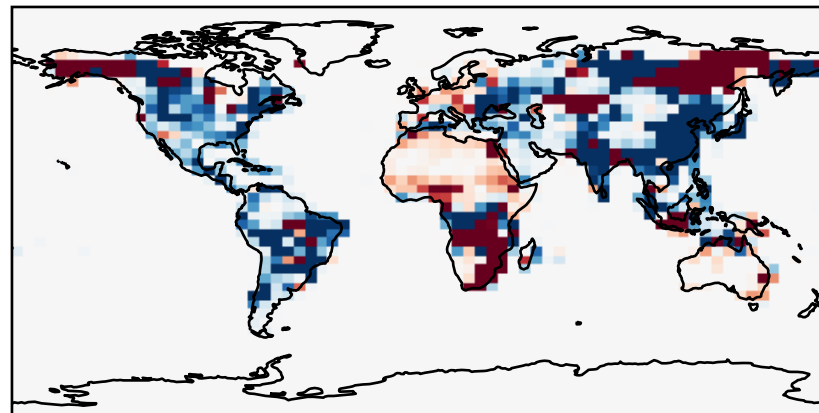
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



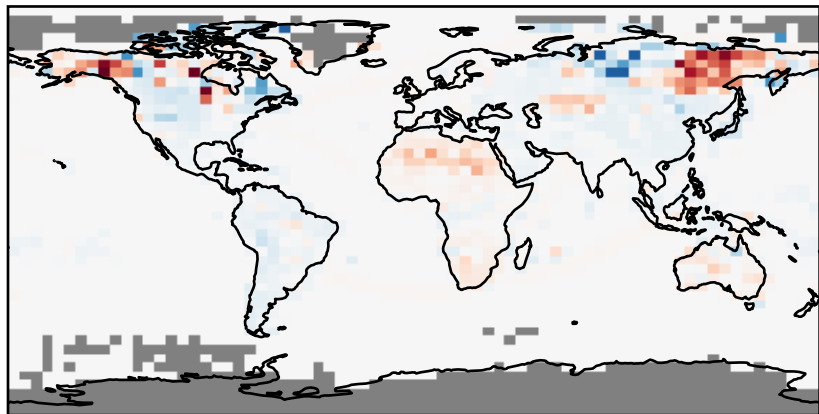
Difference
Dev - Ref, Dynamic Range



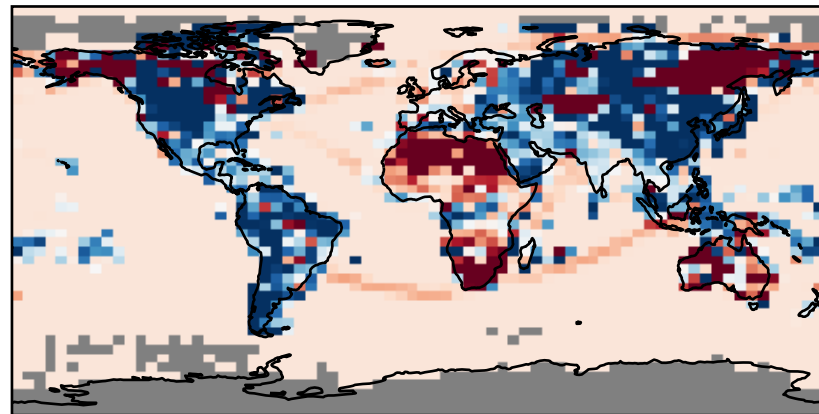
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

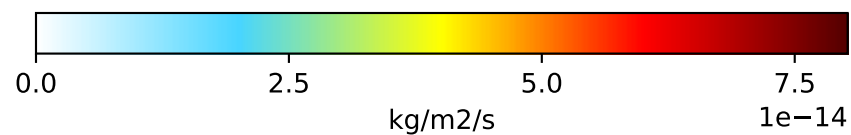
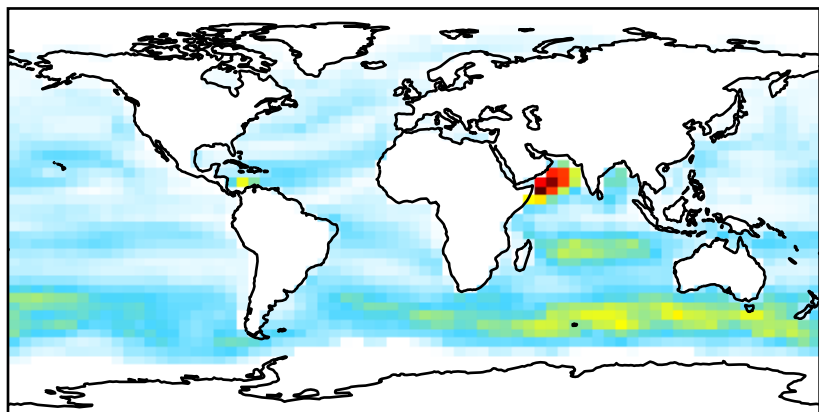


Ratio
Dev/Ref, Fixed Range

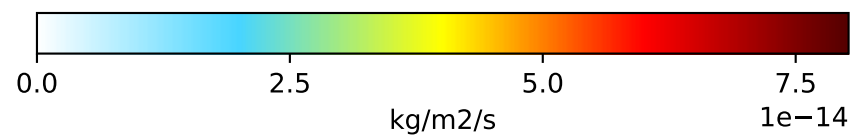
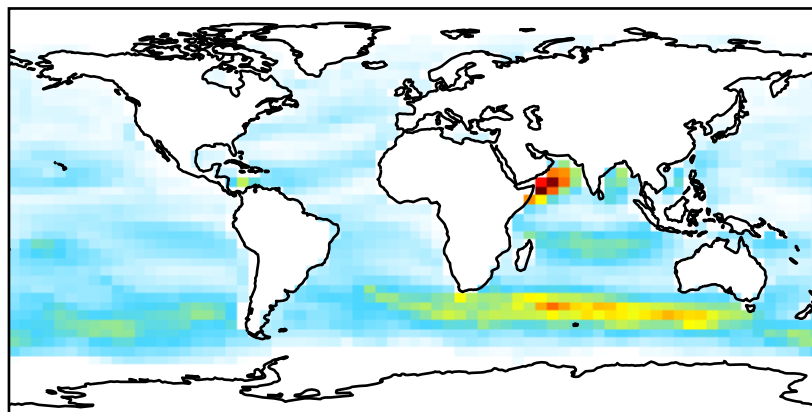


EmisBrSALA_Natural

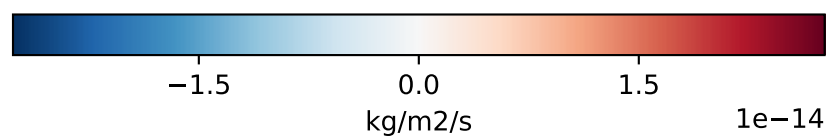
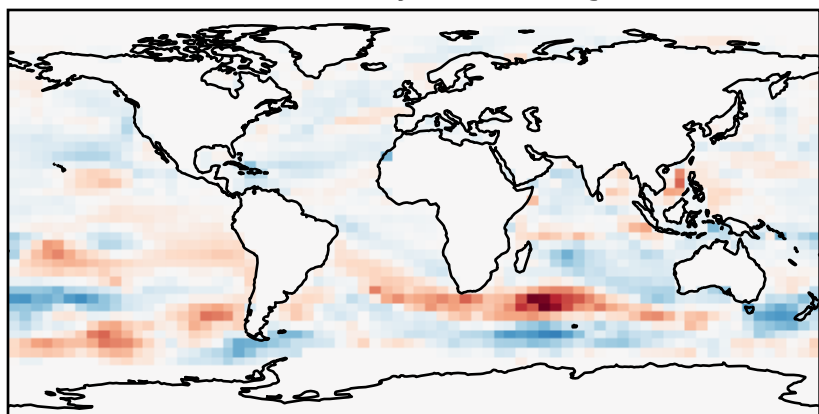
GCC_12.9.0 (Ref)
4.0x5.0



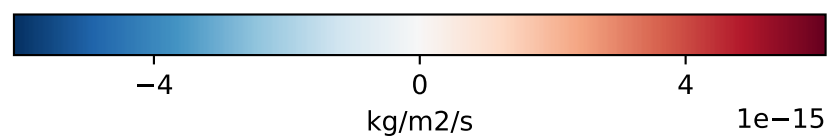
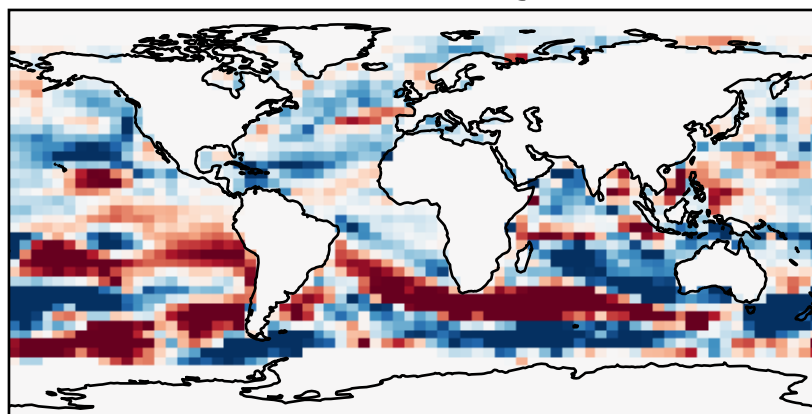
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



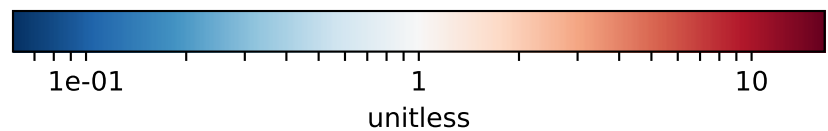
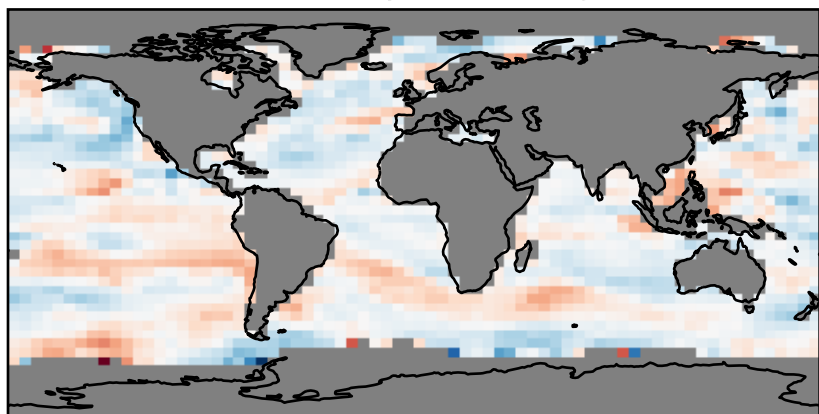
Difference
Dev - Ref, Dynamic Range



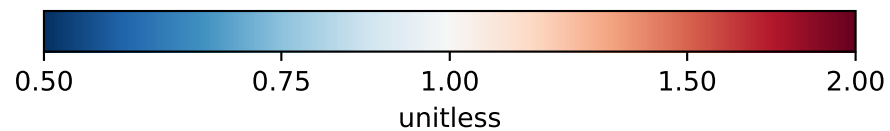
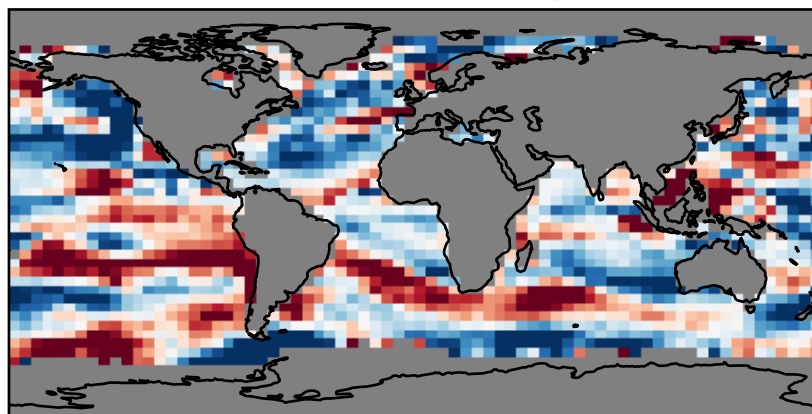
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

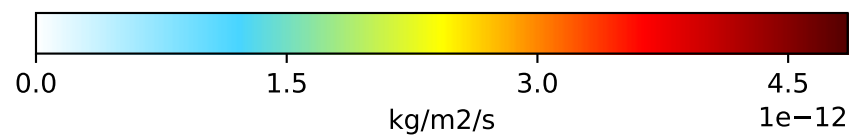
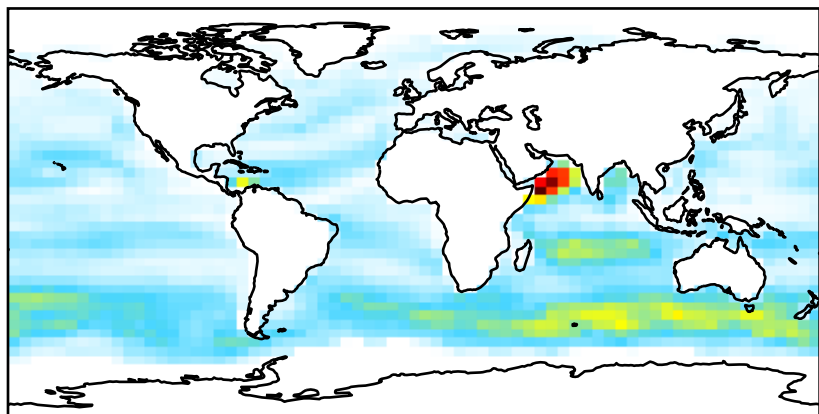


Ratio
Dev/Ref, Fixed Range

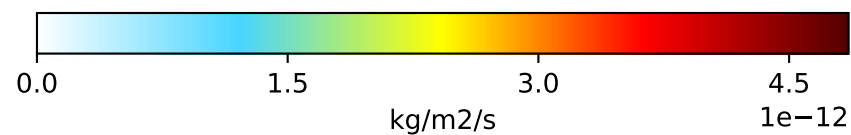
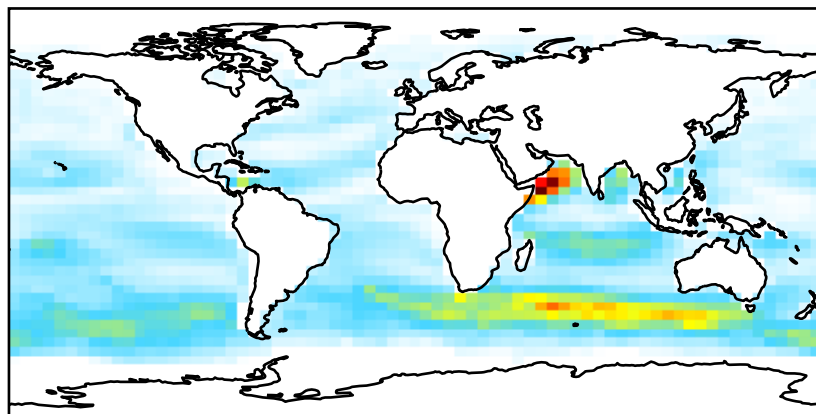


EmisBrSALC_Natural

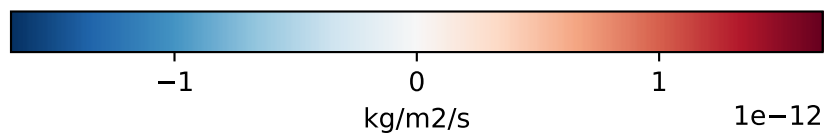
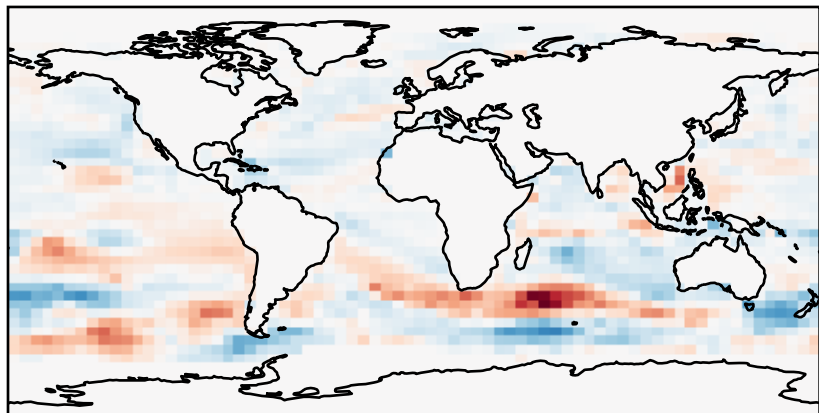
GCC_12.9.0 (Ref)
4.0x5.0



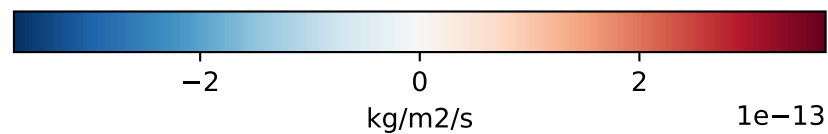
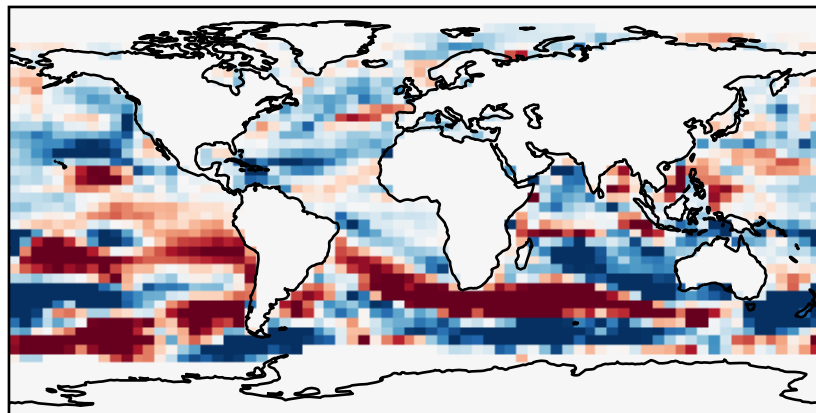
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



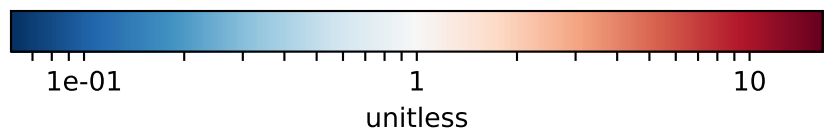
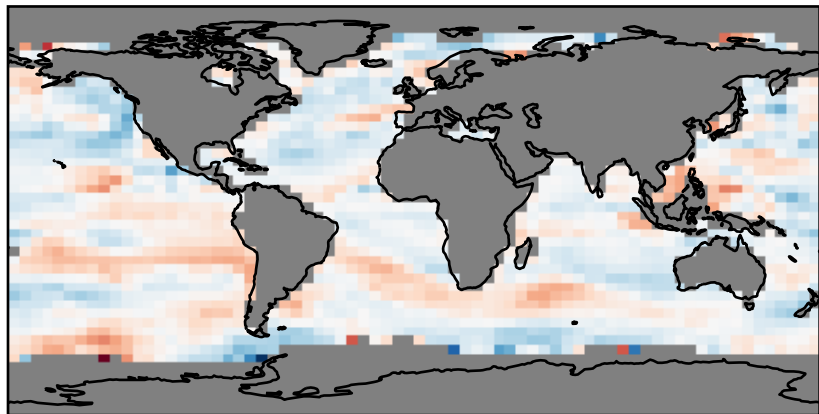
Difference
Dev - Ref, Dynamic Range



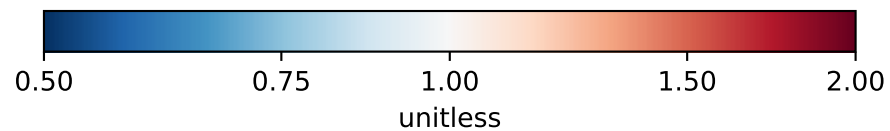
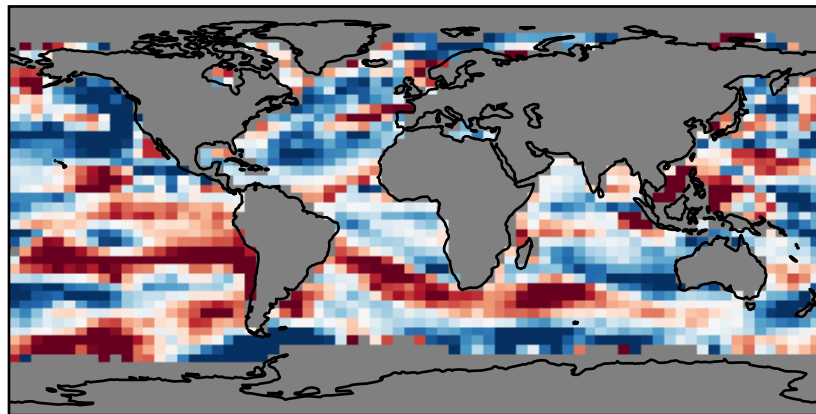
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

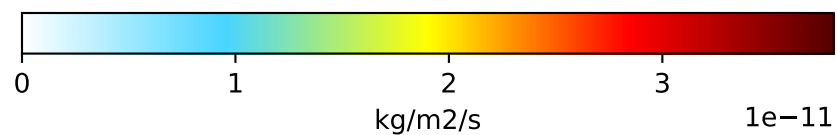
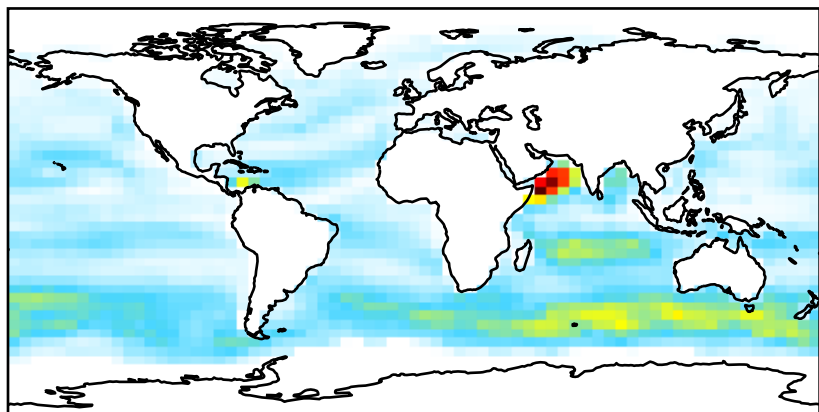


Ratio
Dev/Ref, Fixed Range

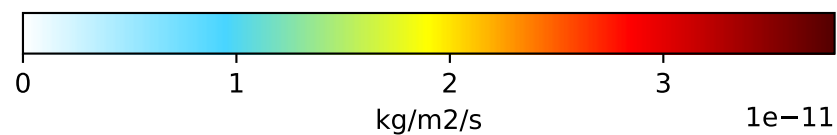
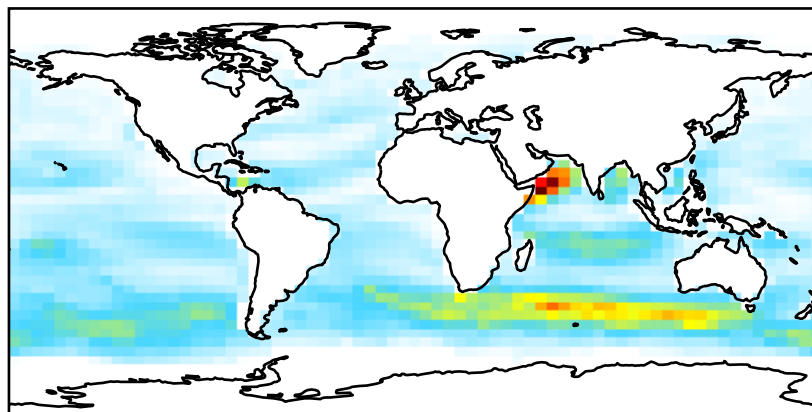


EmisSALA_Natural

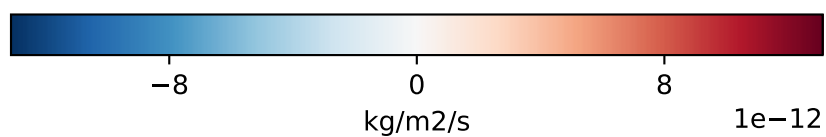
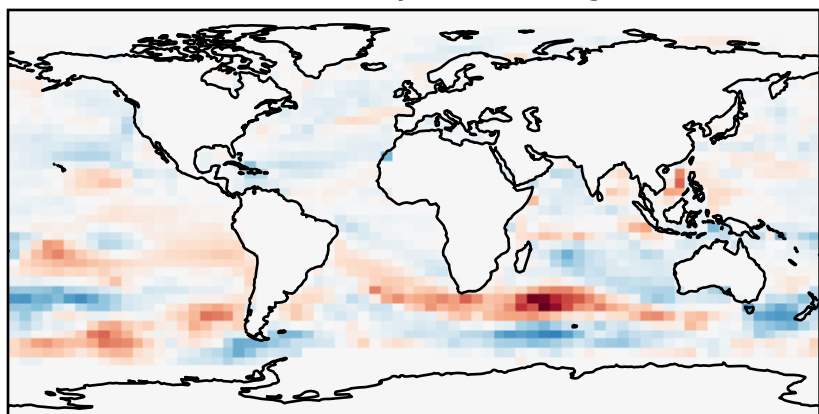
GCC_12.9.0 (Ref)
4.0x5.0



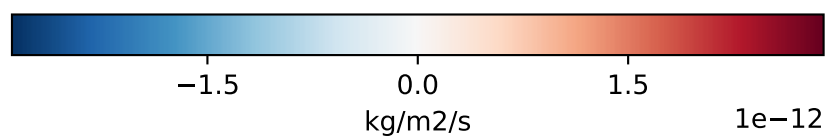
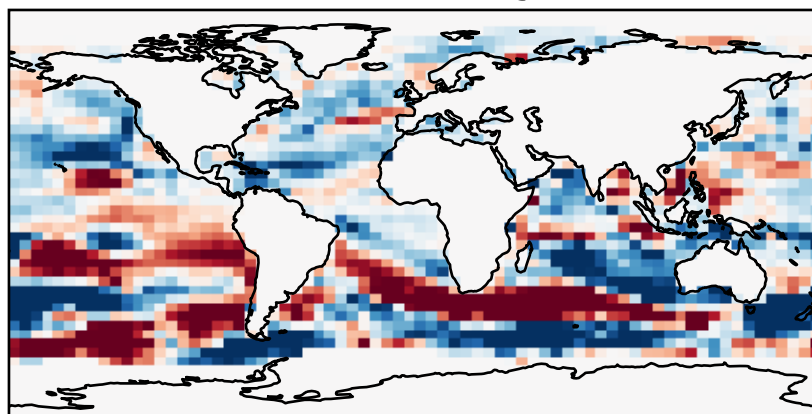
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



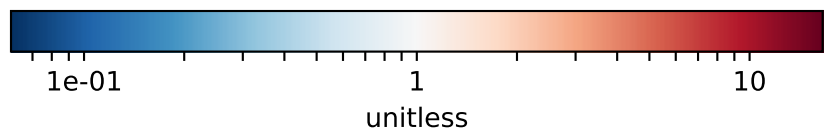
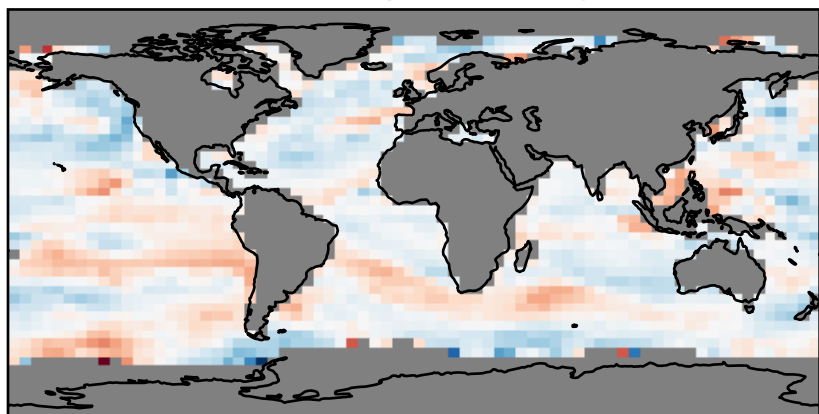
Difference
Dev - Ref, Dynamic Range



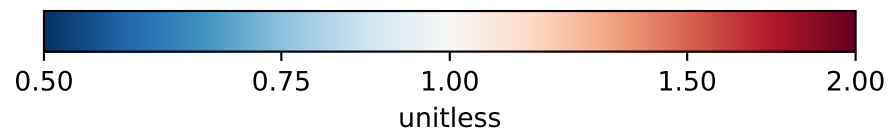
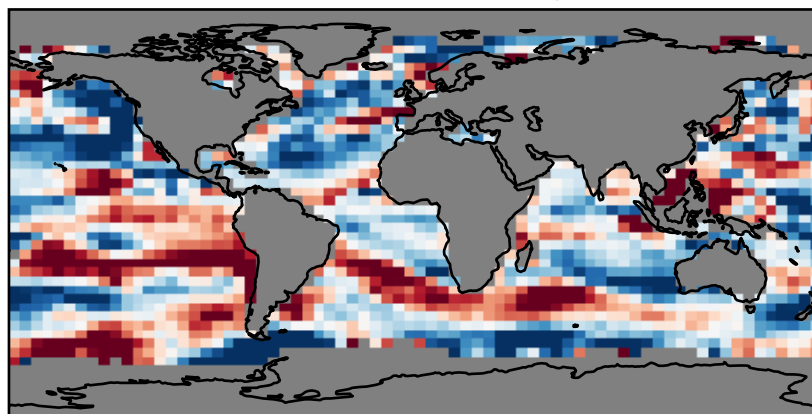
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

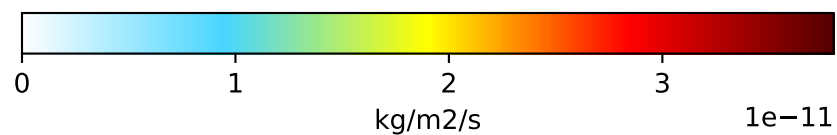
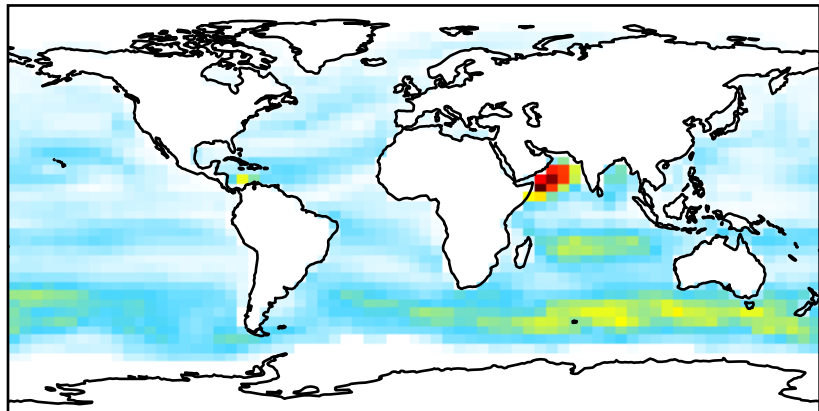


Ratio
Dev/Ref, Fixed Range

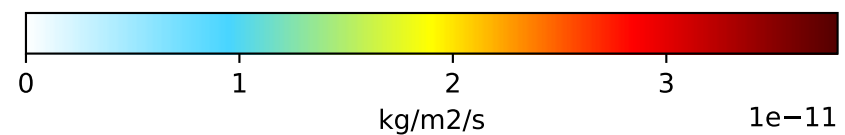
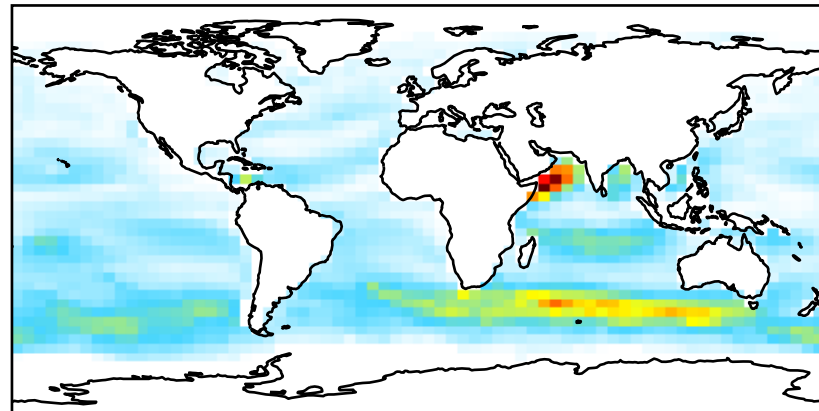


EmisSALAAL_Natural

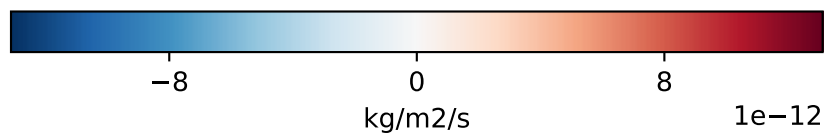
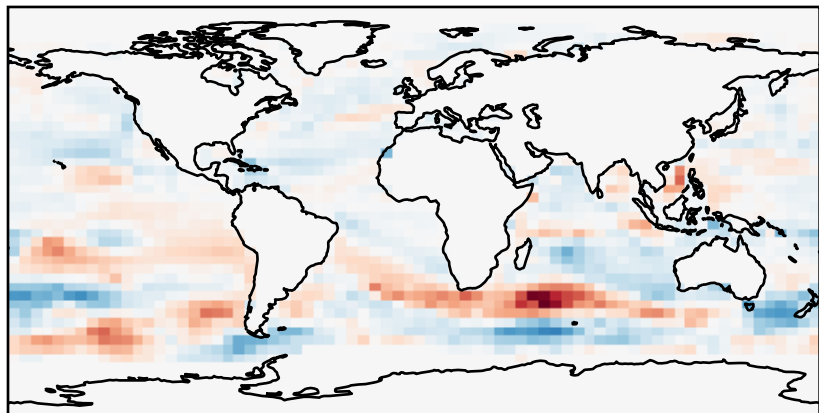
GCC_12.9.0 (Ref)
4.0x5.0



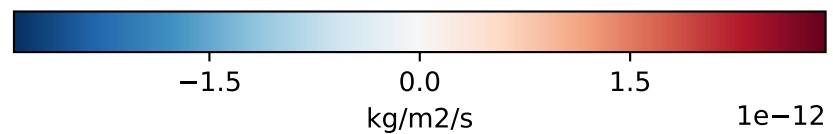
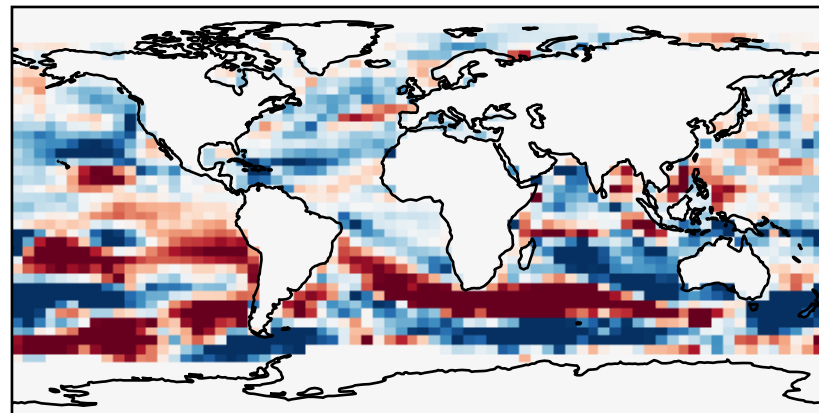
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



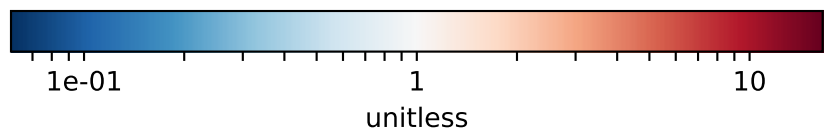
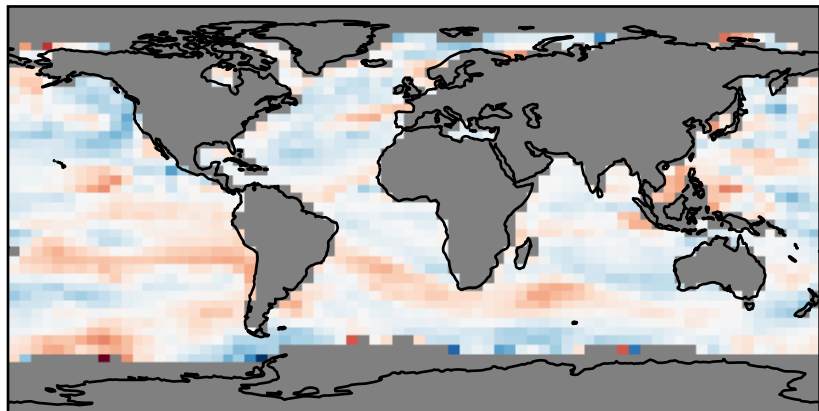
Difference
Dev - Ref, Dynamic Range



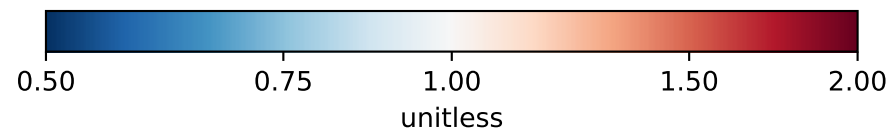
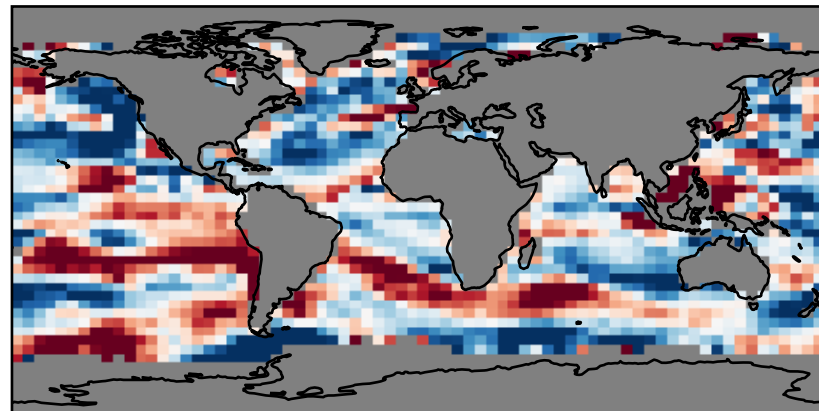
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

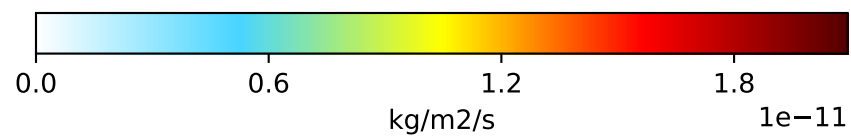
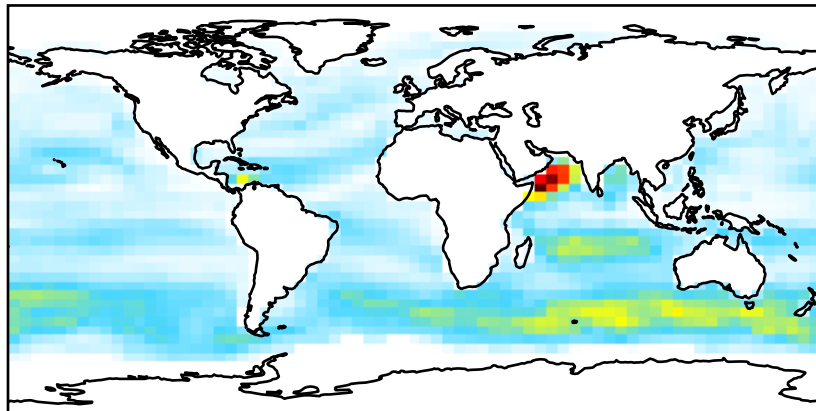


Ratio
Dev/Ref, Fixed Range

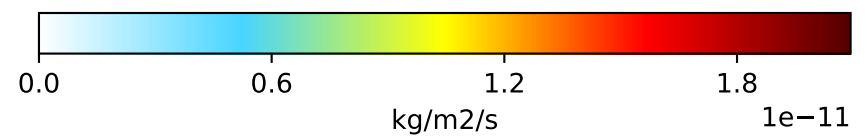
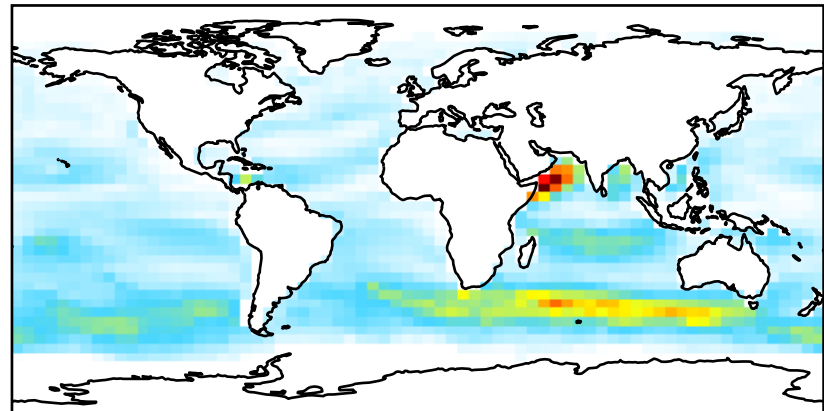


EmisSALACL_Natural

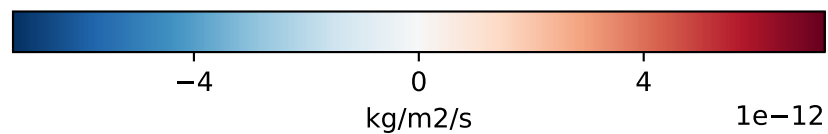
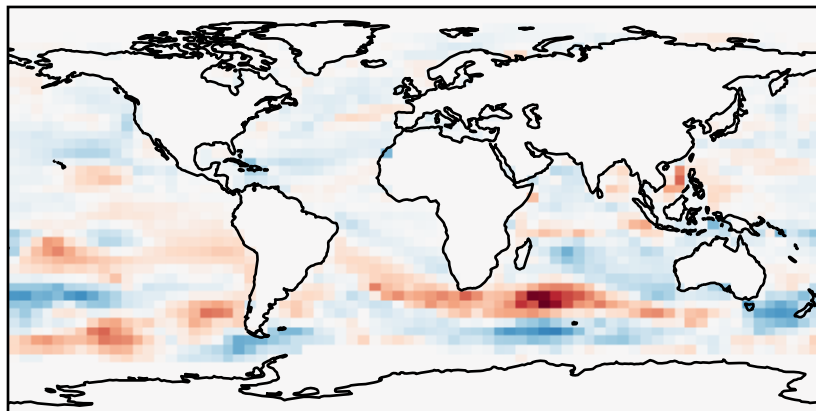
GCC_12.9.0 (Ref)
4.0x5.0



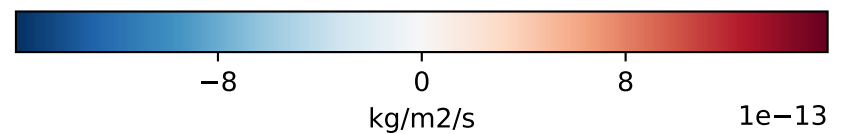
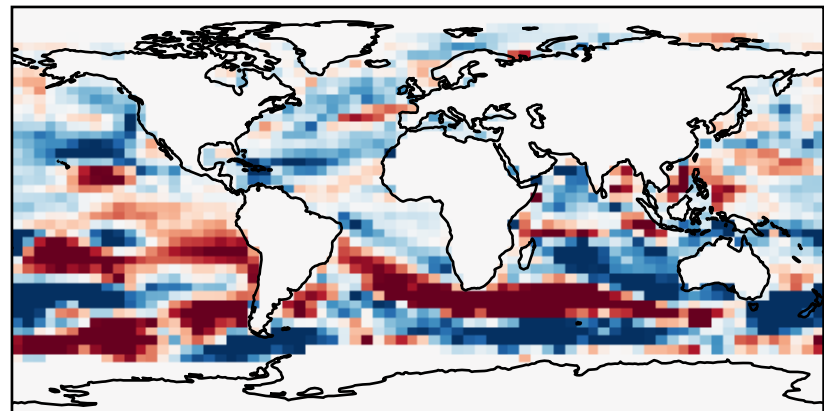
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



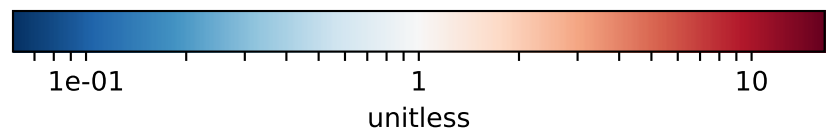
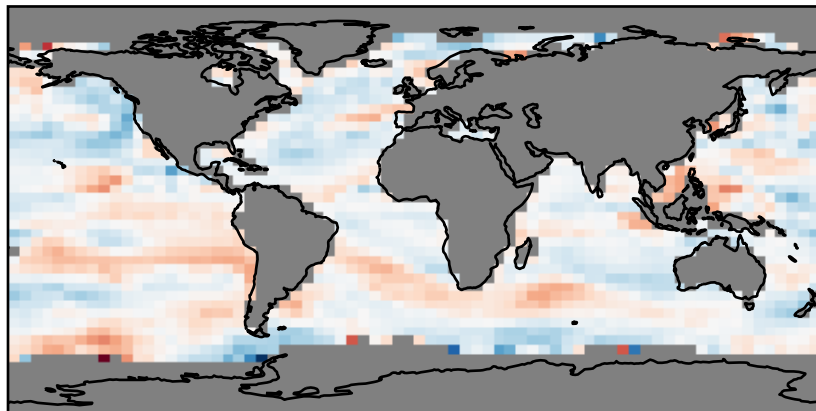
Difference
Dev - Ref, Dynamic Range



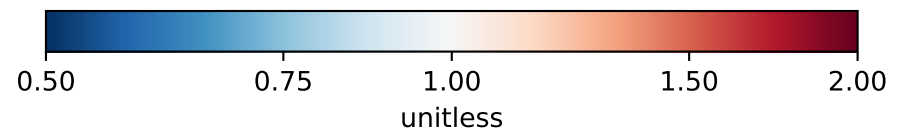
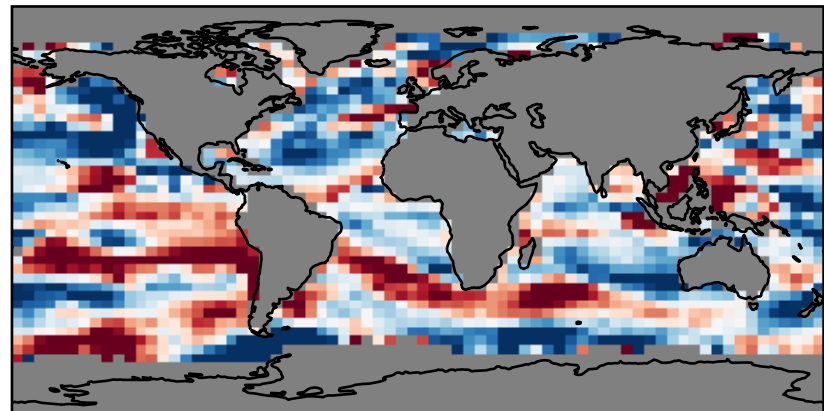
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

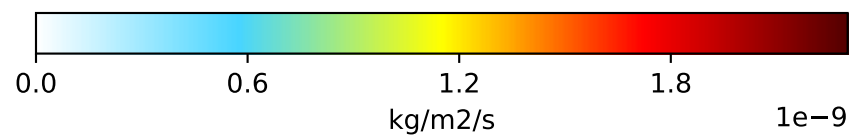
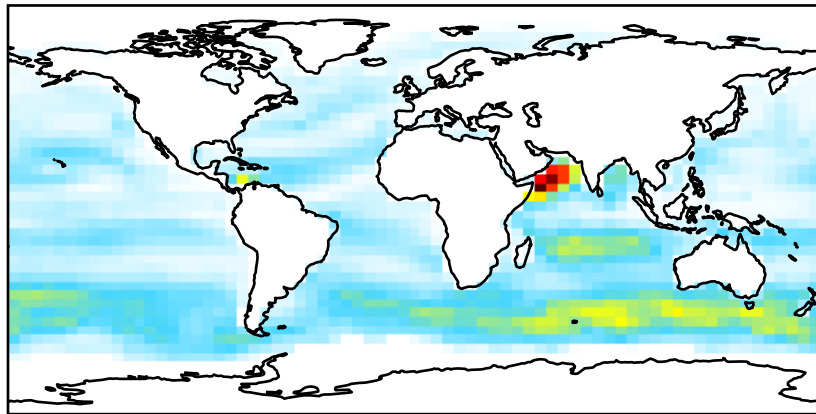


Ratio
Dev/Ref, Fixed Range

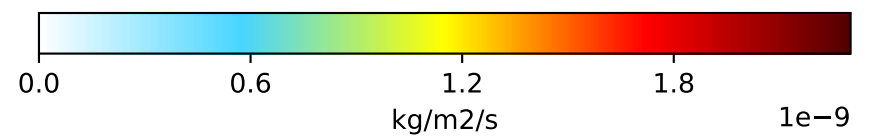
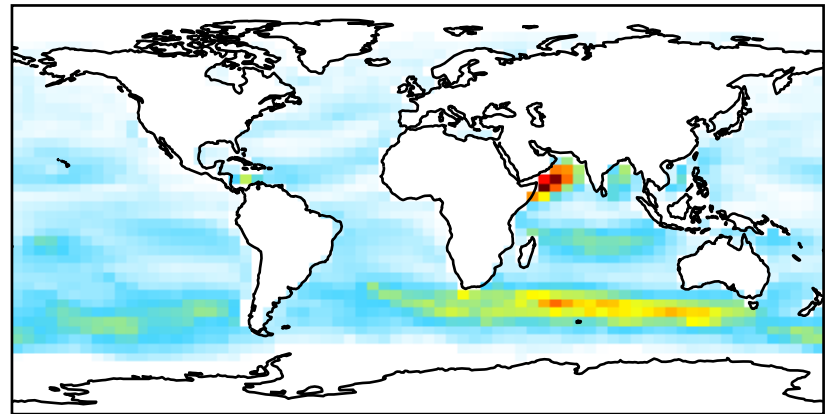


EmisSALC_Natural

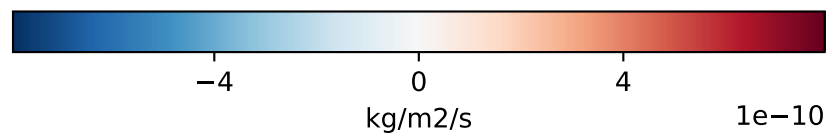
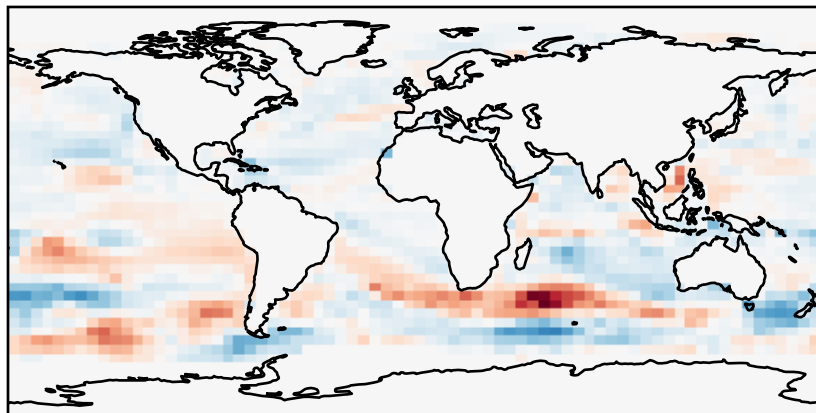
GCC_12.9.0 (Ref)
4.0x5.0



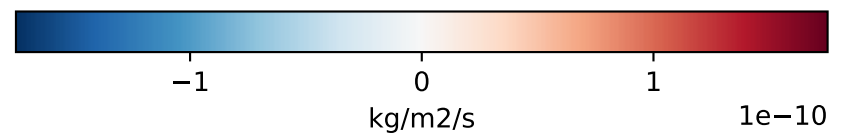
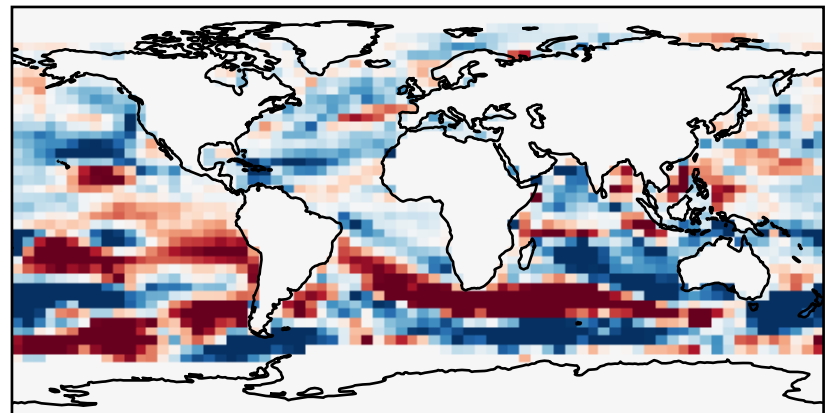
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



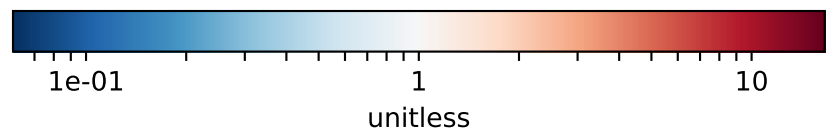
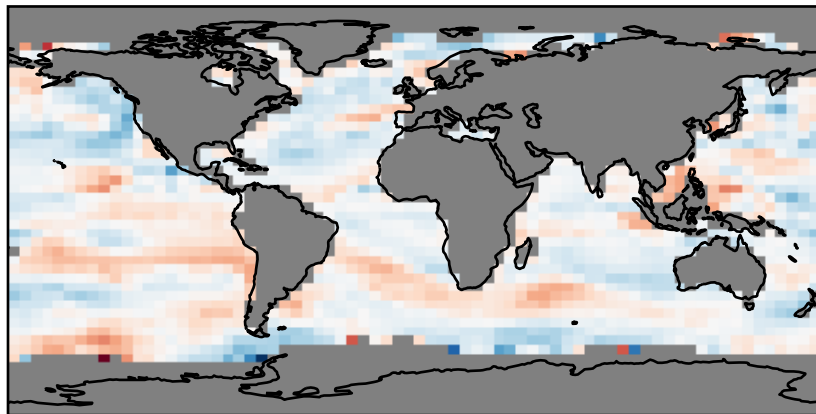
Difference
Dev - Ref, Dynamic Range



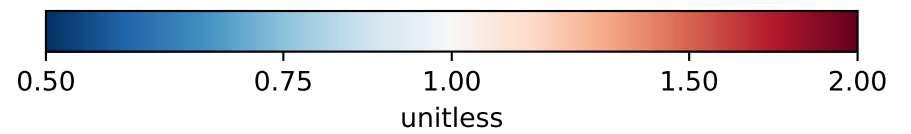
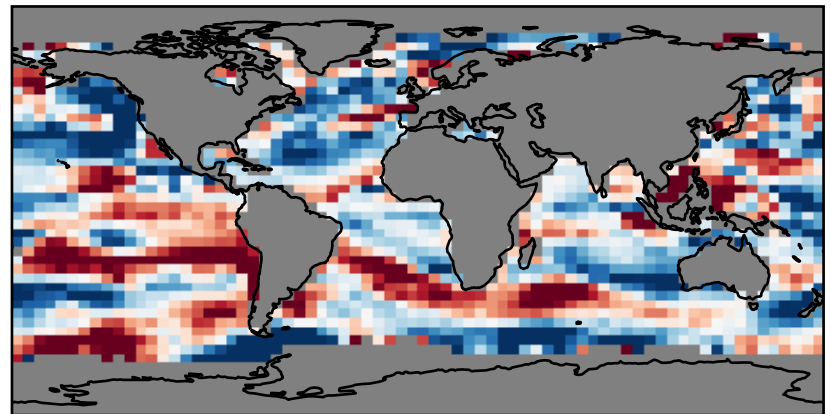
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

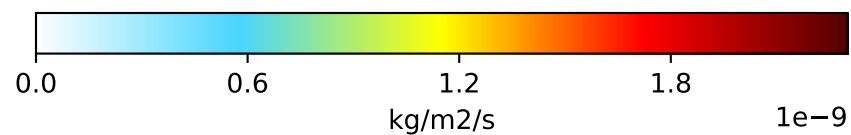
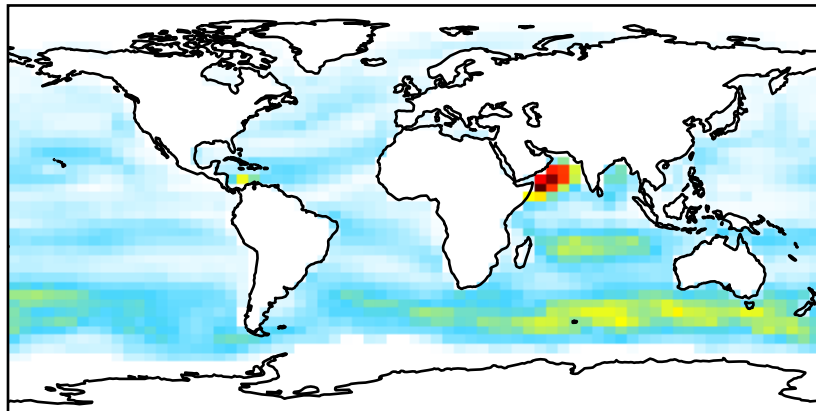


Ratio
Dev/Ref, Fixed Range

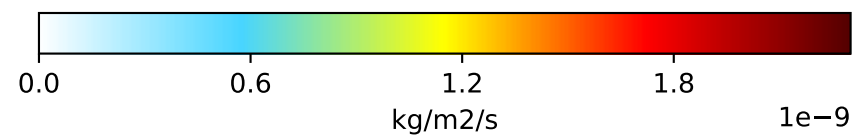
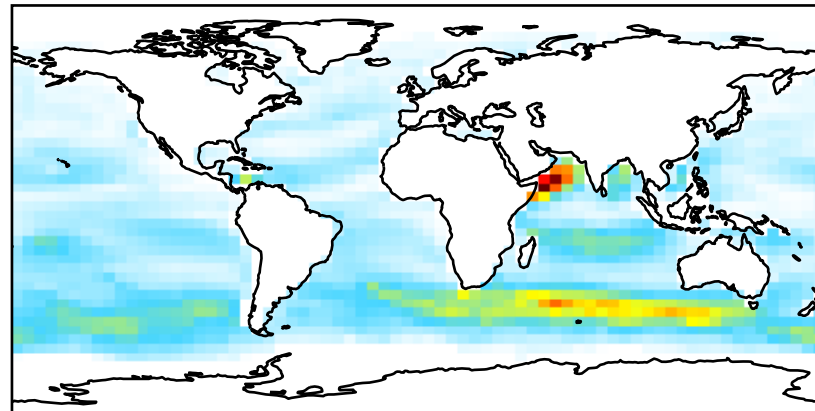


EmisSALCAL_Natural

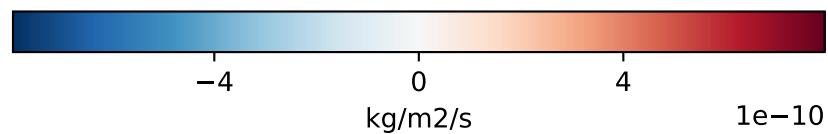
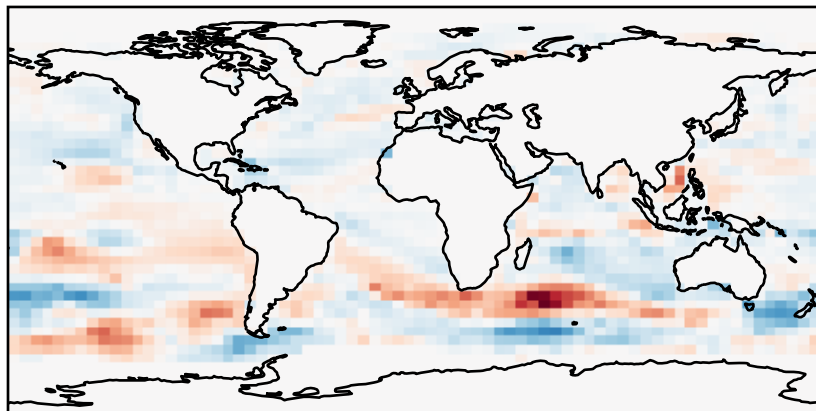
GCC_12.9.0 (Ref)
4.0x5.0



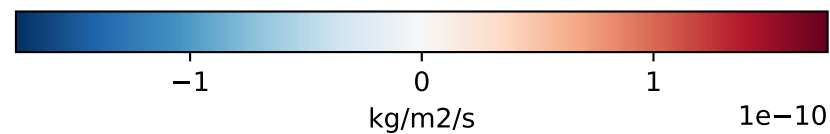
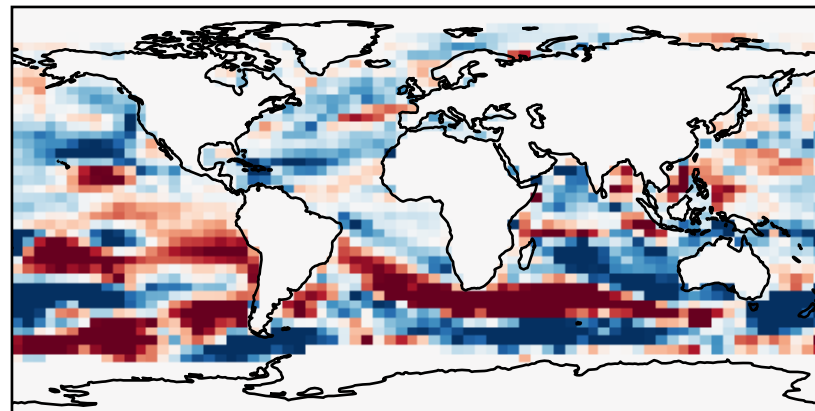
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



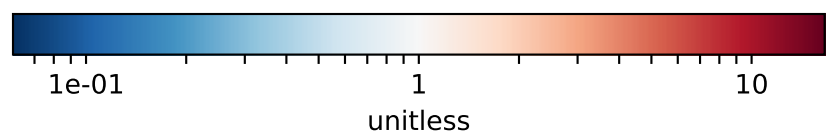
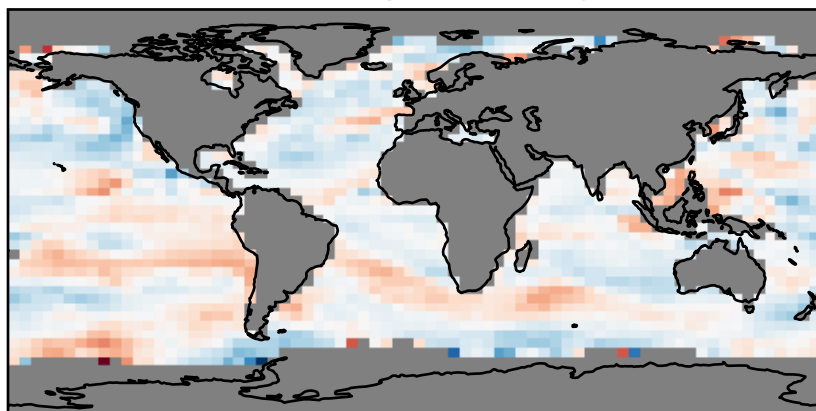
Difference
Dev - Ref, Dynamic Range



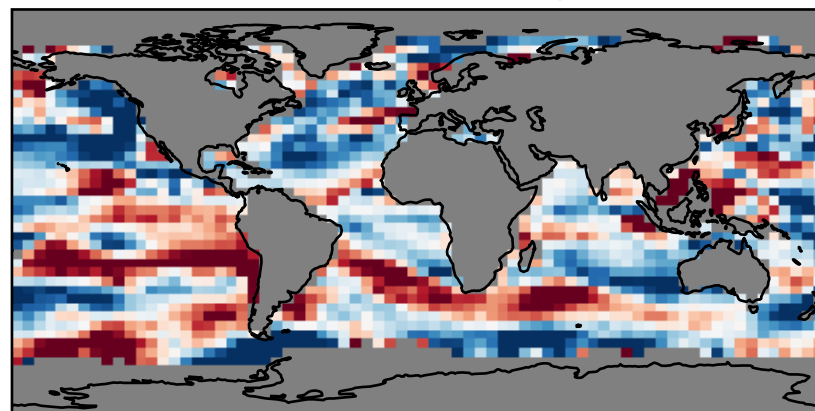
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

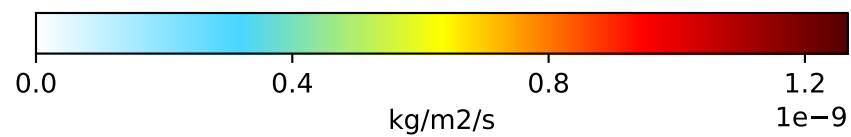
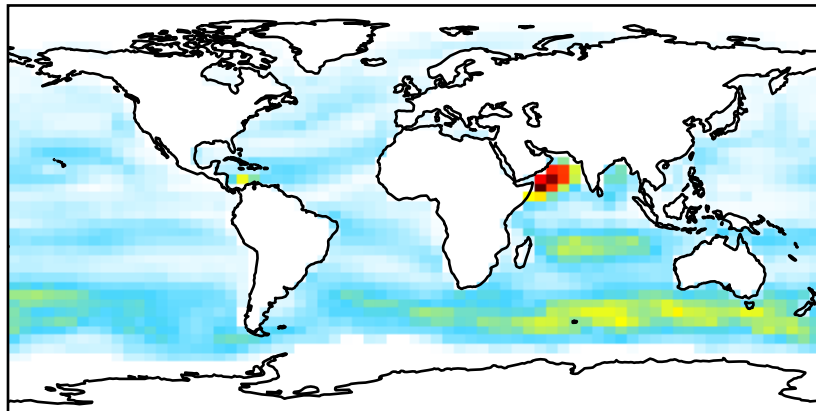


Ratio
Dev/Ref, Fixed Range

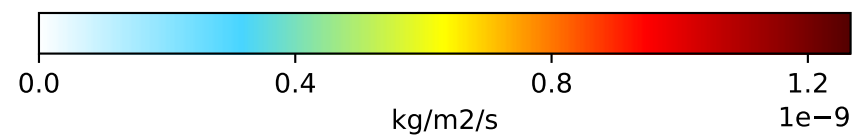
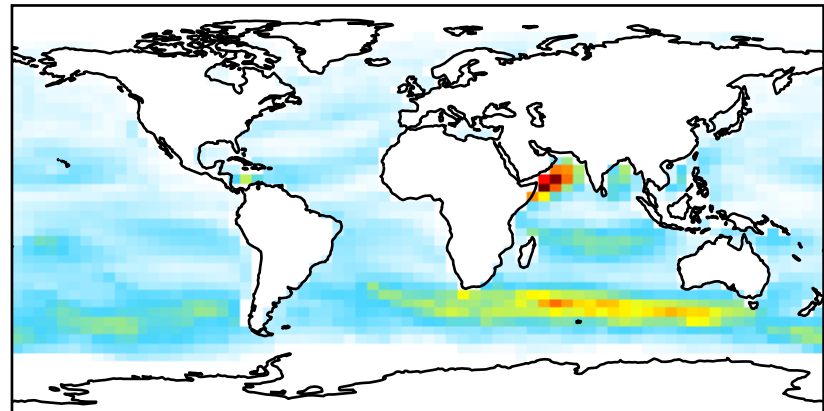


EmisSALCCL_Natural

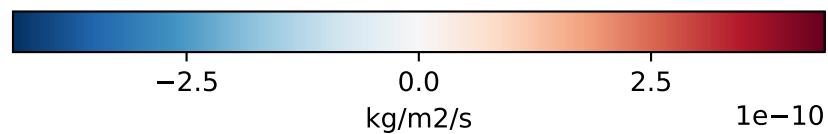
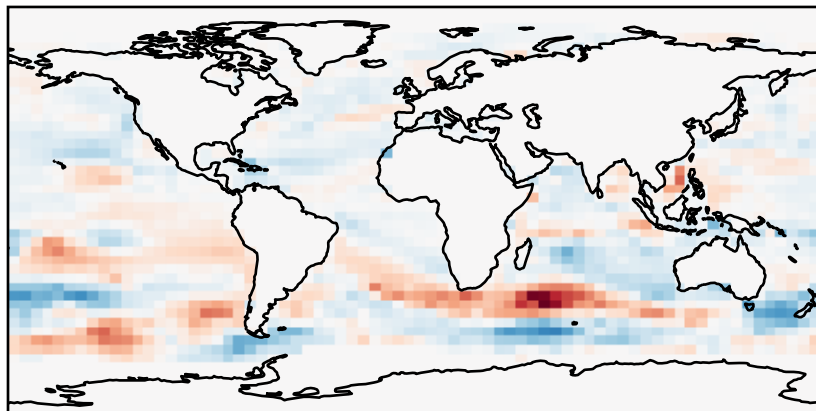
GCC_12.9.0 (Ref)
4.0x5.0



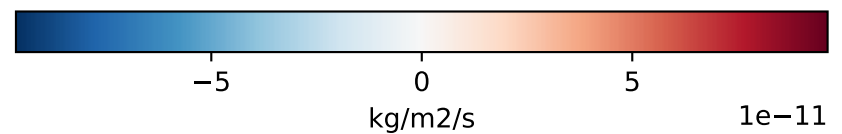
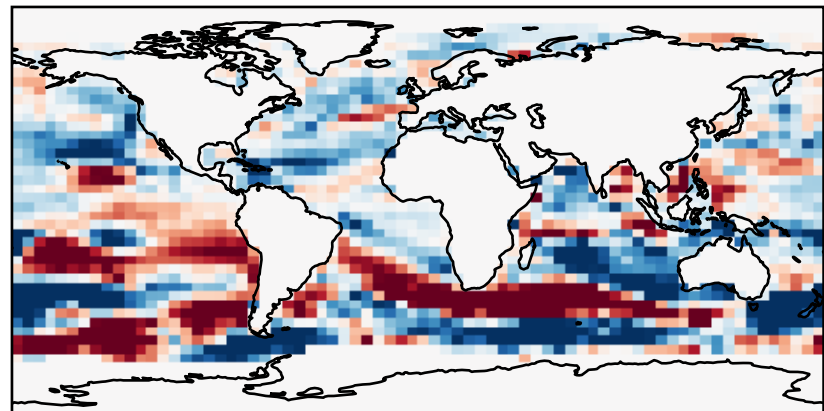
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



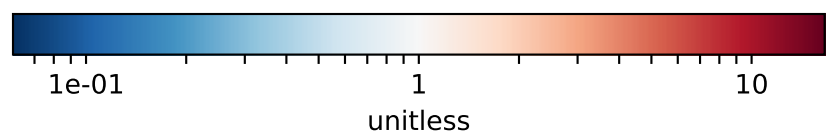
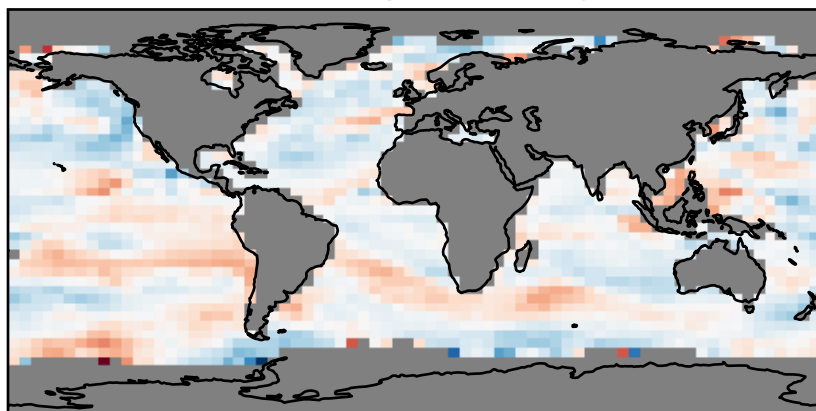
Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range



Ratio
Dev/Ref, Fixed Range

