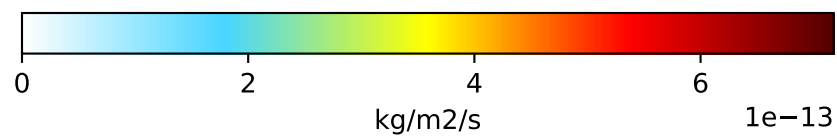
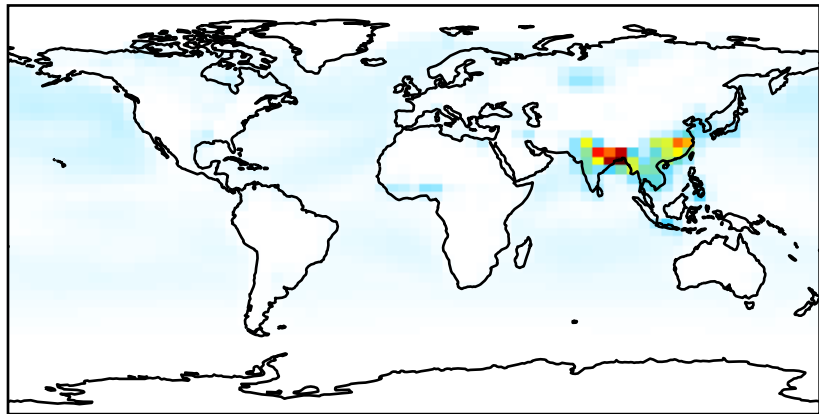
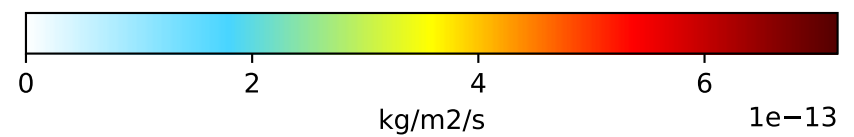
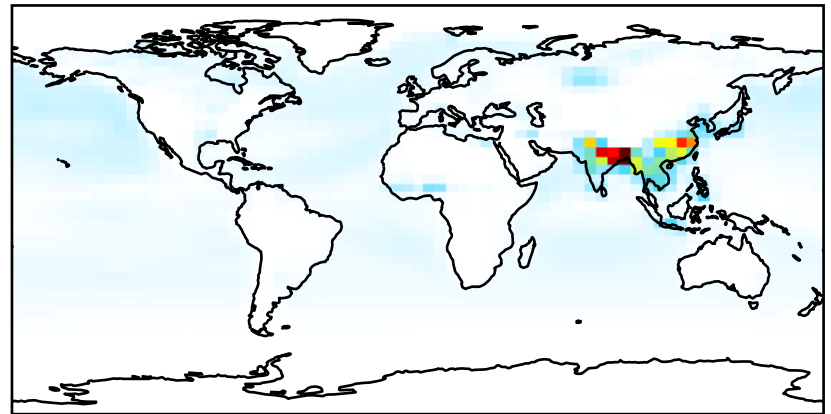


# EmisCH3I\_Ocean

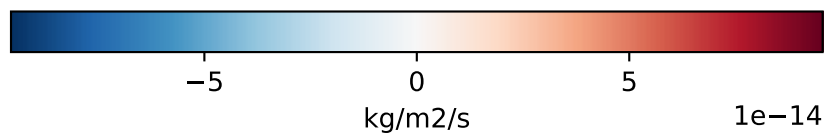
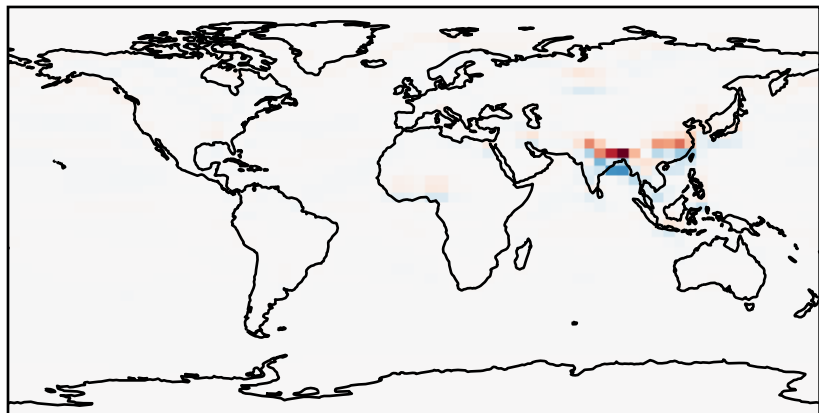
GCC\_12.9.0 (Ref)  
4.0x5.0



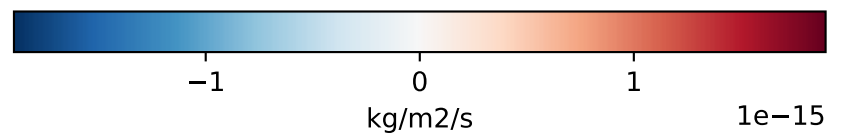
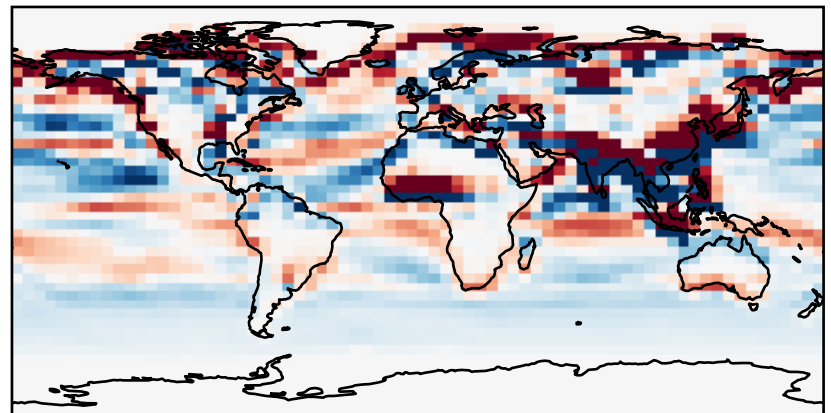
GCC\_13.0.0-beta.0 (Dev)  
4.0x5.0



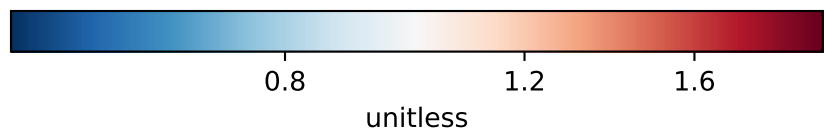
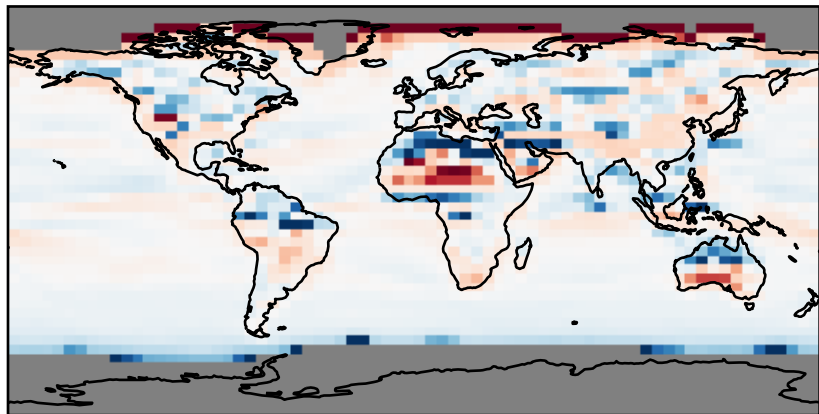
Difference  
Dev - Ref, Dynamic Range



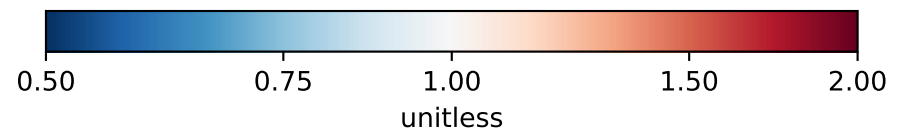
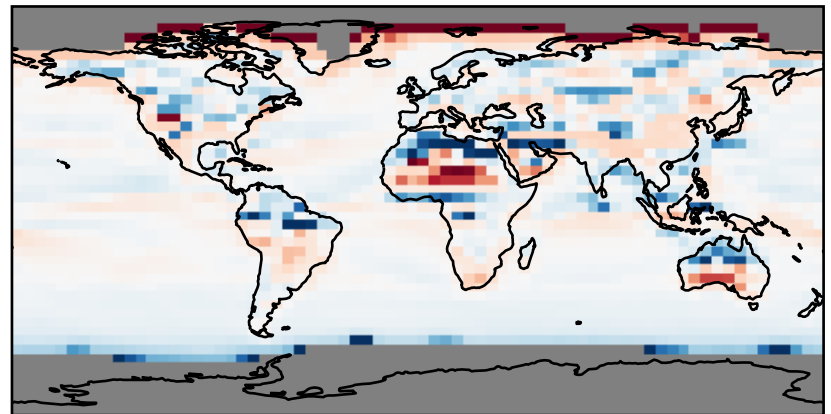
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

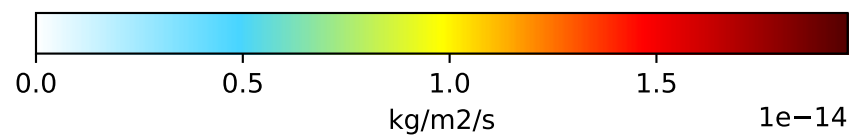
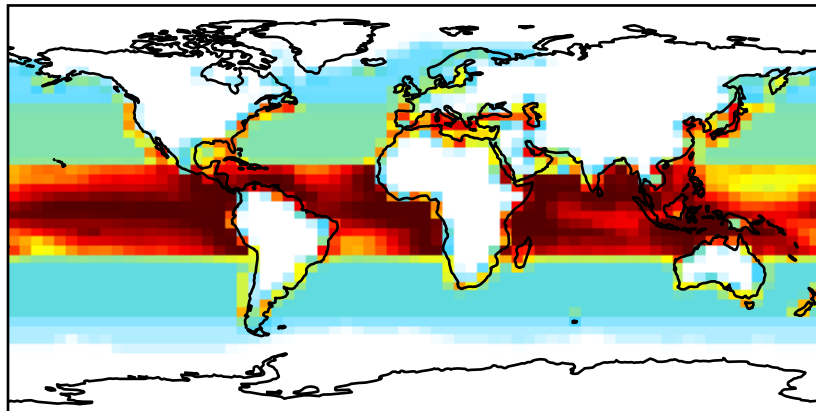


Ratio  
Dev/Ref, Fixed Range

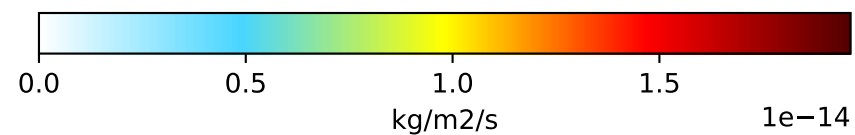
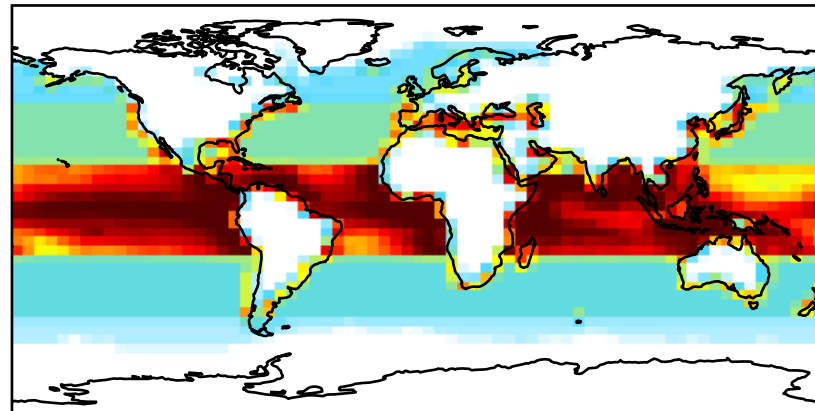


# EmisCH2I2\_Ocean

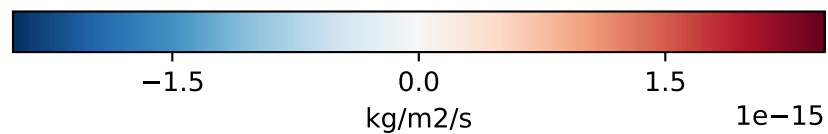
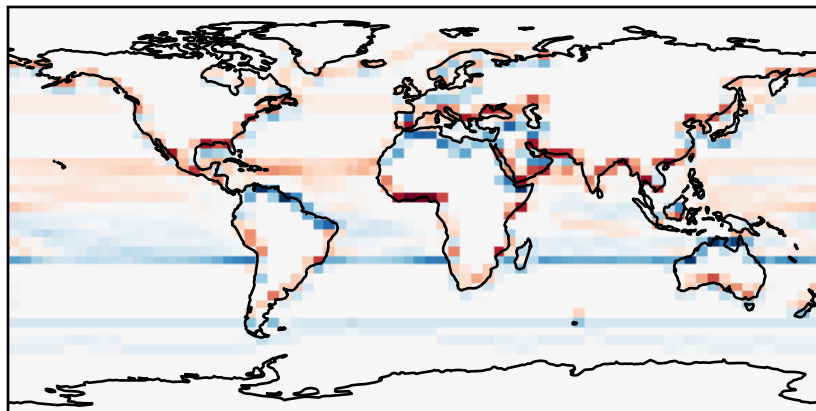
GCC\_12.9.0 (Ref)  
4.0x5.0



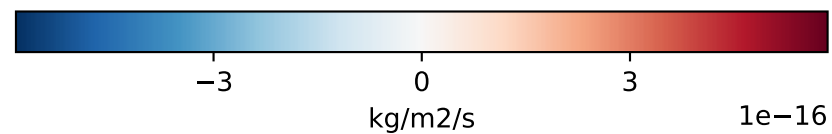
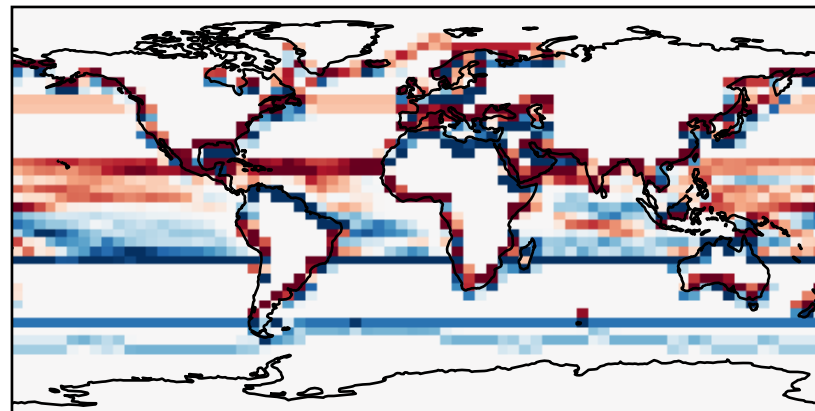
GCC\_13.0.0-beta.0 (Dev)  
4.0x5.0



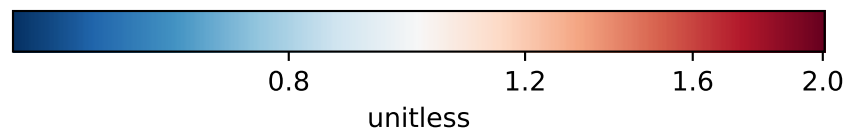
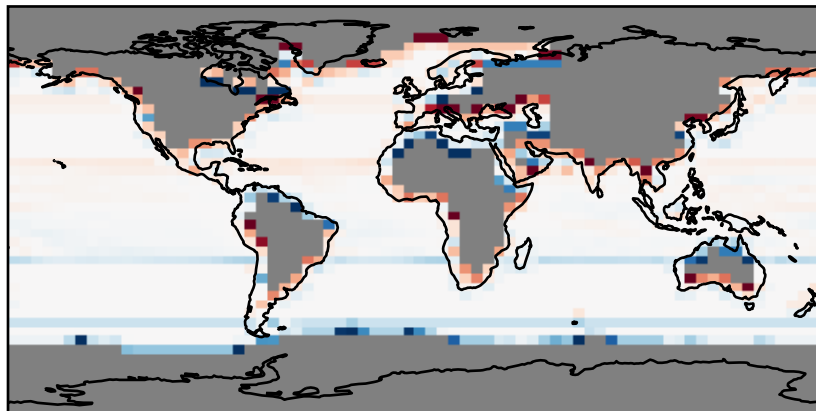
Difference  
Dev - Ref, Dynamic Range



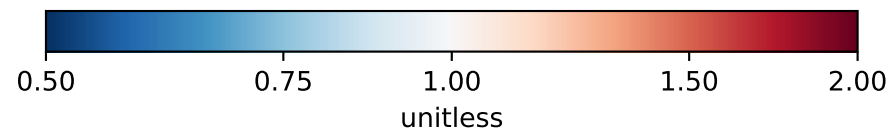
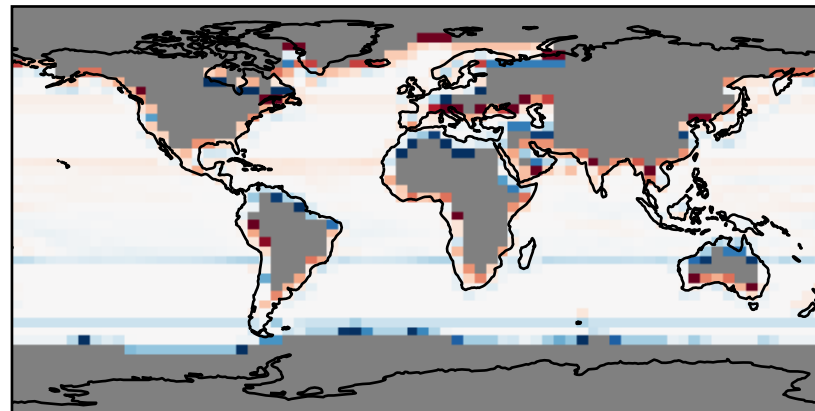
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

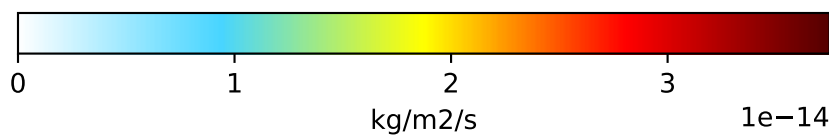
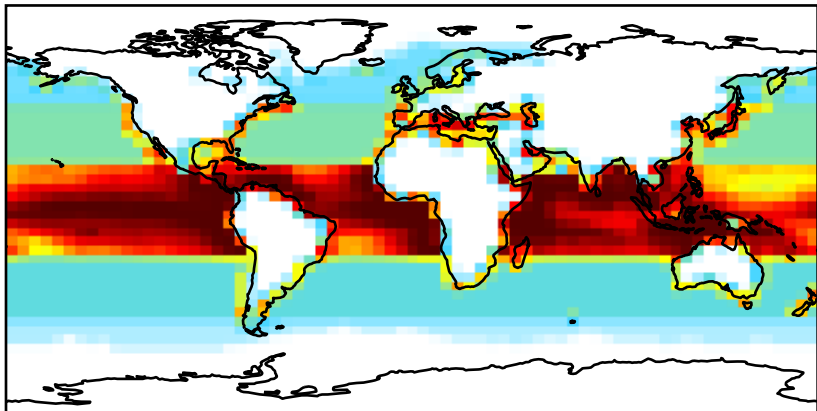


Ratio  
Dev/Ref, Fixed Range

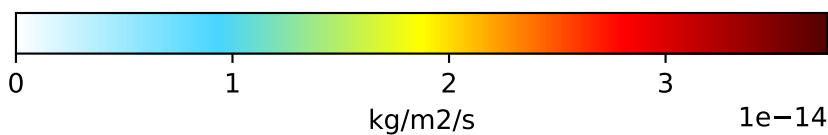
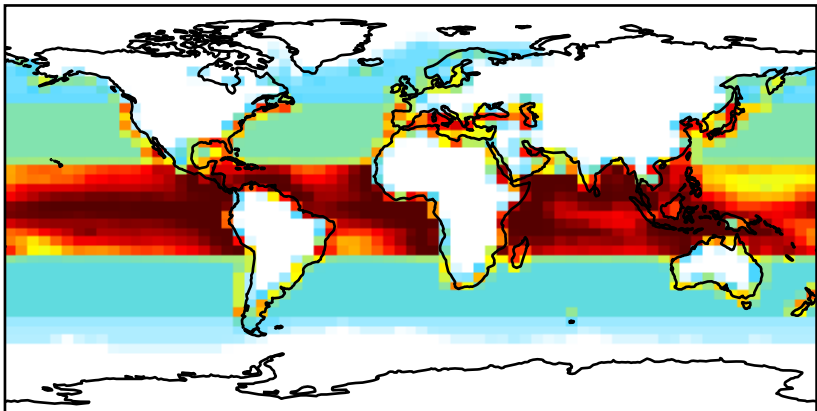


# EmisCH2ICl\_Ocean

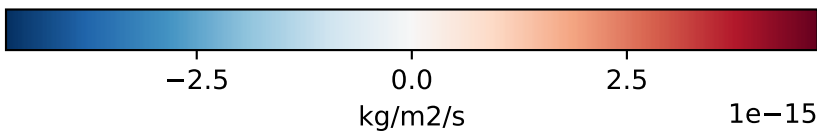
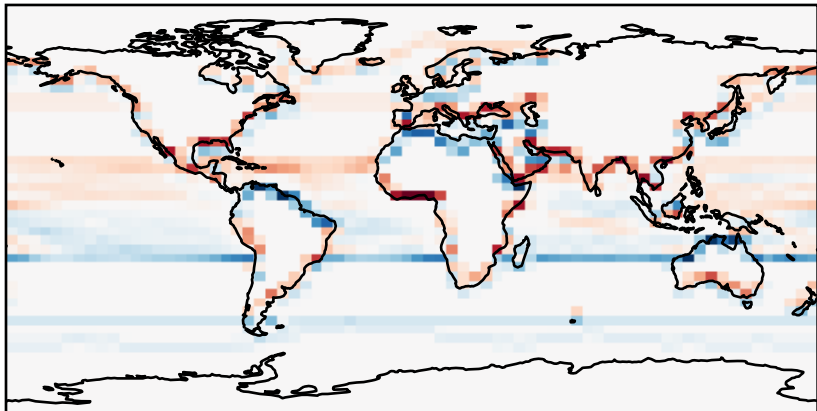
GCC\_12.9.0 (Ref)  
4.0x5.0



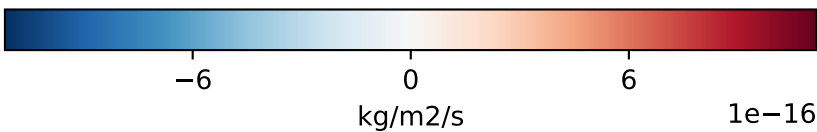
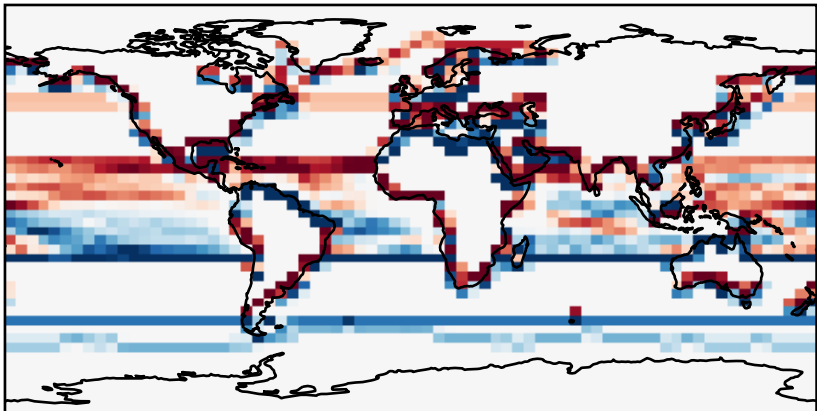
GCC\_13.0.0-beta.0 (Dev)  
4.0x5.0



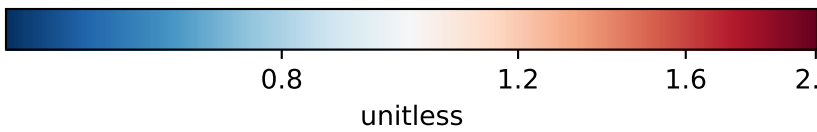
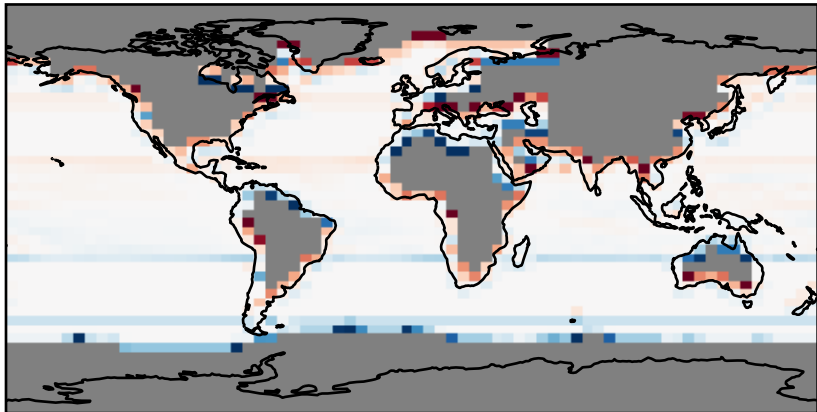
Difference  
Dev - Ref, Dynamic Range



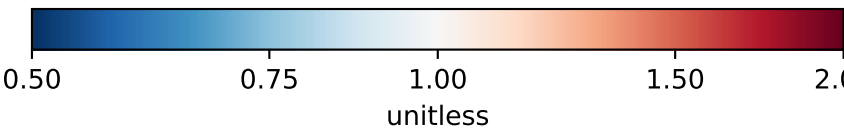
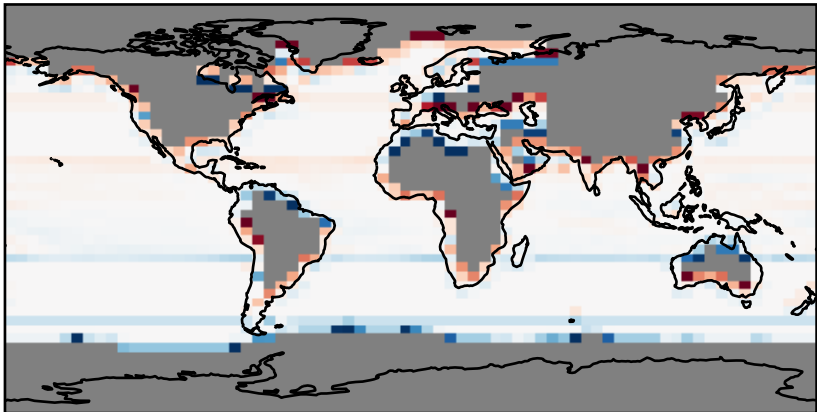
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

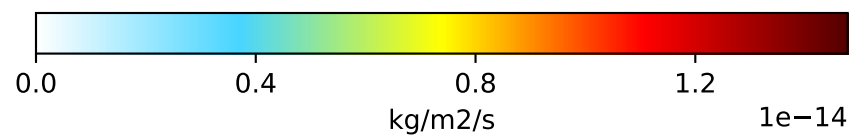
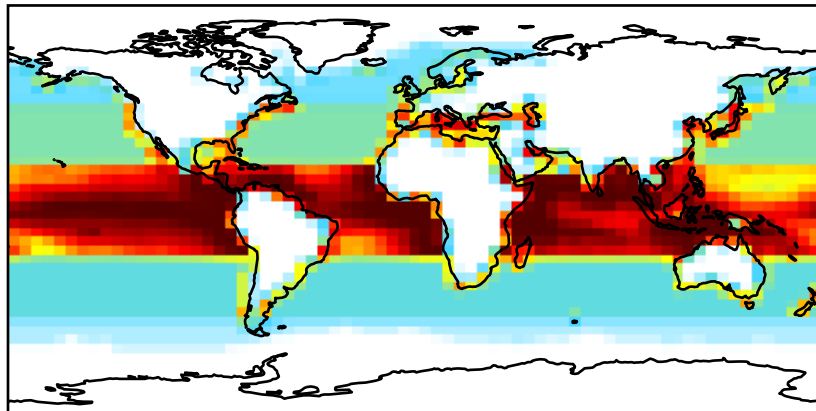


Ratio  
Dev/Ref, Fixed Range

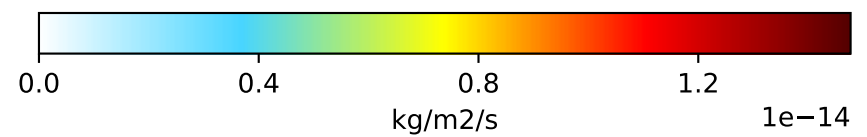
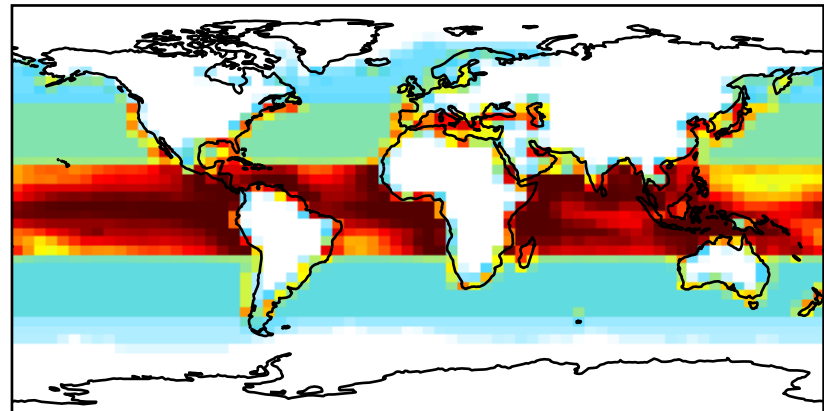


# EmisCH2IBr\_Ocean

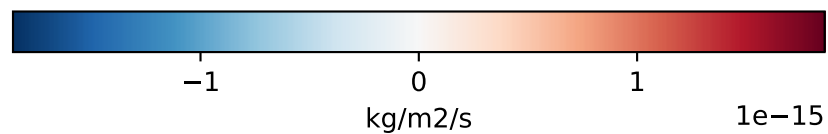
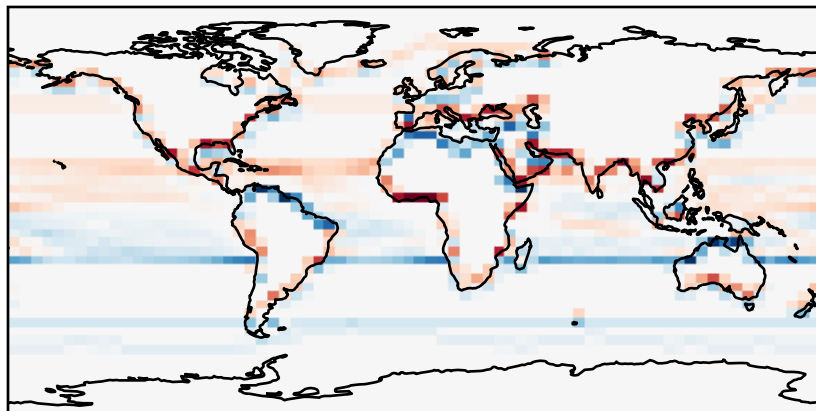
GCC\_12.9.0 (Ref)  
4.0x5.0



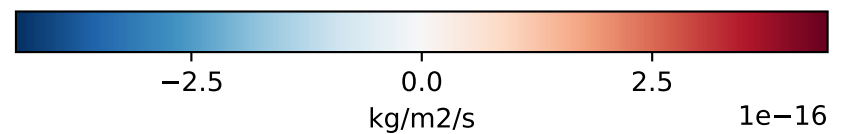
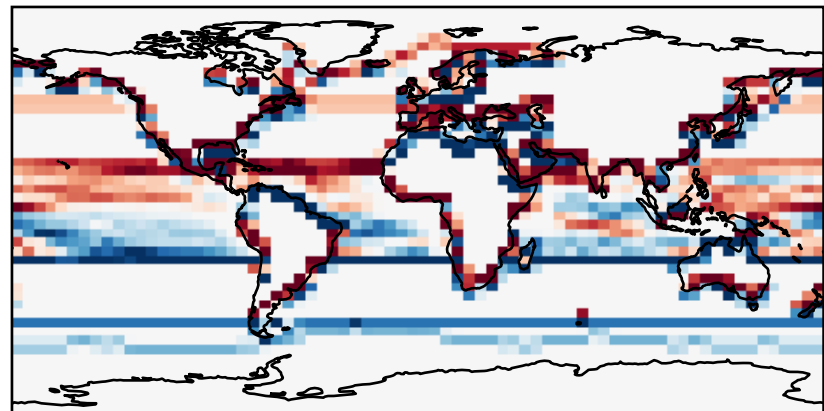
GCC\_13.0.0-beta.0 (Dev)  
4.0x5.0



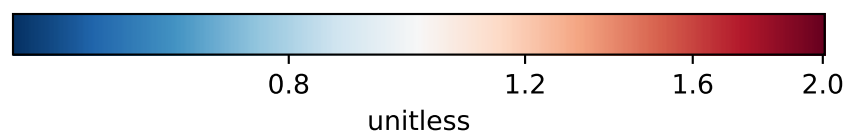
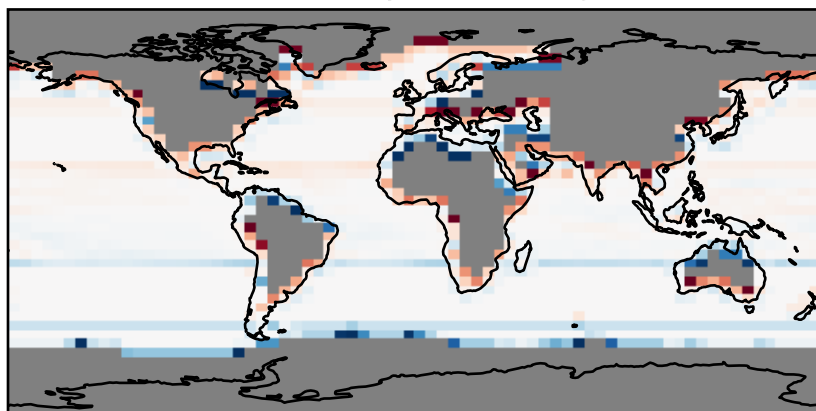
Difference  
Dev - Ref, Dynamic Range



Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

