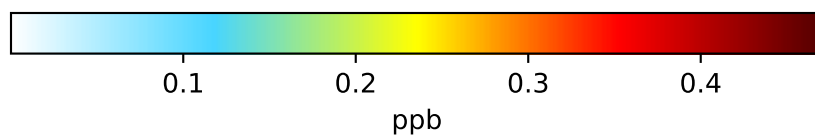
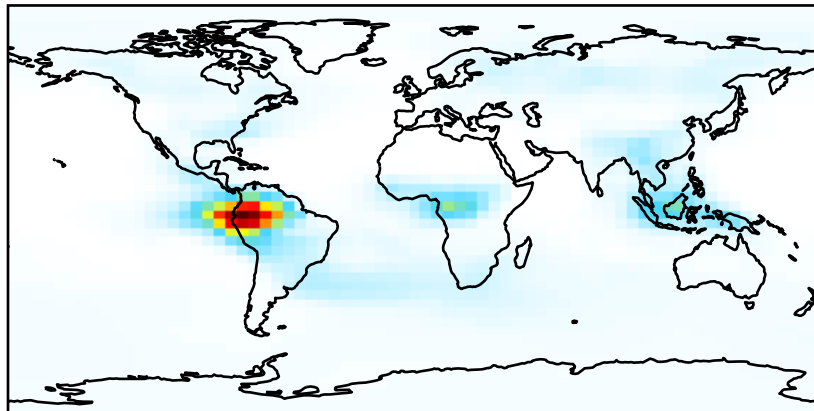
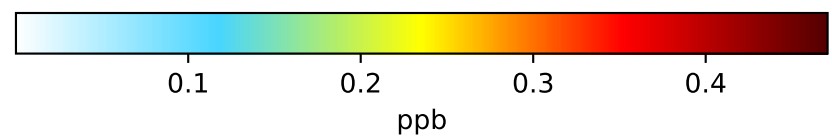
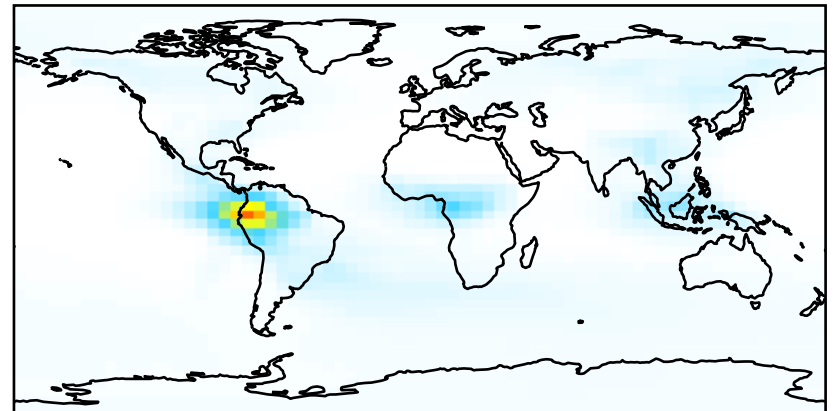


SpeciesConc_EOH

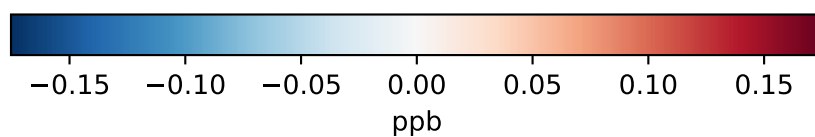
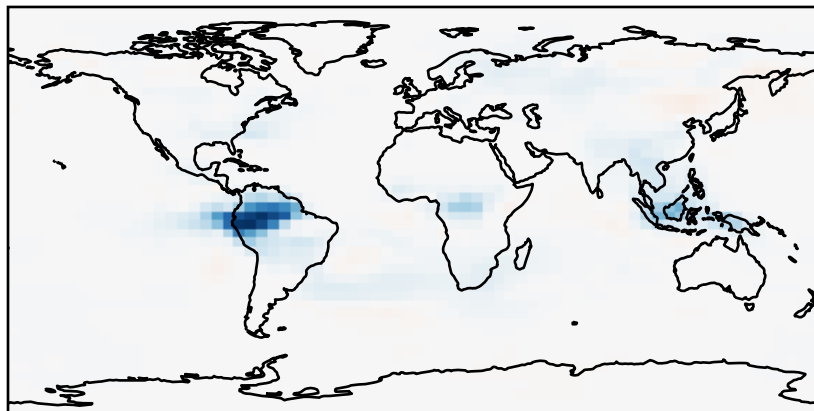
GCC_12.9.0 (Ref)
4.0x5.0



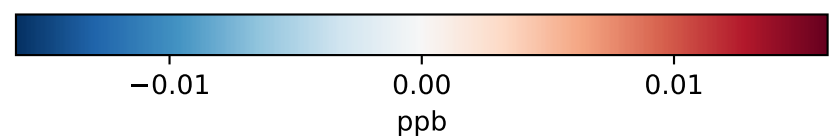
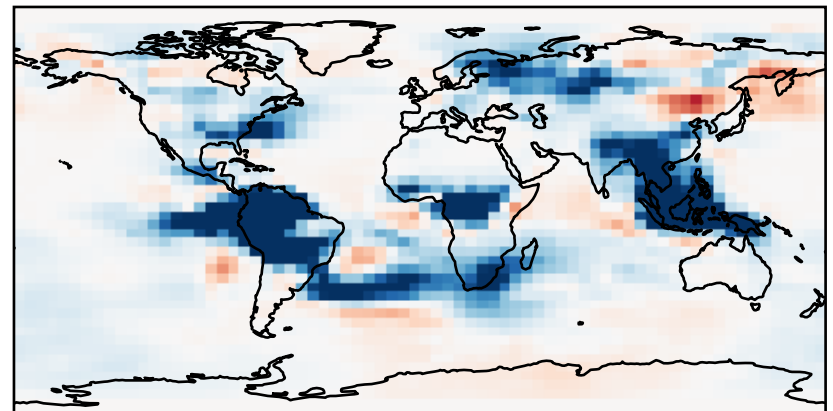
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



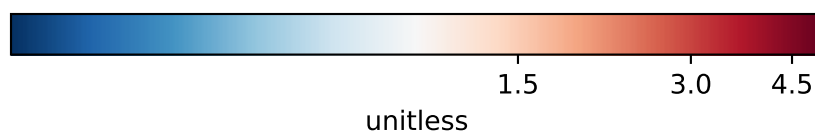
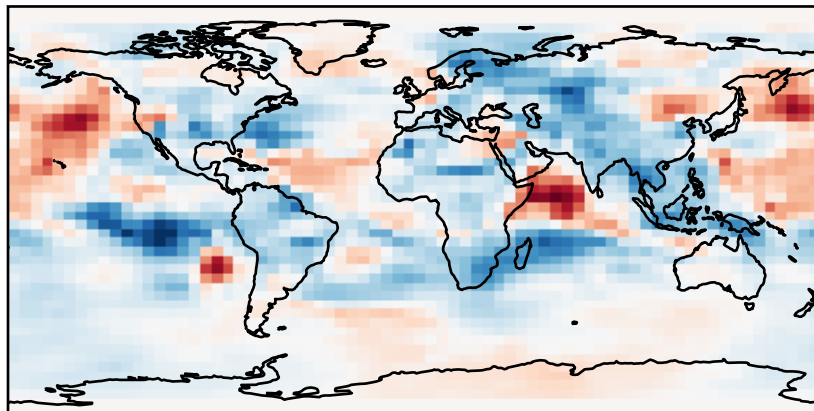
Difference
Dev - Ref, Dynamic Range



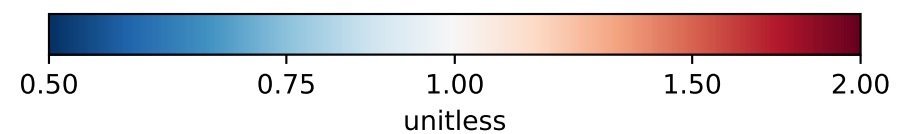
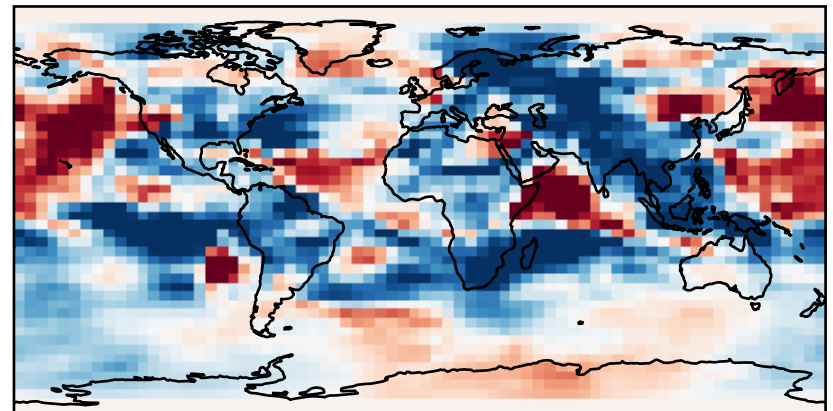
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

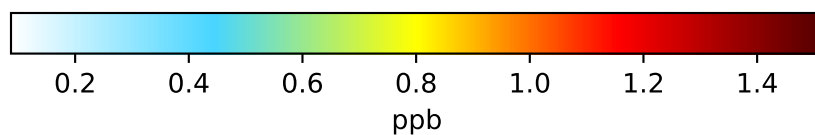
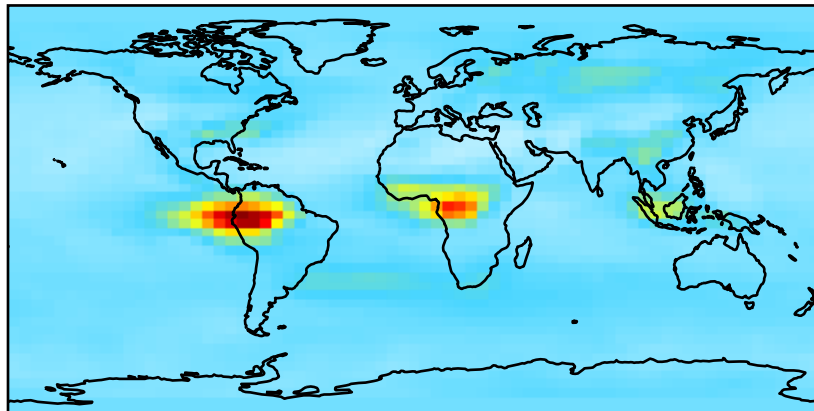


Ratio
Dev/Ref, Fixed Range

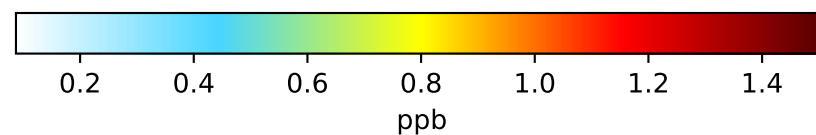
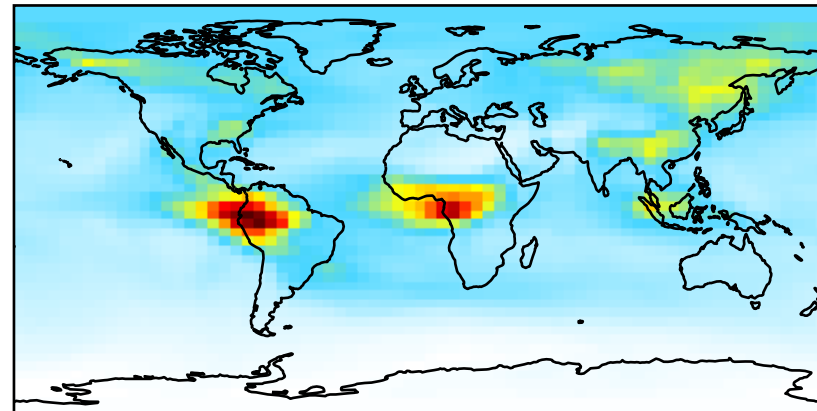


SpeciesConc_MOH

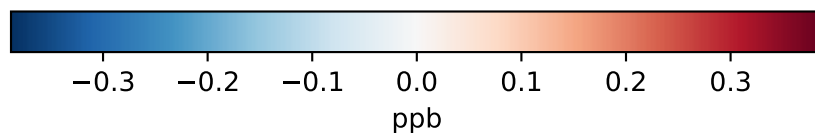
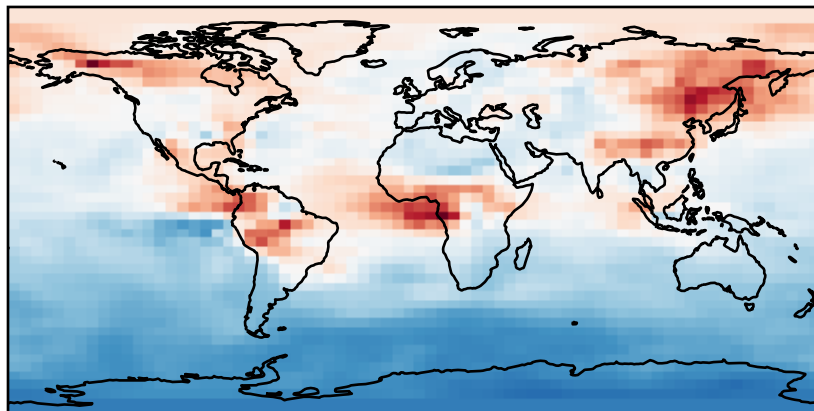
GCC_12.9.0 (Ref)
4.0x5.0



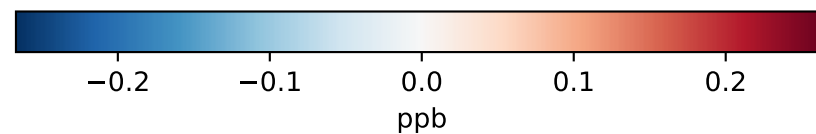
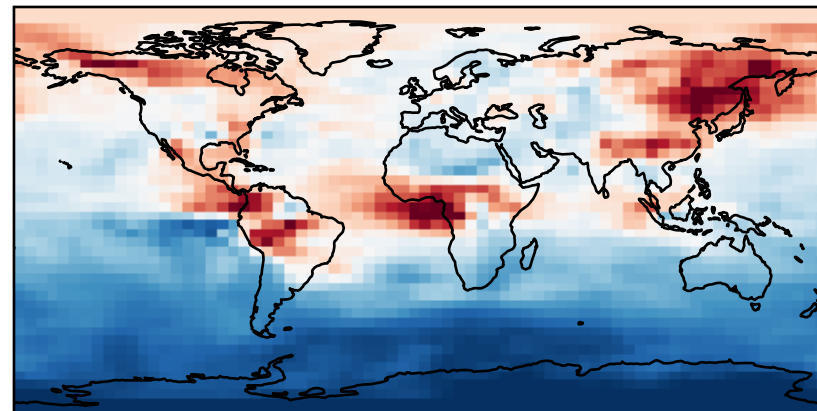
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



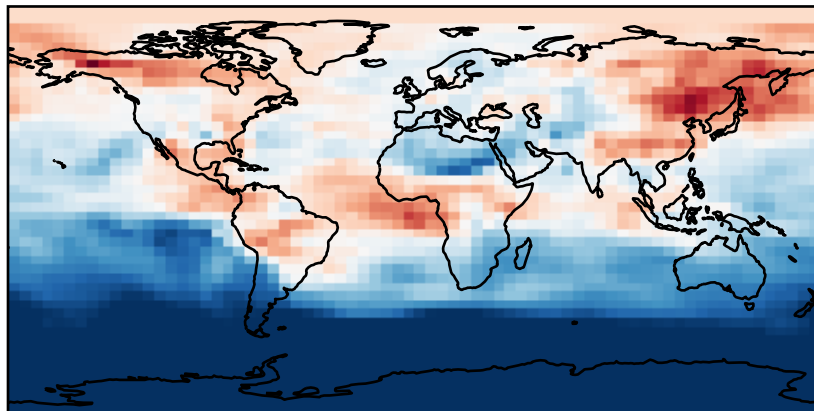
Difference
Dev - Ref, Dynamic Range



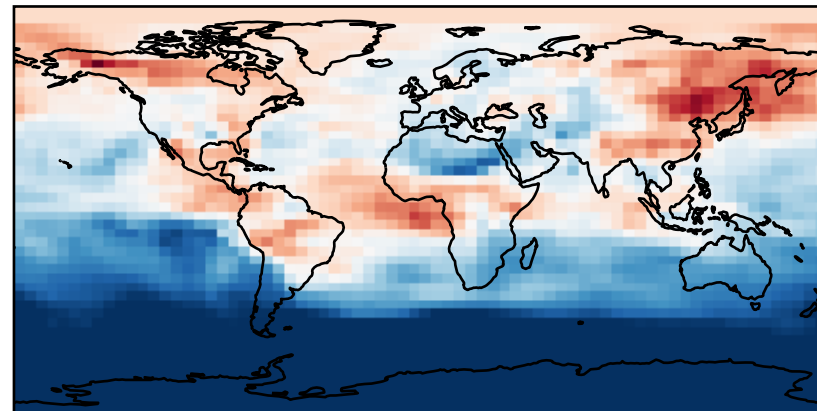
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

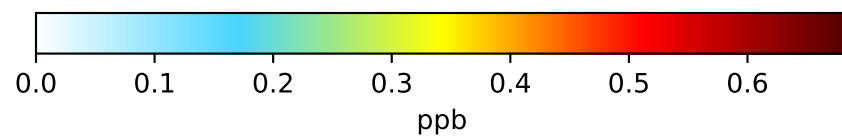
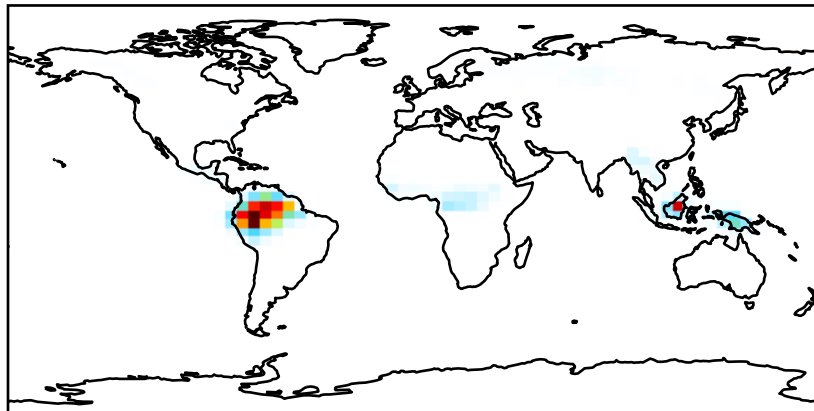


Ratio
Dev/Ref, Fixed Range

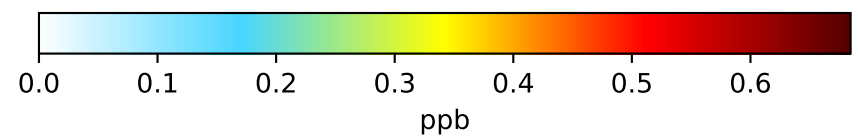
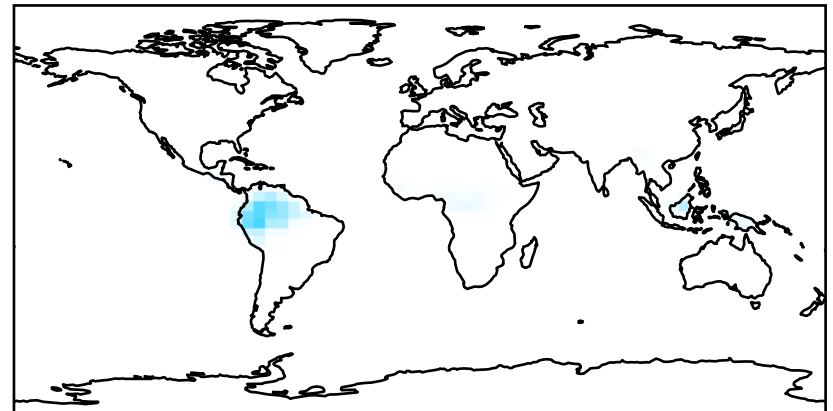


SpeciesConc_ISOP

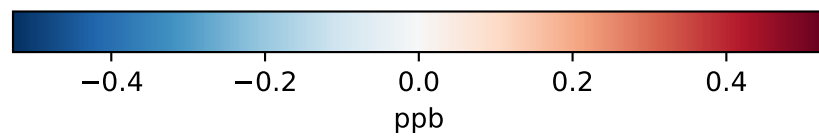
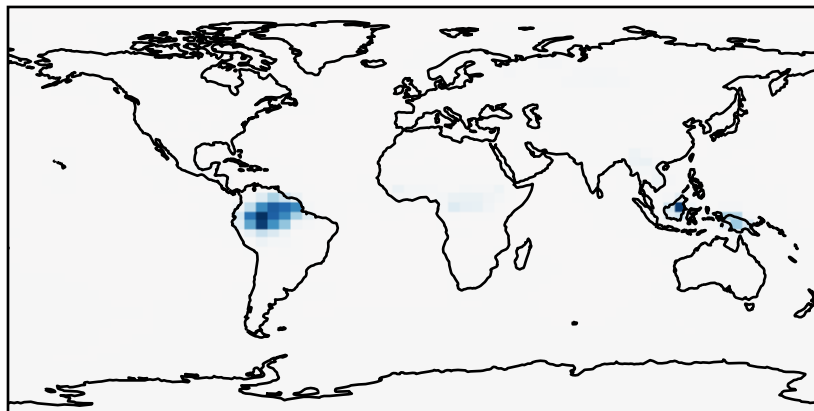
GCC_12.9.0 (Ref)
4.0x5.0



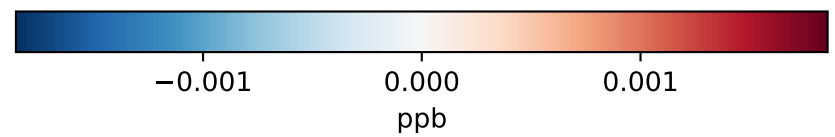
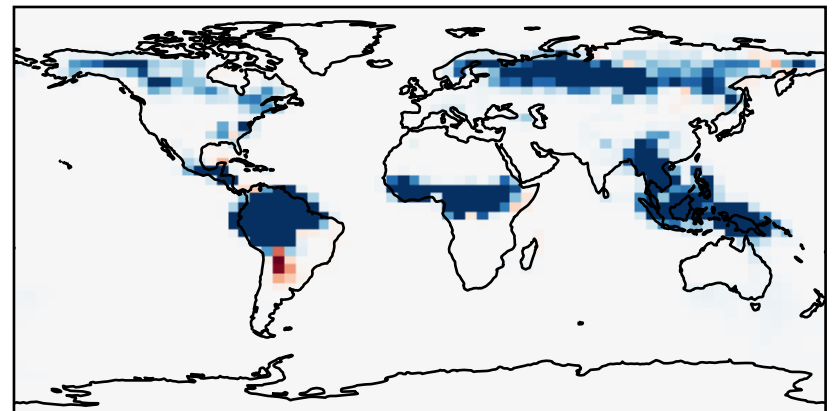
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



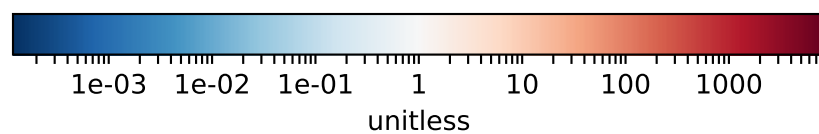
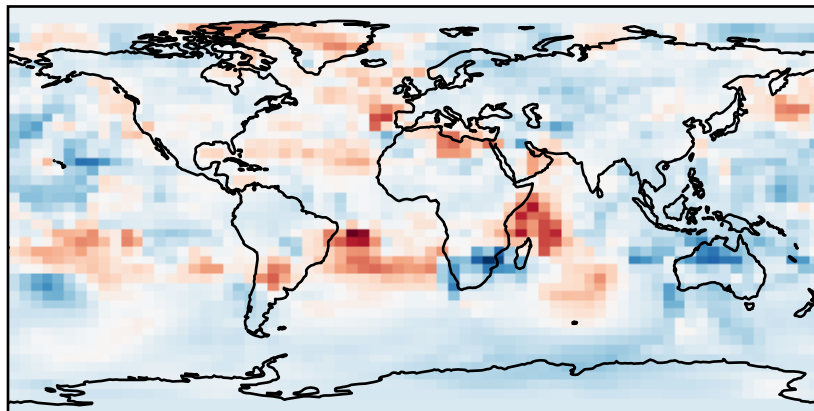
Difference
Dev - Ref, Dynamic Range



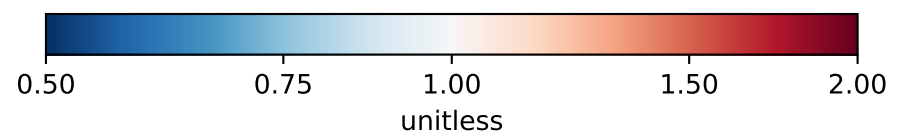
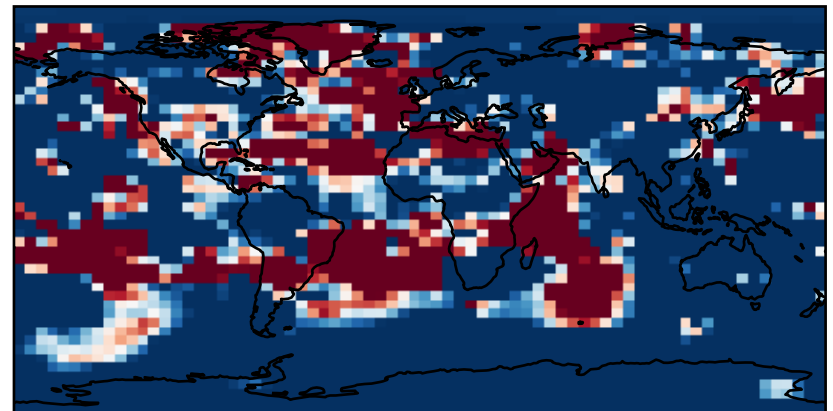
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

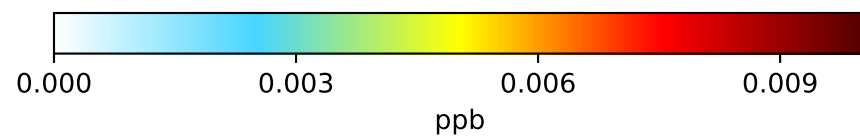
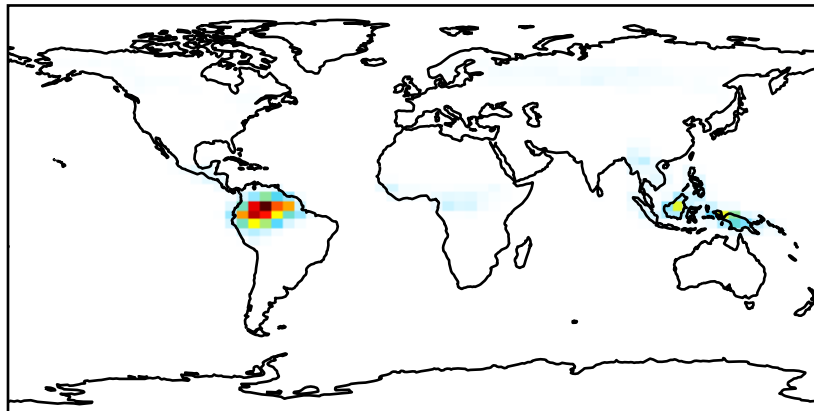


Ratio
Dev/Ref, Fixed Range

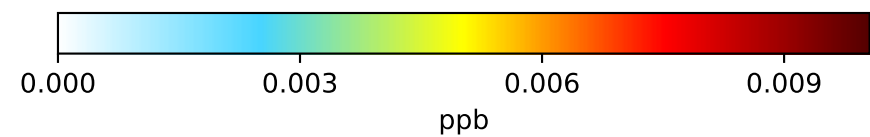
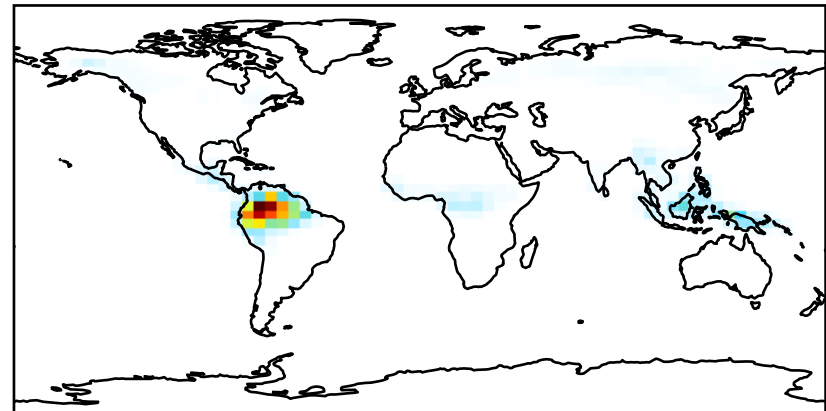


SpeciesConc_MTPA

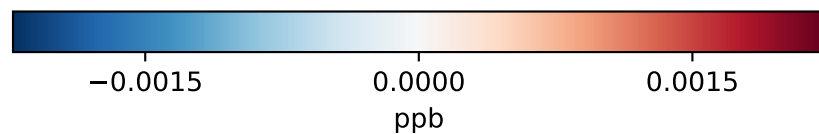
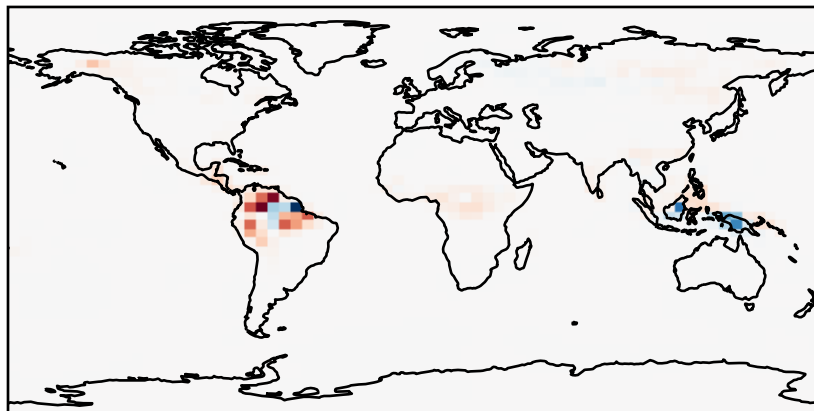
GCC_12.9.0 (Ref)
4.0x5.0



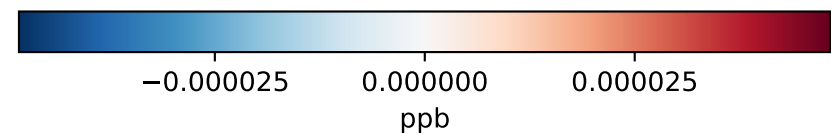
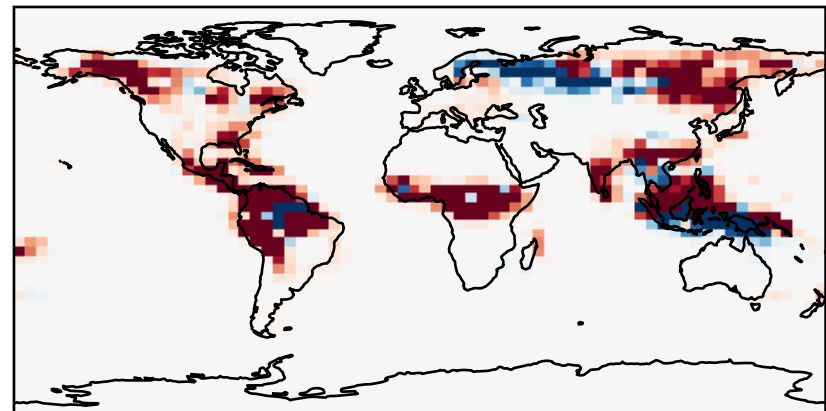
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



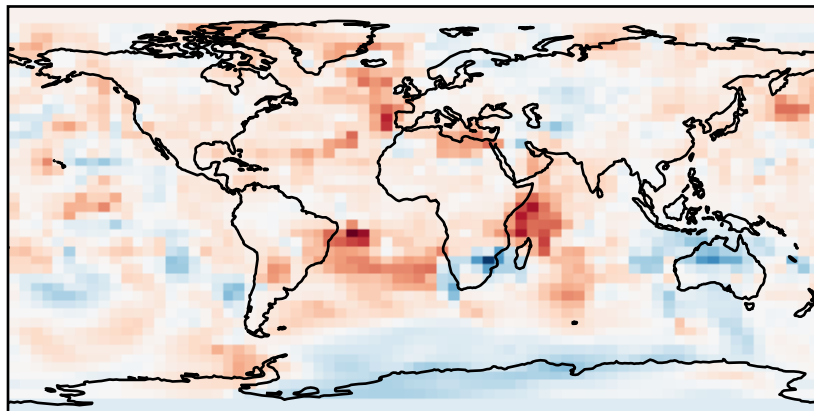
Difference
Dev - Ref, Dynamic Range



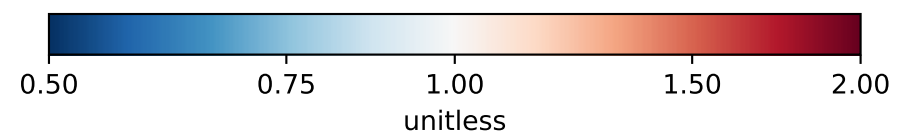
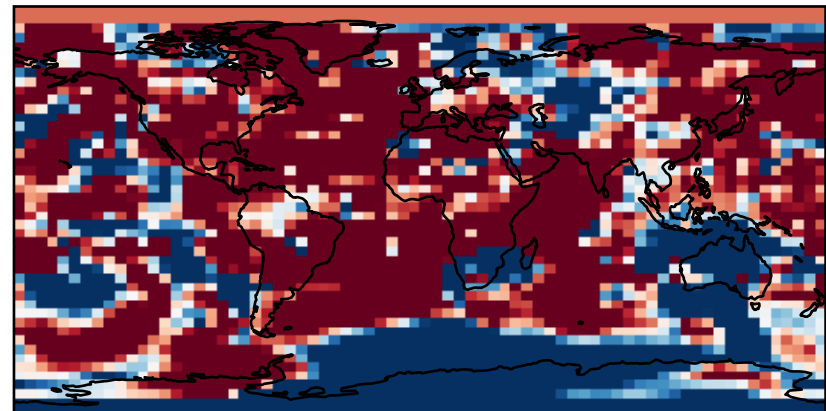
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

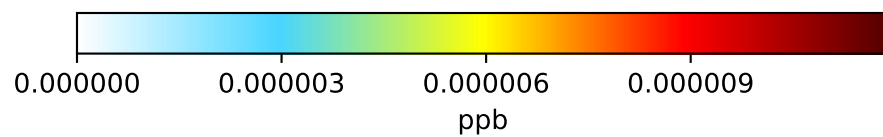
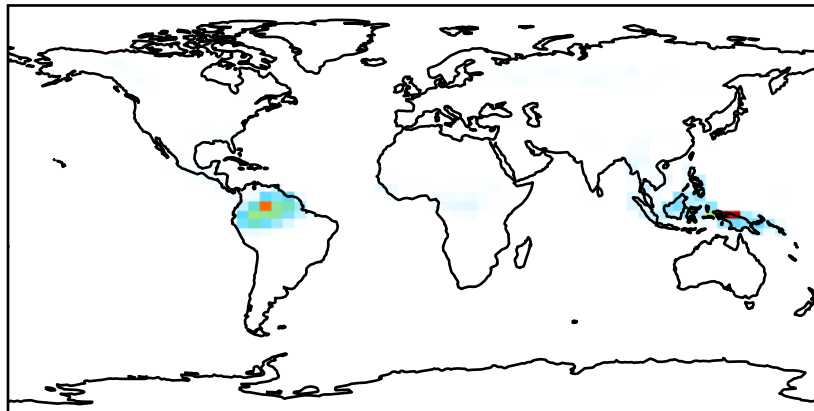


Ratio
Dev/Ref, Fixed Range

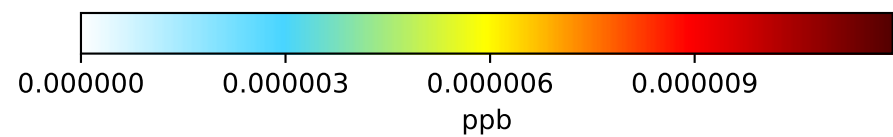
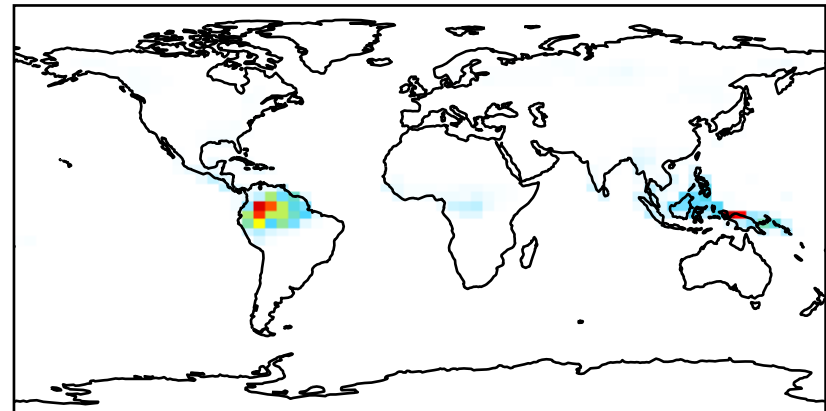


SpeciesConc_MTPO

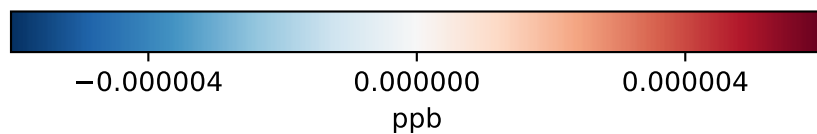
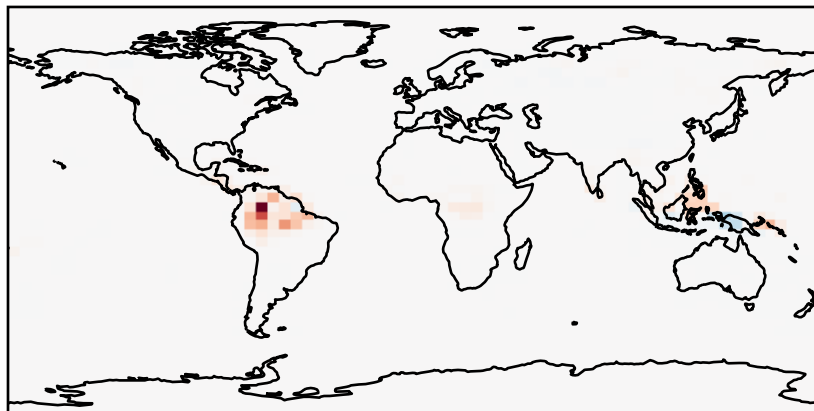
GCC_12.9.0 (Ref)
4.0x5.0



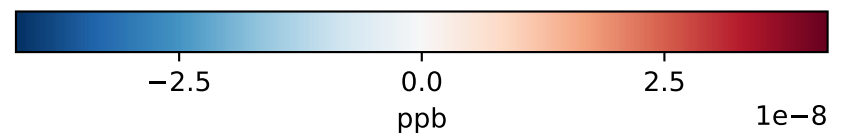
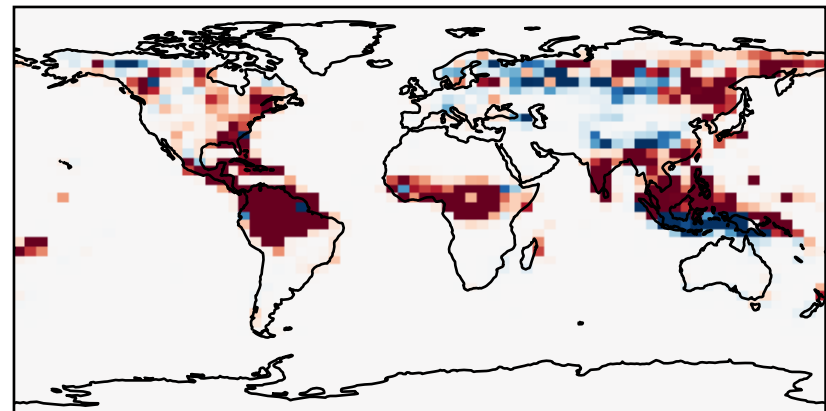
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



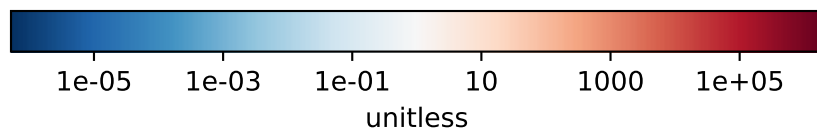
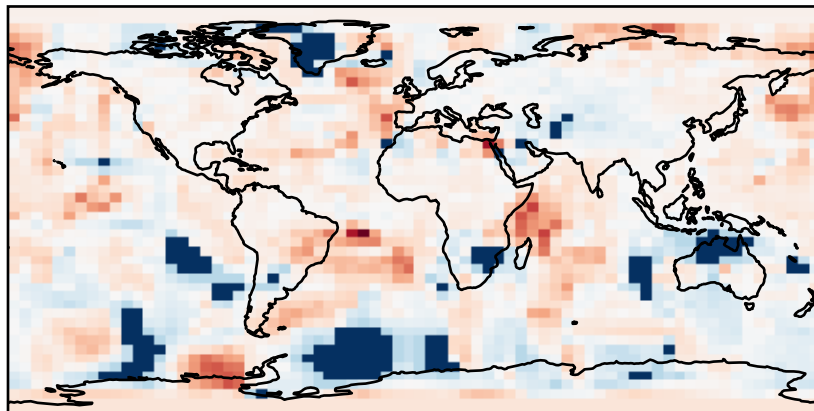
Difference
Dev - Ref, Dynamic Range



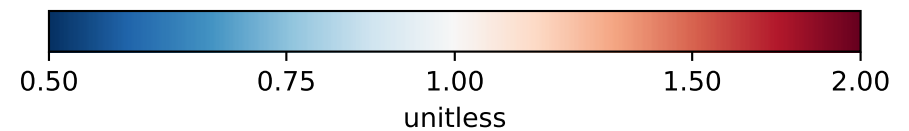
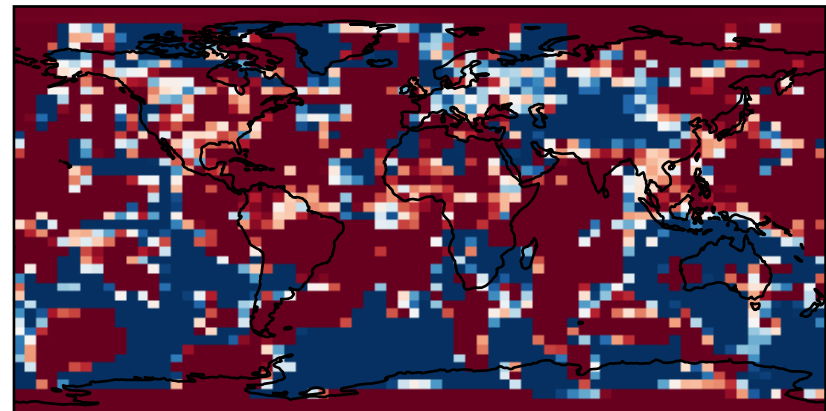
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

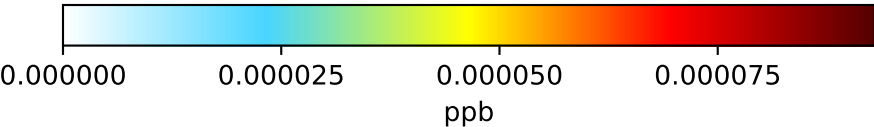
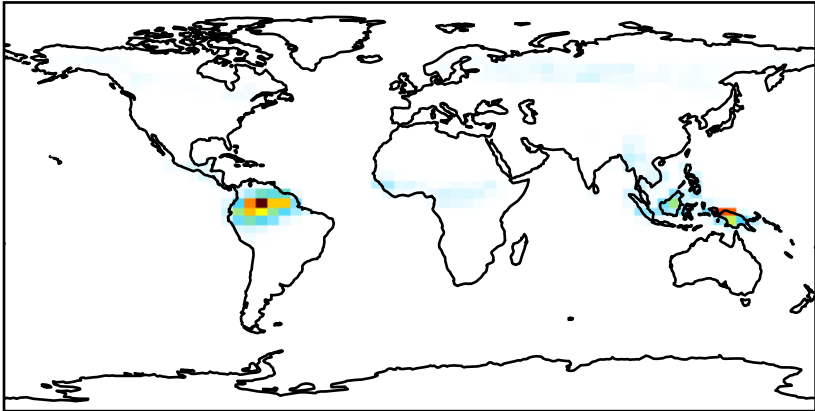


Ratio
Dev/Ref, Fixed Range

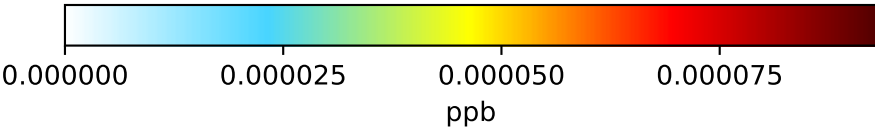
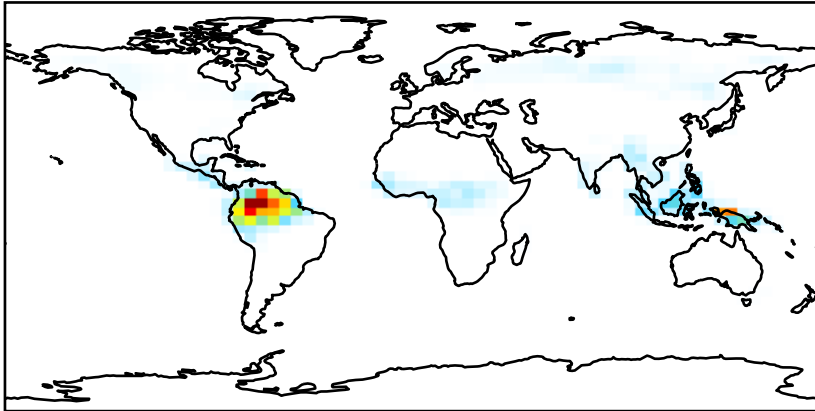


SpeciesConc_LIMO

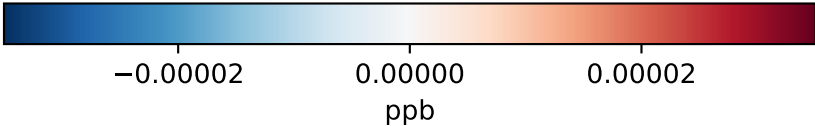
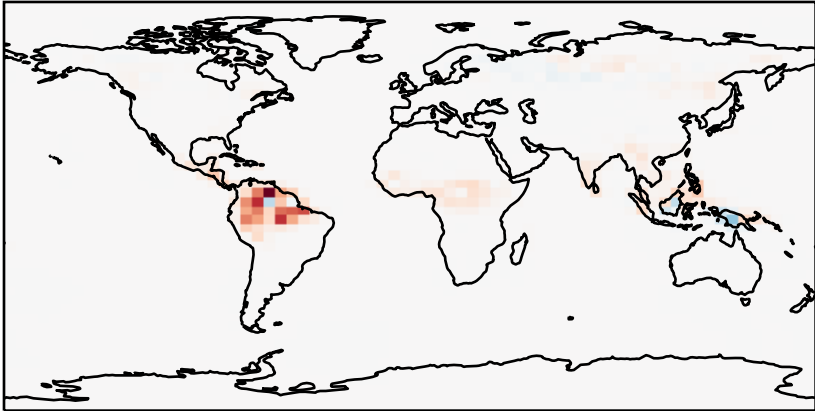
GCC_12.9.0 (Ref)
4.0x5.0



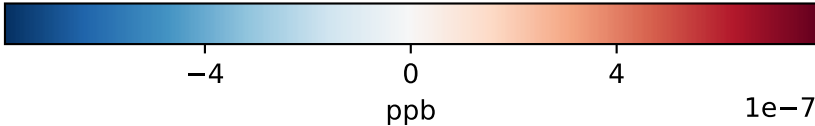
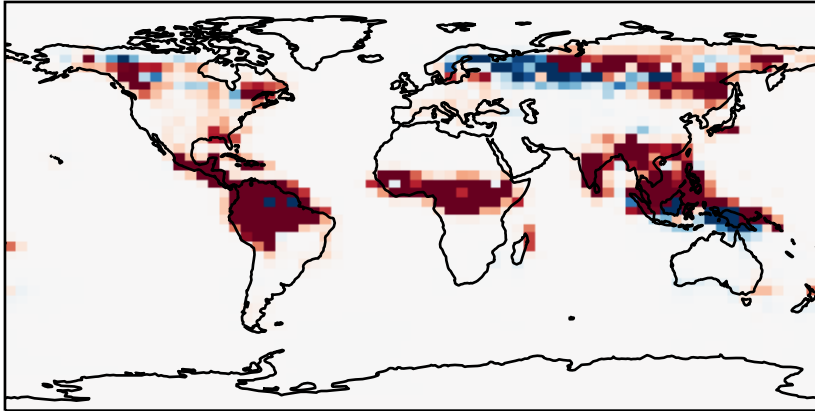
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



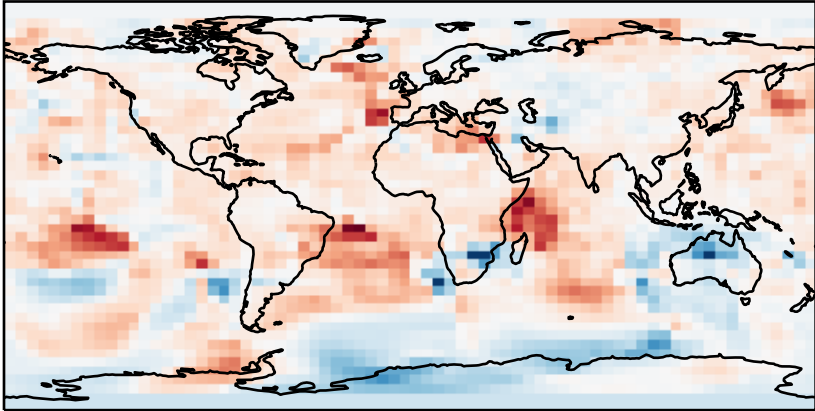
Difference
Dev - Ref, Dynamic Range



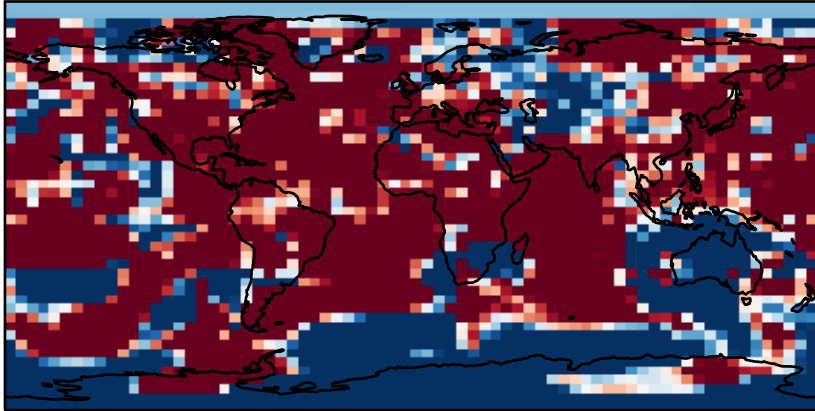
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

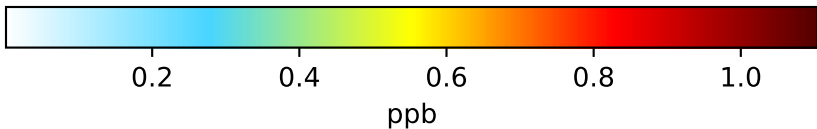
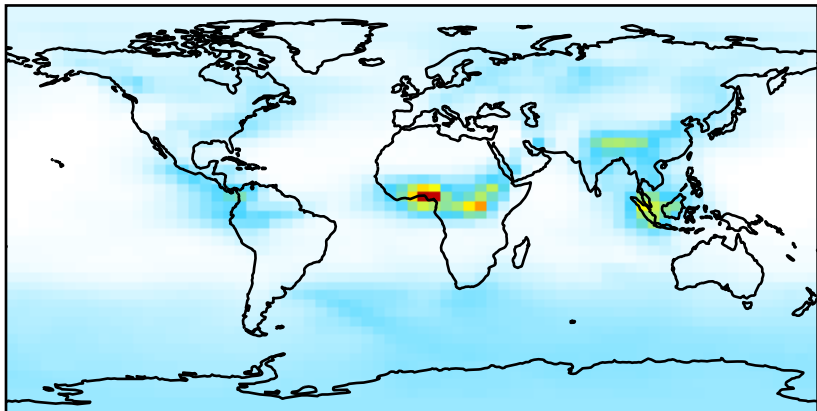


Ratio
Dev/Ref, Fixed Range

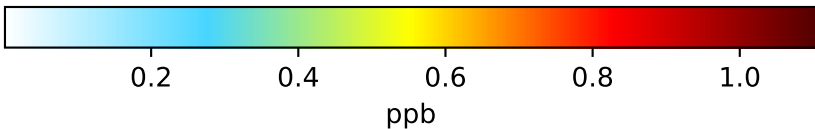
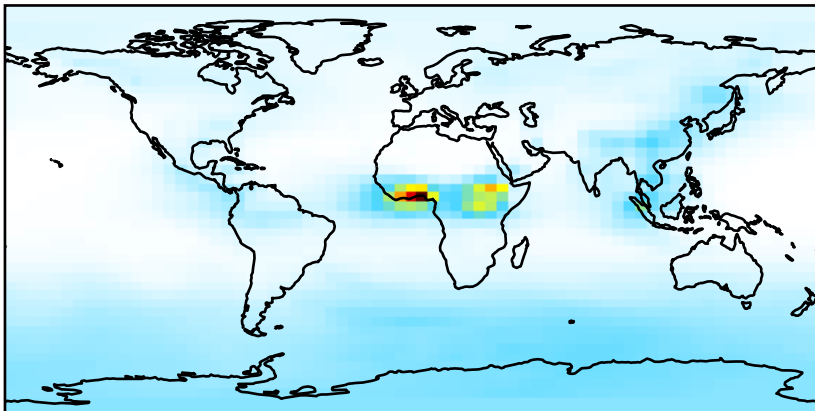


SpeciesConc_ALK4

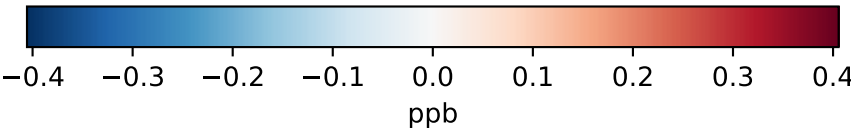
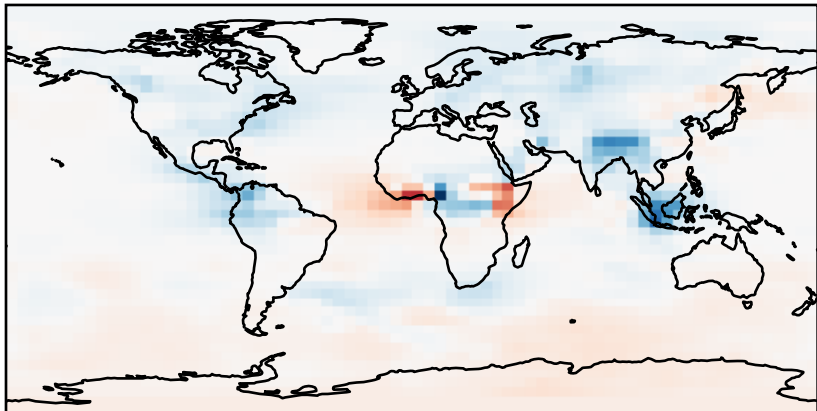
GCC_12.9.0 (Ref)
4.0x5.0



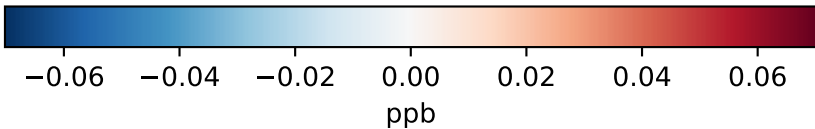
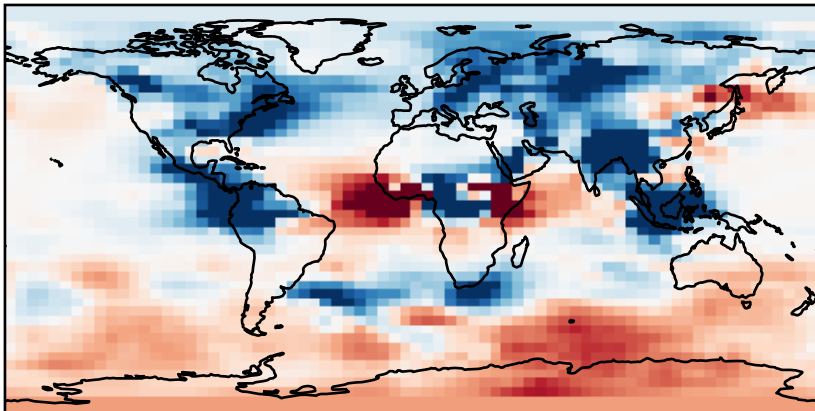
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



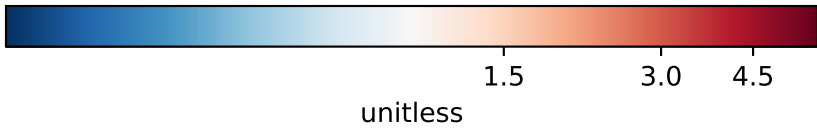
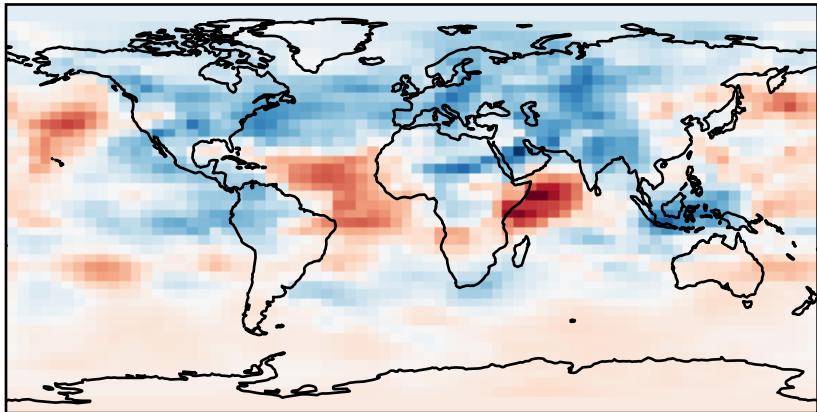
Difference
Dev - Ref, Dynamic Range



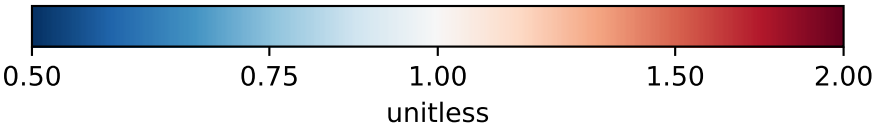
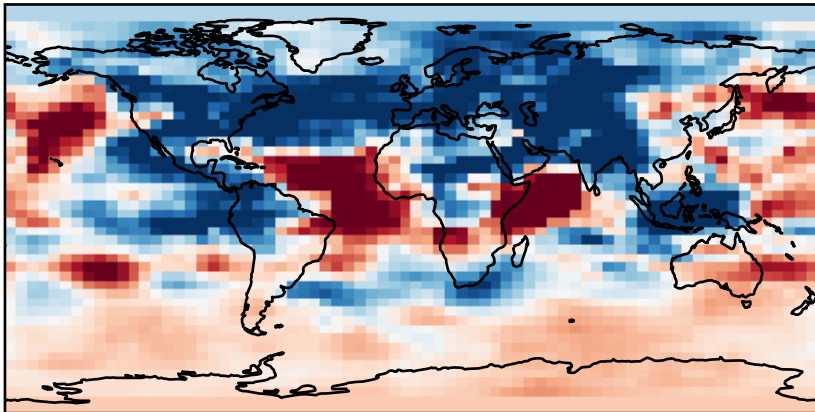
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

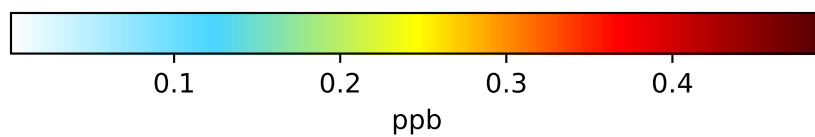
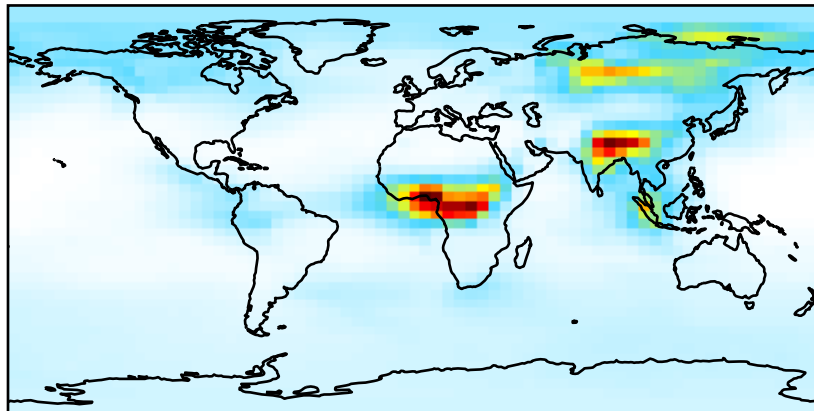


Ratio
Dev/Ref, Fixed Range

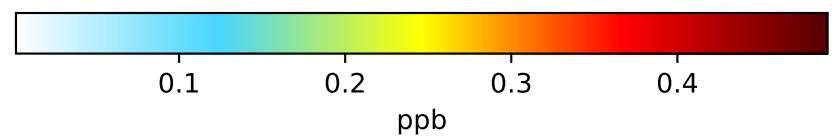
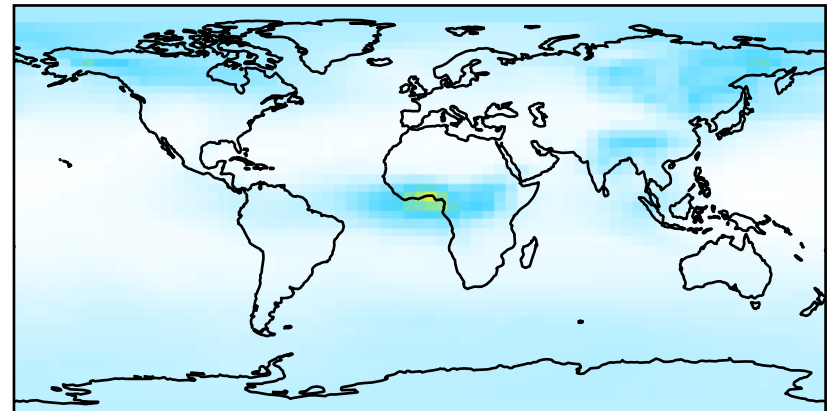


SpeciesConc_BENZ

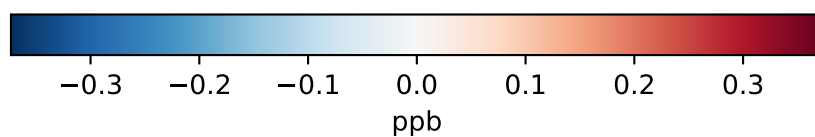
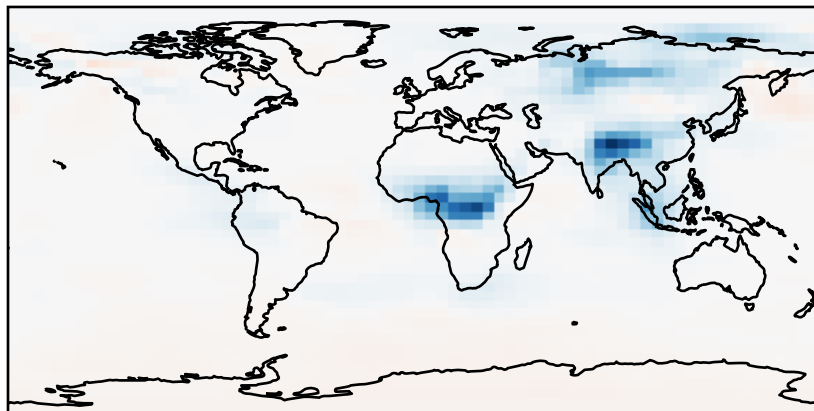
GCC_12.9.0 (Ref)
4.0x5.0



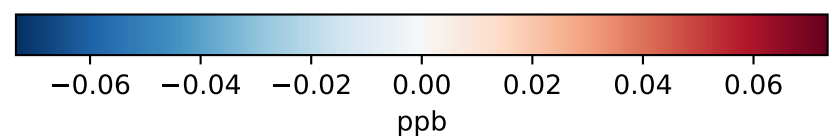
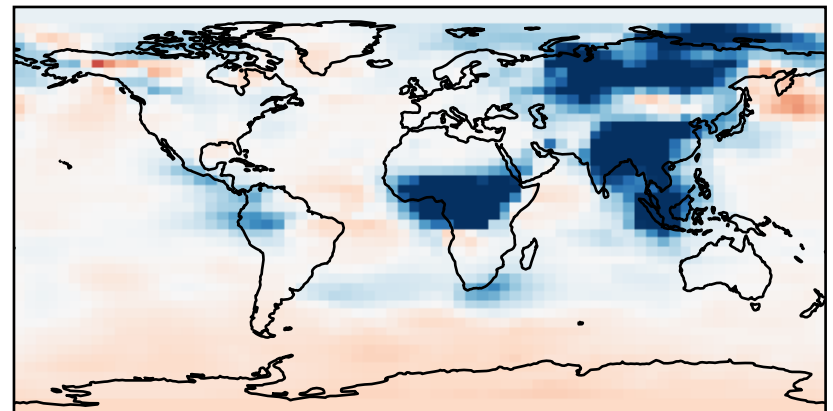
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



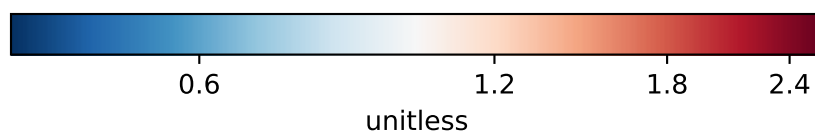
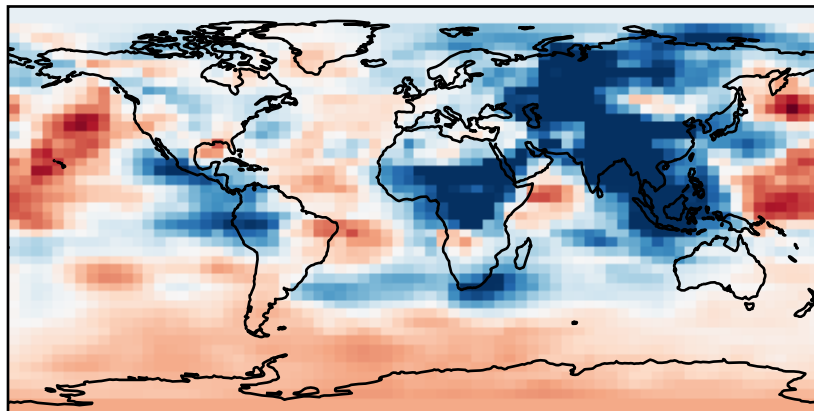
Difference
Dev - Ref, Dynamic Range



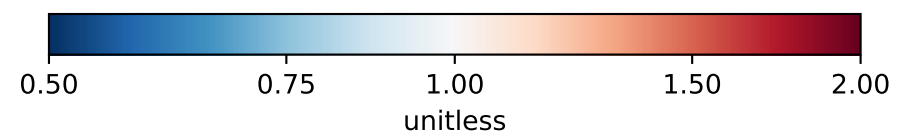
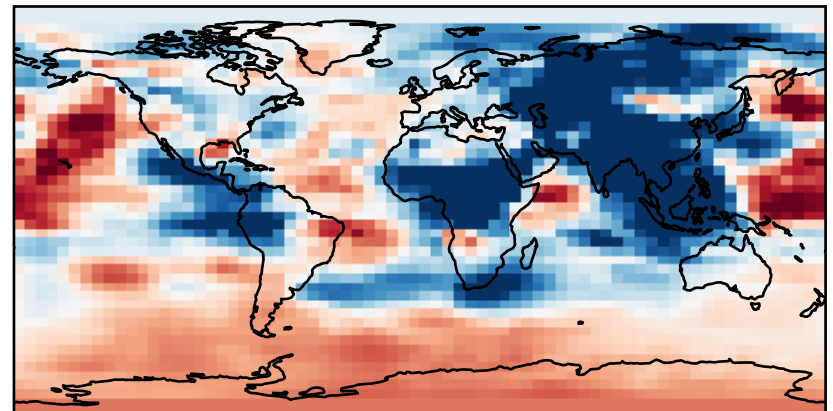
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

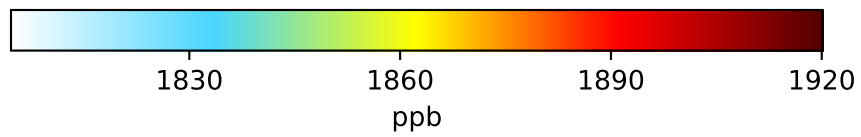
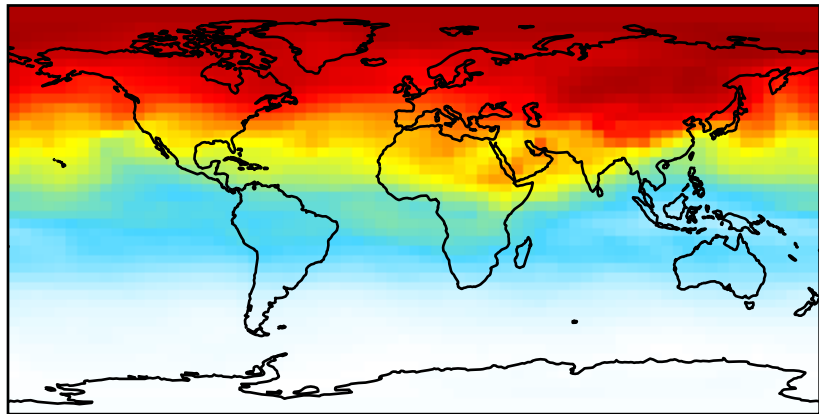


Ratio
Dev/Ref, Fixed Range

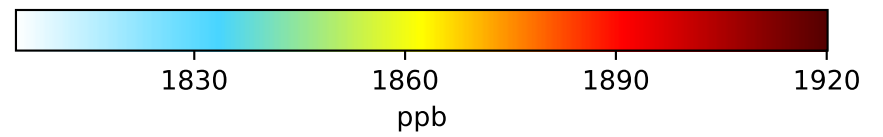
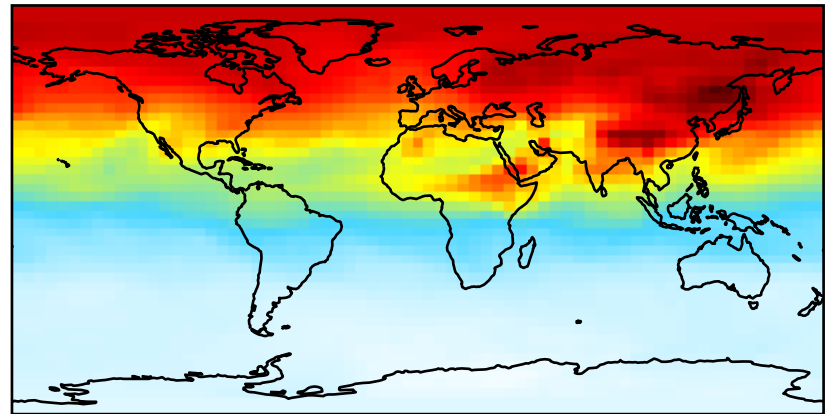


SpeciesConc_CH4

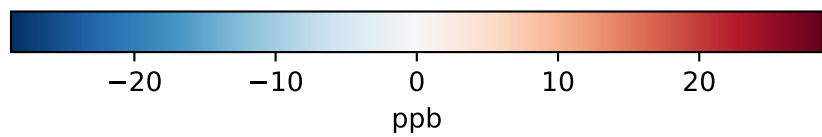
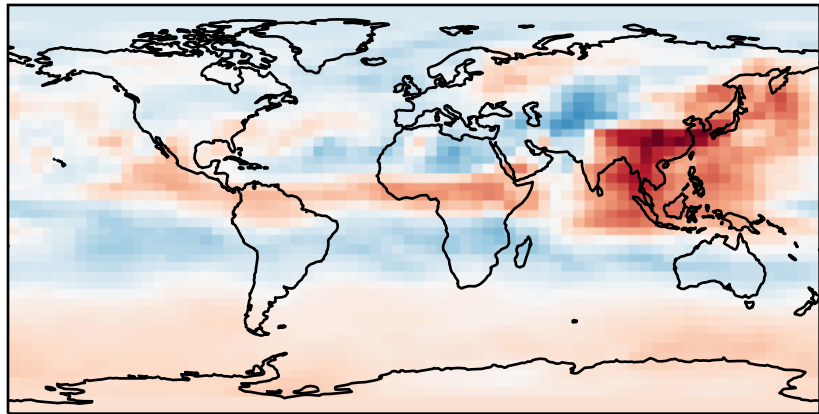
GCC_12.9.0 (Ref)
4.0x5.0



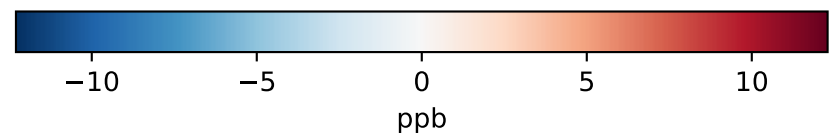
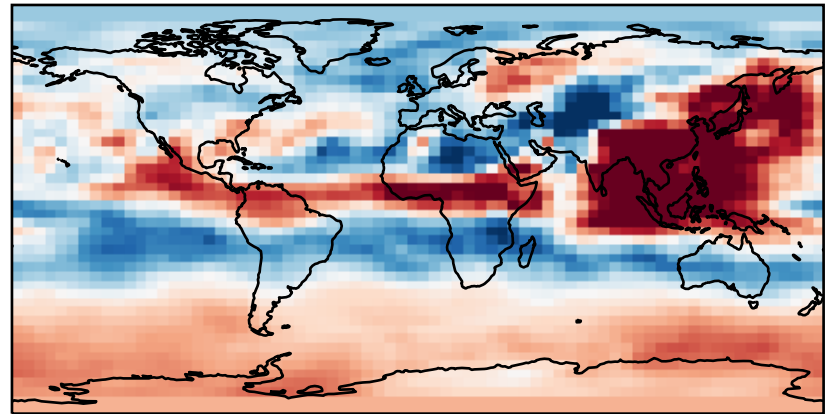
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



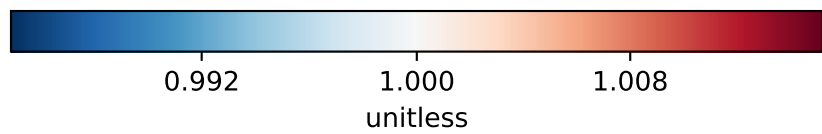
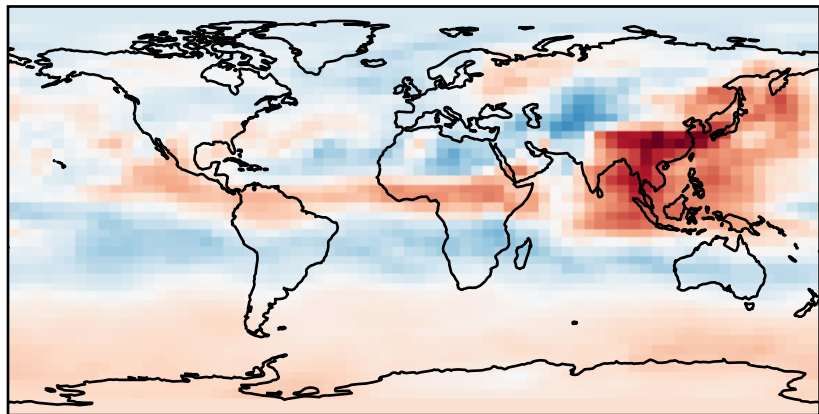
Difference
Dev - Ref, Dynamic Range



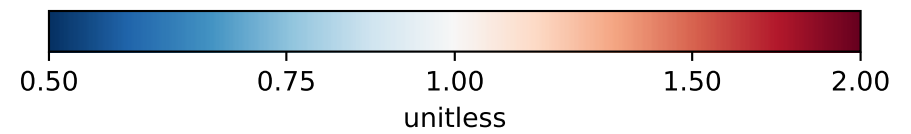
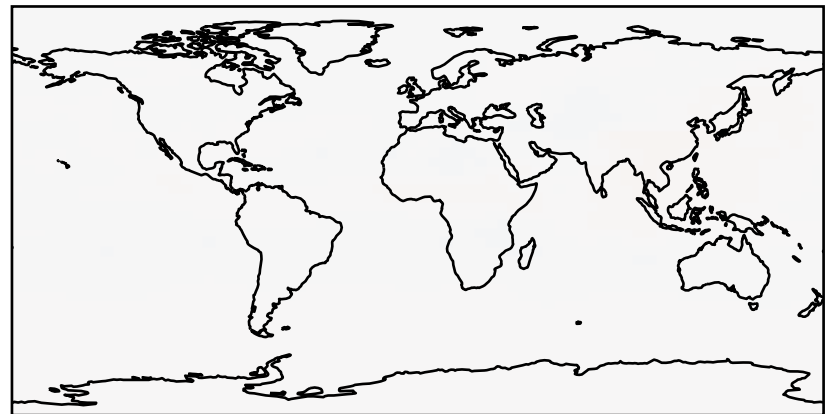
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

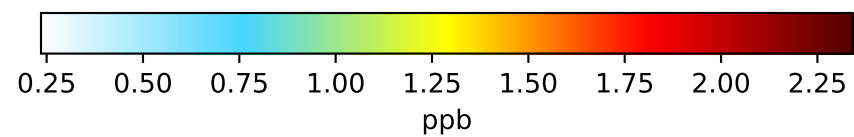
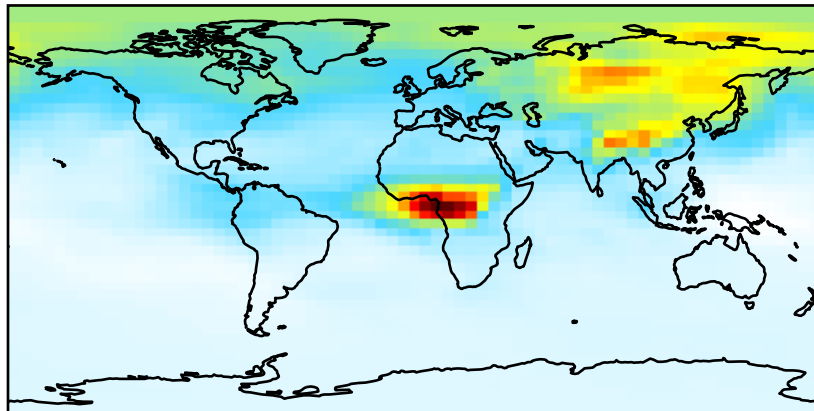


Ratio
Dev/Ref, Fixed Range

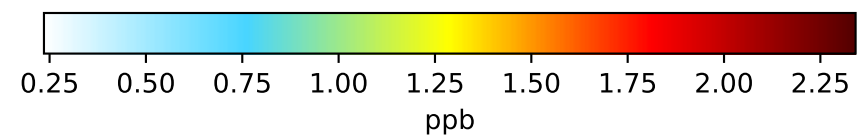
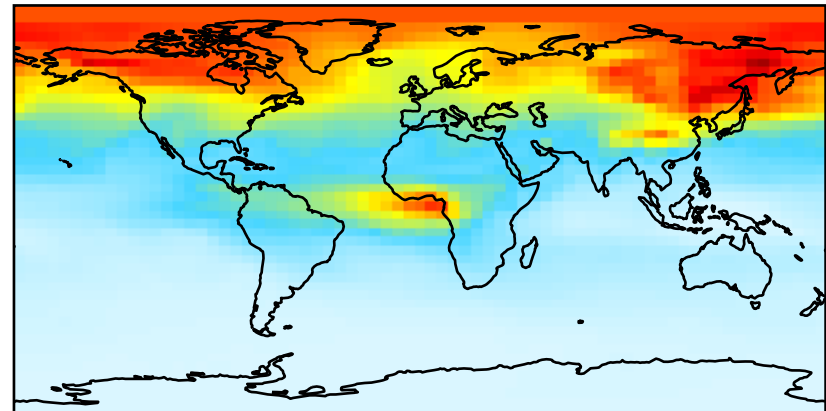


SpeciesConc_C2H6

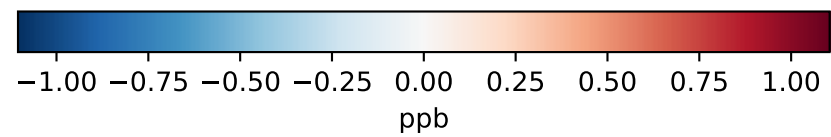
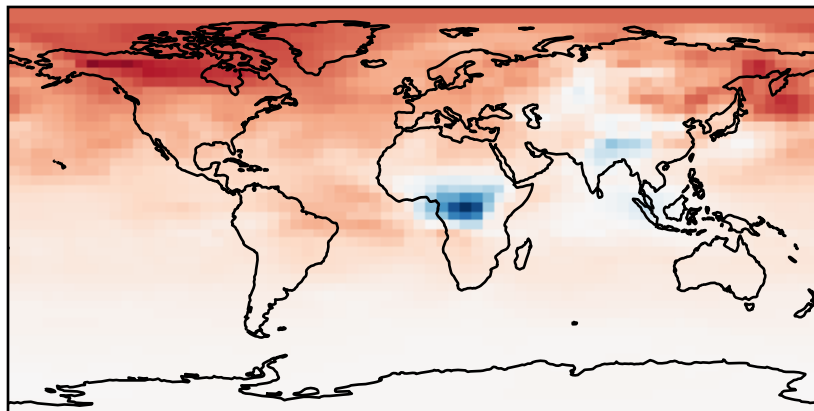
GCC_12.9.0 (Ref)
4.0x5.0



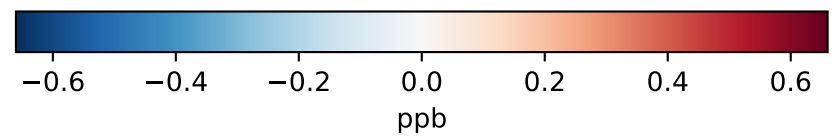
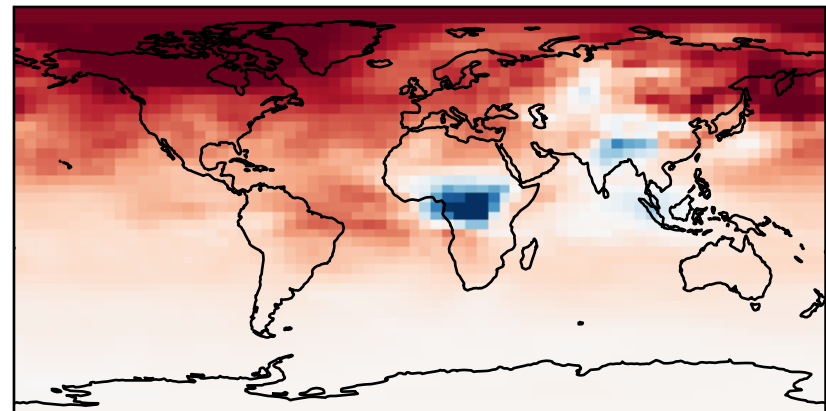
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



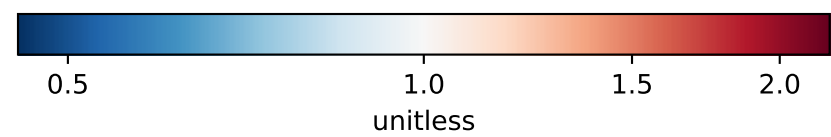
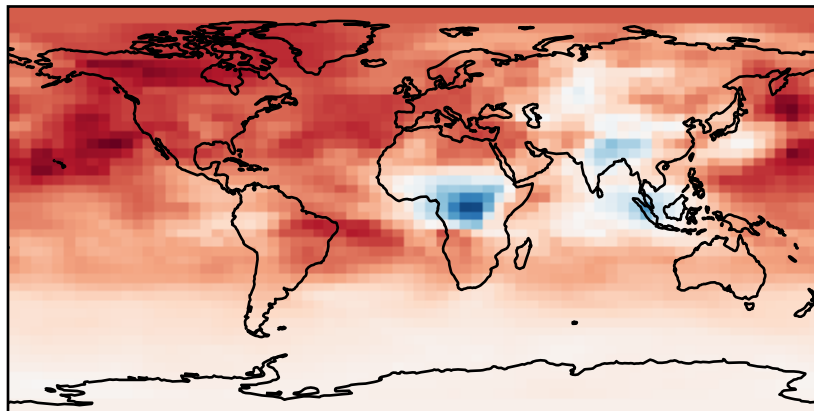
Difference
Dev - Ref, Dynamic Range



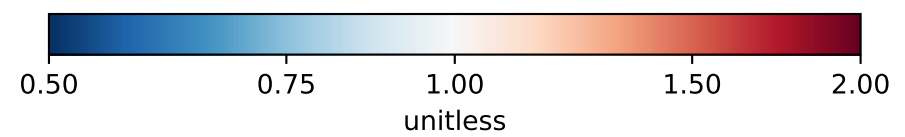
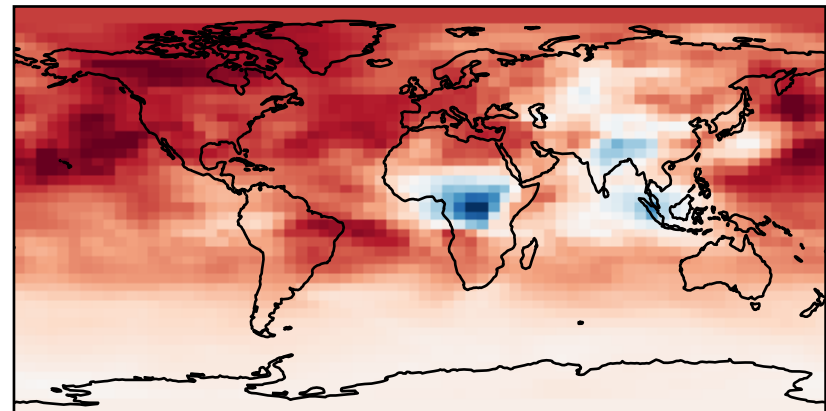
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

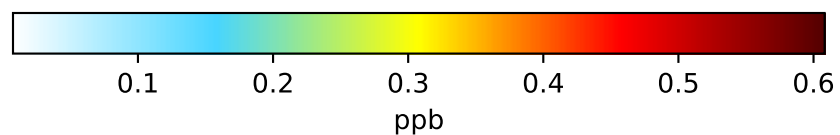
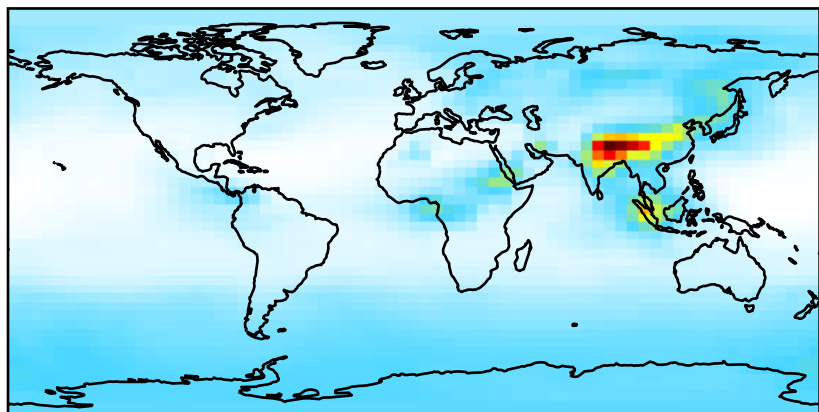


Ratio
Dev/Ref, Fixed Range

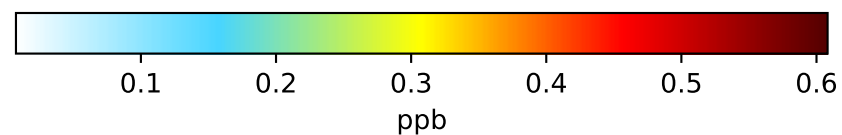
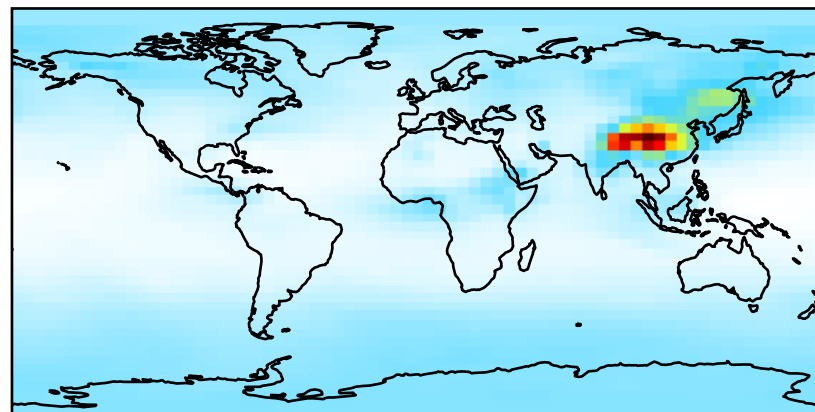


SpeciesConc_C3H8

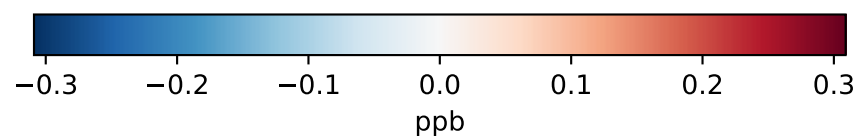
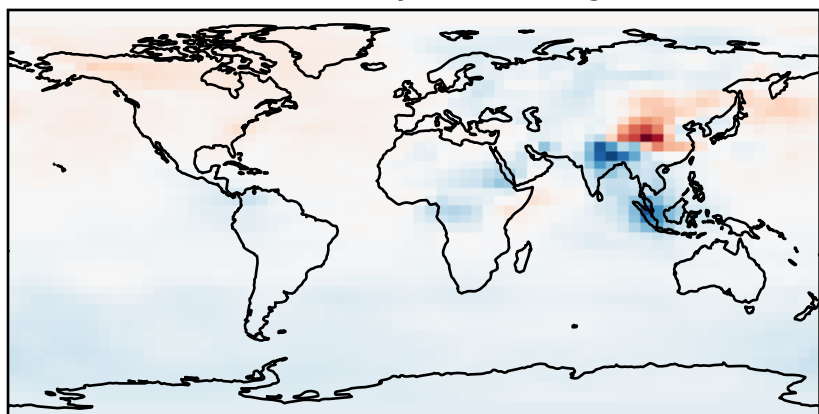
GCC_12.9.0 (Ref)
4.0x5.0



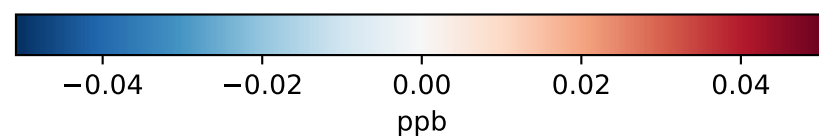
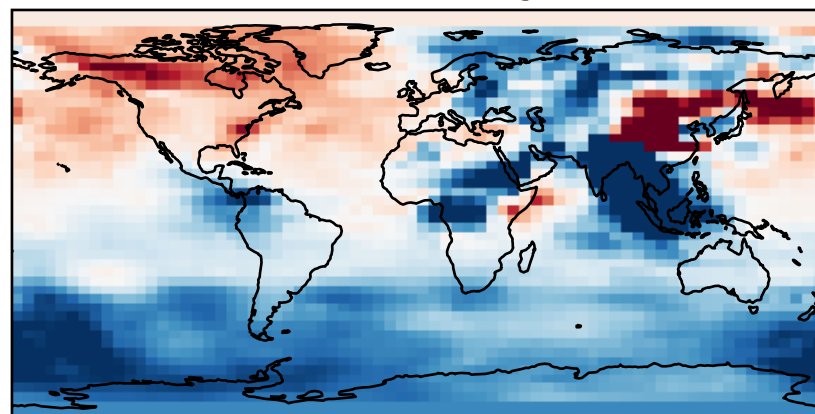
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



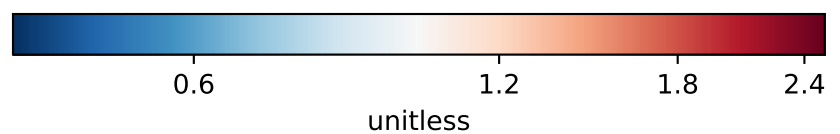
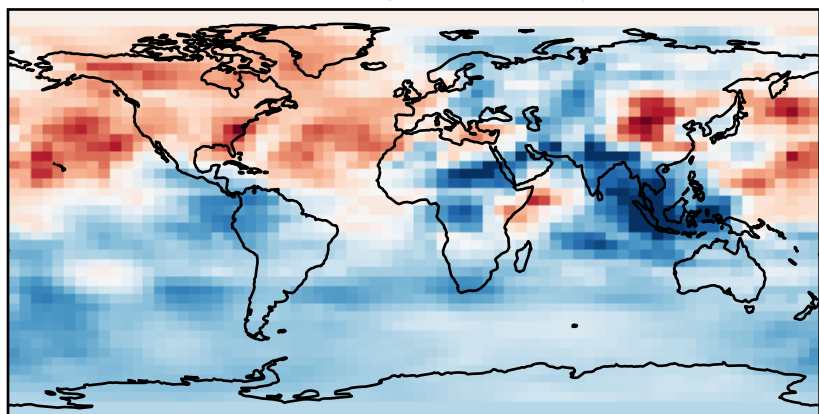
Difference
Dev - Ref, Dynamic Range



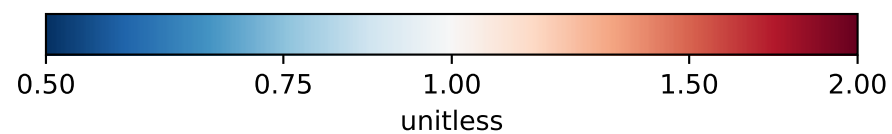
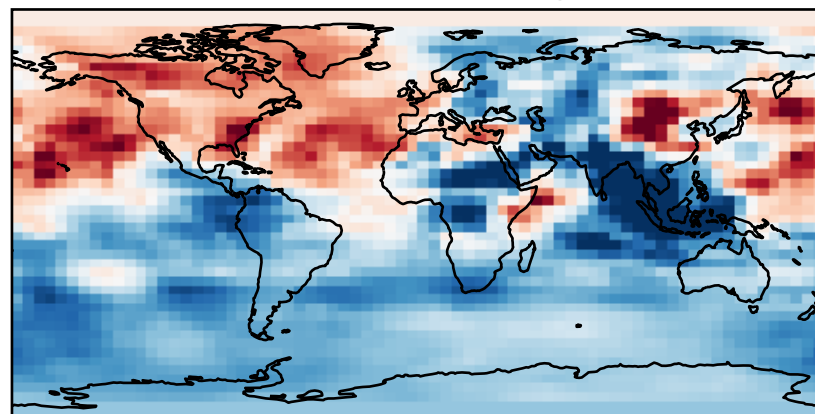
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

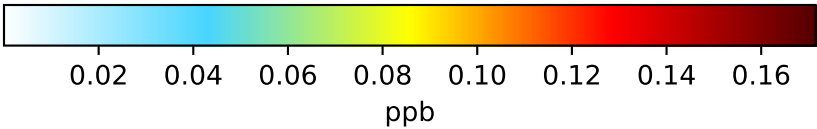
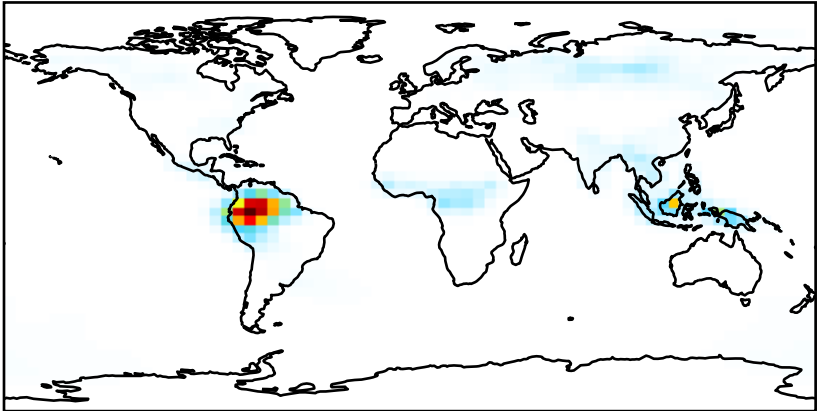


Ratio
Dev/Ref, Fixed Range

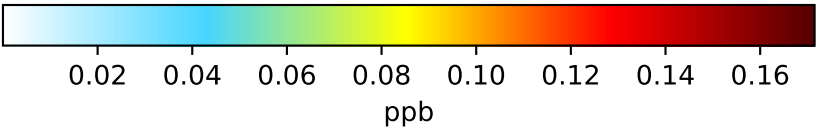
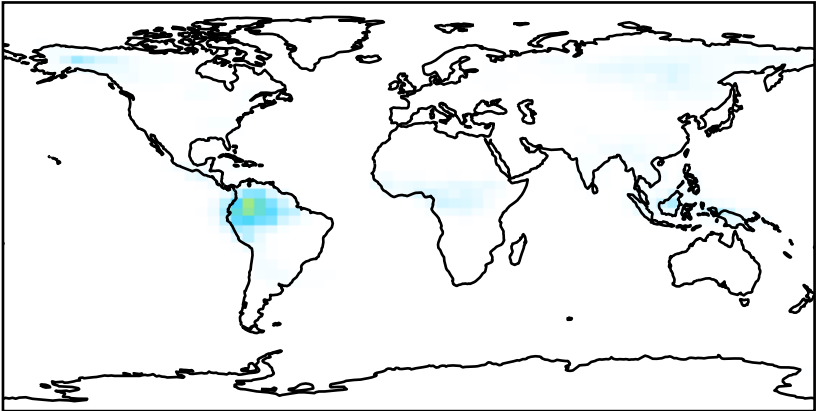


SpeciesConc_PRPE

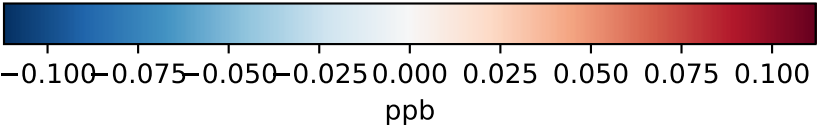
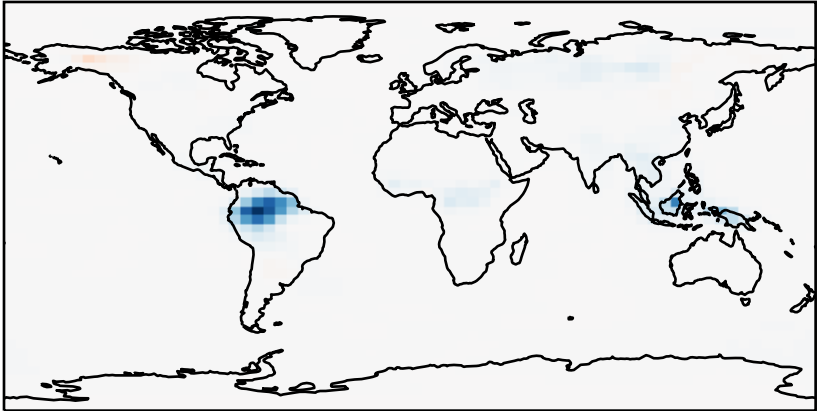
GCC_12.9.0 (Ref)
4.0x5.0



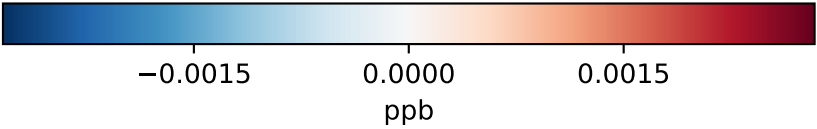
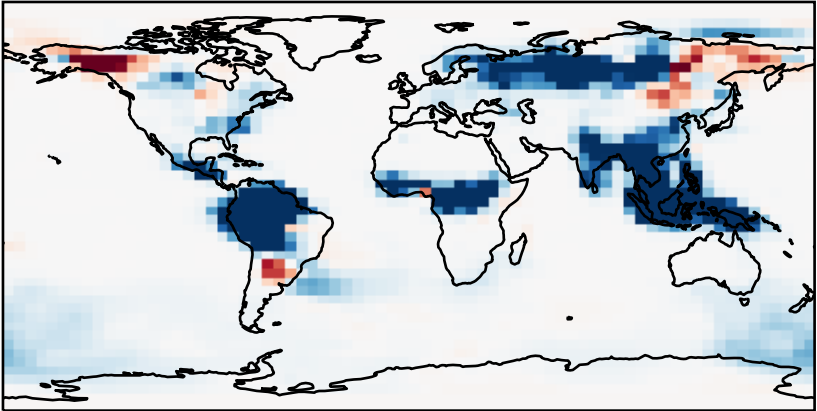
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



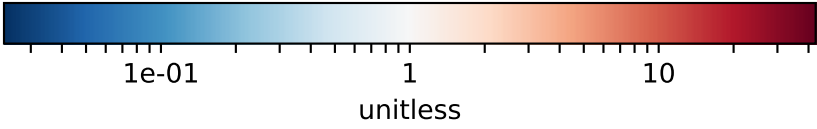
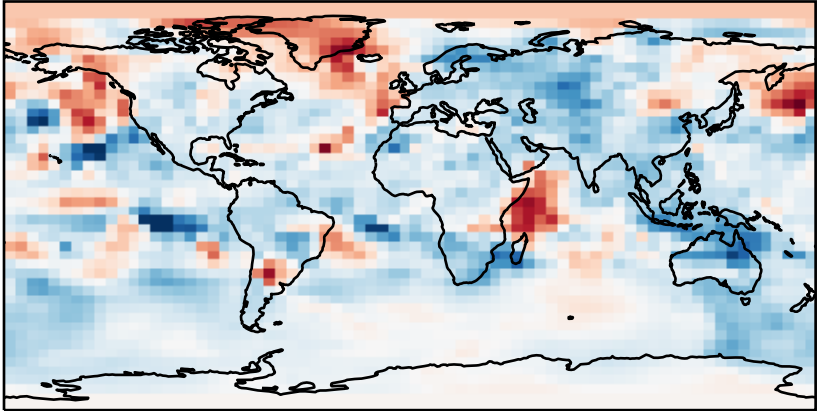
Difference
Dev - Ref, Dynamic Range



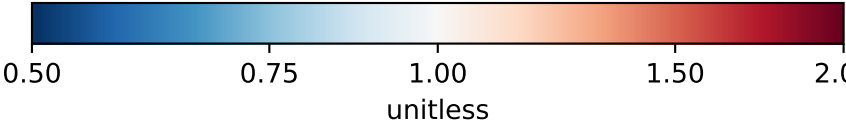
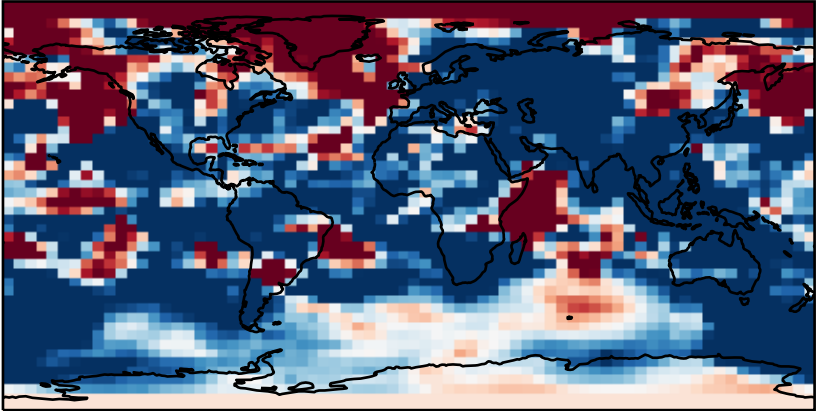
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

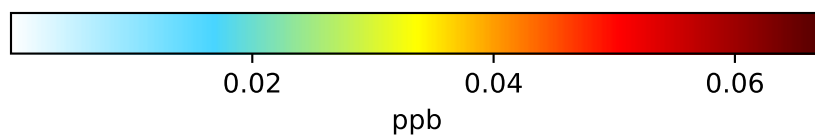
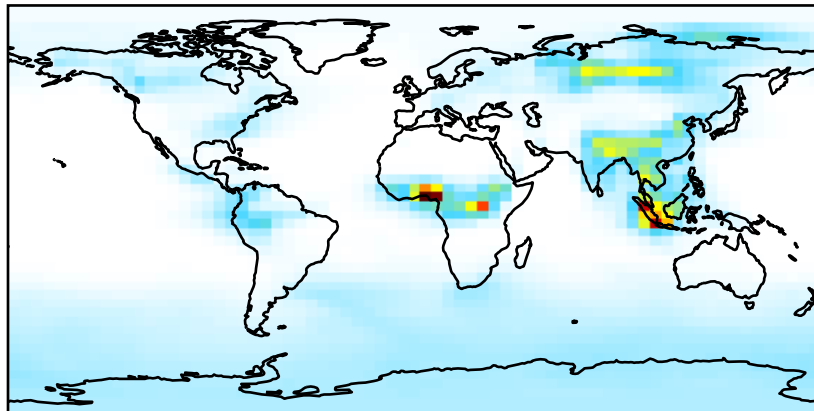


Ratio
Dev/Ref, Fixed Range

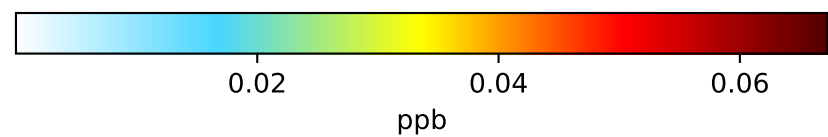
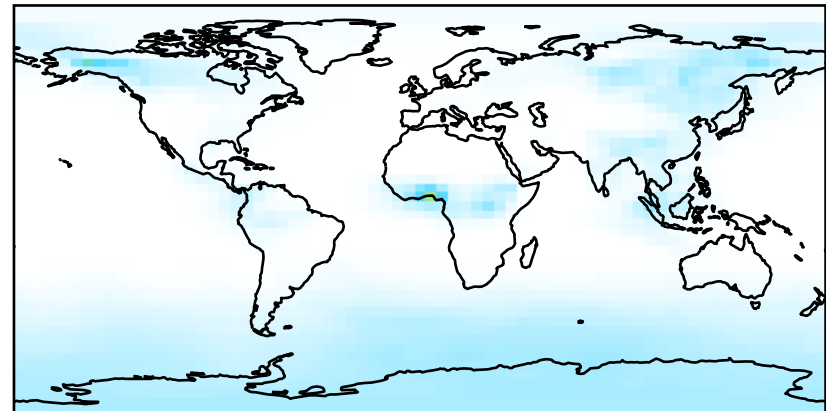


SpeciesConc_TOLU

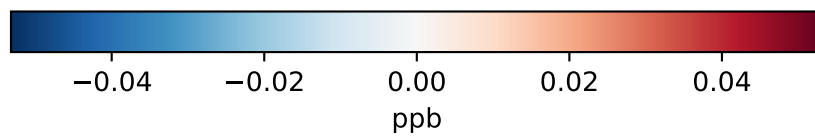
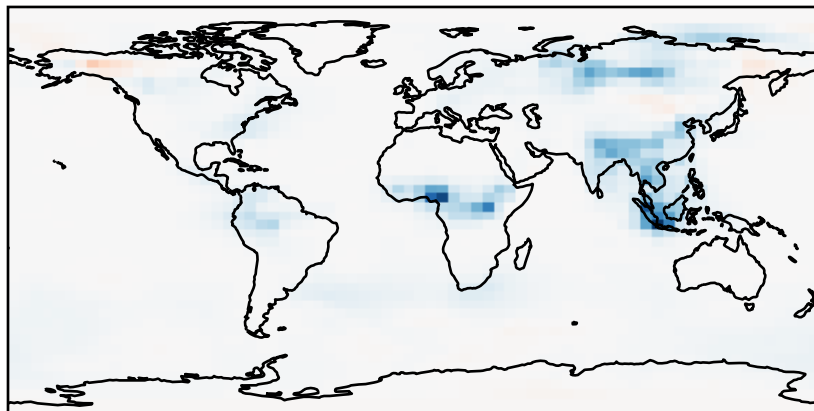
GCC_12.9.0 (Ref)
4.0x5.0



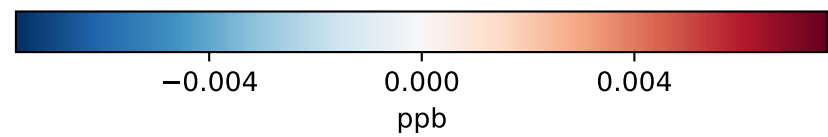
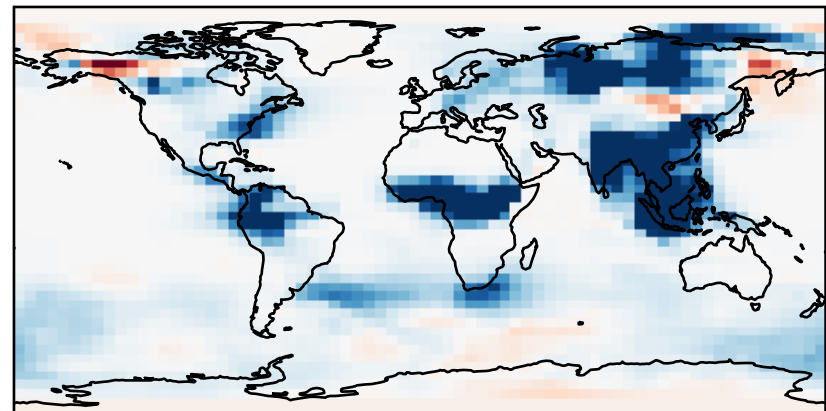
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



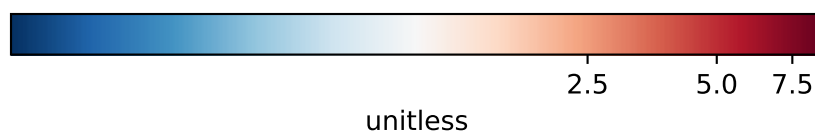
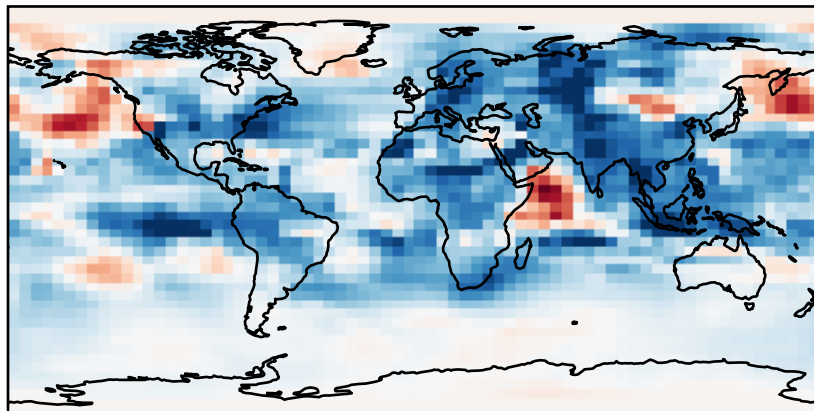
Difference
Dev - Ref, Dynamic Range



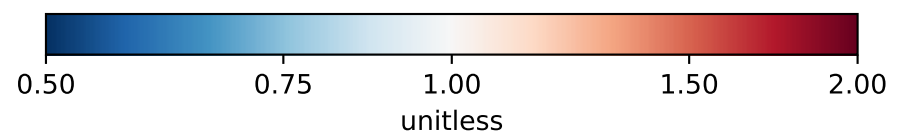
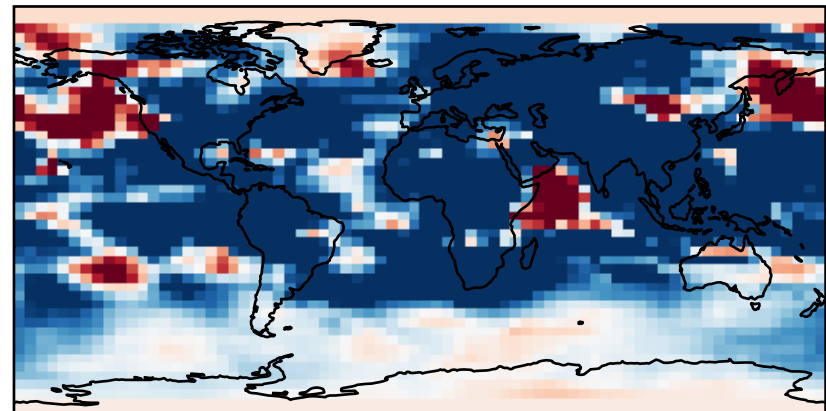
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

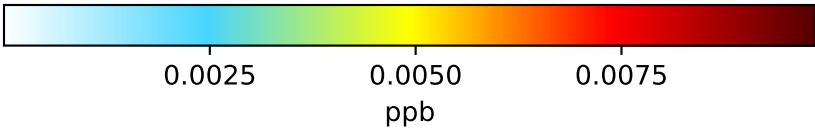
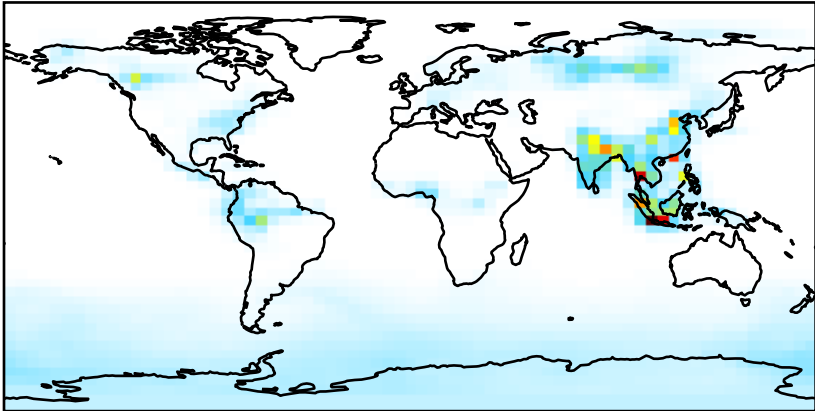


Ratio
Dev/Ref, Fixed Range

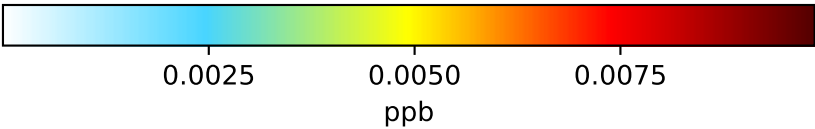
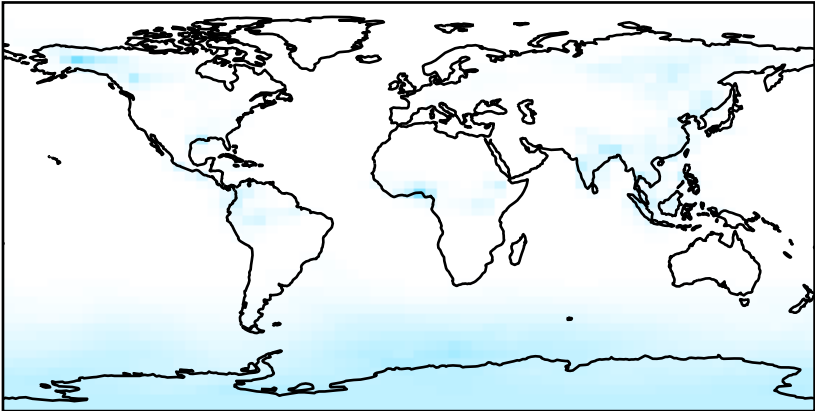


SpeciesConc_XYLE

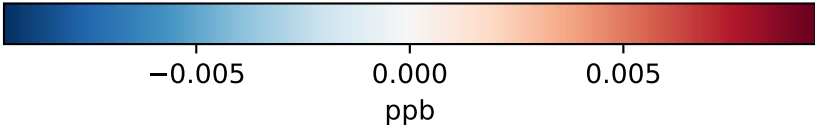
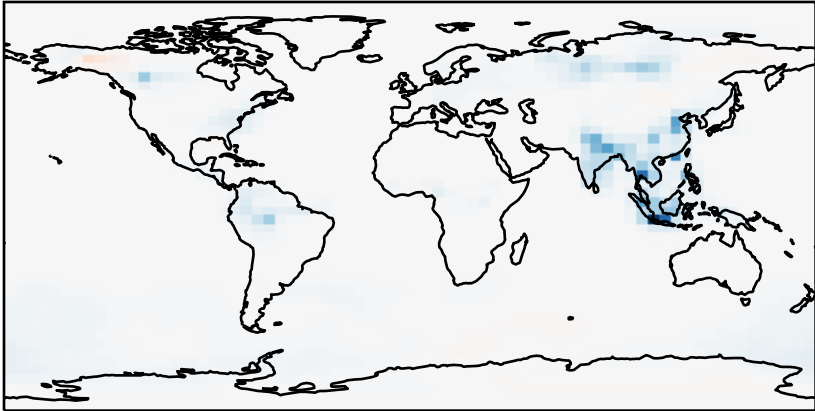
GCC_12.9.0 (Ref)
4.0x5.0



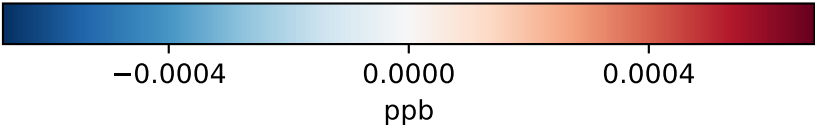
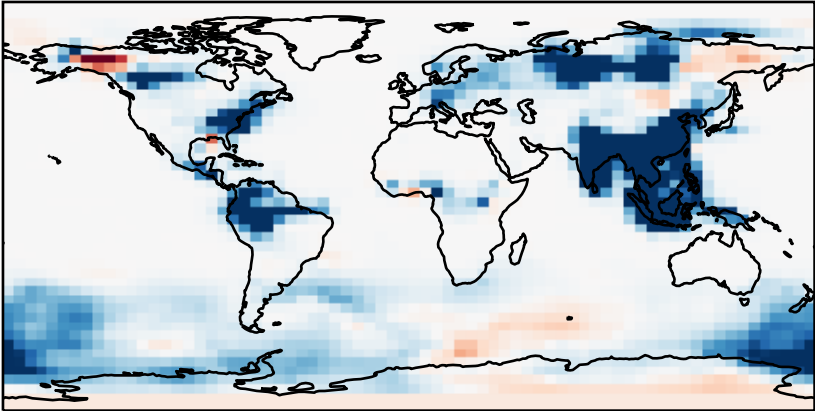
GCC_13.0.0-beta.0 (Dev)
4.0x5.0



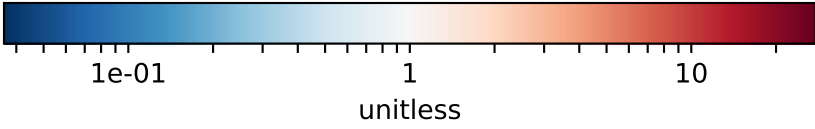
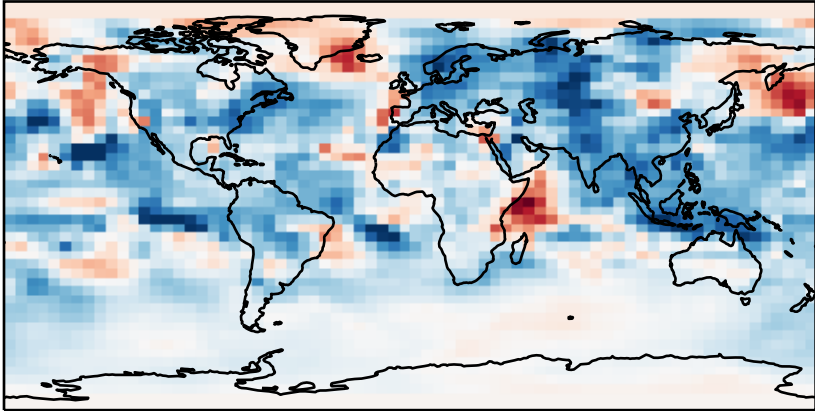
Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range



Ratio
Dev/Ref, Fixed Range

