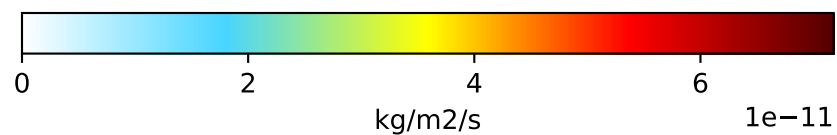
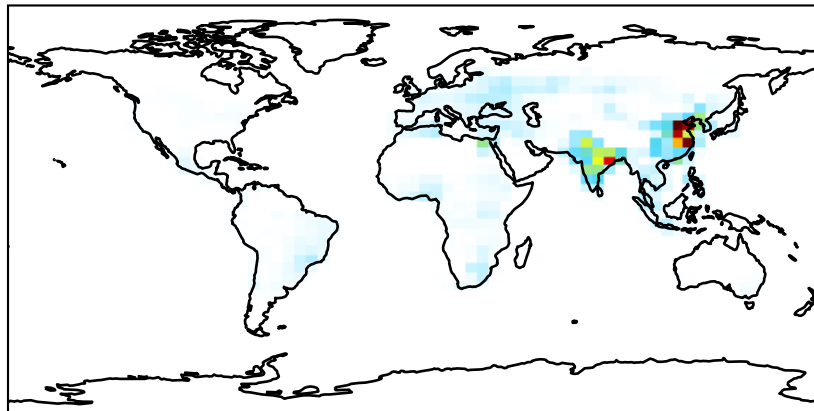
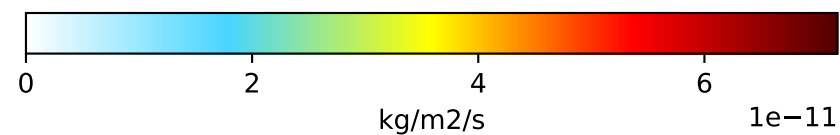
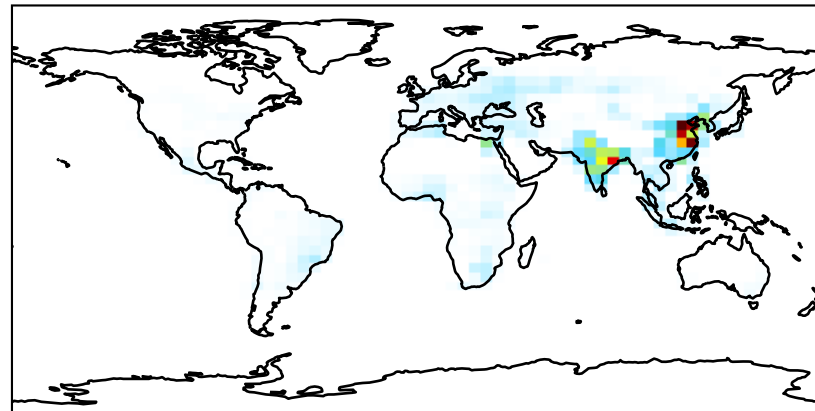


# EmisDST1\_Anthro

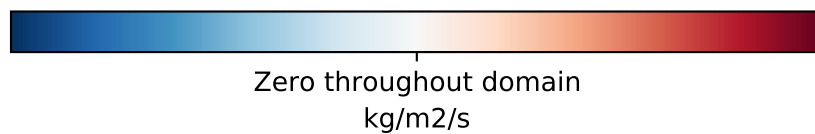
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



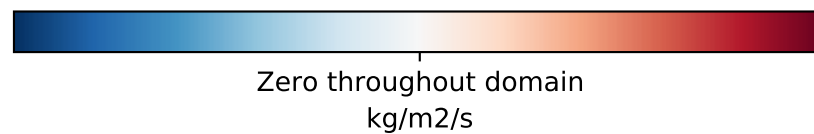
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



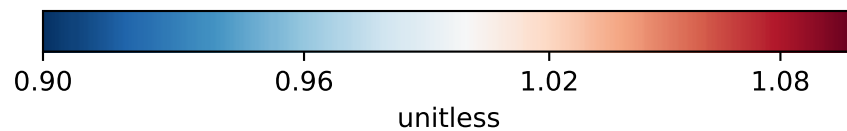
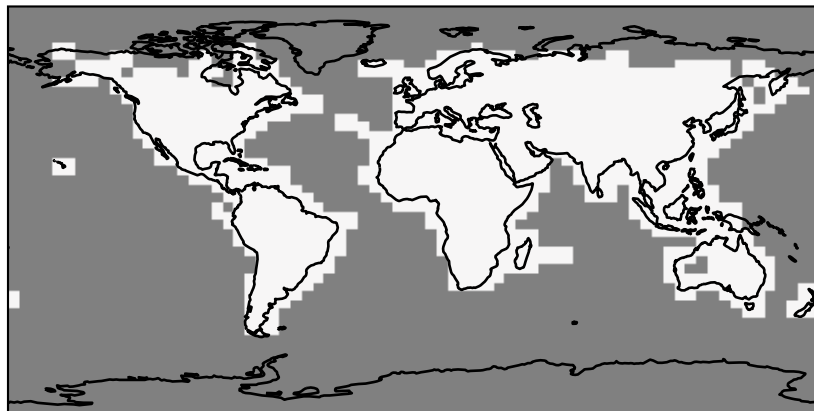
Difference  
Dev - Ref, Dynamic Range



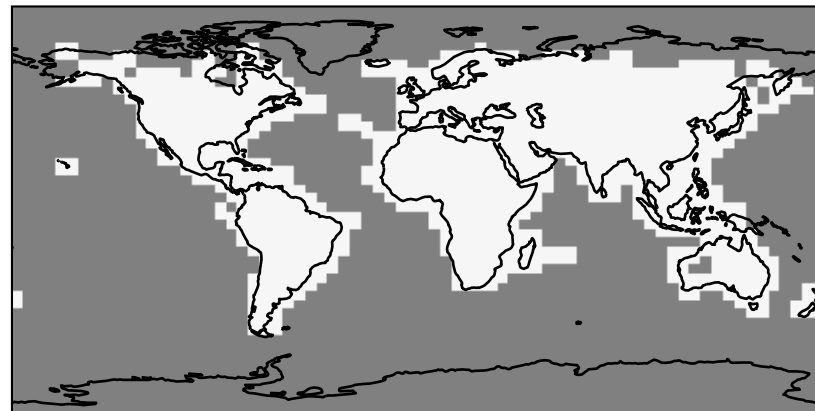
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

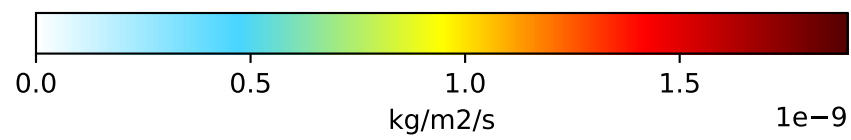
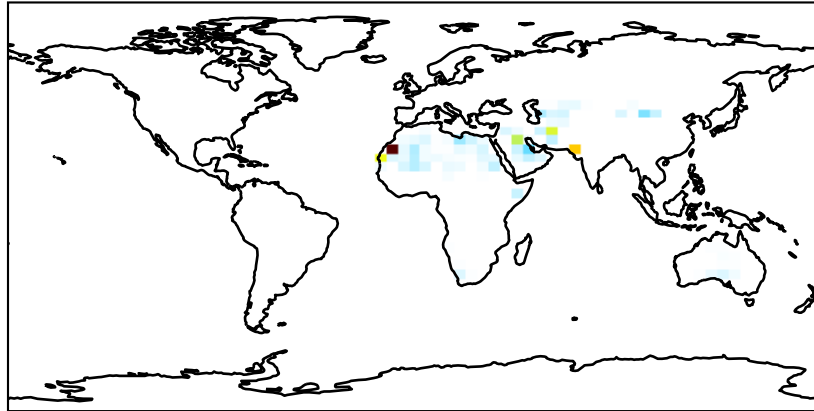


Ratio  
Dev/Ref, Fixed Range

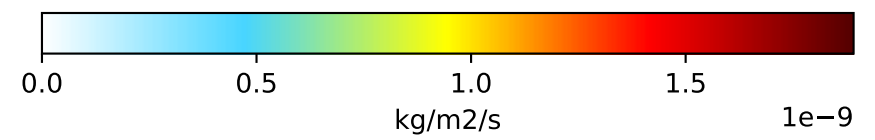
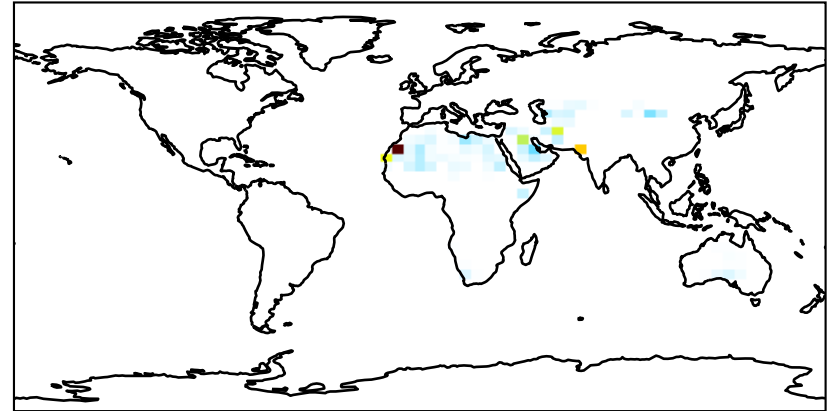


# EmisDST1\_Natural

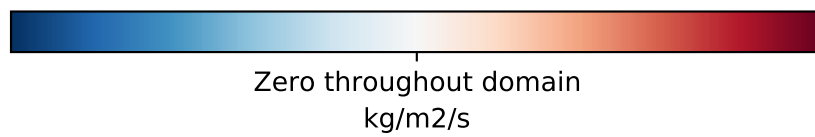
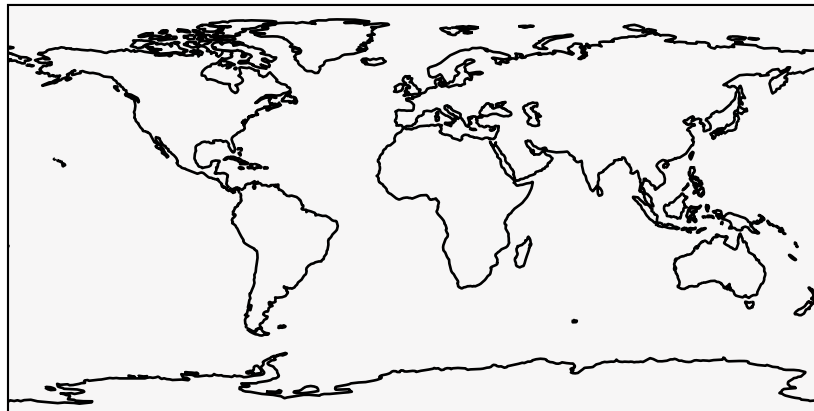
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



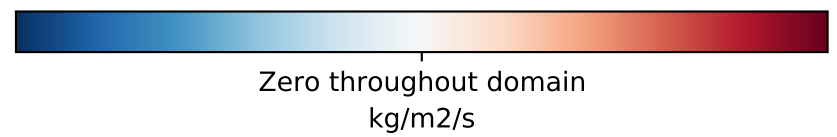
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



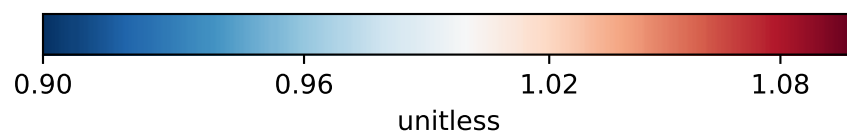
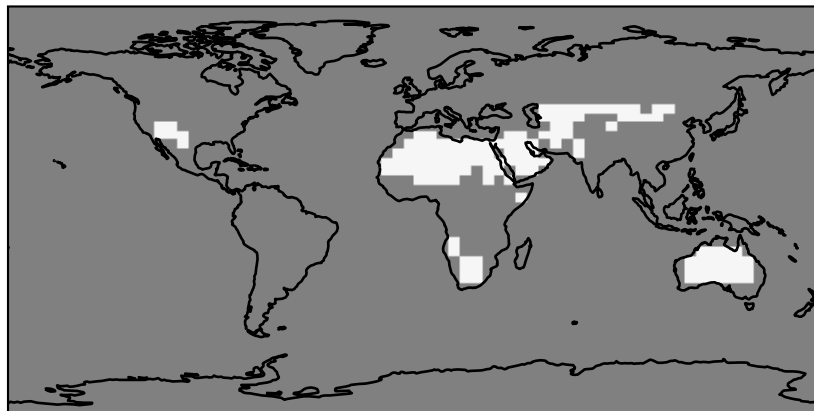
Difference  
Dev - Ref, Dynamic Range



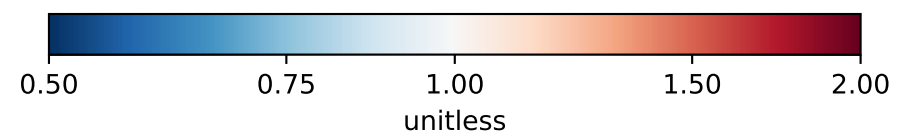
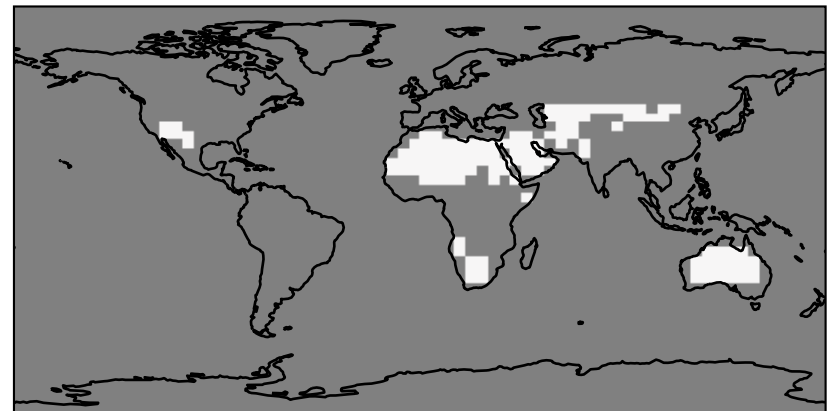
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

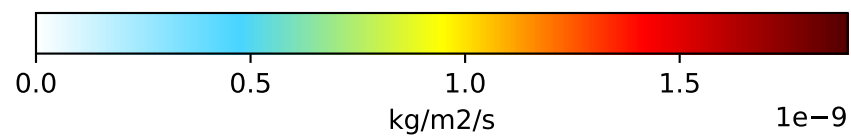
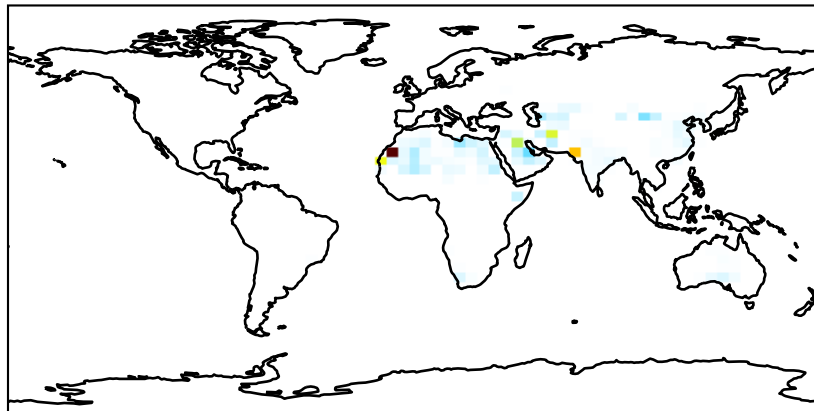


Ratio  
Dev/Ref, Fixed Range

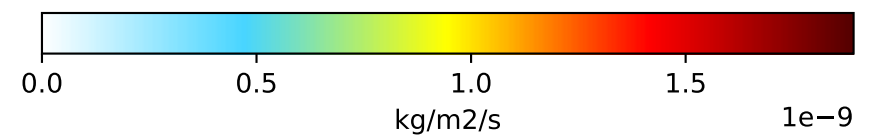
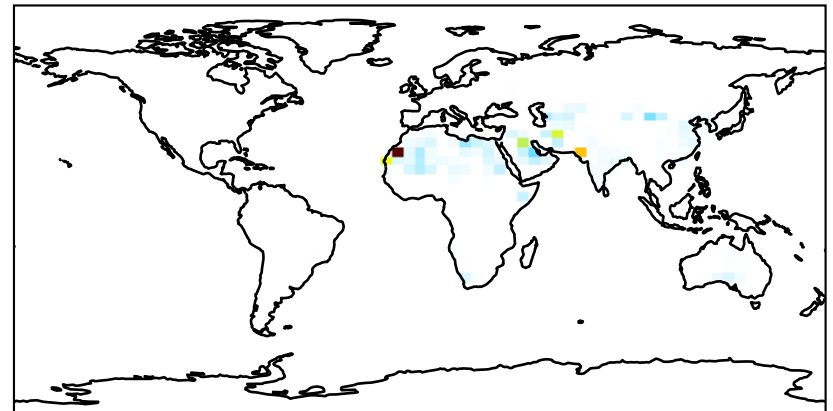


# EmisDST1\_Total

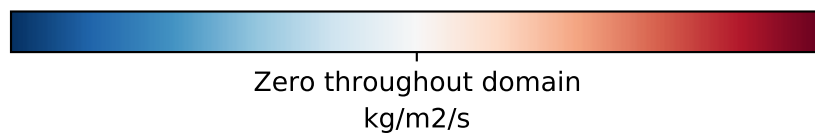
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



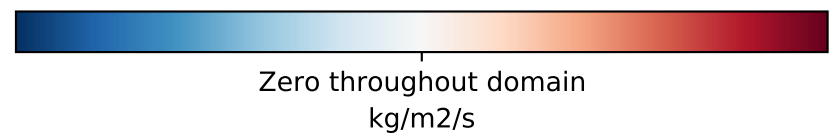
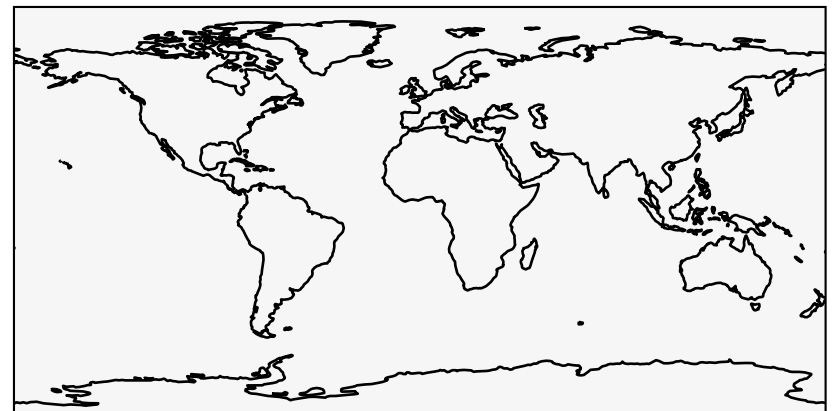
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



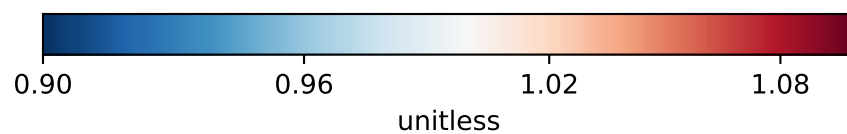
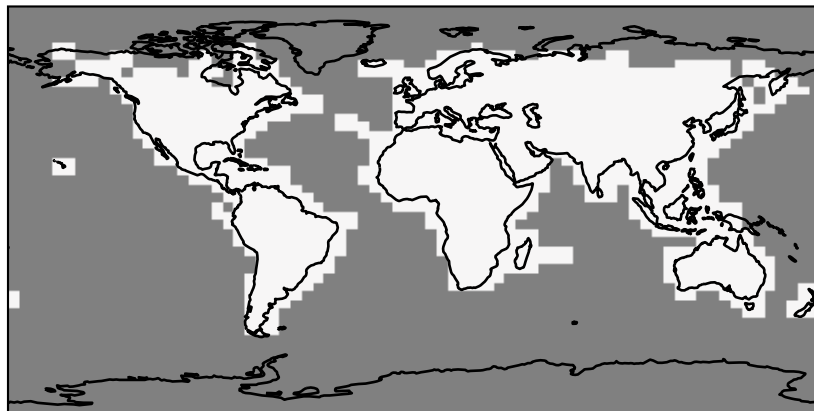
Difference  
Dev - Ref, Dynamic Range



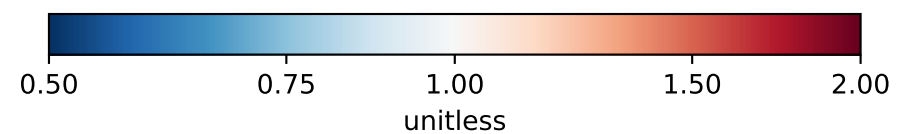
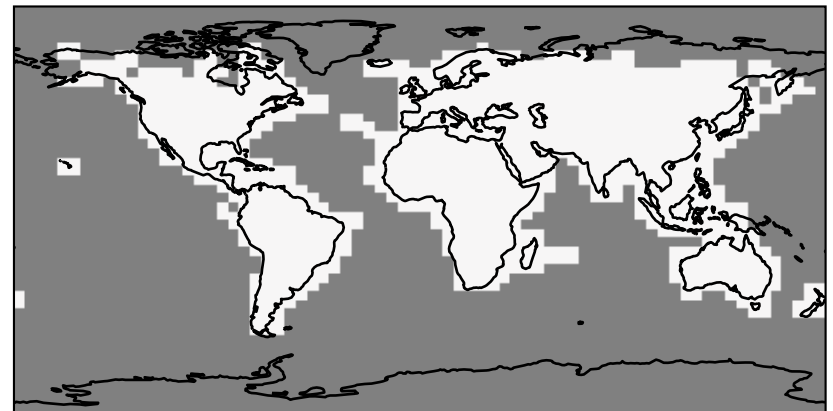
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

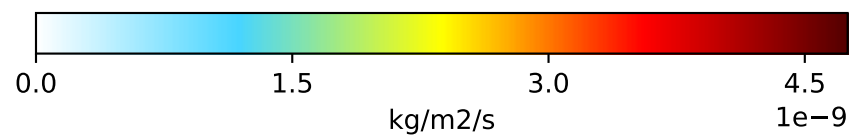
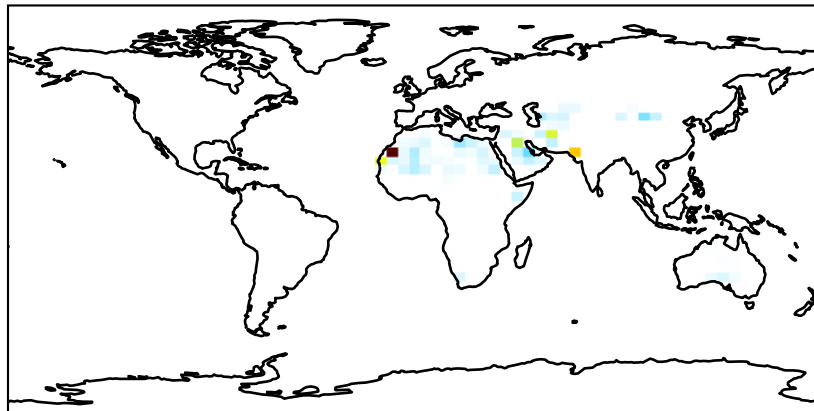


Ratio  
Dev/Ref, Fixed Range

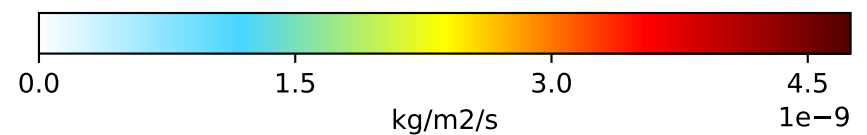
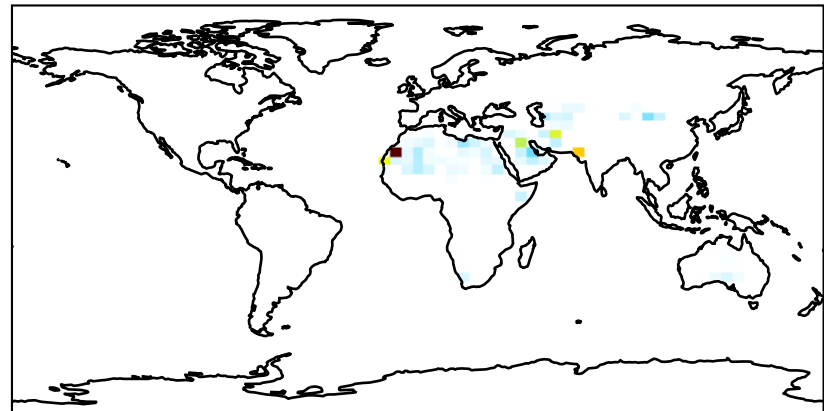


# EmisDST2\_Natural

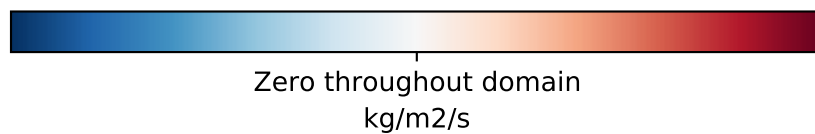
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



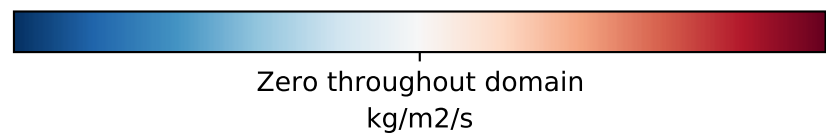
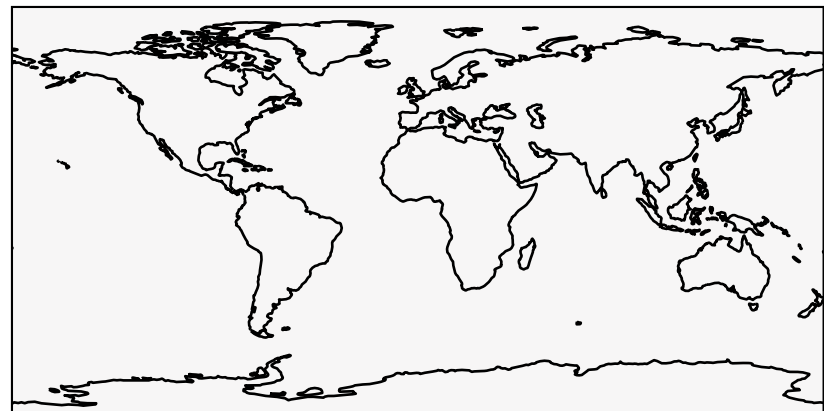
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



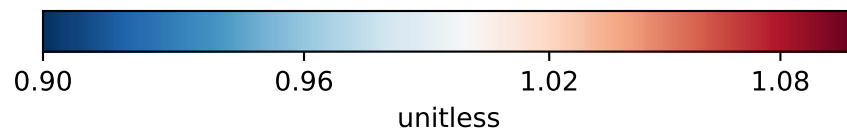
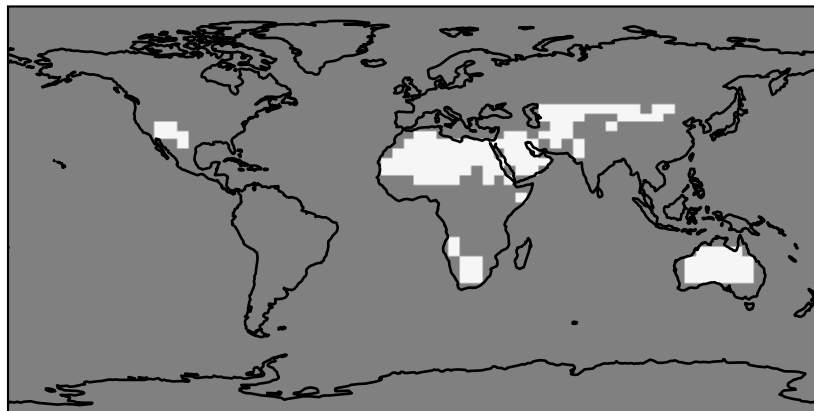
Difference  
Dev - Ref, Dynamic Range



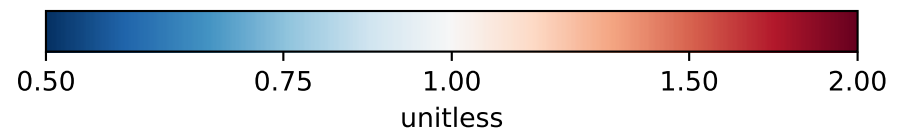
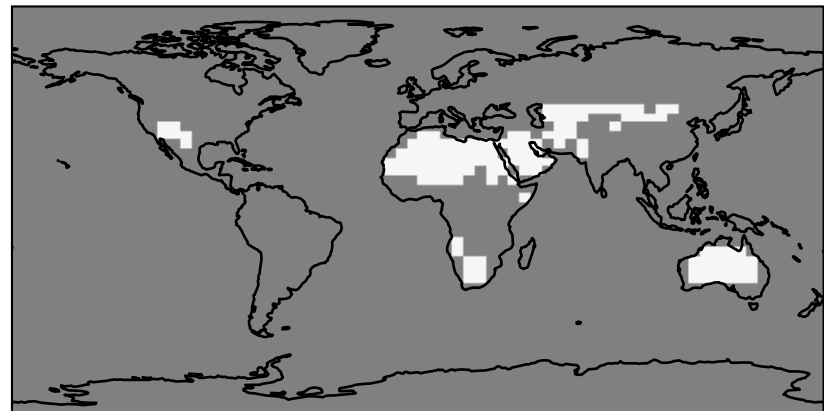
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



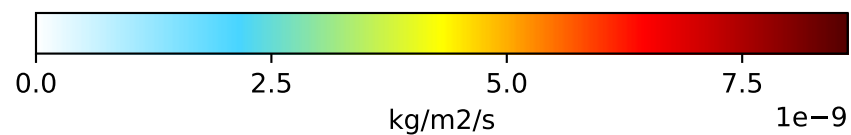
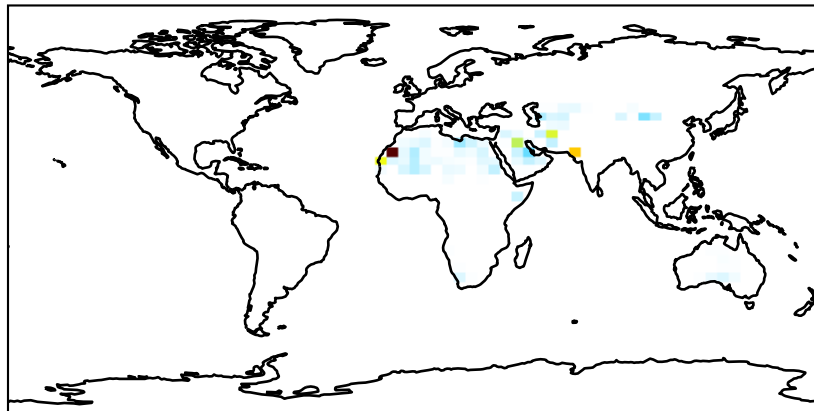
Ratio  
Dev/Ref, Fixed Range



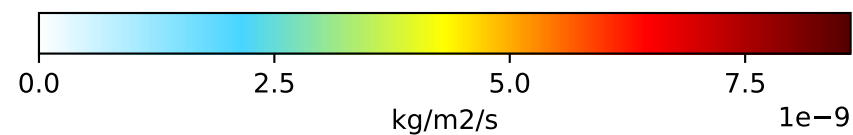
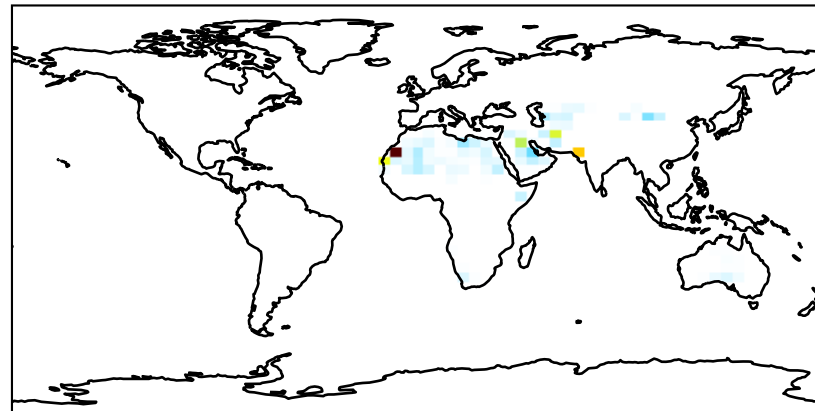


# EmisDST3\_Natural

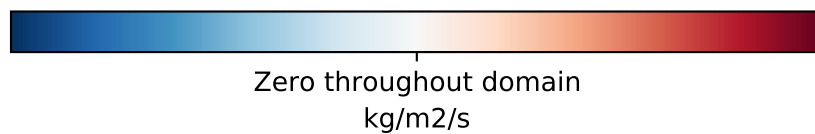
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



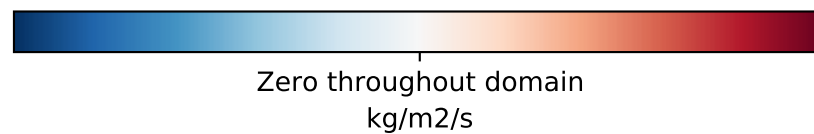
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



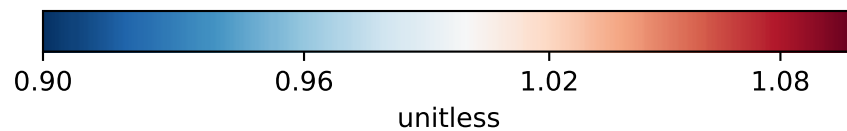
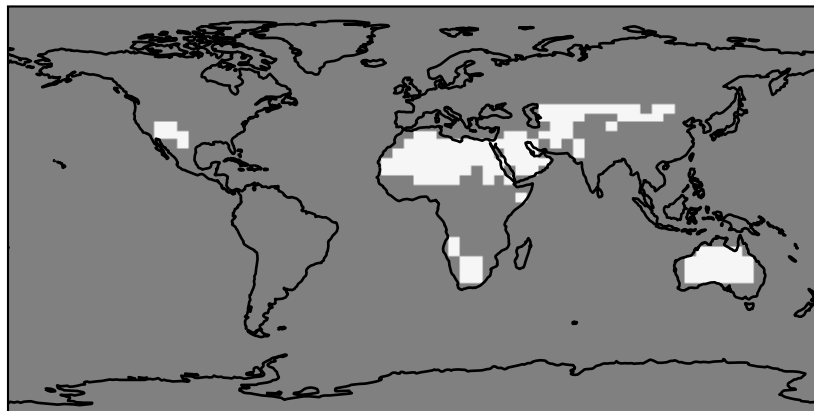
Difference  
Dev - Ref, Dynamic Range



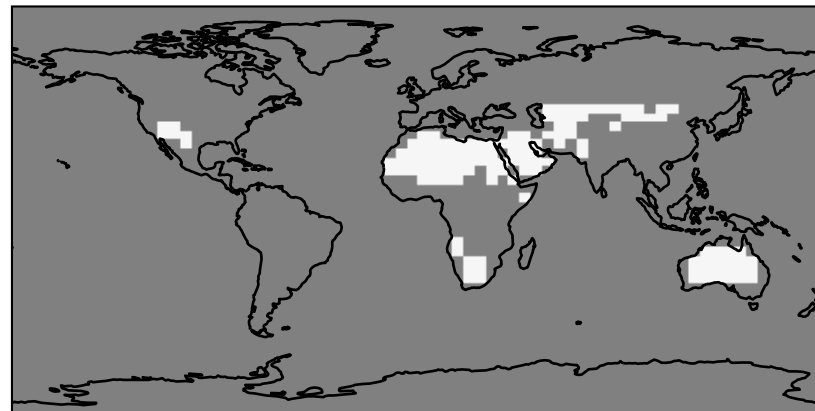
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

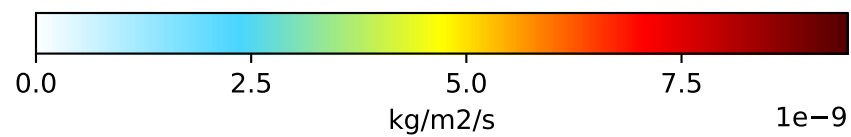
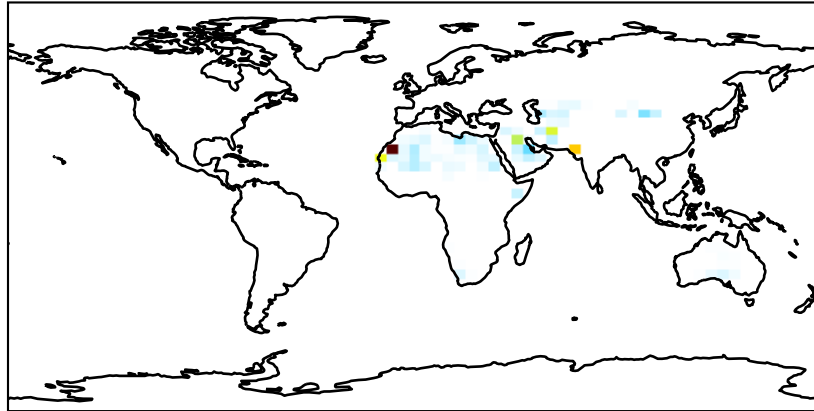


Ratio  
Dev/Ref, Fixed Range

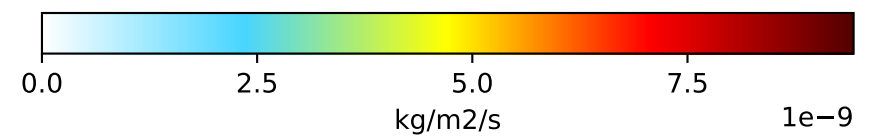
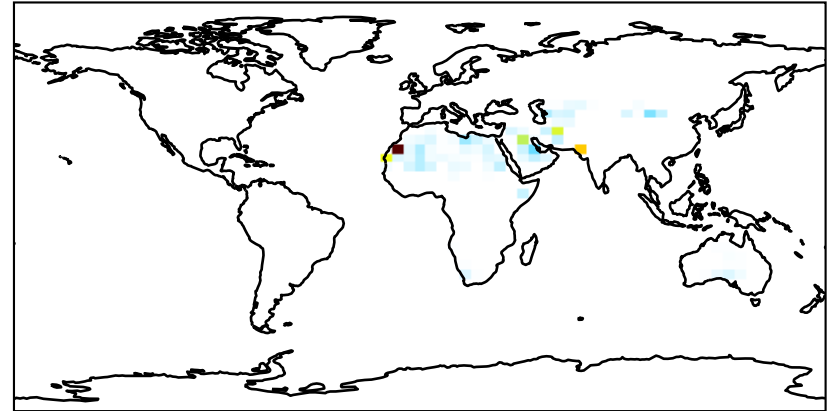


# EmisDST4\_Natural

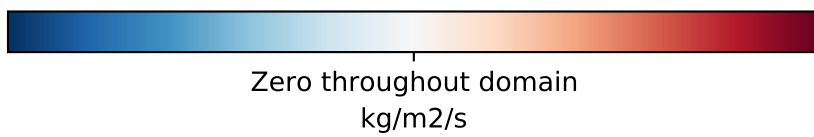
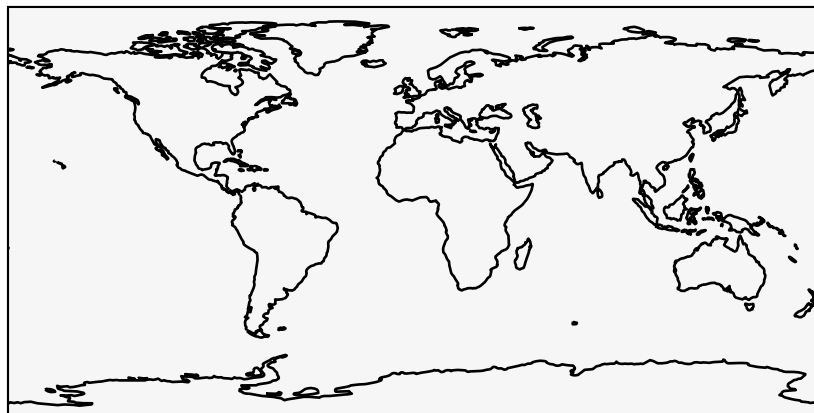
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



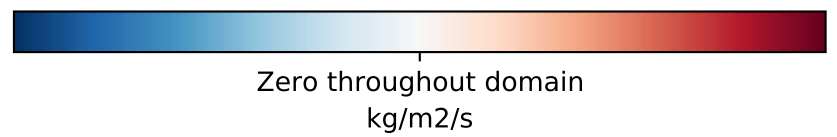
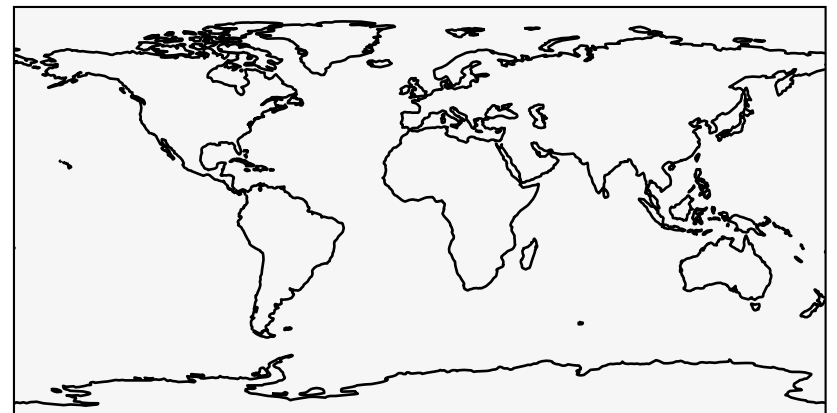
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



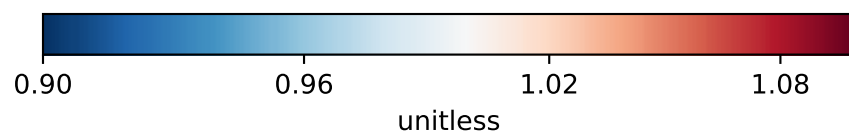
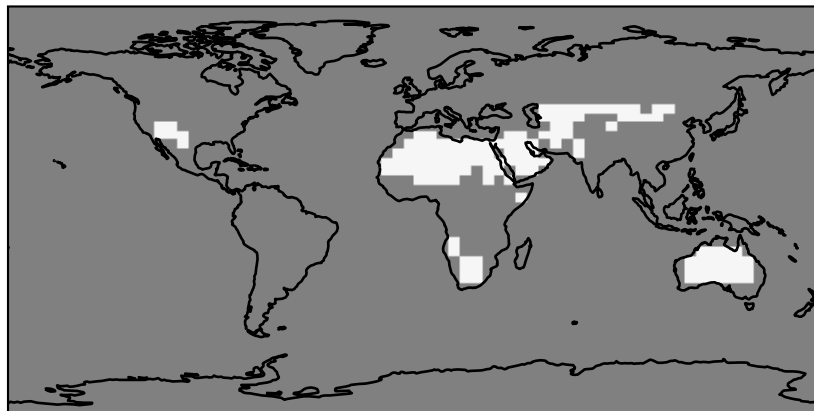
Difference  
Dev - Ref, Dynamic Range



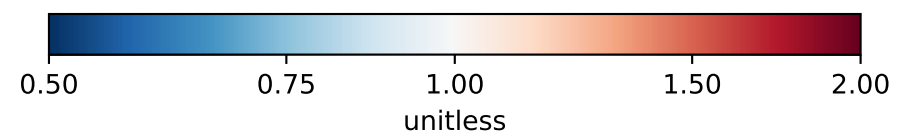
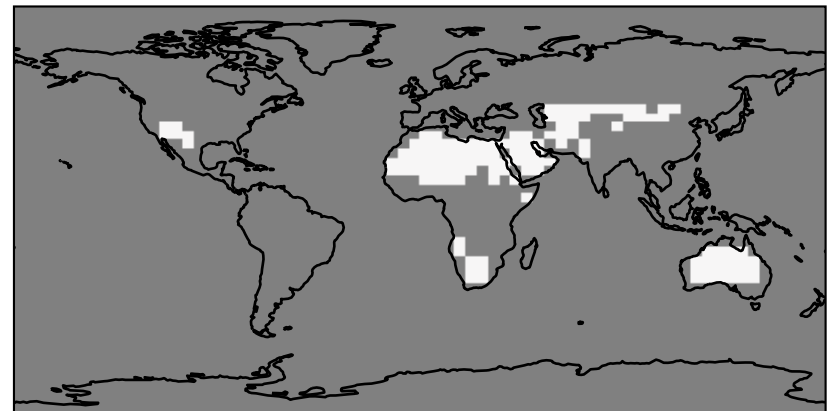
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

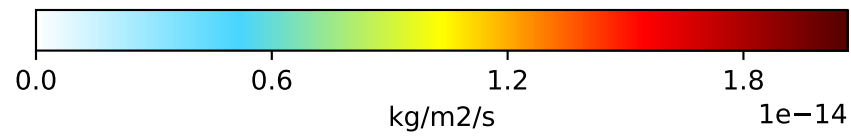
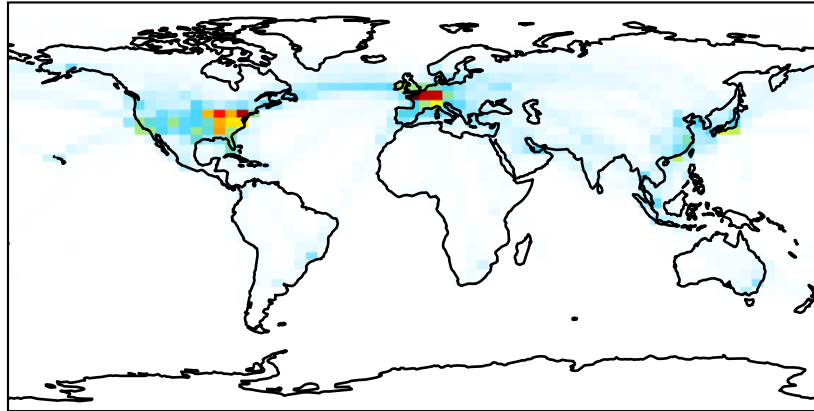


Ratio  
Dev/Ref, Fixed Range

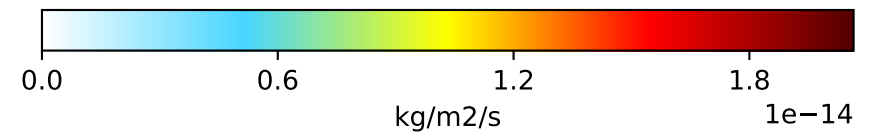
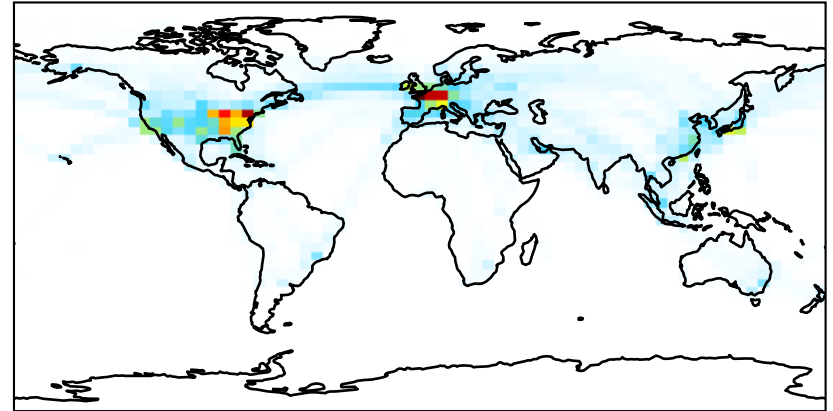


# EmisSO4\_Aircraft

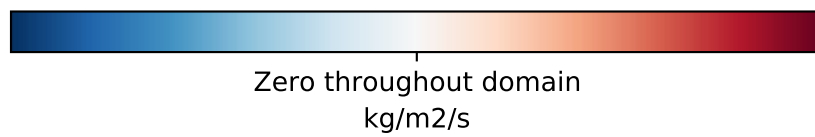
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



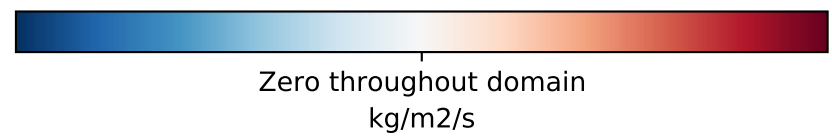
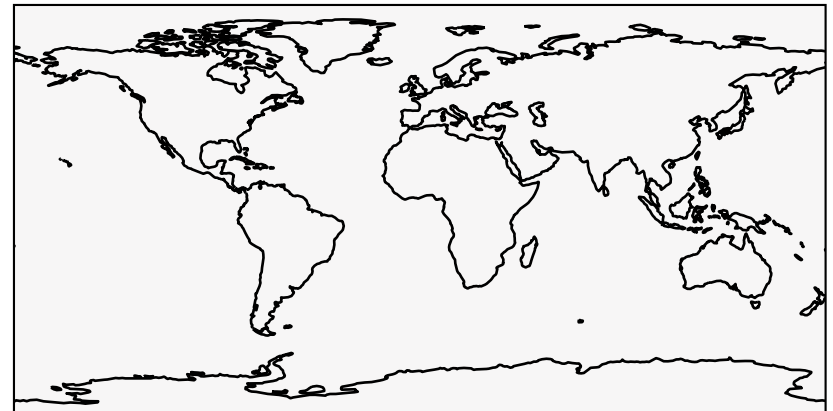
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



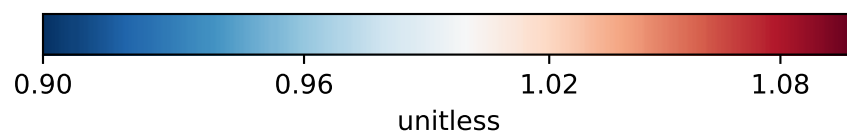
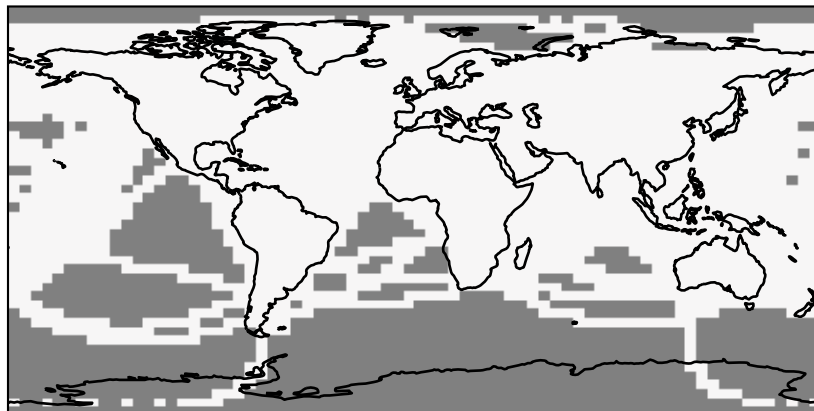
Difference  
Dev - Ref, Dynamic Range



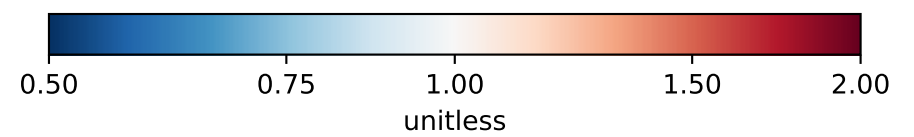
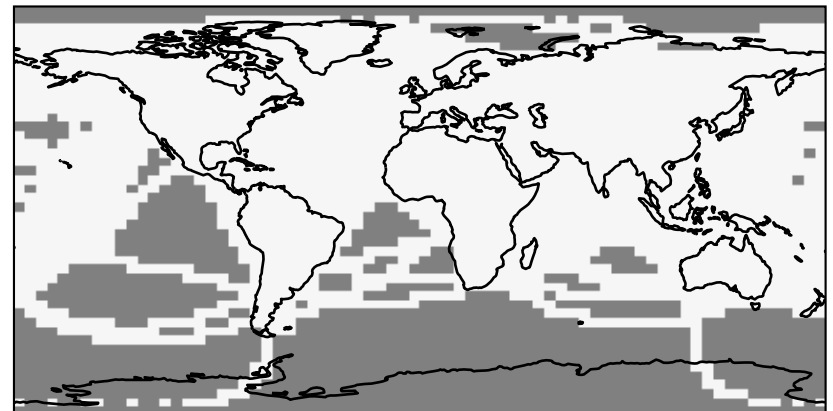
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

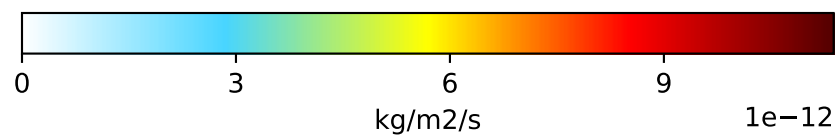
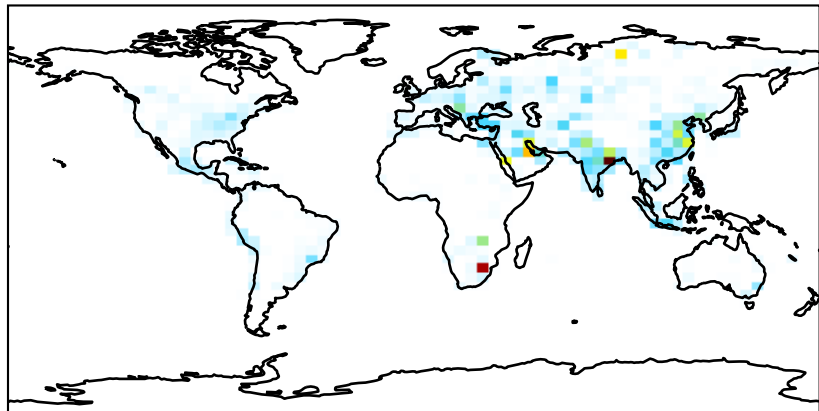


Ratio  
Dev/Ref, Fixed Range

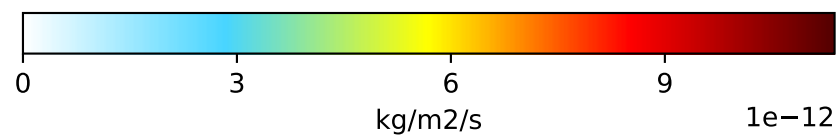
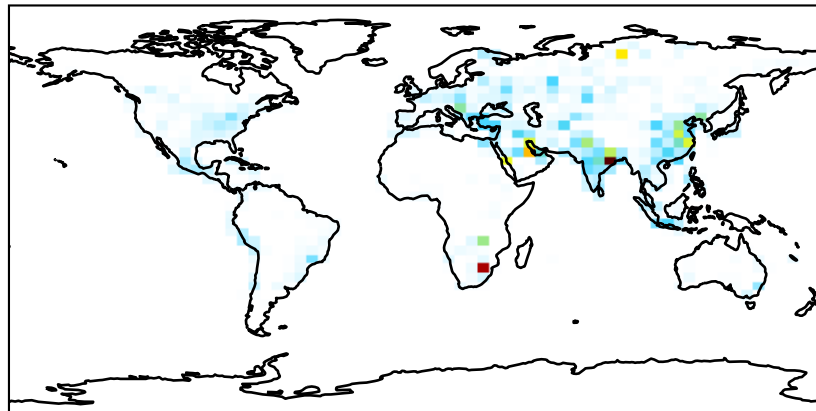


# EmisSO4\_Anthro

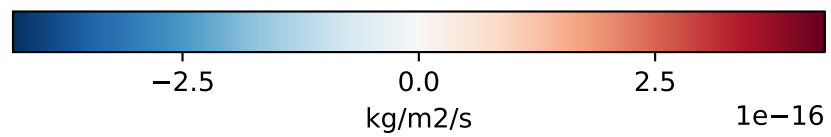
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



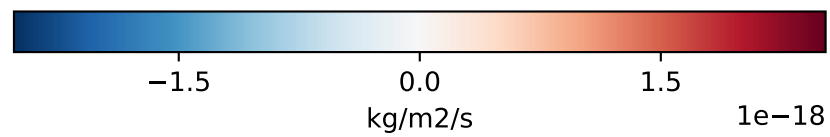
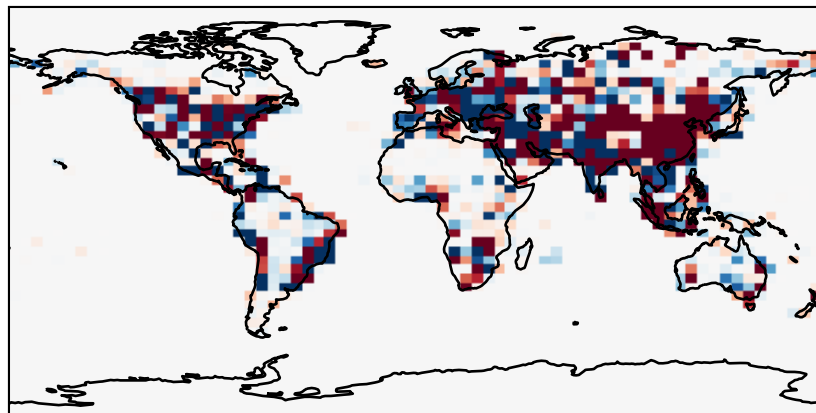
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



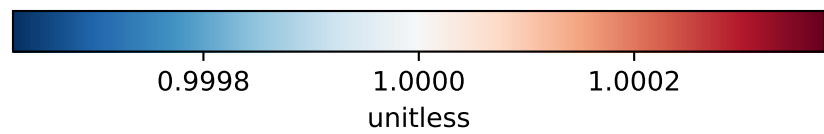
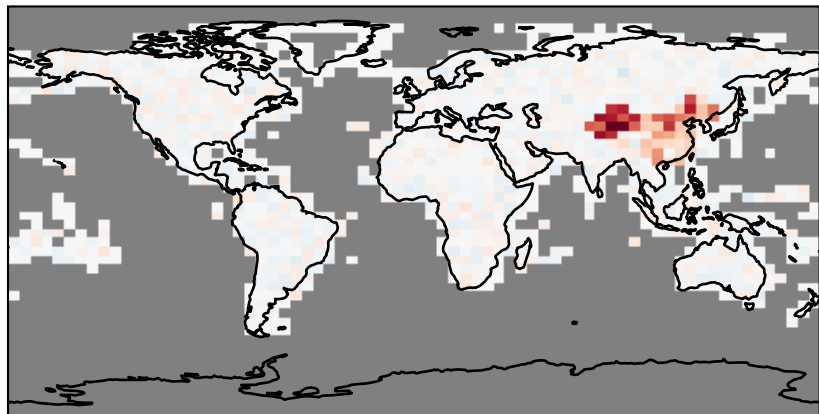
Difference  
Dev - Ref, Dynamic Range



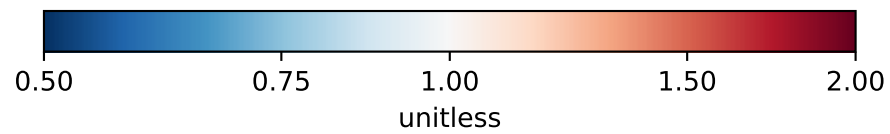
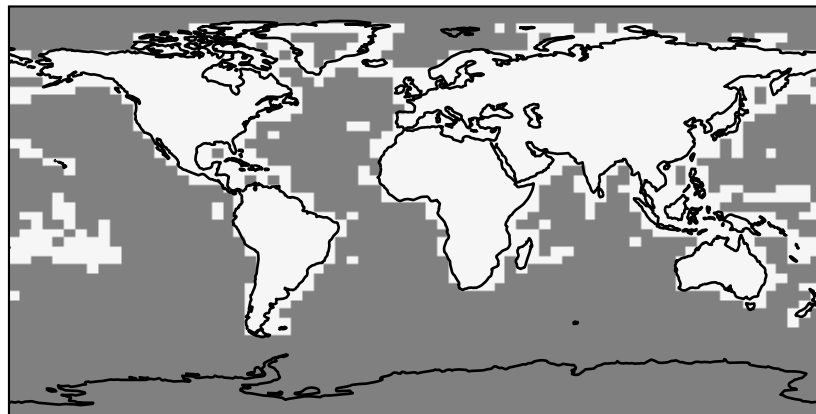
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

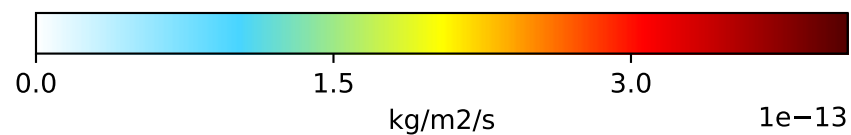
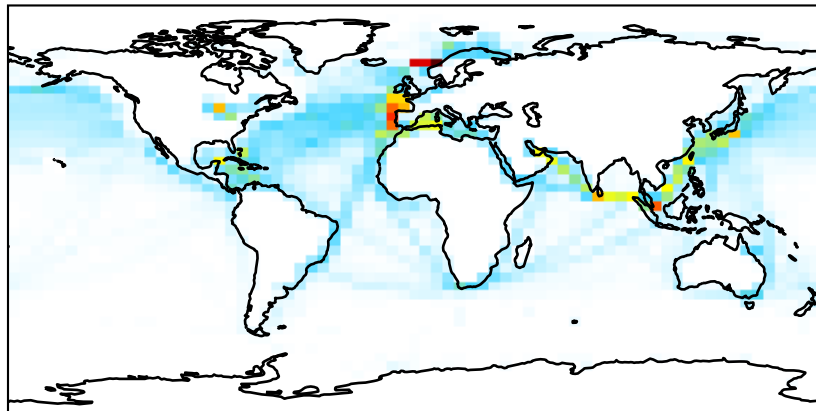


Ratio  
Dev/Ref, Fixed Range

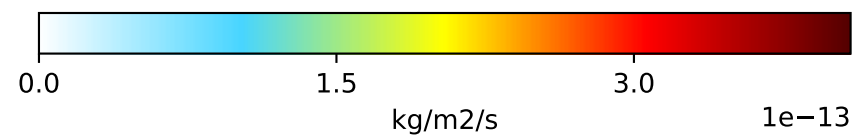
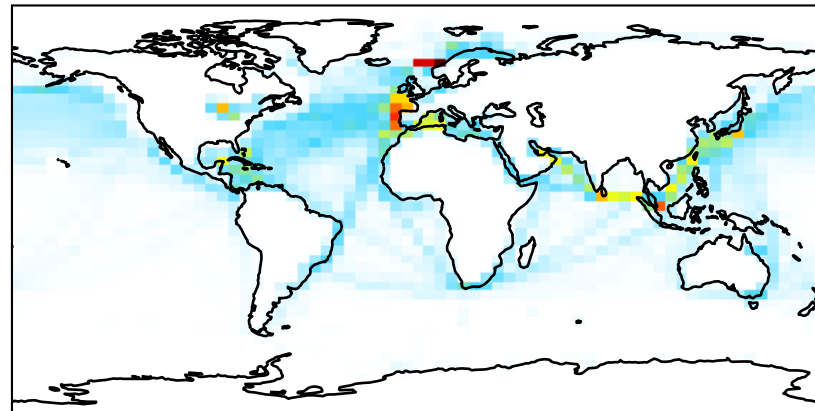


# EmisSO4\_Ship

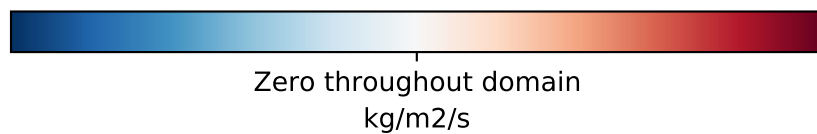
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



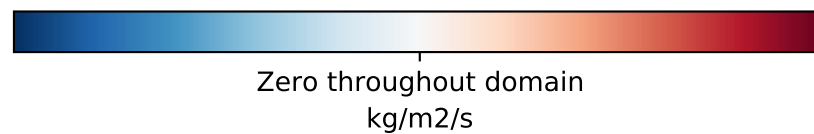
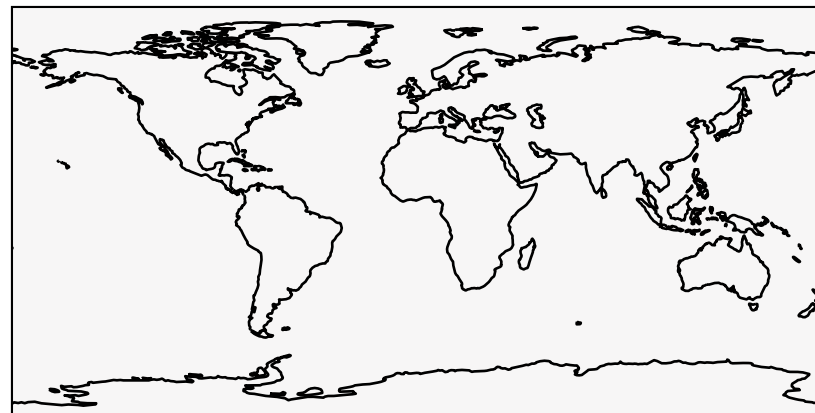
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



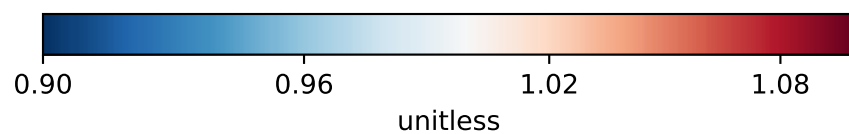
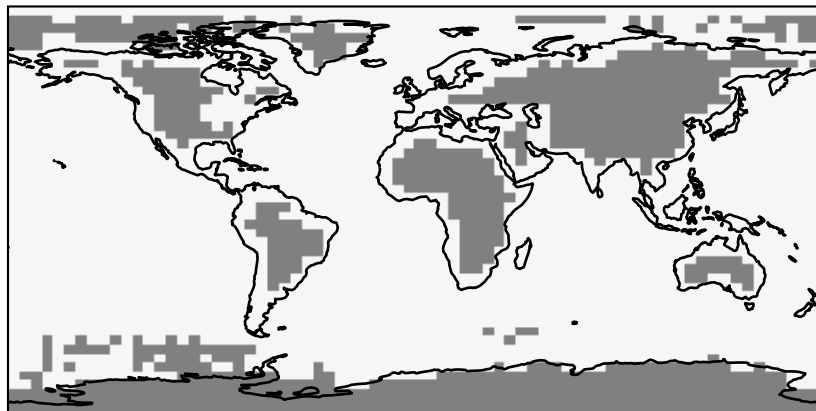
Difference  
Dev - Ref, Dynamic Range



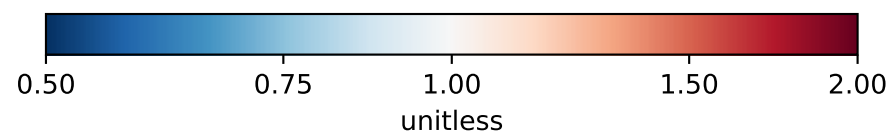
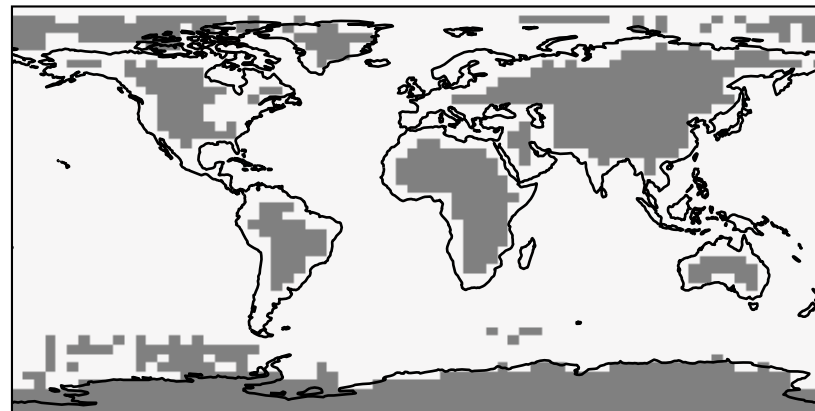
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



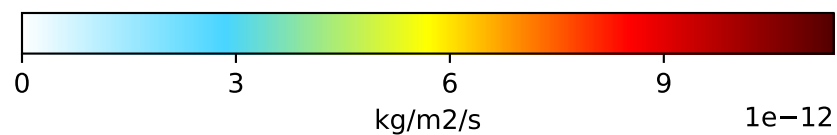
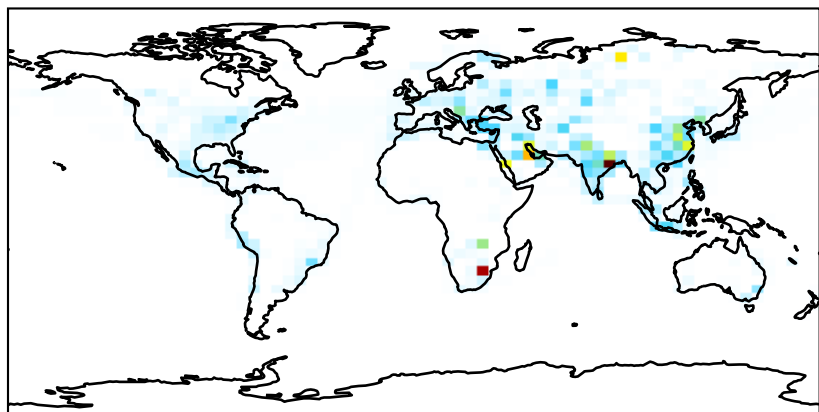
Ratio  
Dev/Ref, Fixed Range



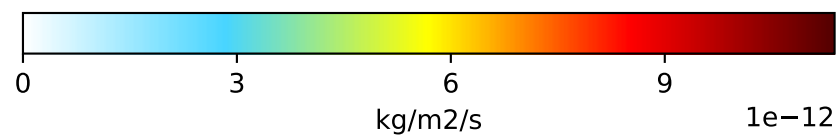
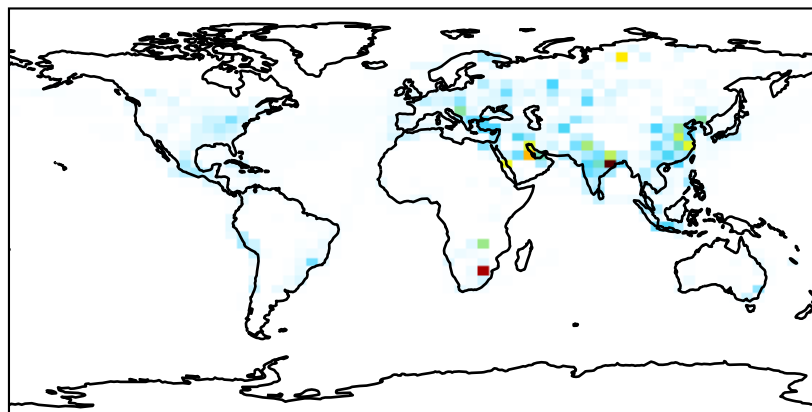


# EmisSO4\_Total

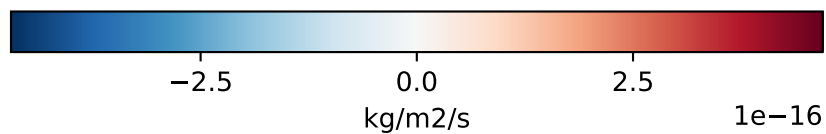
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



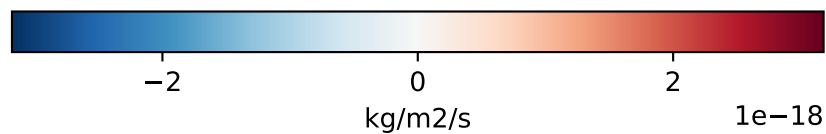
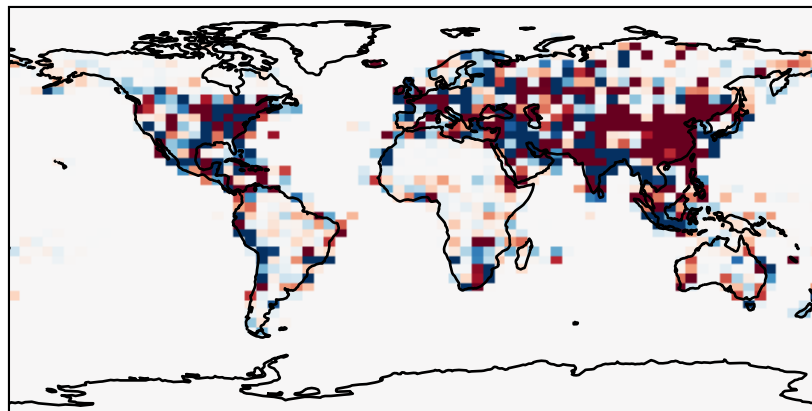
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



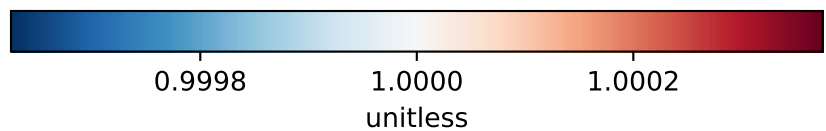
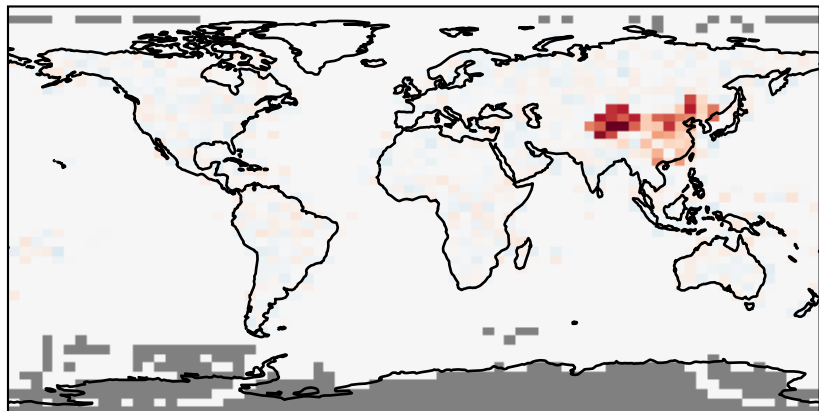
Difference  
Dev - Ref, Dynamic Range



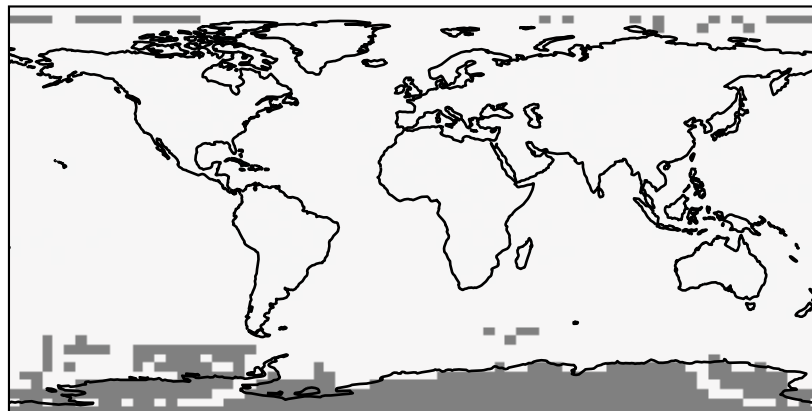
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



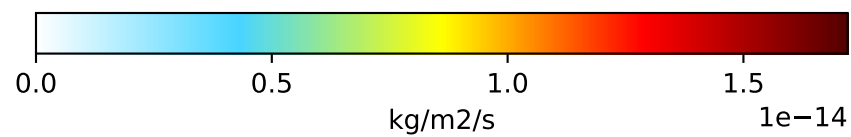
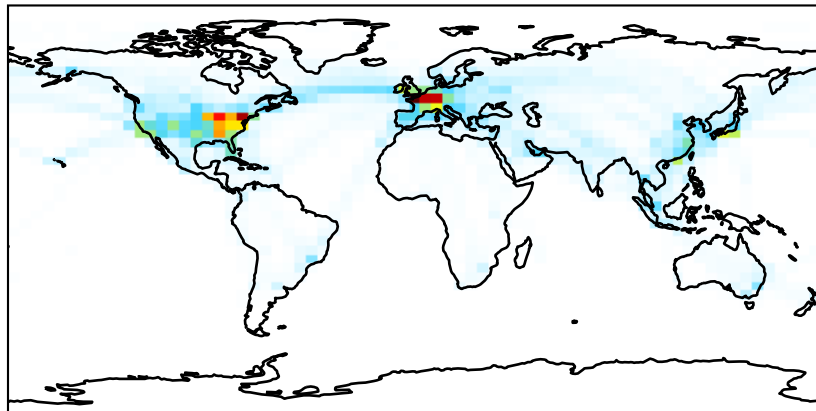
Ratio  
Dev/Ref, Fixed Range



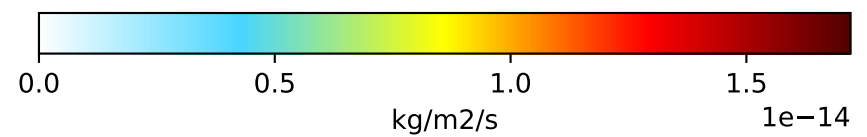
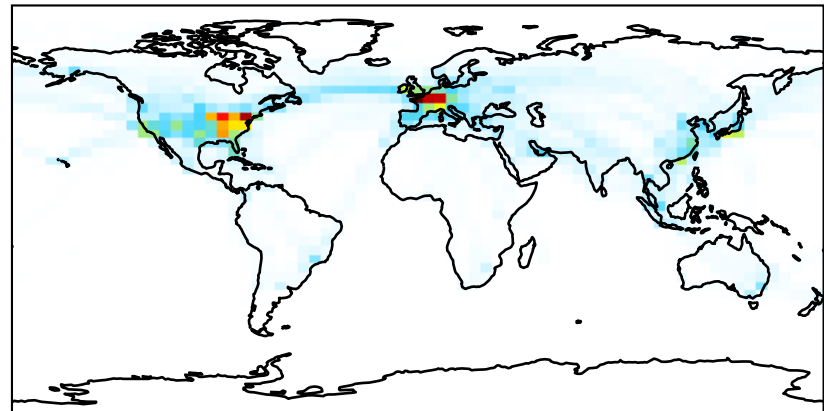


# EmisBCPI\_Aircraft

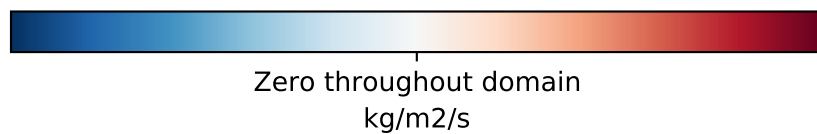
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



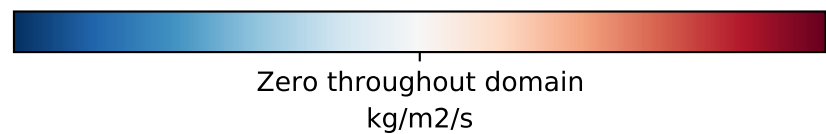
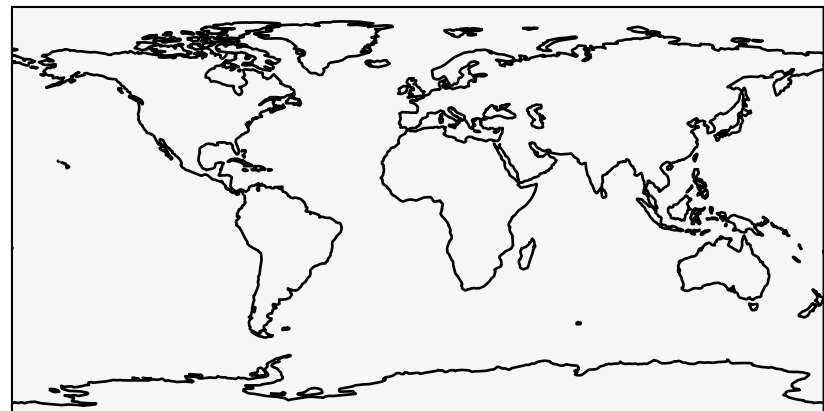
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



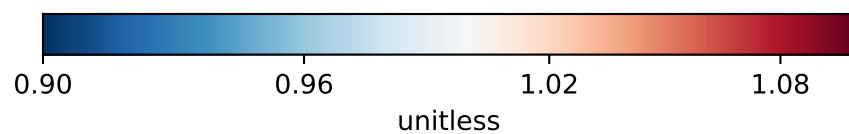
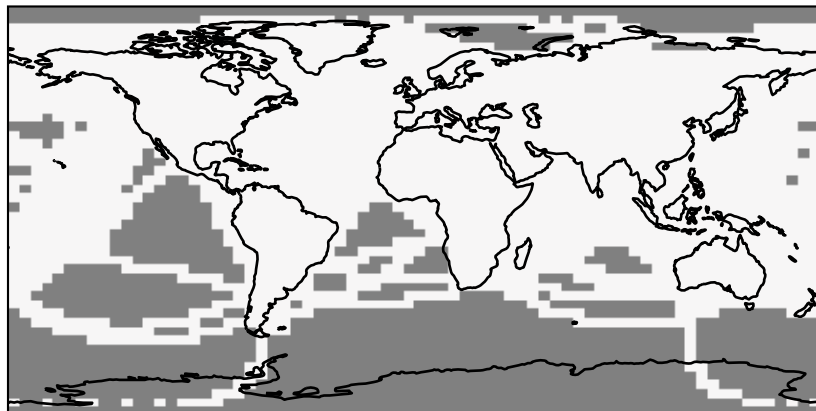
Difference  
Dev - Ref, Dynamic Range



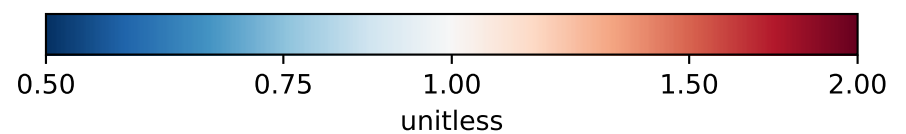
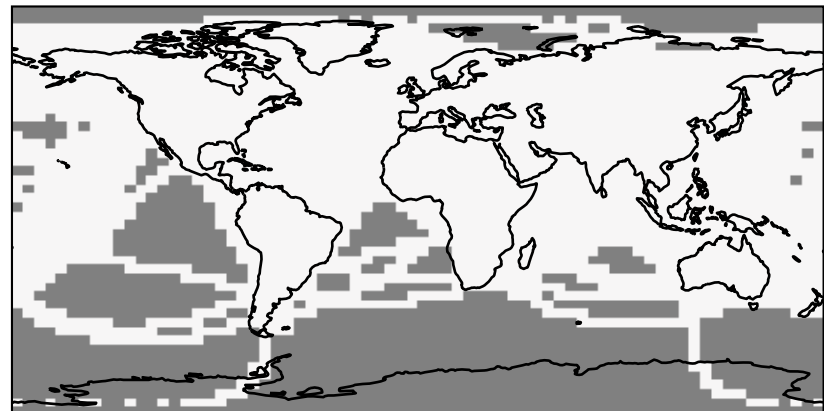
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

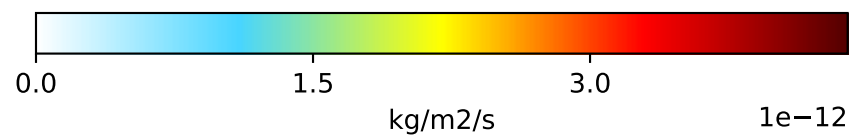
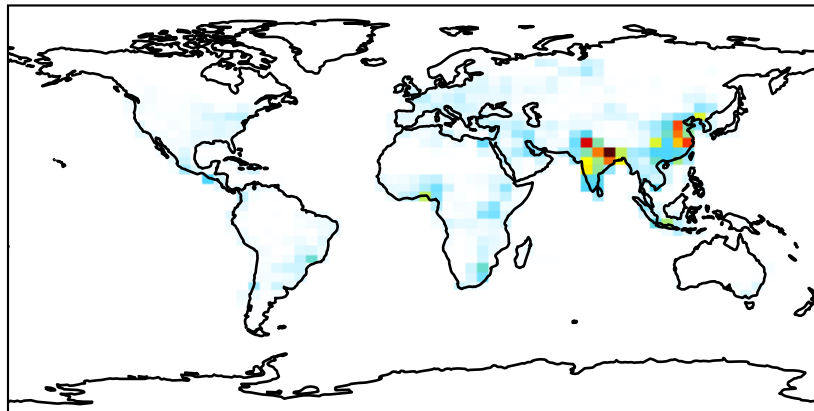


Ratio  
Dev/Ref, Fixed Range

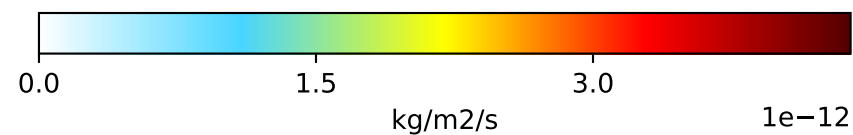
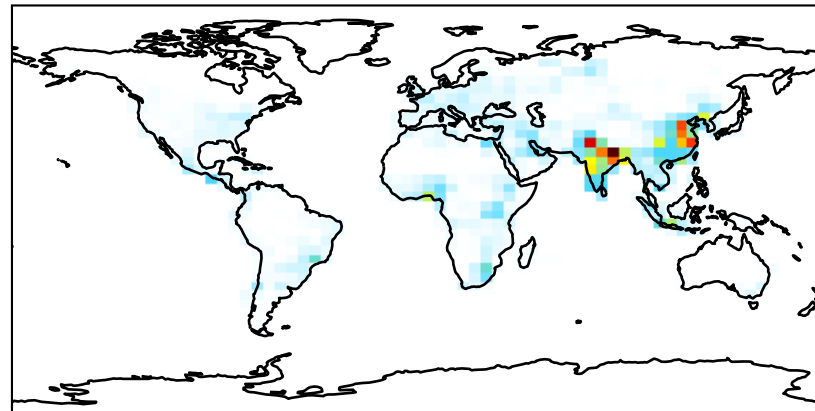


# EmisBCPI\_Anthro

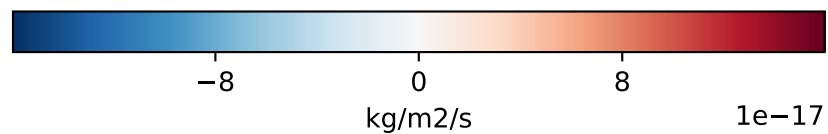
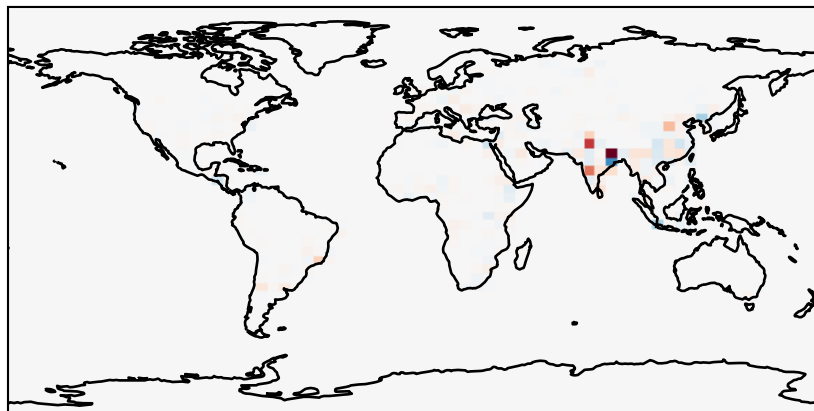
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



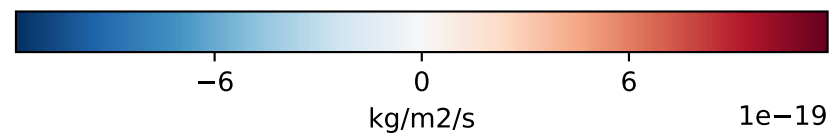
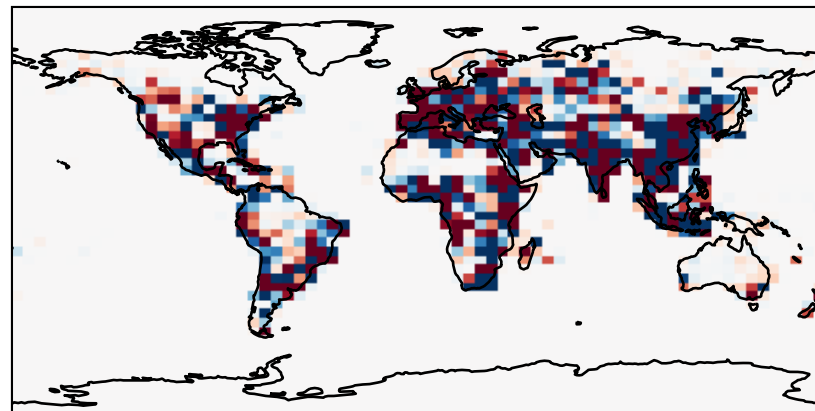
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



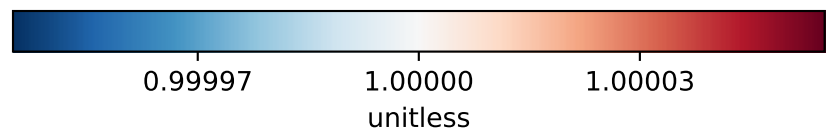
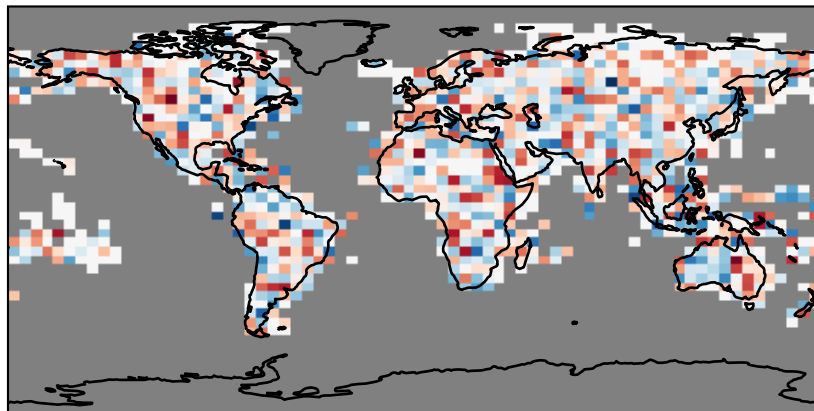
Difference  
Dev - Ref, Dynamic Range



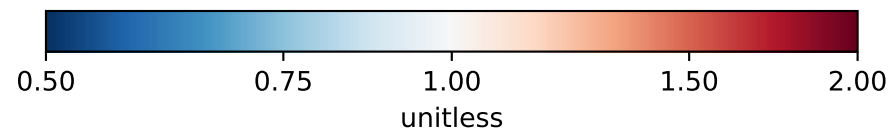
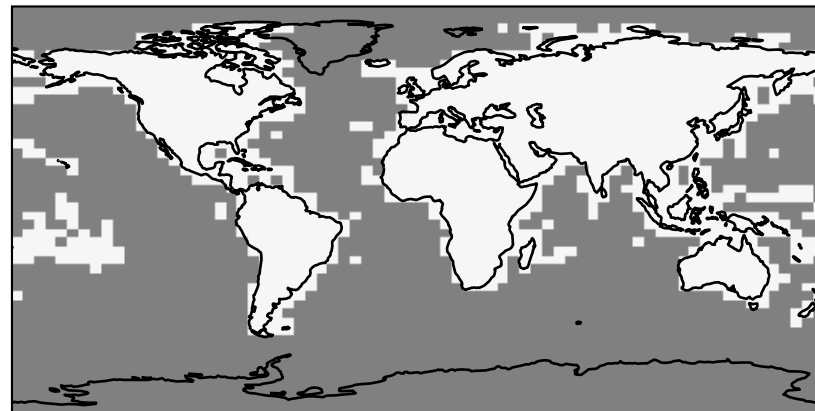
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

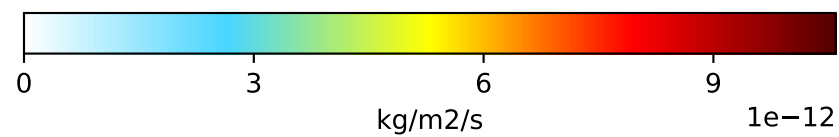
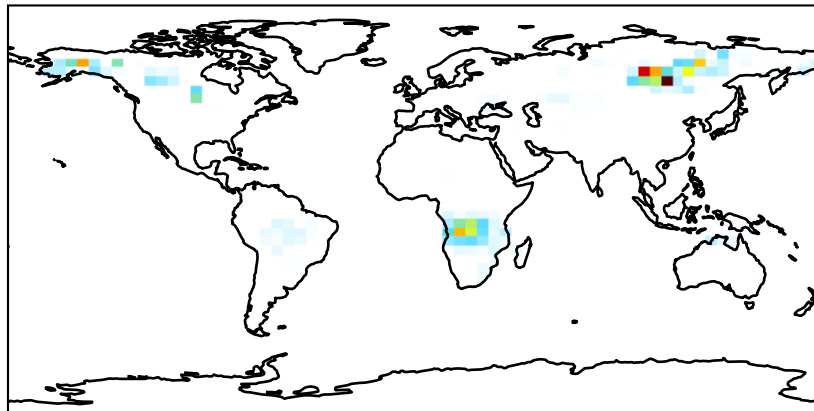


Ratio  
Dev/Ref, Fixed Range

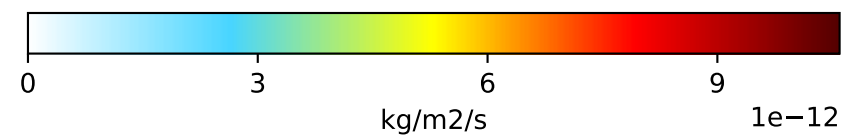
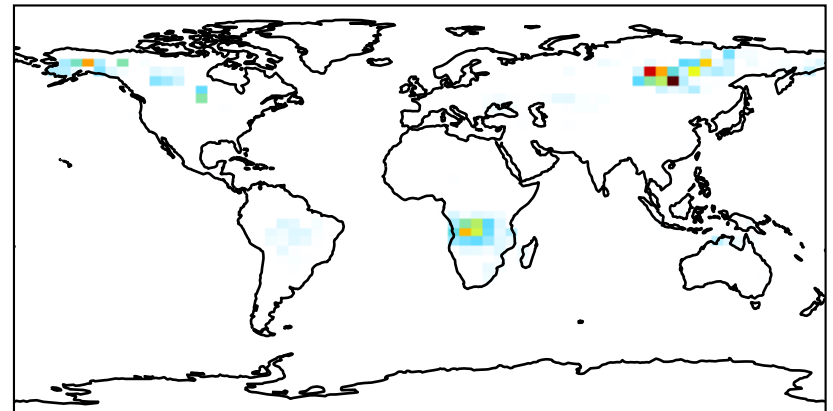


# EmisBCPI\_BioBurn

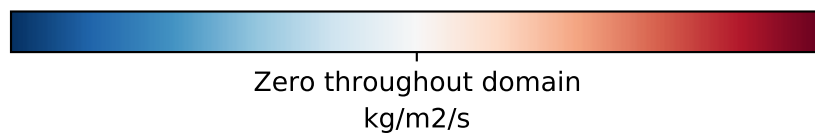
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



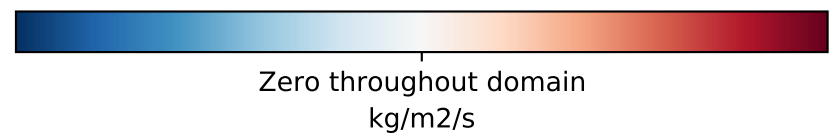
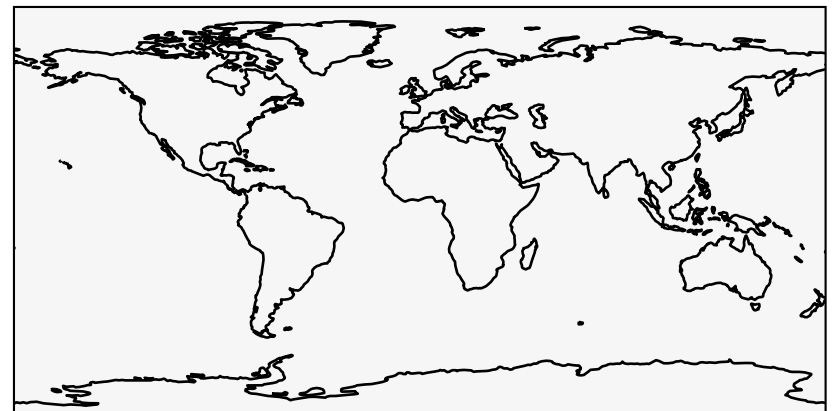
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



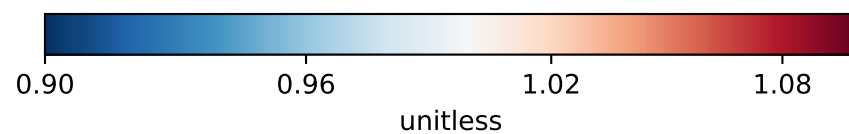
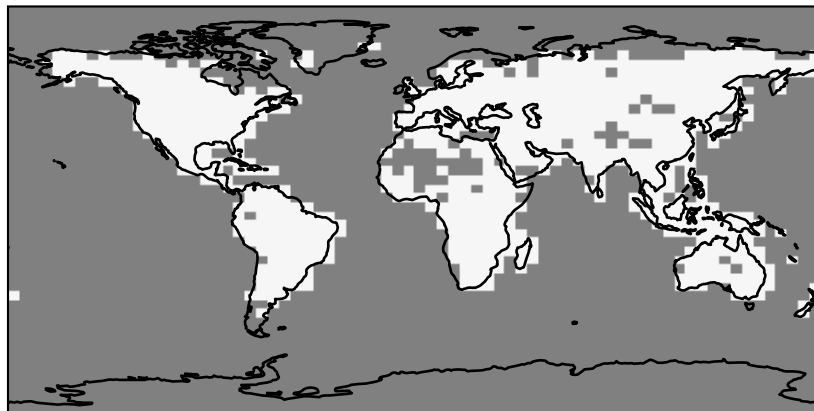
Difference  
Dev - Ref, Dynamic Range



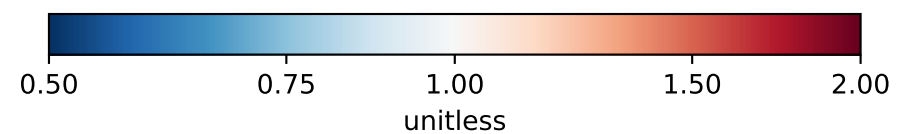
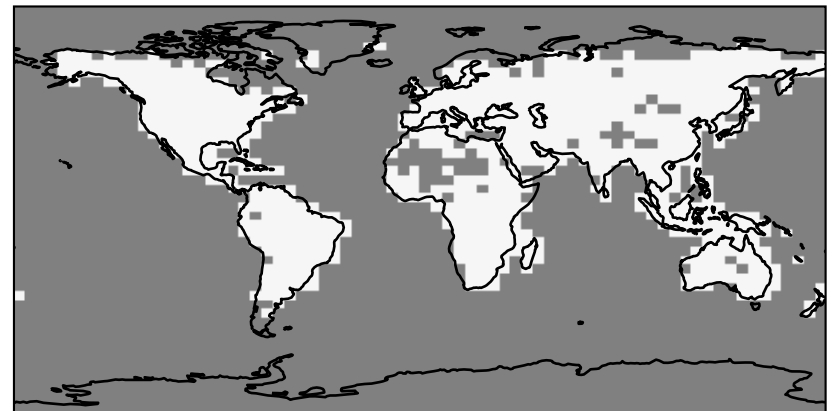
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

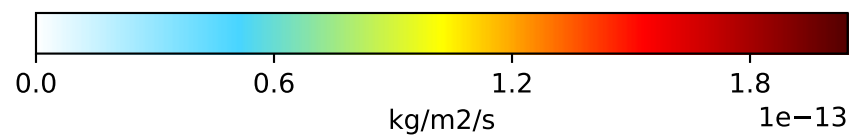
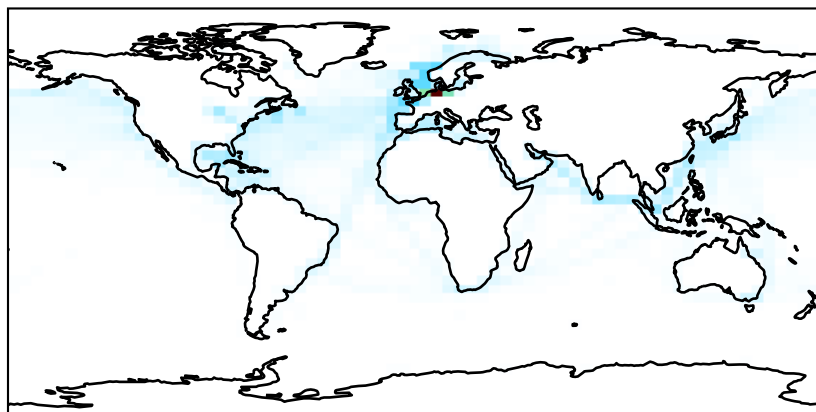


Ratio  
Dev/Ref, Fixed Range

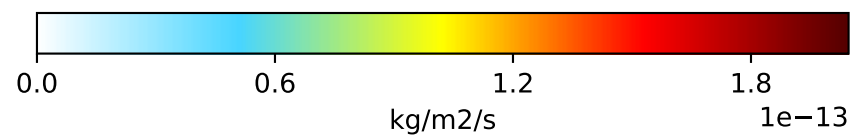
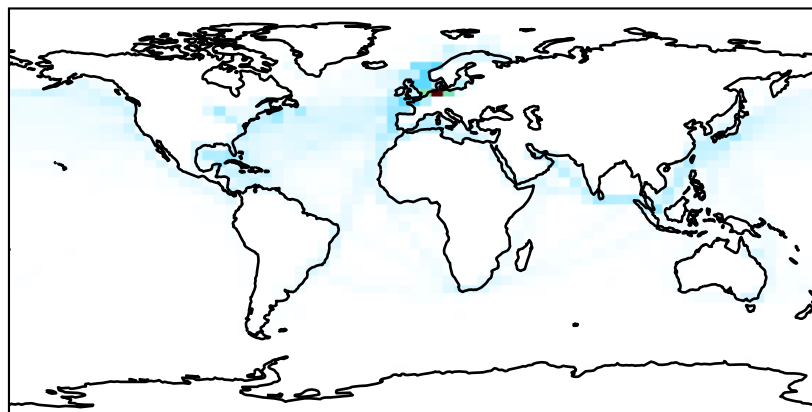


# EmisBCPI\_Ship

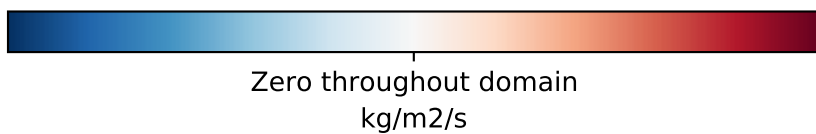
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



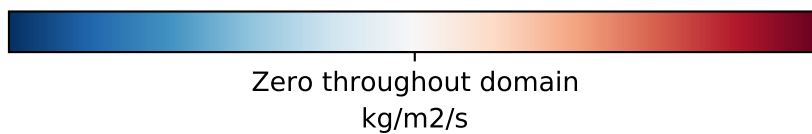
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



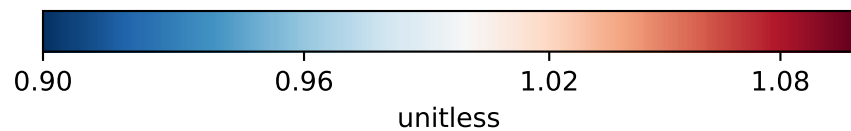
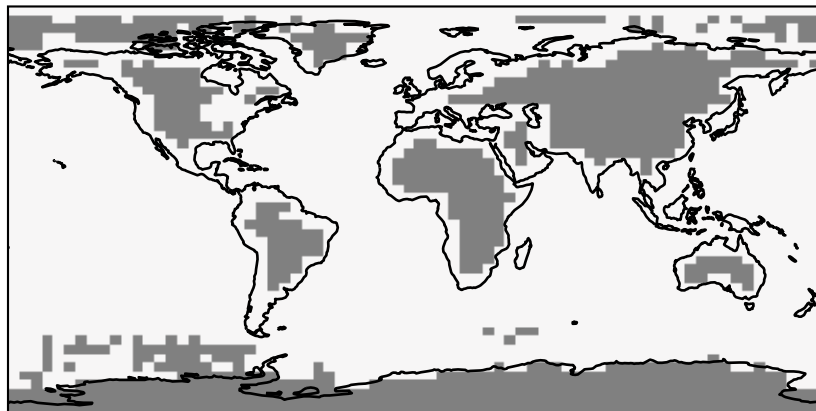
Difference  
Dev - Ref, Dynamic Range



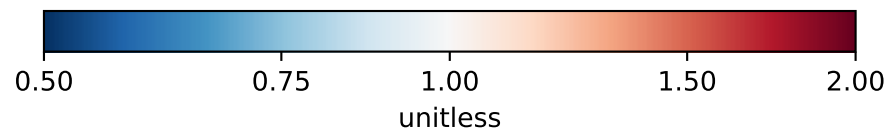
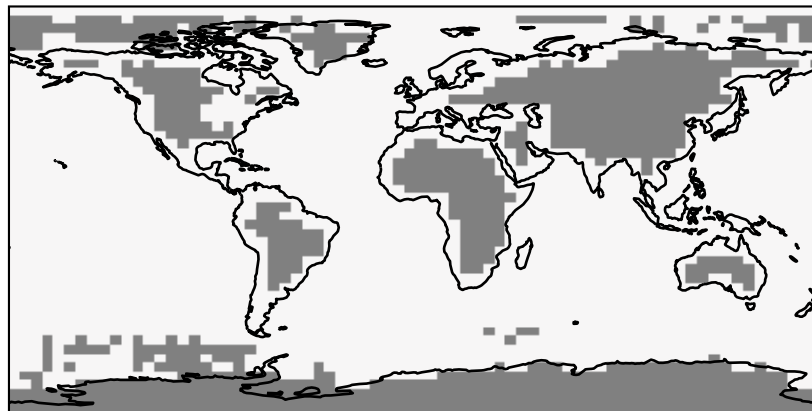
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

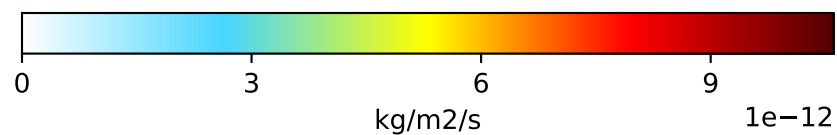
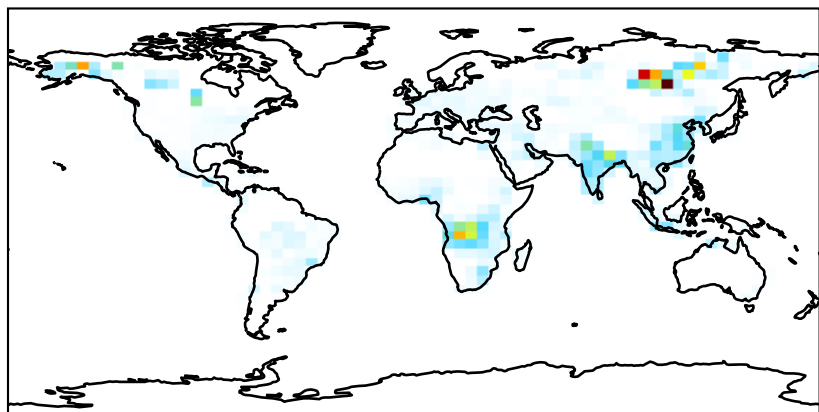


Ratio  
Dev/Ref, Fixed Range

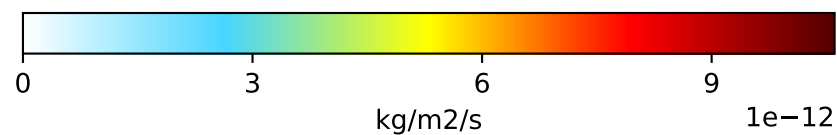
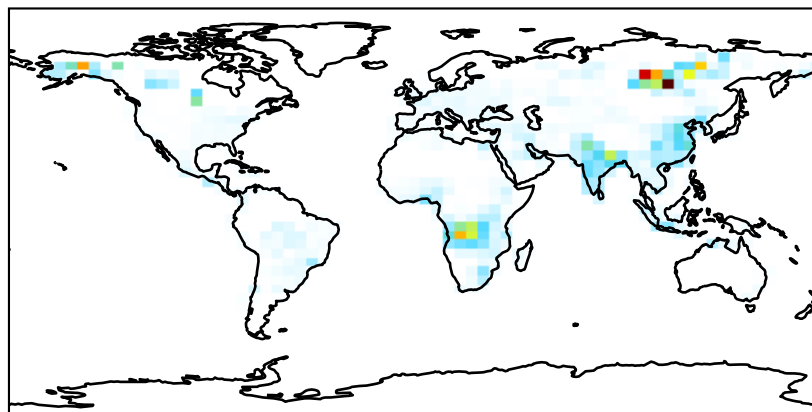


# EmisBCPI\_Total

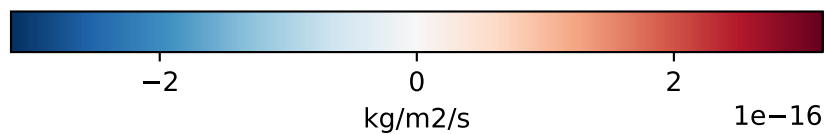
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



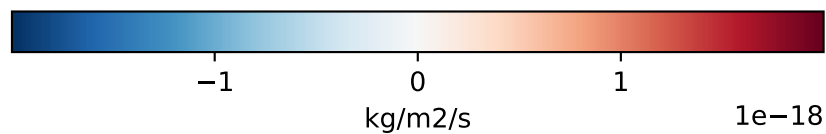
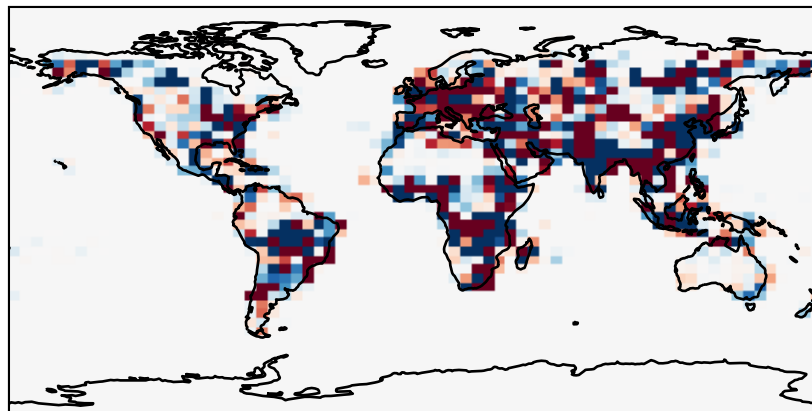
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



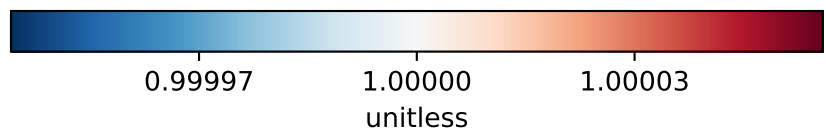
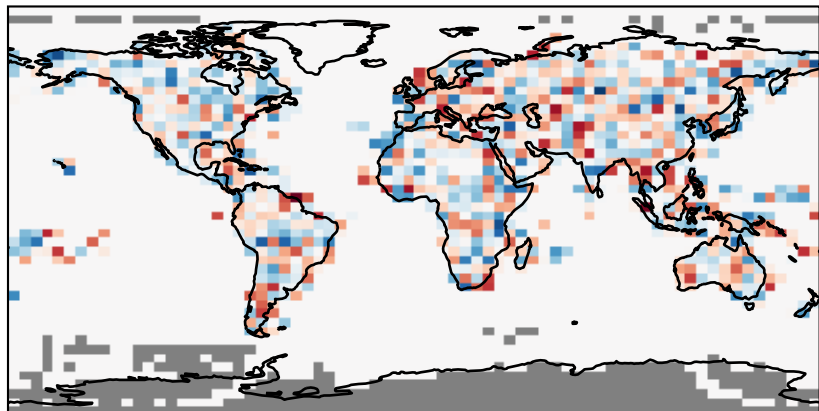
Difference  
Dev - Ref, Dynamic Range



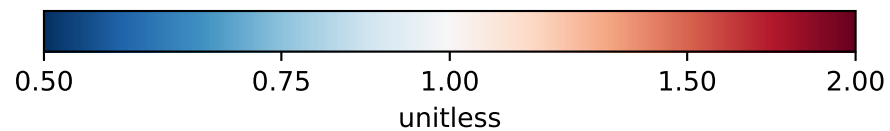
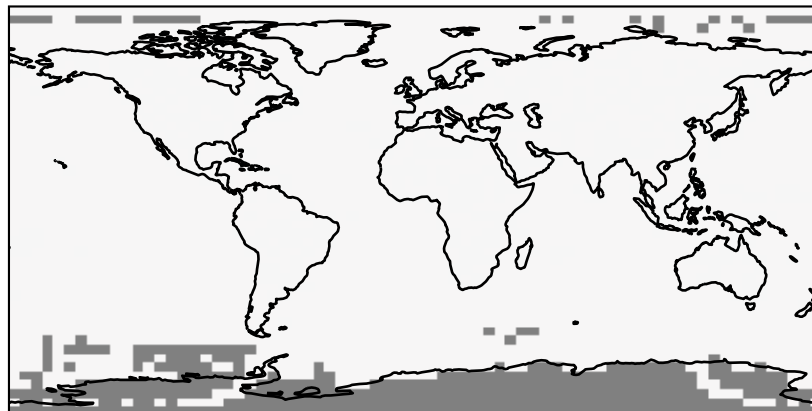
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



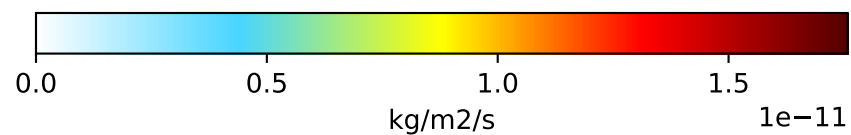
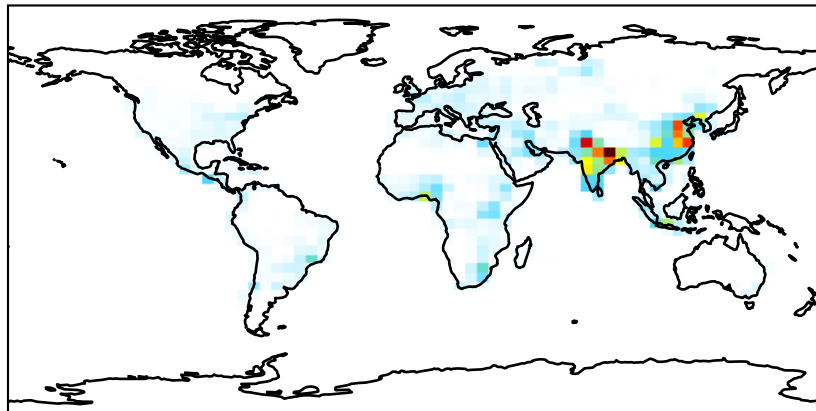
Ratio  
Dev/Ref, Fixed Range



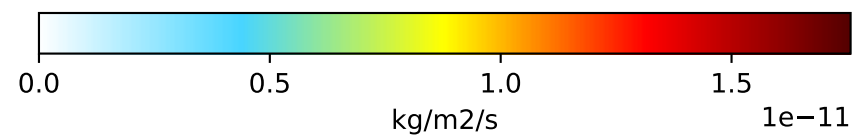
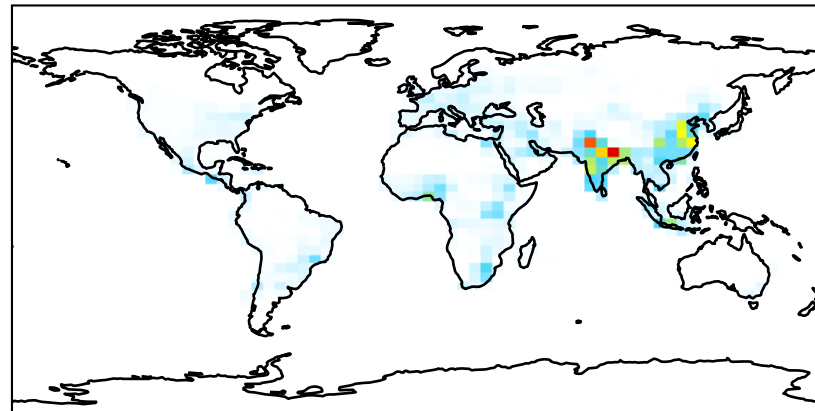


# EmisBCPO\_Anthro

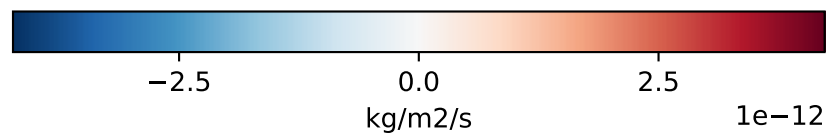
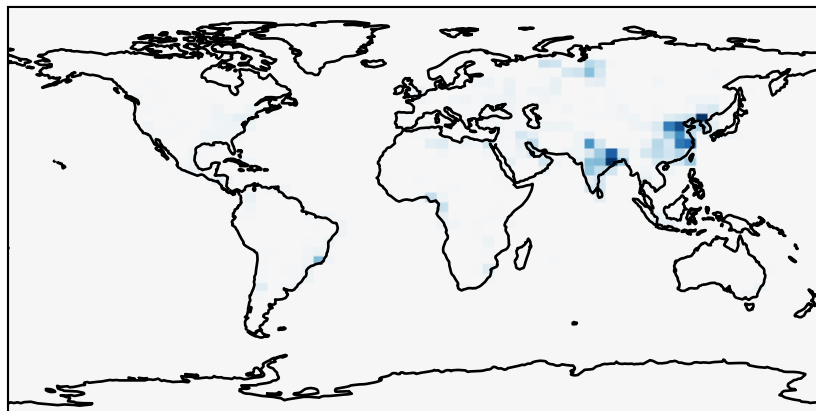
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



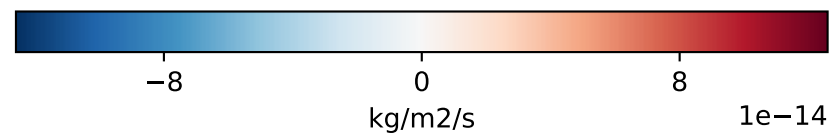
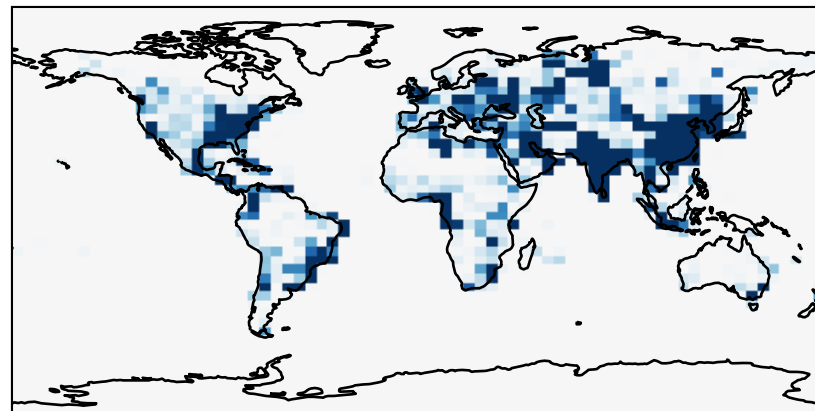
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



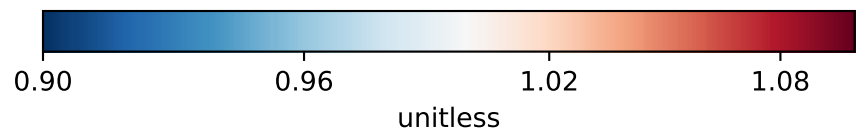
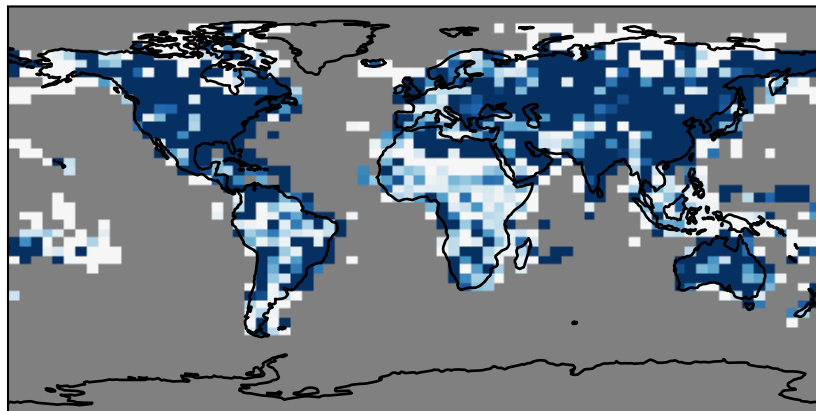
Difference  
Dev - Ref, Dynamic Range



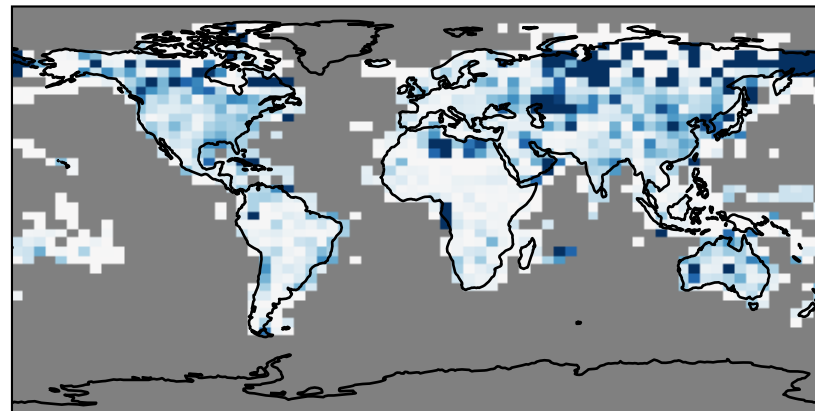
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

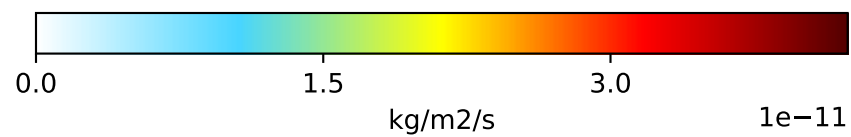
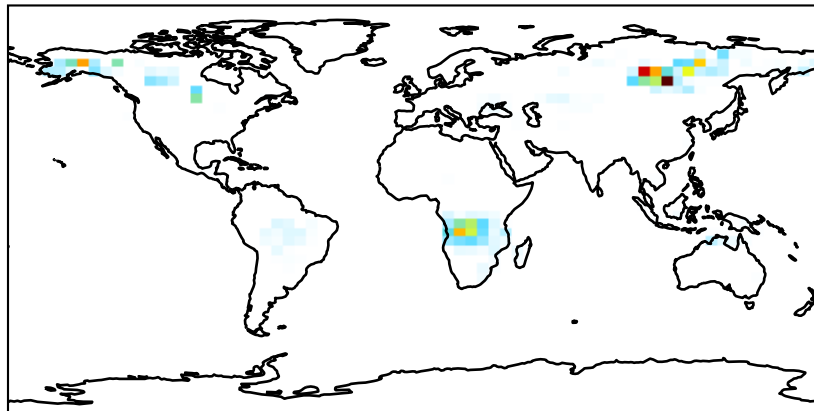


Ratio  
Dev/Ref, Fixed Range

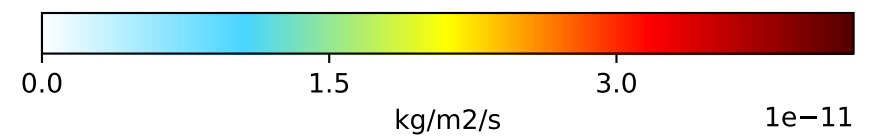
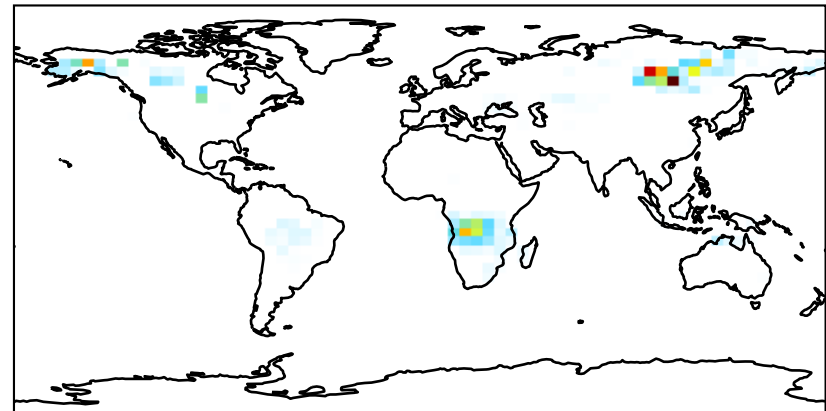


# EmisBCPO\_BioBurn

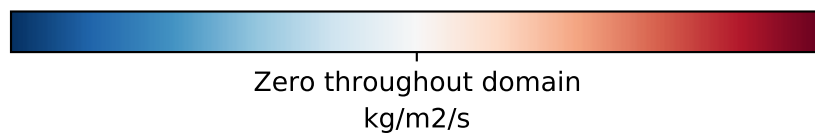
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



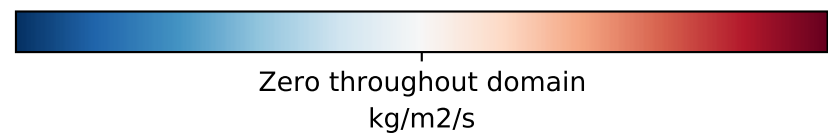
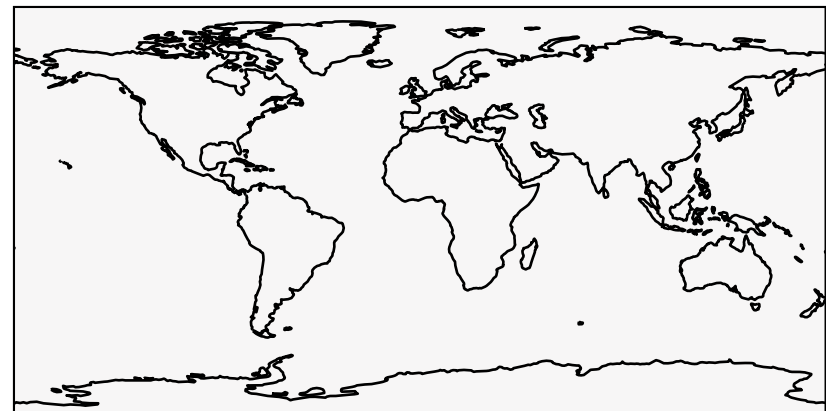
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



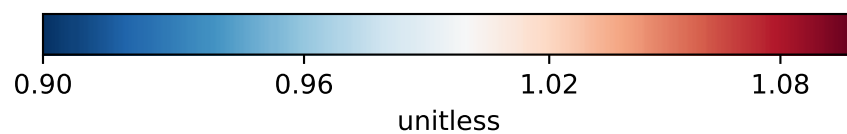
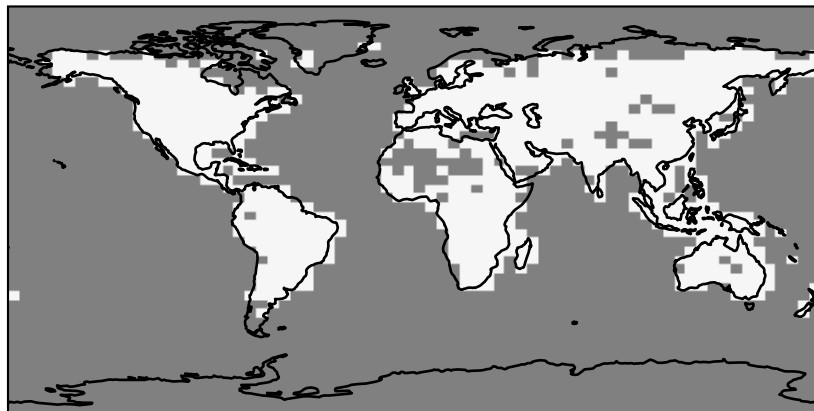
Difference  
Dev - Ref, Dynamic Range



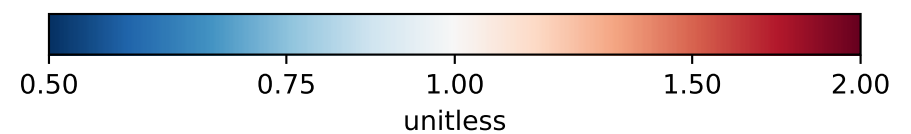
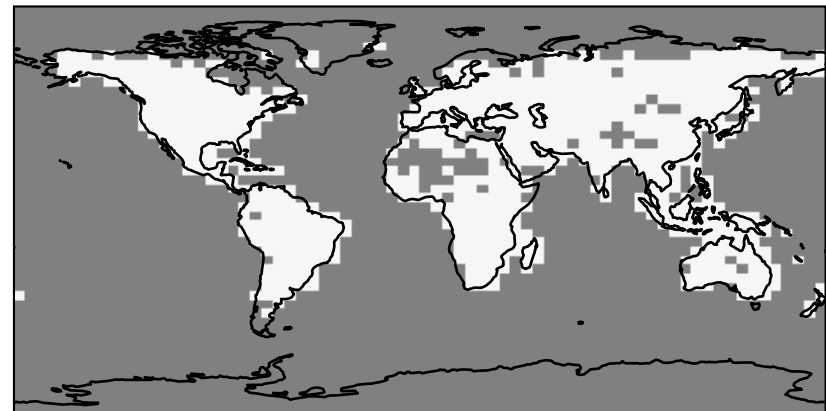
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

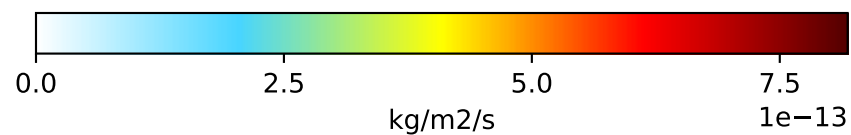
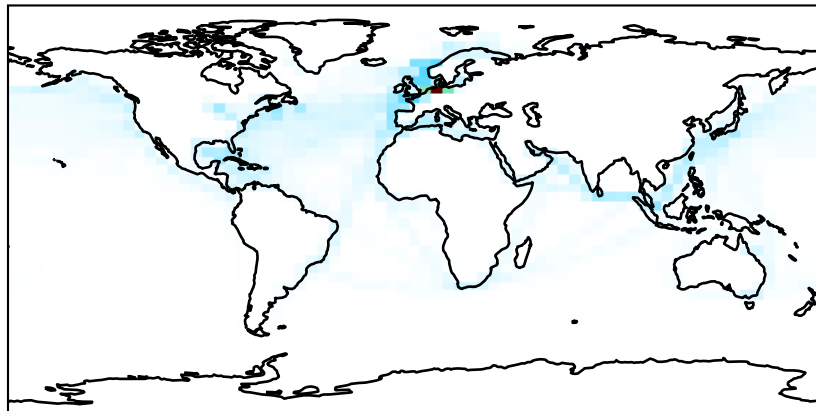


Ratio  
Dev/Ref, Fixed Range

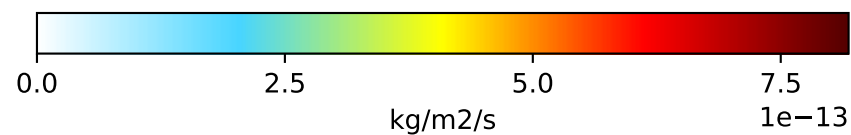
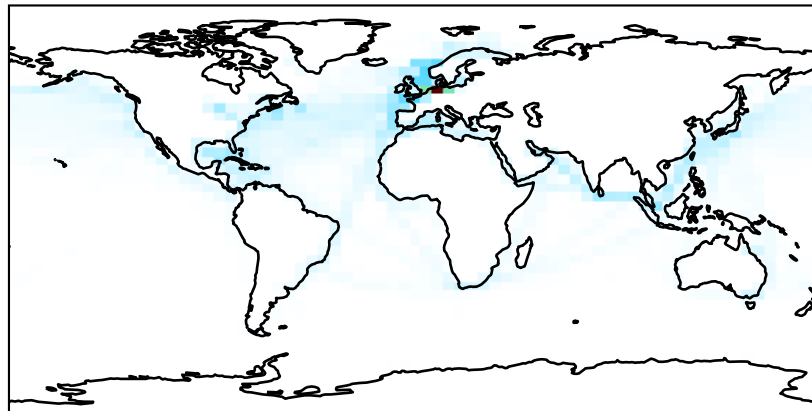


# EmisBCPO\_Ship

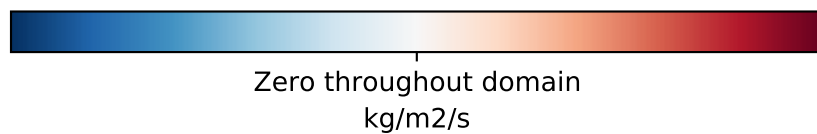
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



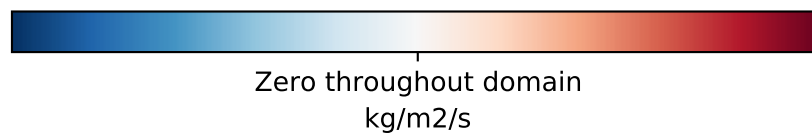
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



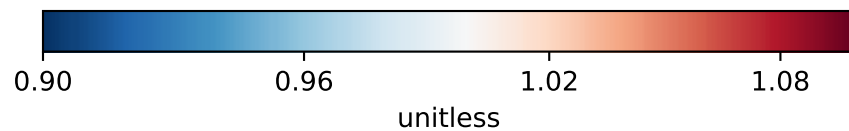
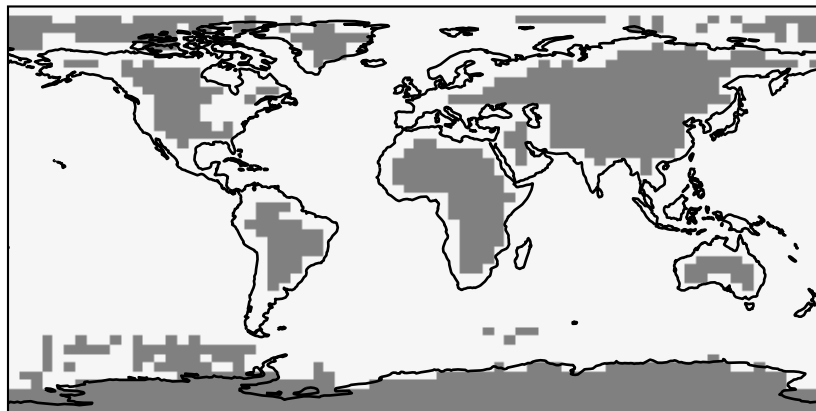
Difference  
Dev - Ref, Dynamic Range



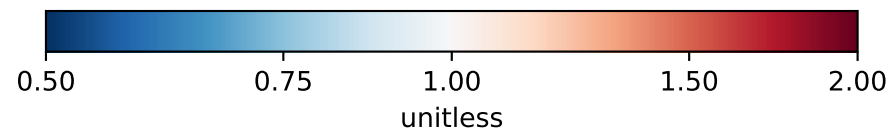
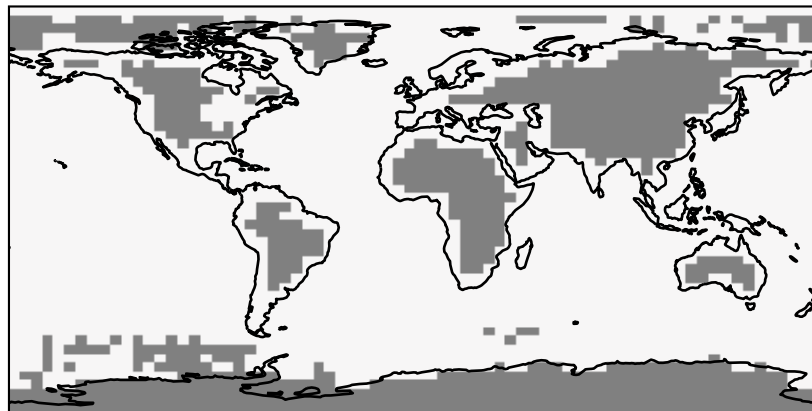
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

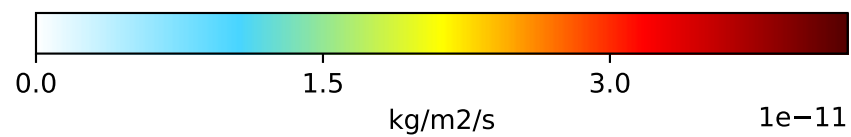
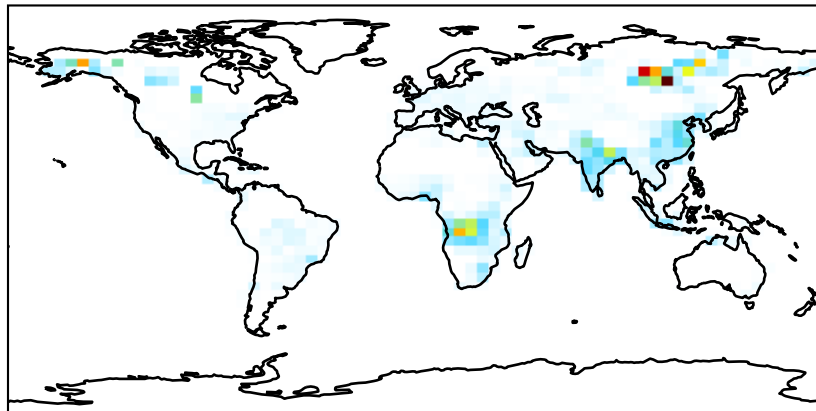


Ratio  
Dev/Ref, Fixed Range

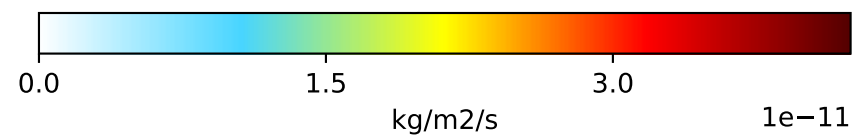
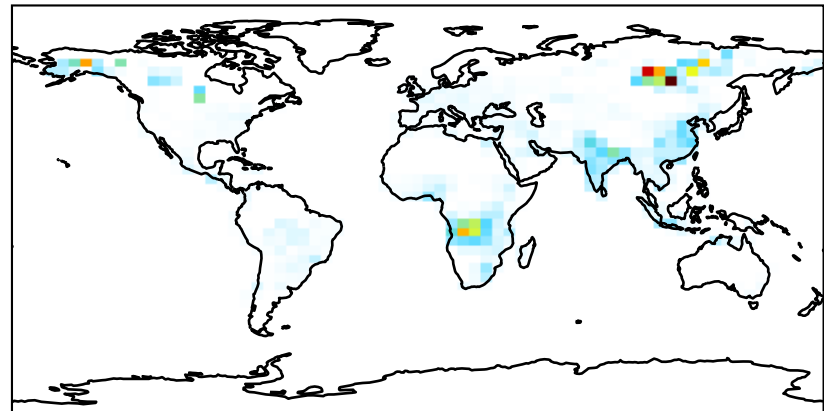


# EmisBCPO\_Total

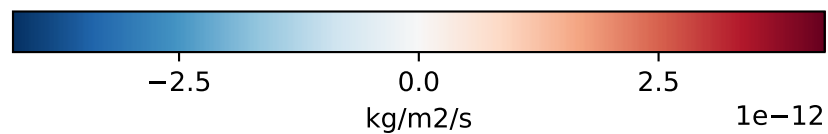
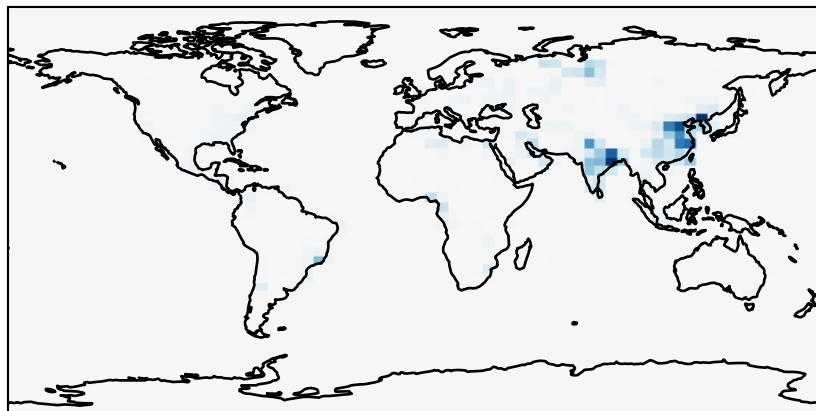
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



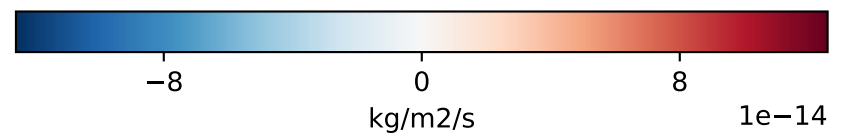
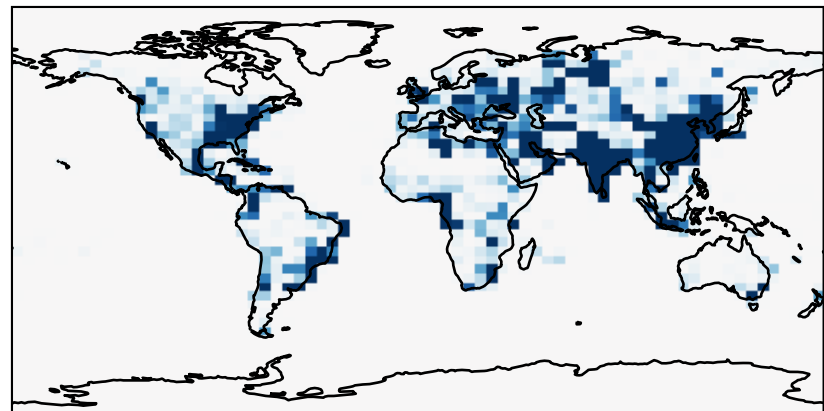
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



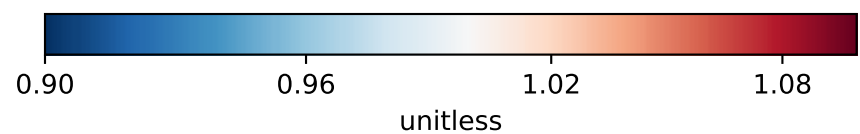
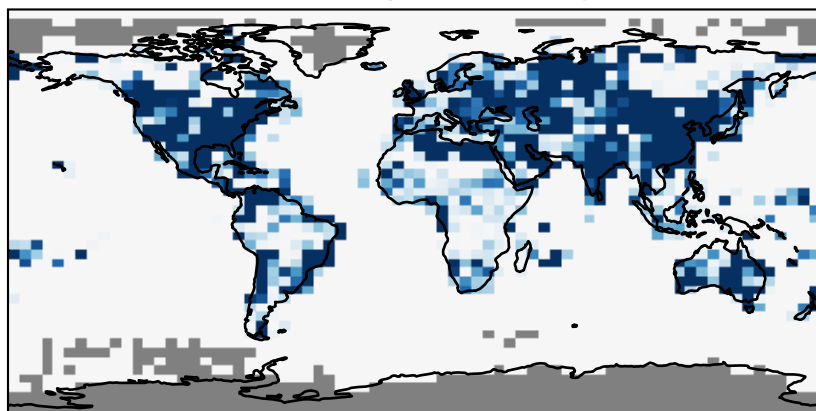
Difference  
Dev - Ref, Dynamic Range



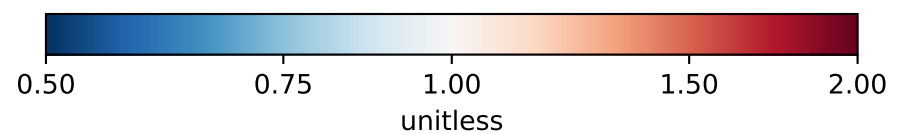
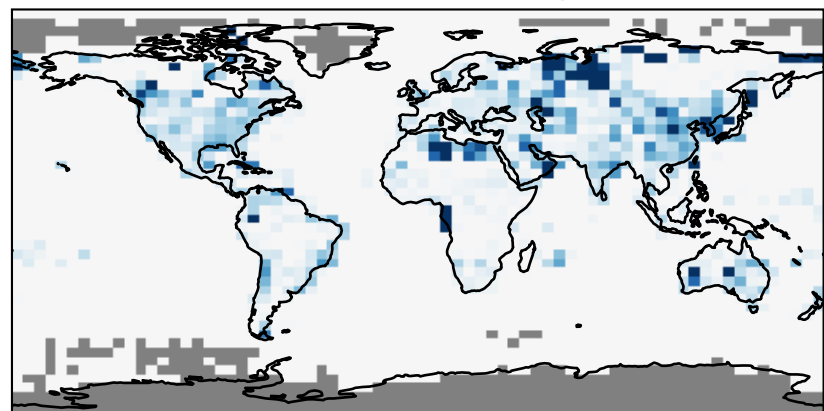
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

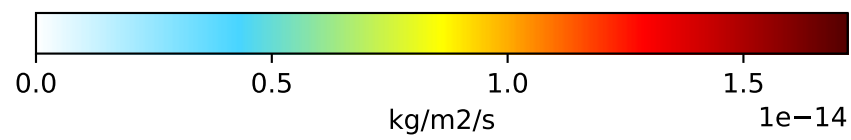
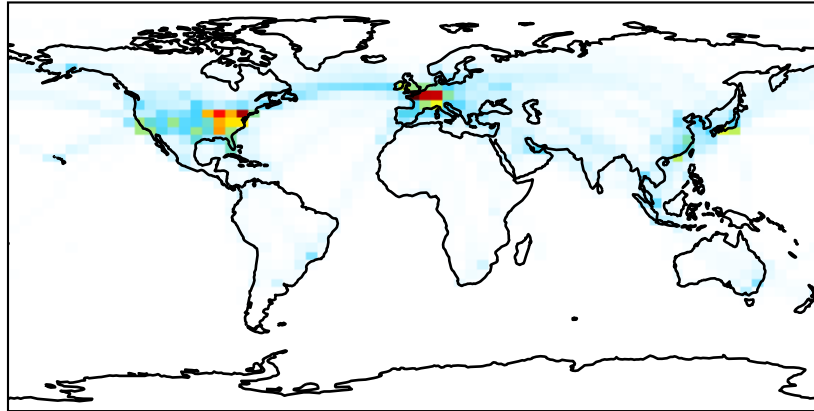


Ratio  
Dev/Ref, Fixed Range

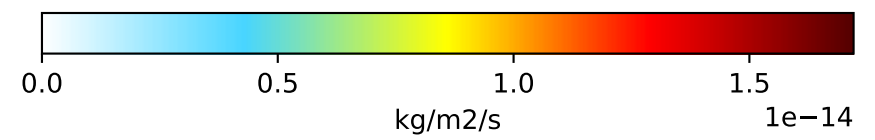
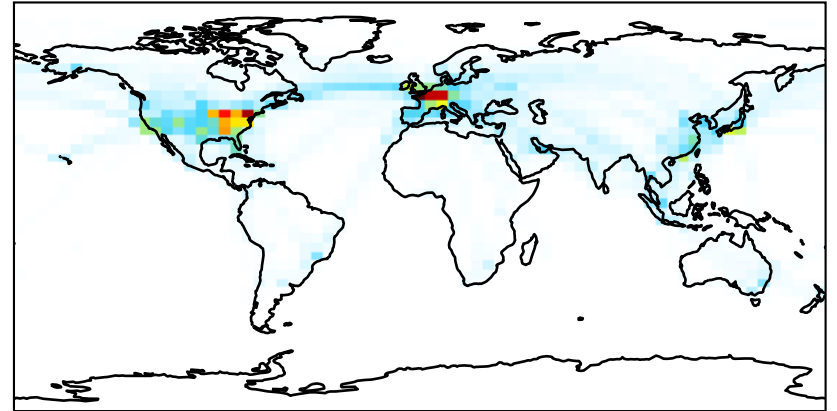


# EmisOCPI\_Aircraft

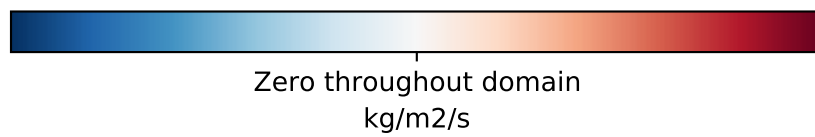
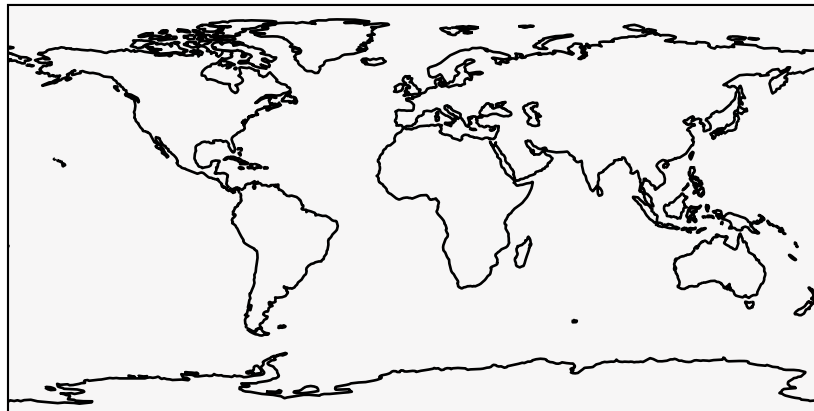
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



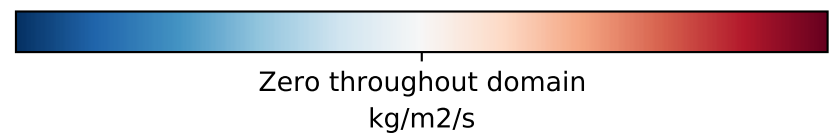
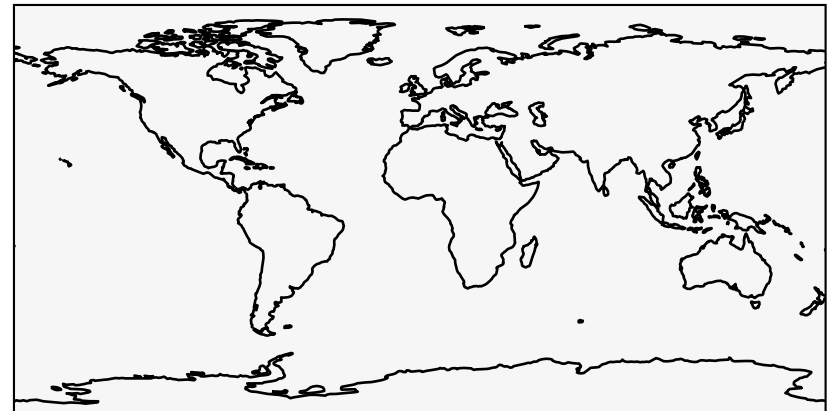
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



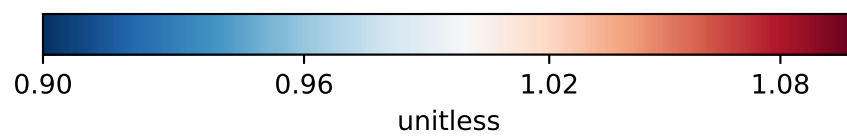
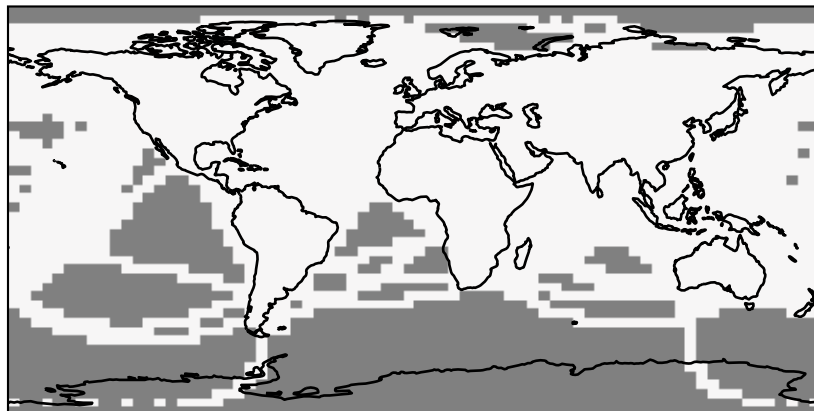
Difference  
Dev - Ref, Dynamic Range



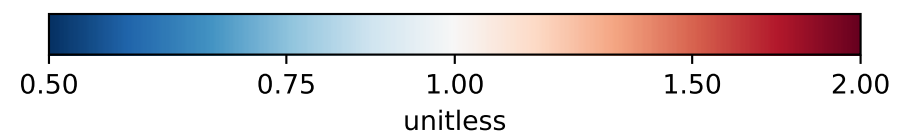
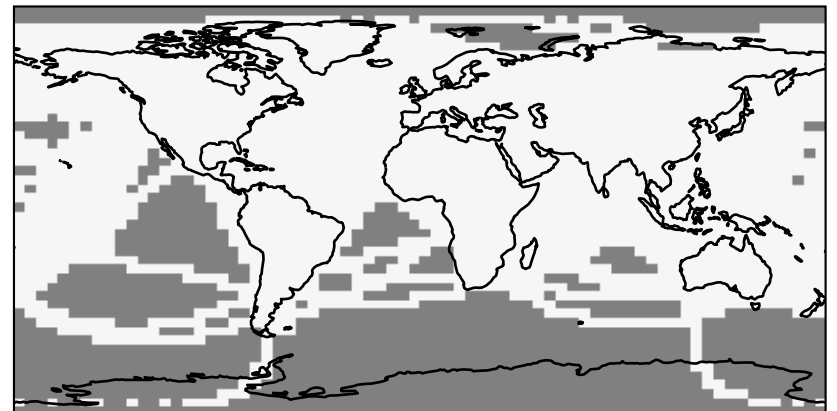
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



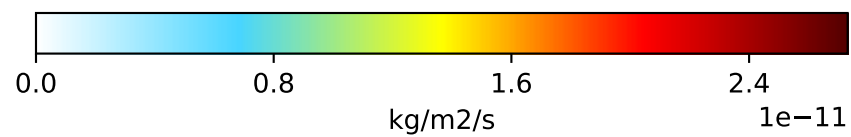
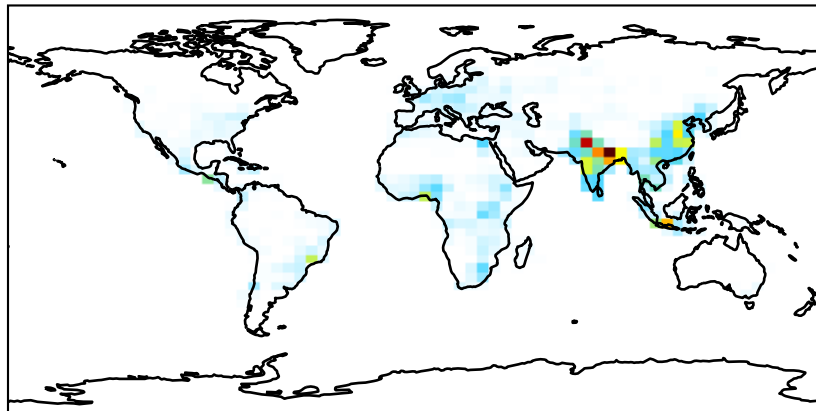
Ratio  
Dev/Ref, Fixed Range



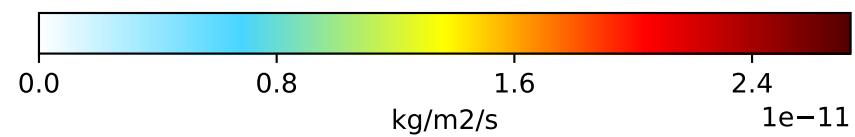
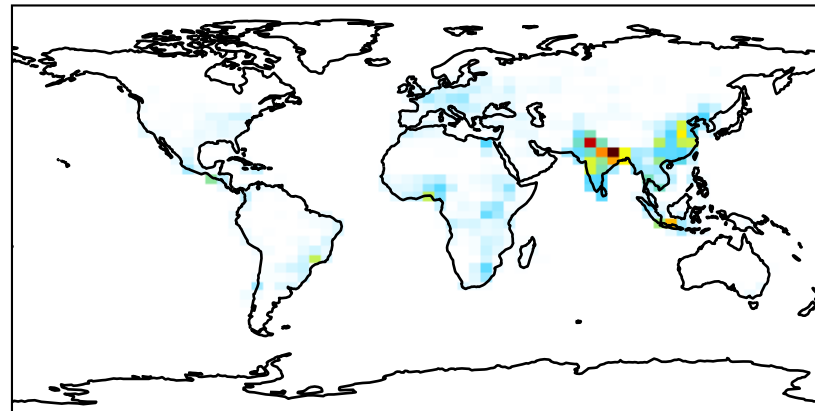


# EmisOCPI\_Anthro

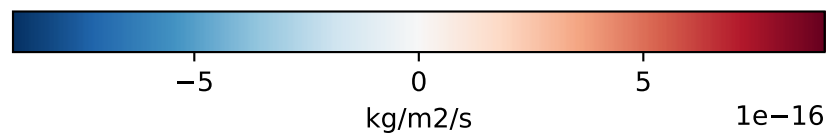
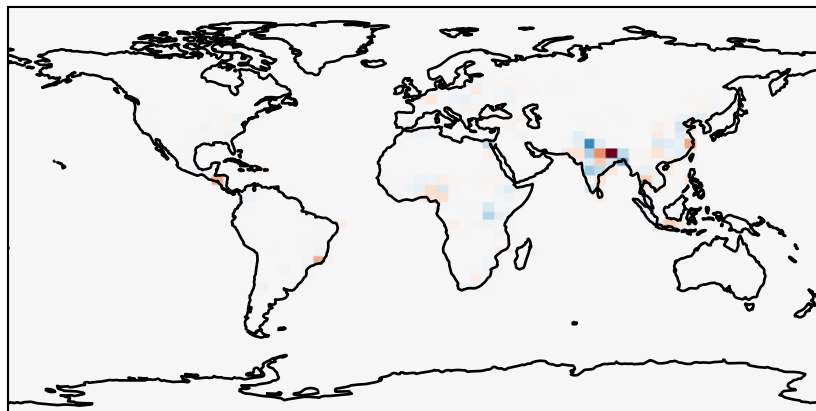
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



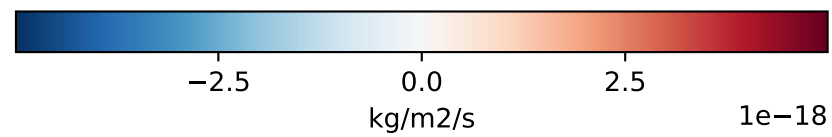
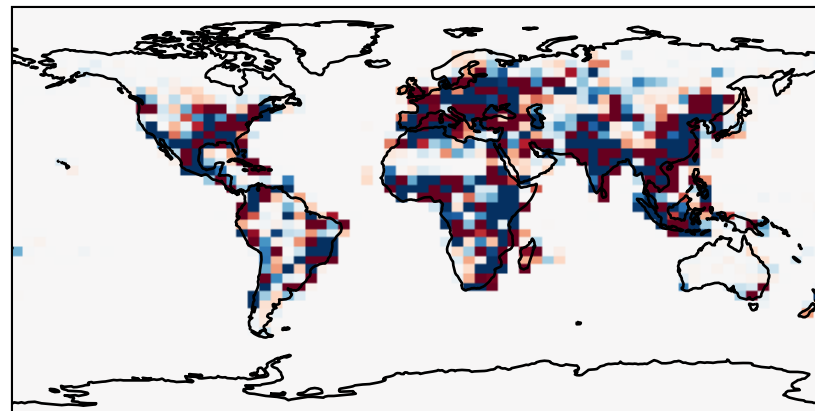
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



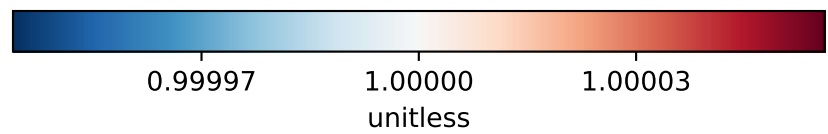
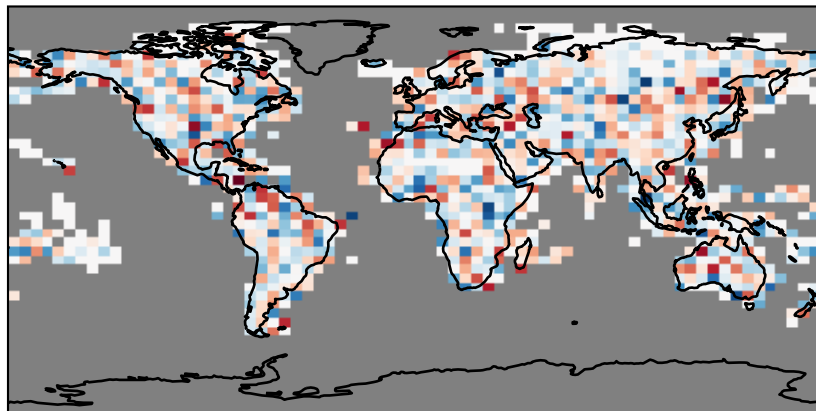
Difference  
Dev - Ref, Dynamic Range



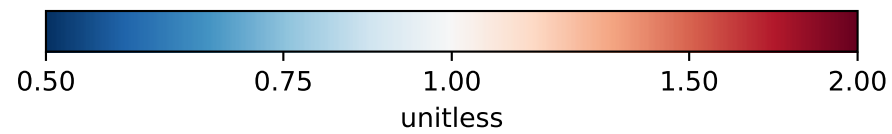
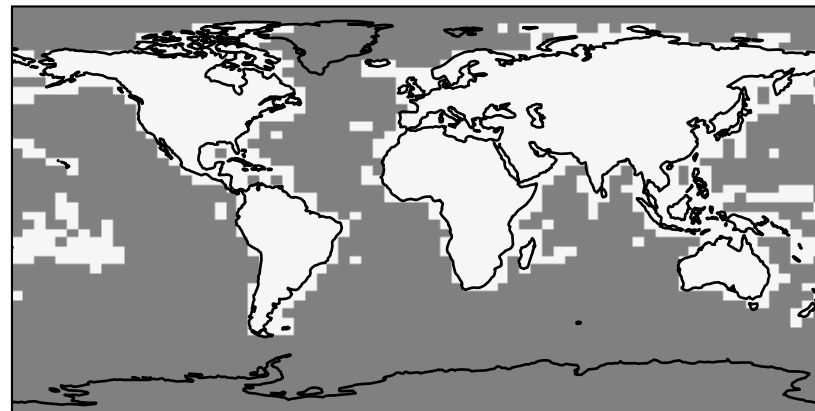
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

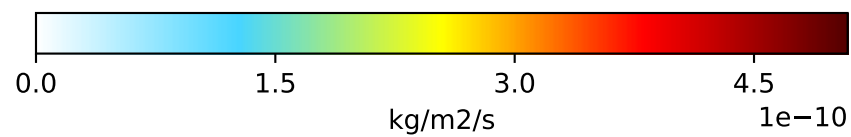
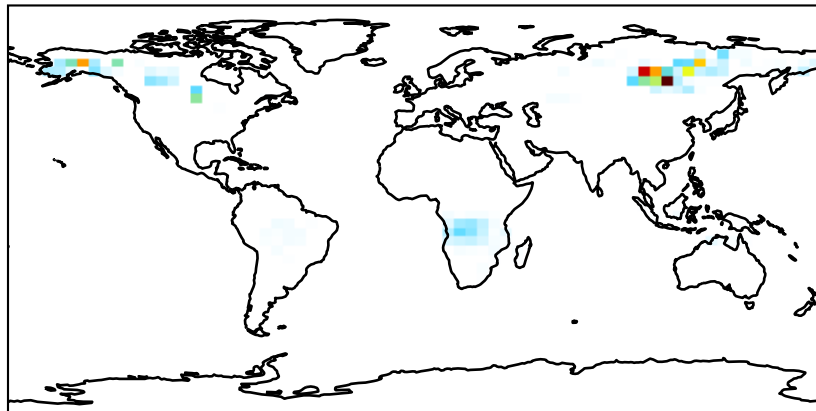


Ratio  
Dev/Ref, Fixed Range

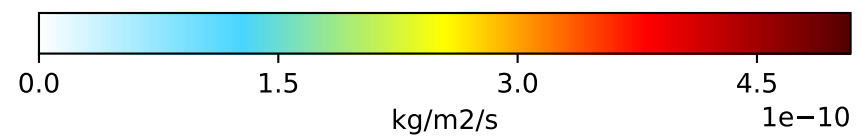
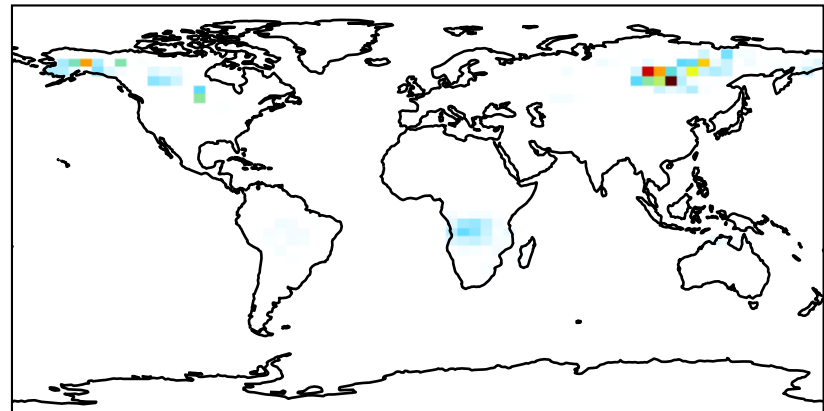


# EmisOCPI\_BioBurn

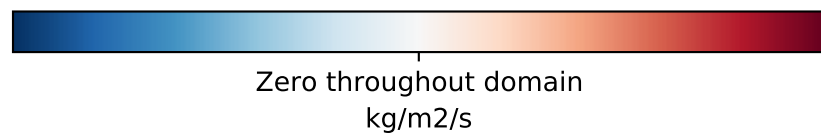
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



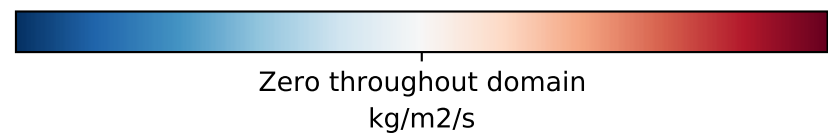
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



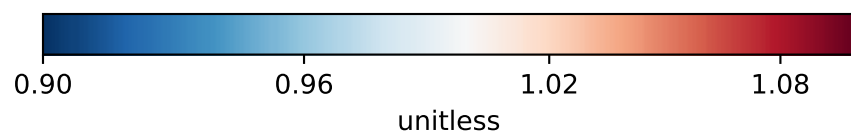
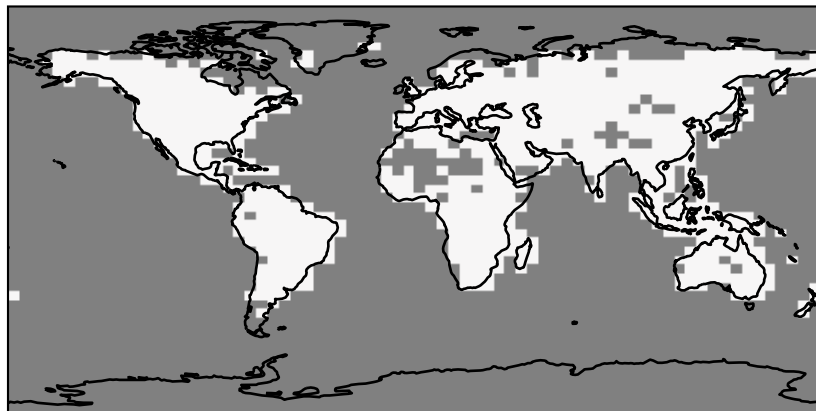
Difference  
Dev - Ref, Dynamic Range



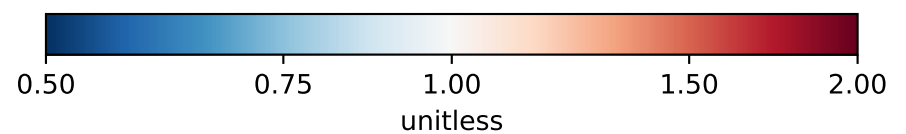
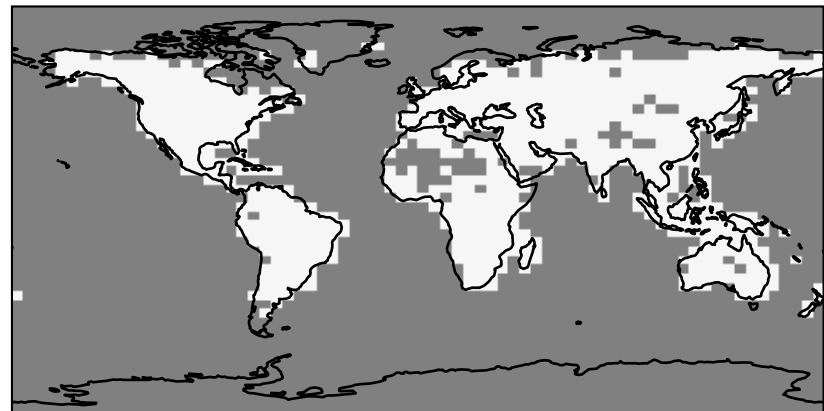
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

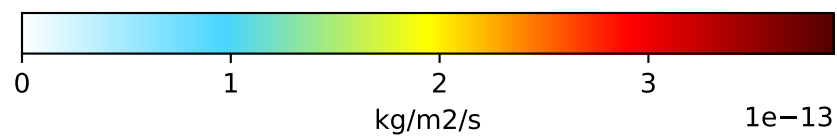
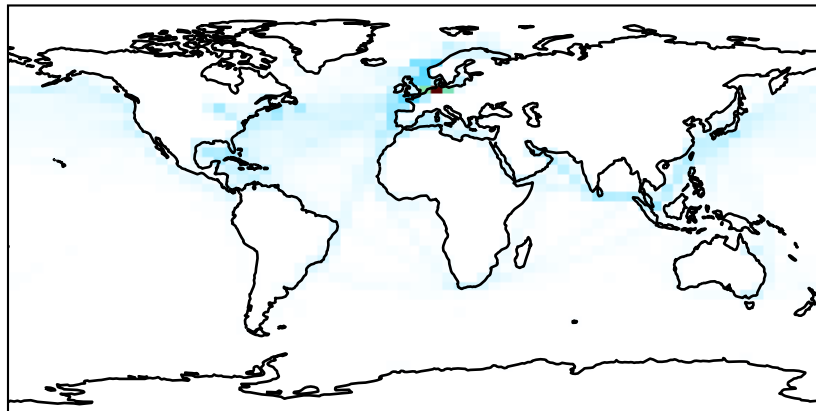


Ratio  
Dev/Ref, Fixed Range

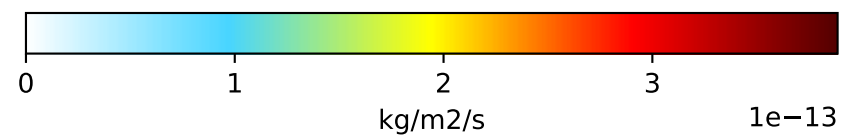
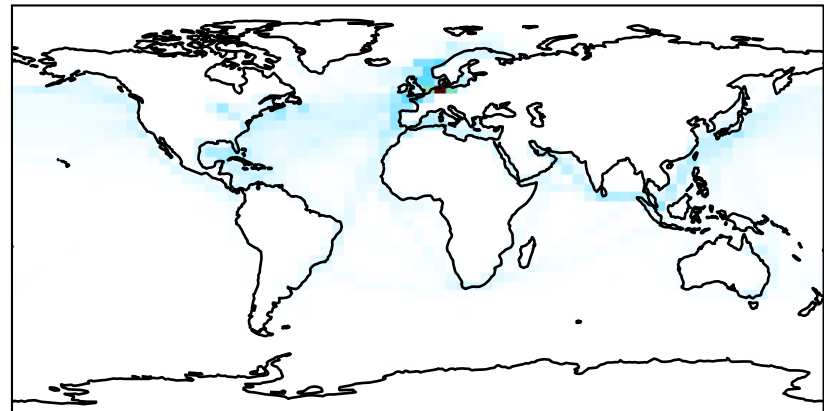


# EmisOCPI\_Ship

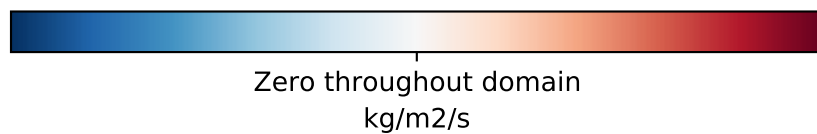
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



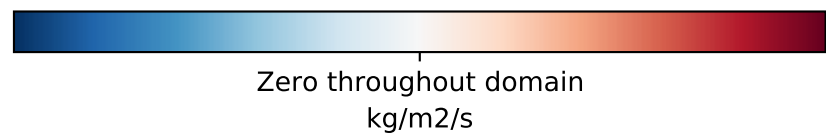
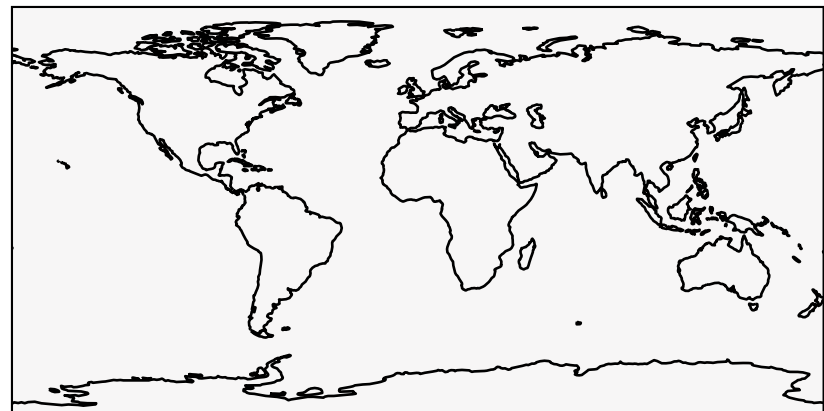
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



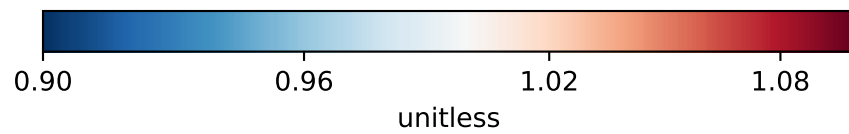
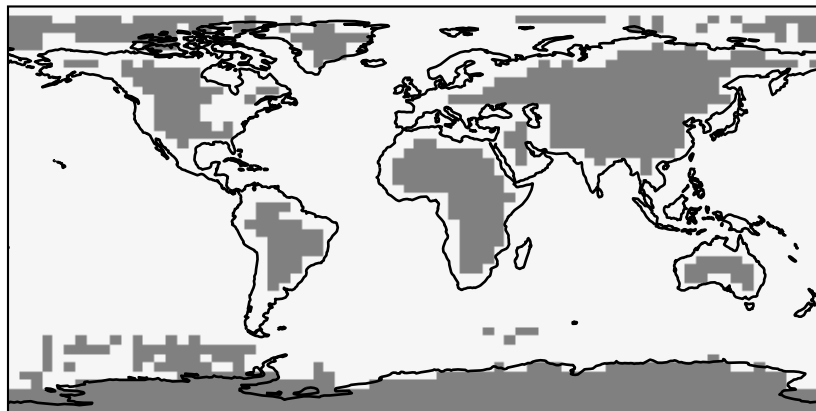
Difference  
Dev - Ref, Dynamic Range



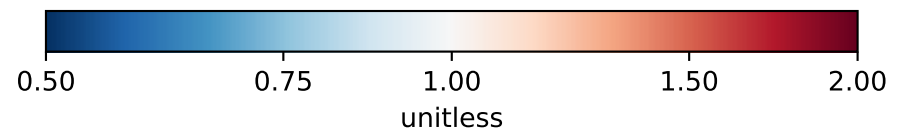
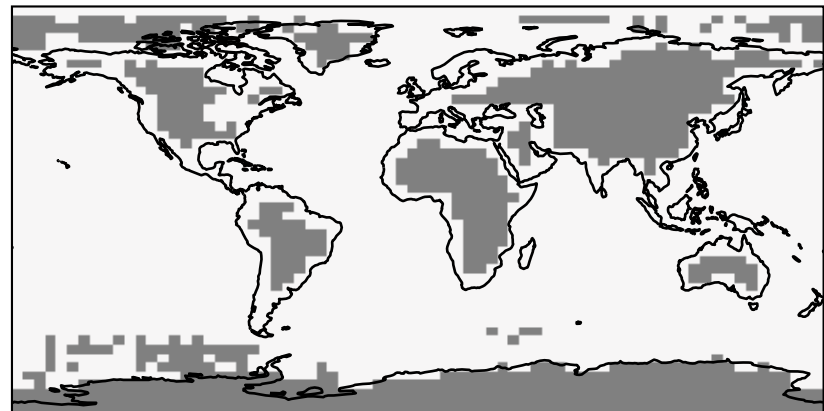
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

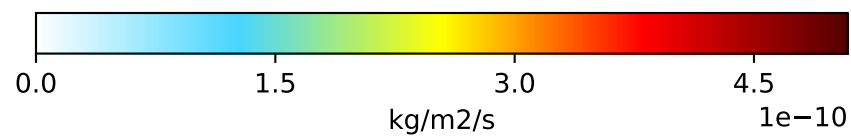
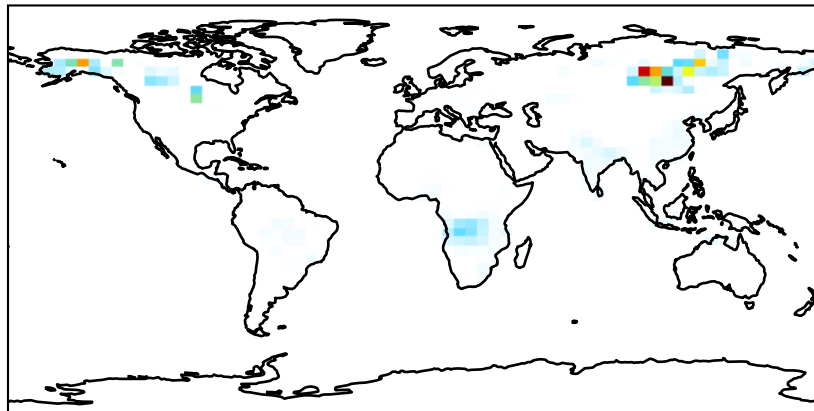


Ratio  
Dev/Ref, Fixed Range

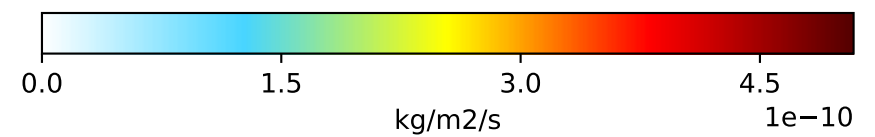
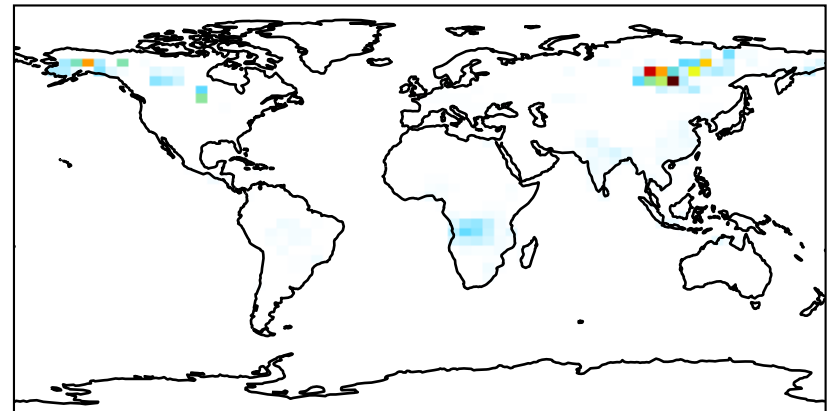


# EmisOCPI\_Total

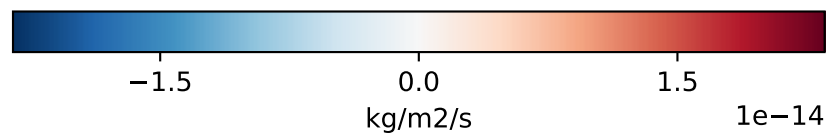
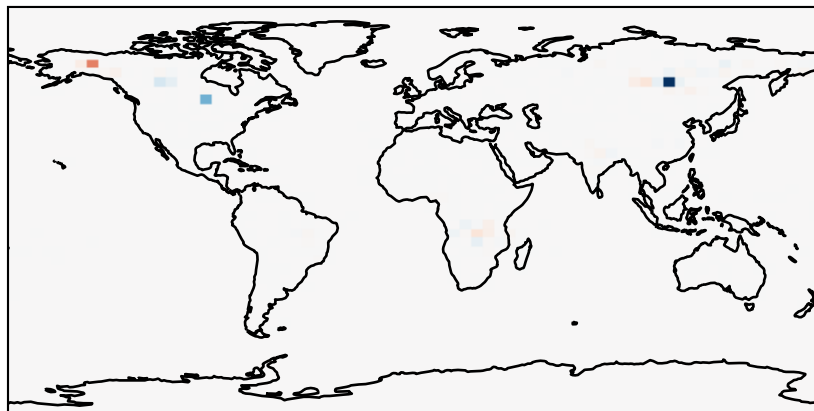
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



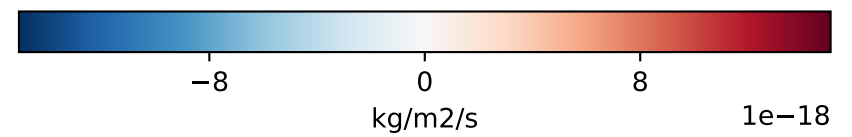
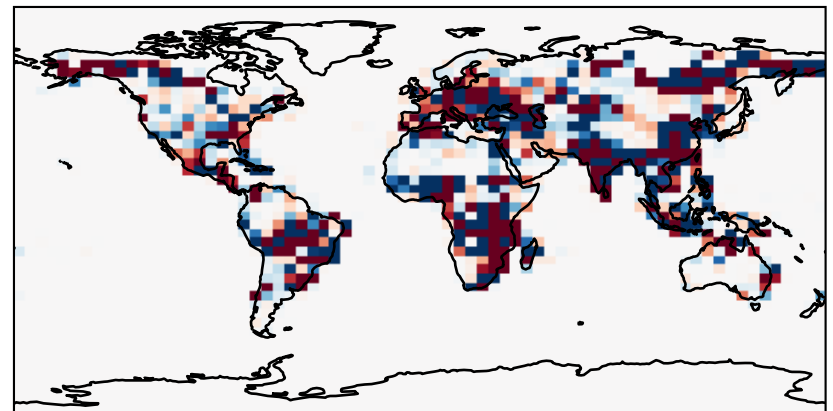
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



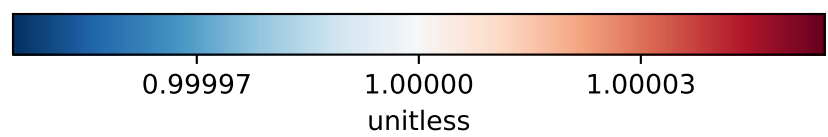
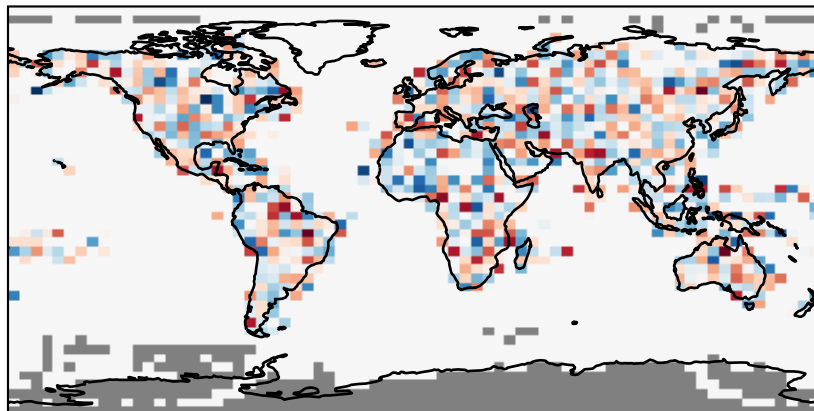
Difference  
Dev - Ref, Dynamic Range



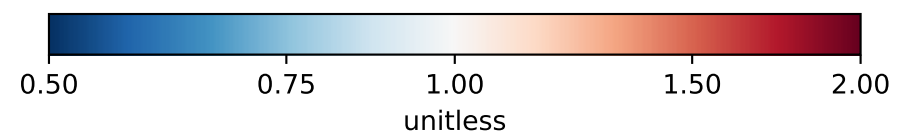
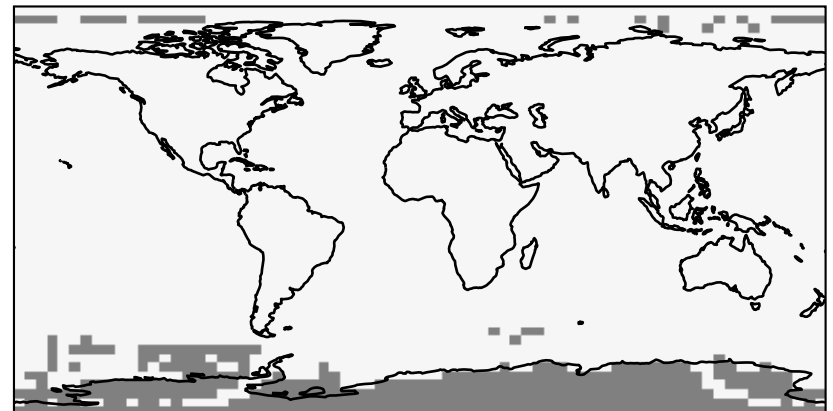
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

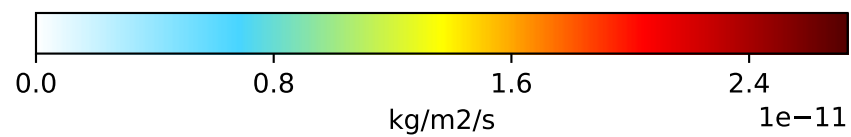
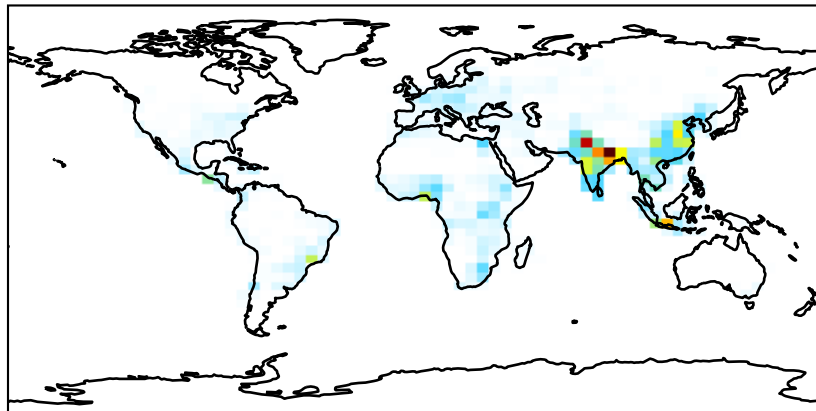


Ratio  
Dev/Ref, Fixed Range

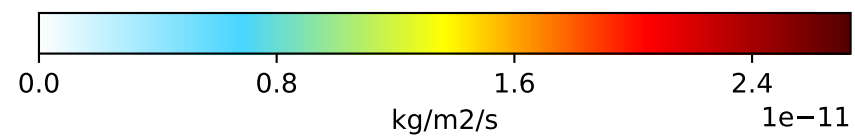
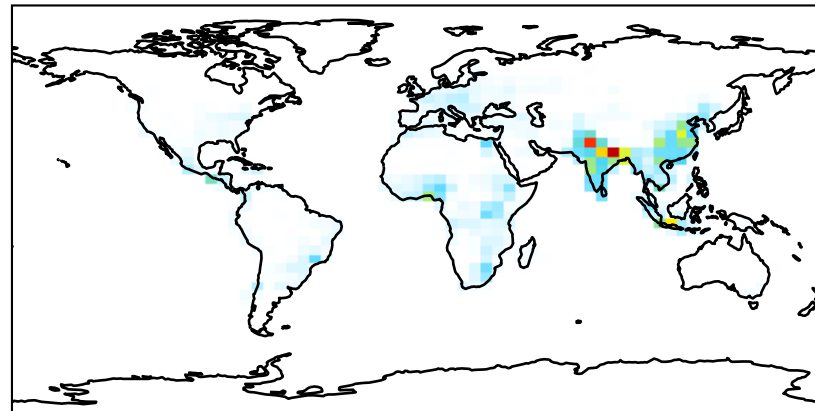


# EmisOCPO\_Anthro

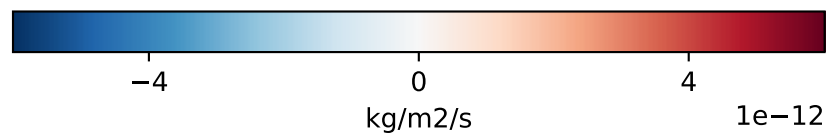
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



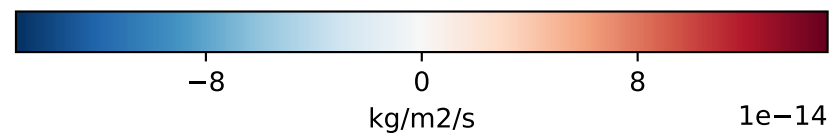
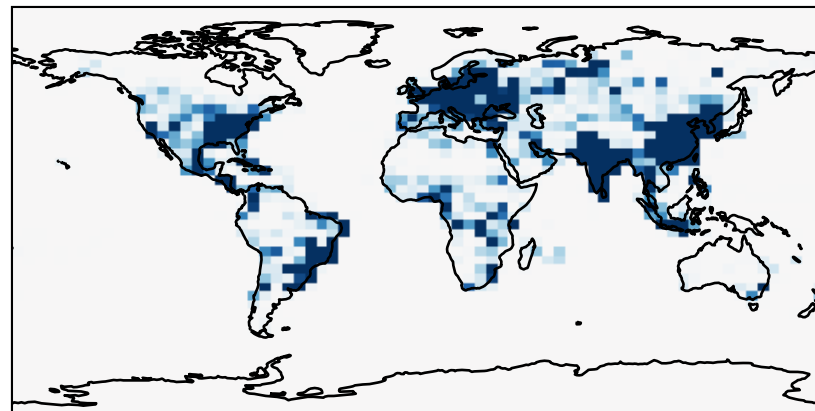
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



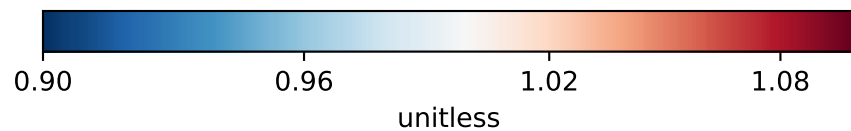
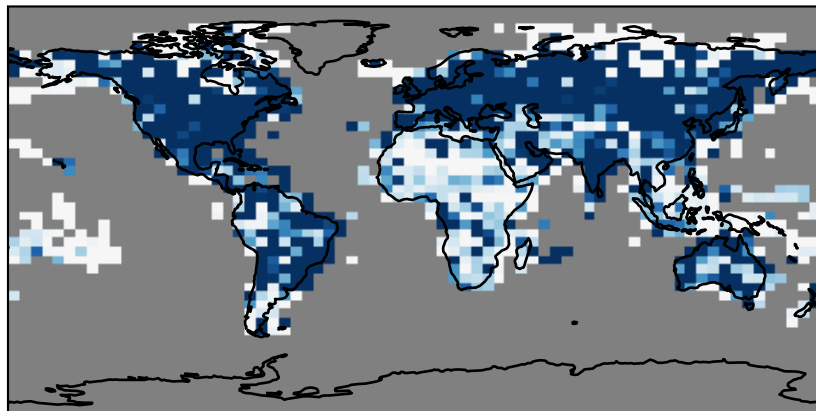
Difference  
Dev - Ref, Dynamic Range



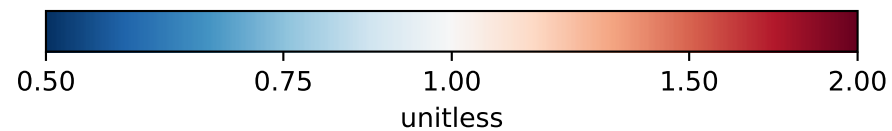
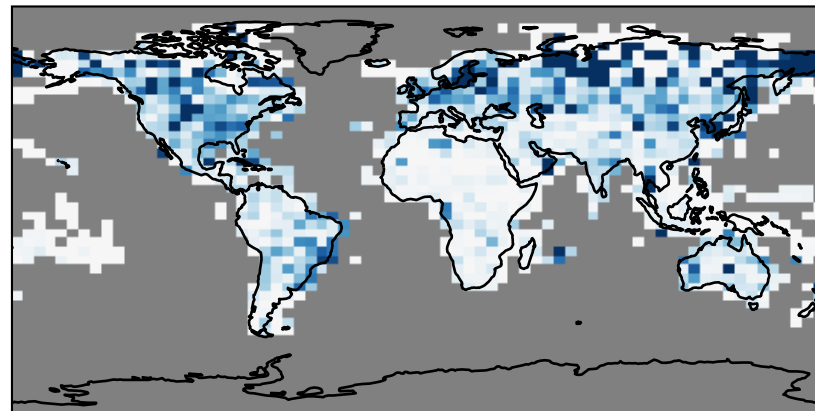
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



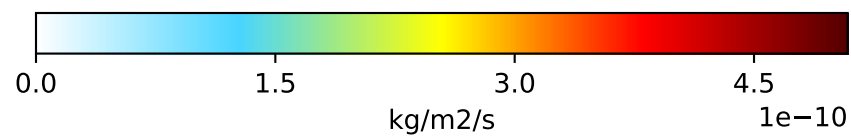
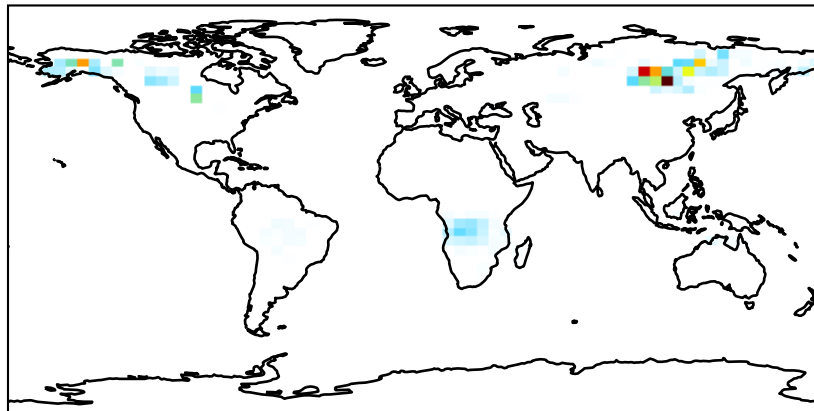
Ratio  
Dev/Ref, Fixed Range



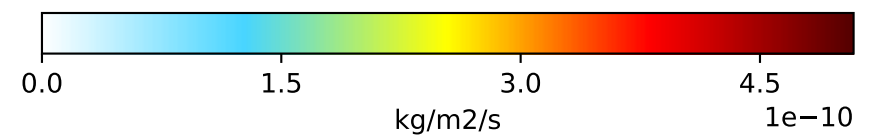
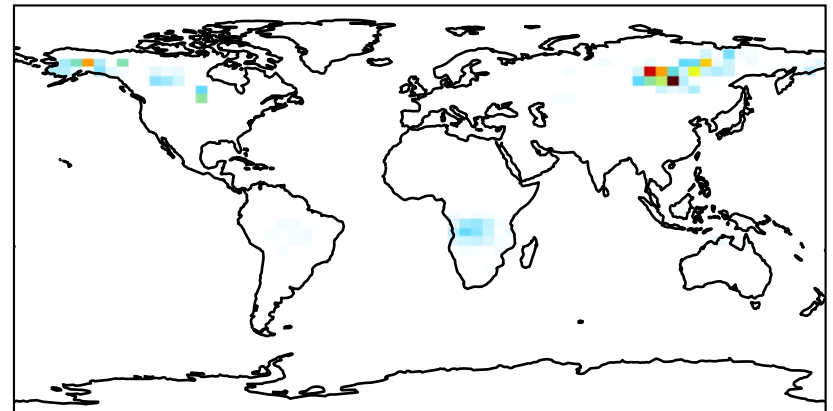


# EmisOCPO\_BioBurn

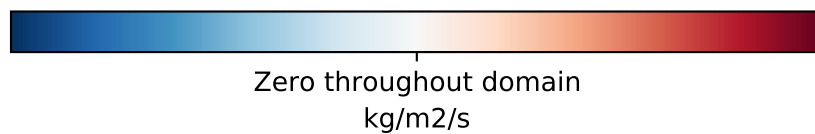
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



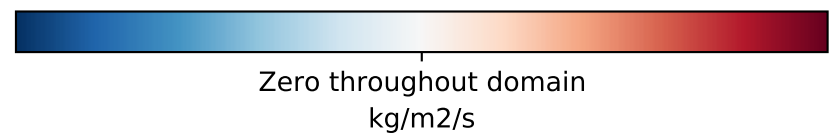
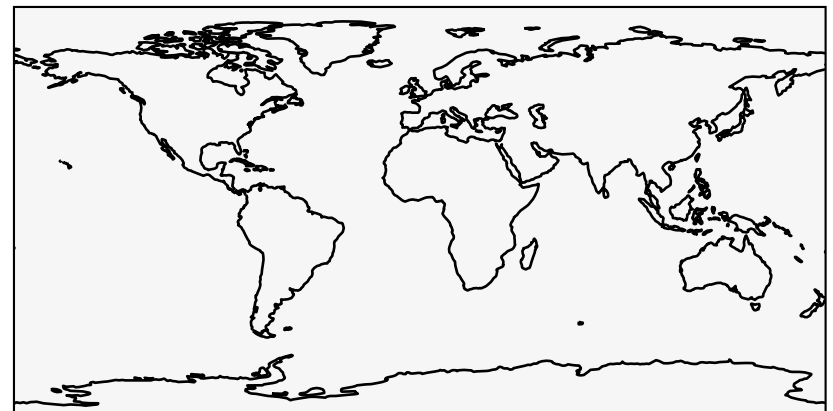
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



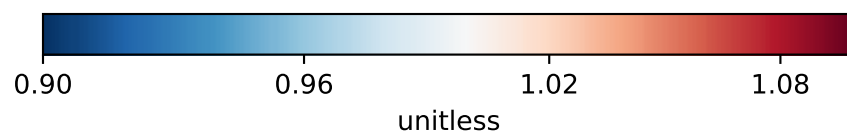
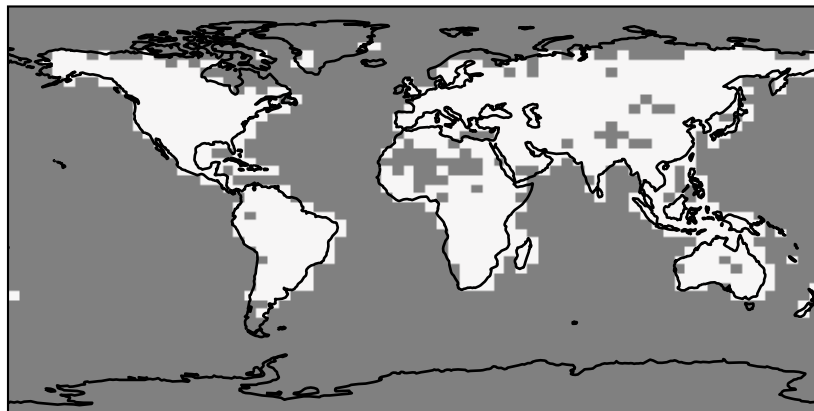
Difference  
Dev - Ref, Dynamic Range



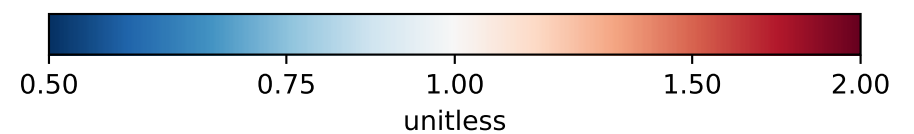
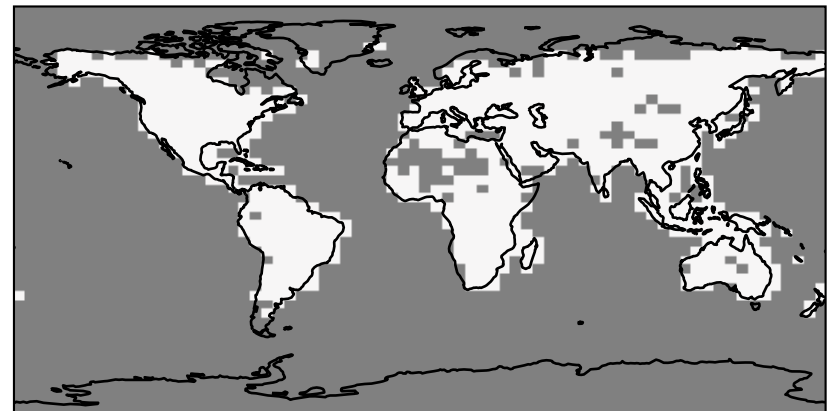
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

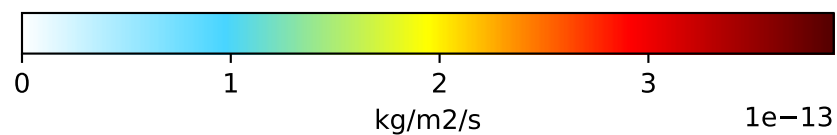
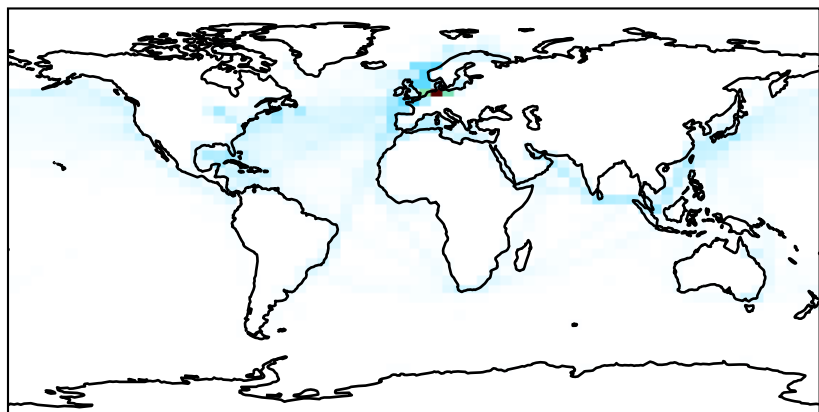


Ratio  
Dev/Ref, Fixed Range

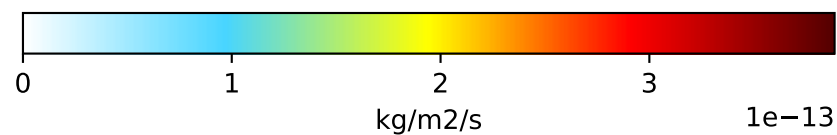
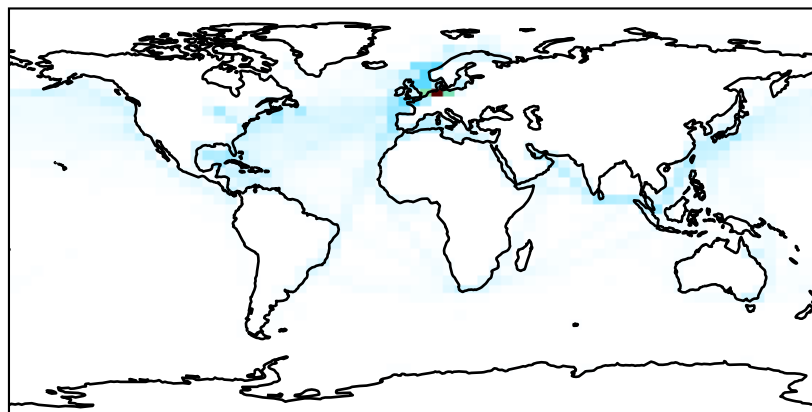


# EmisOCPO\_Ship

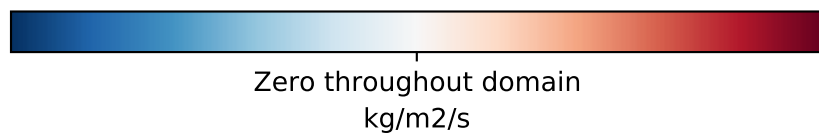
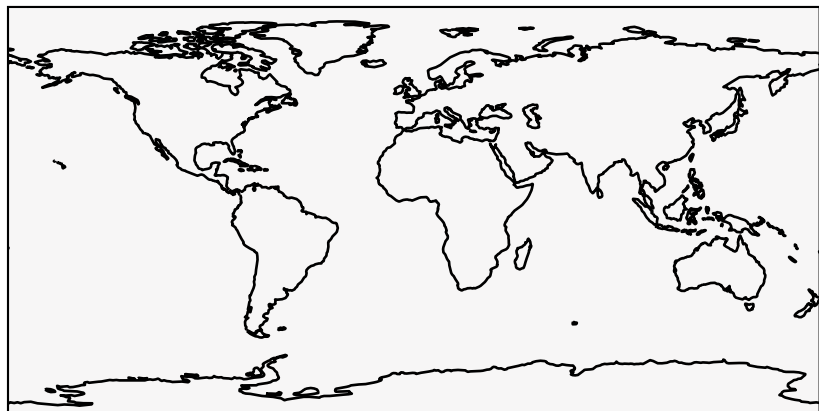
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



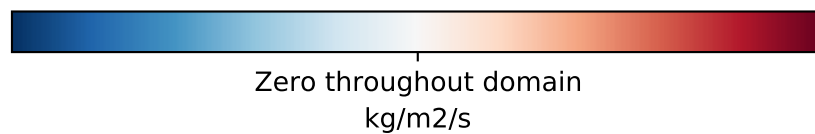
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



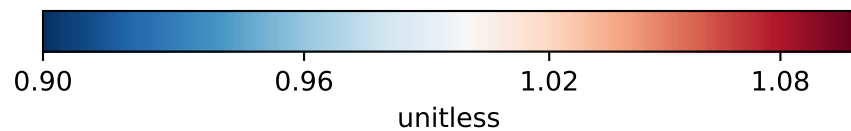
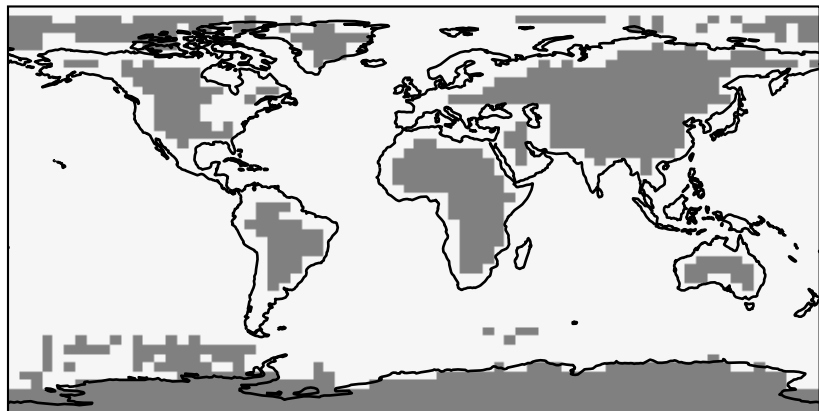
Difference  
Dev - Ref, Dynamic Range



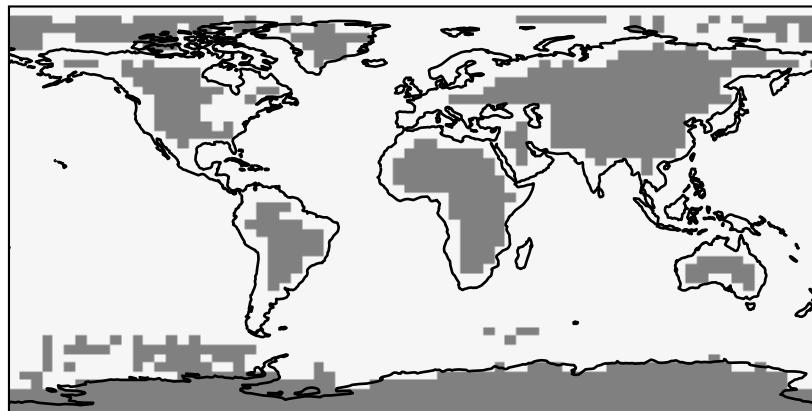
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

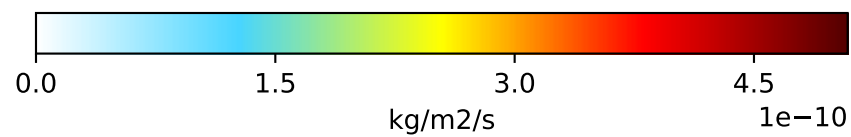
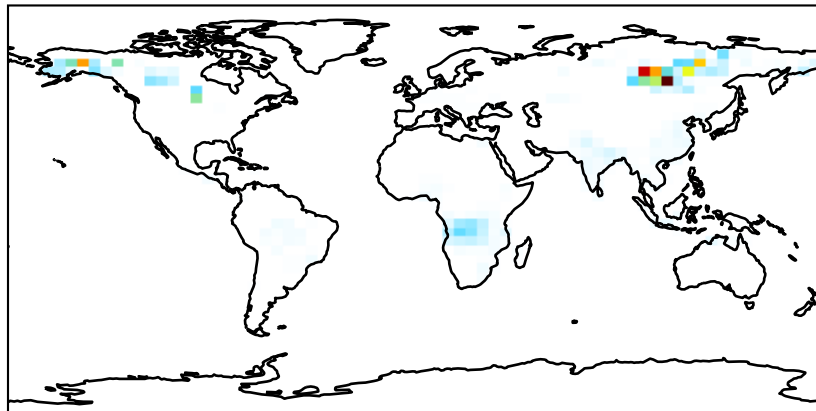


Ratio  
Dev/Ref, Fixed Range

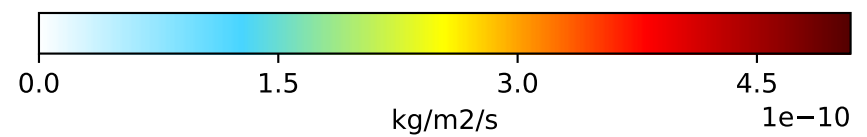
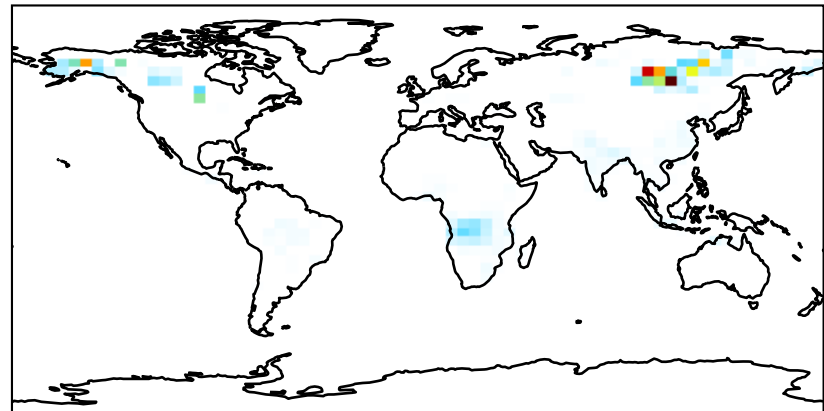


# EmisOCPO\_Total

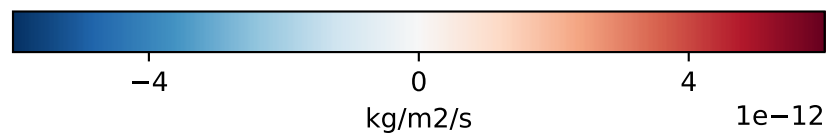
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



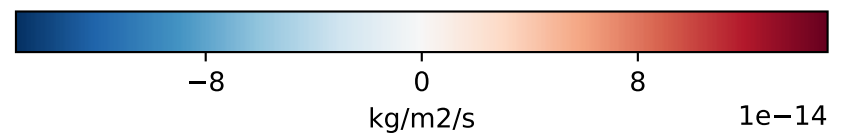
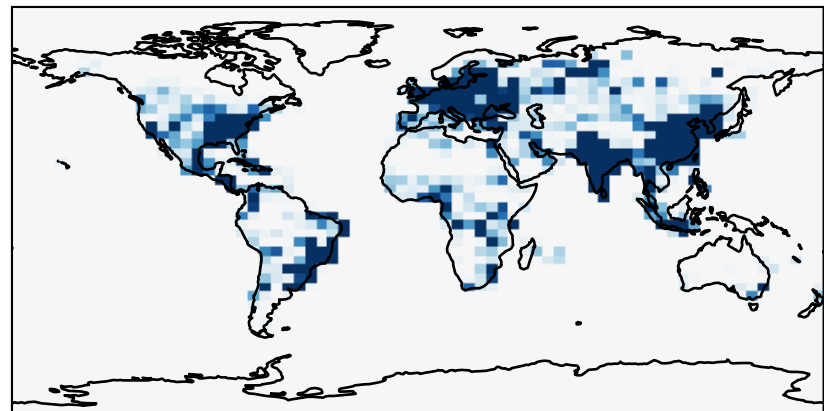
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



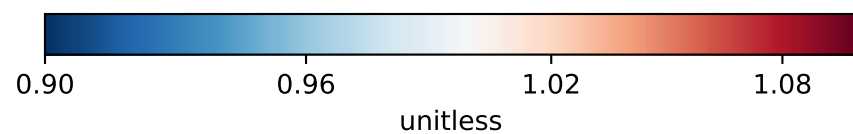
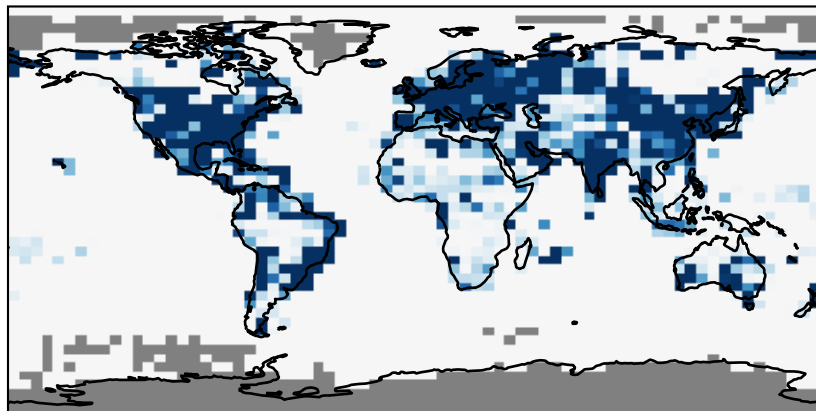
Difference  
Dev - Ref, Dynamic Range



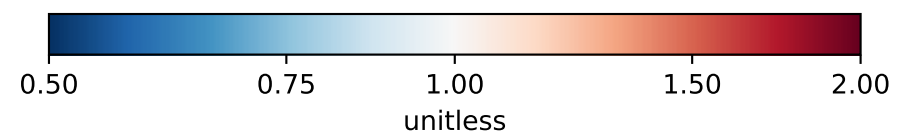
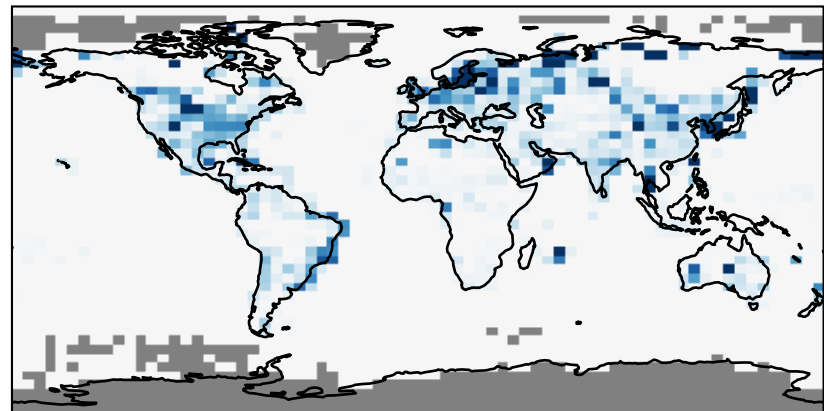
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

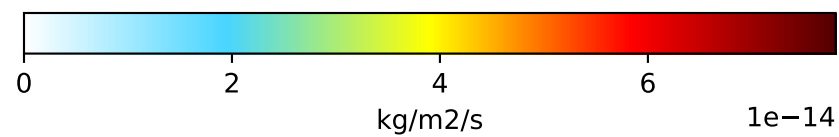
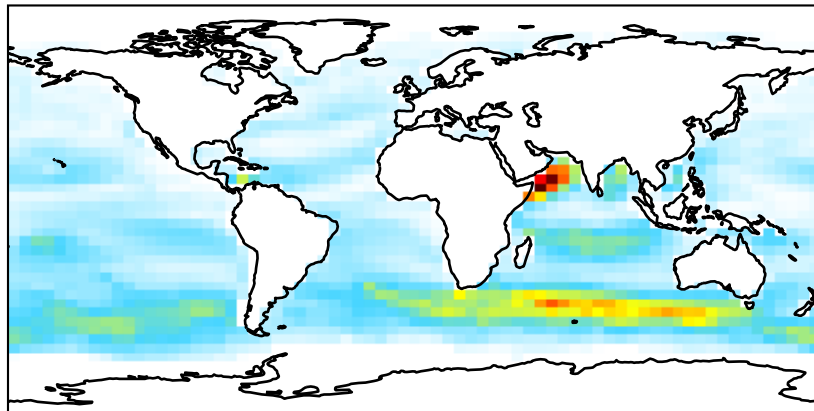


Ratio  
Dev/Ref, Fixed Range

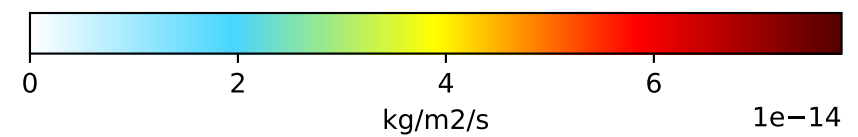
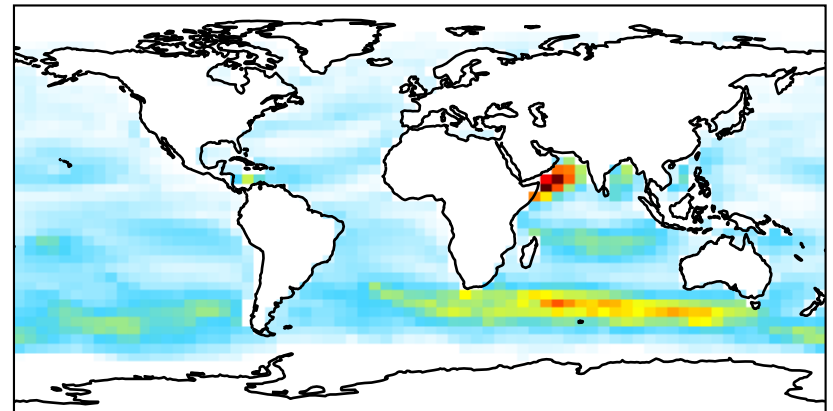


# EmisBrSALA\_Natural

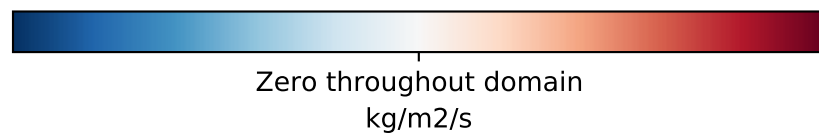
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



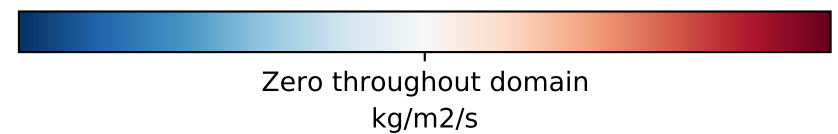
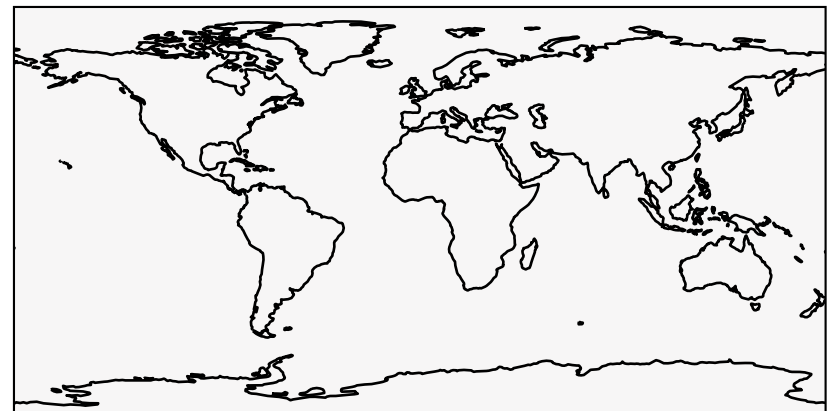
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



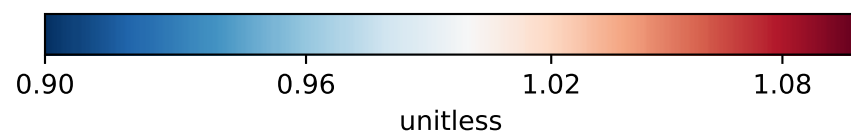
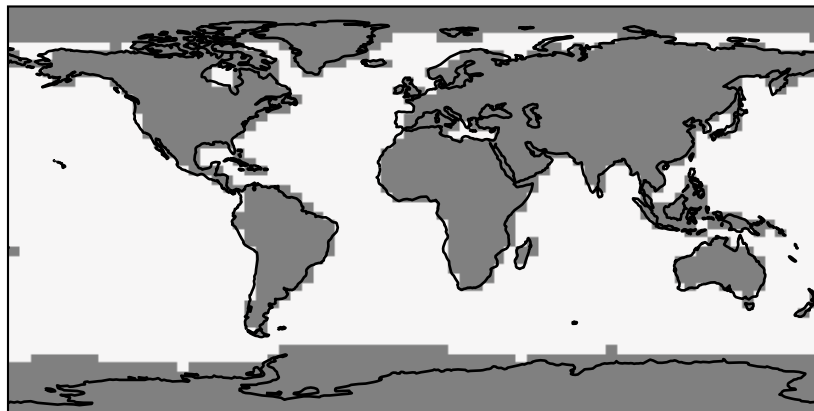
Difference  
Dev - Ref, Dynamic Range



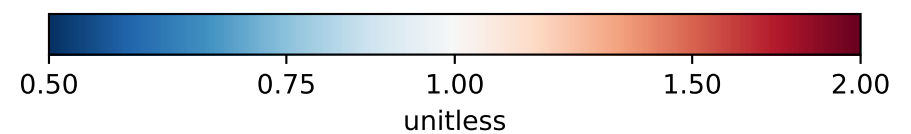
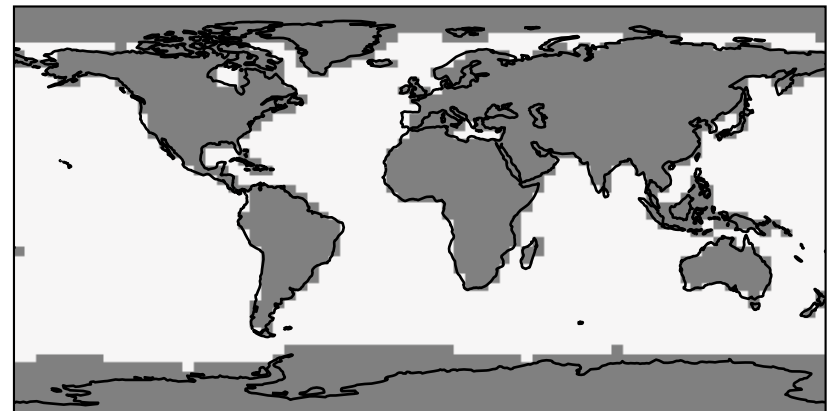
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

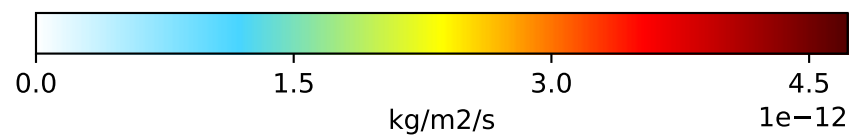
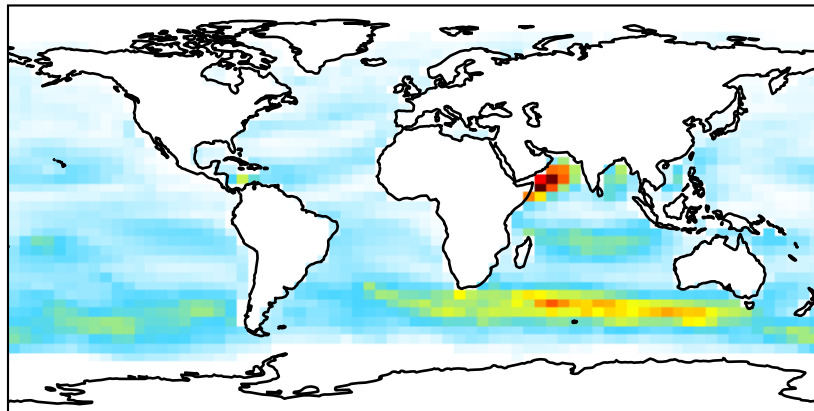


Ratio  
Dev/Ref, Fixed Range

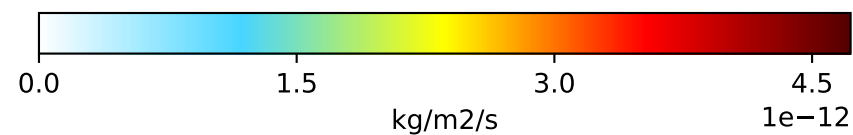
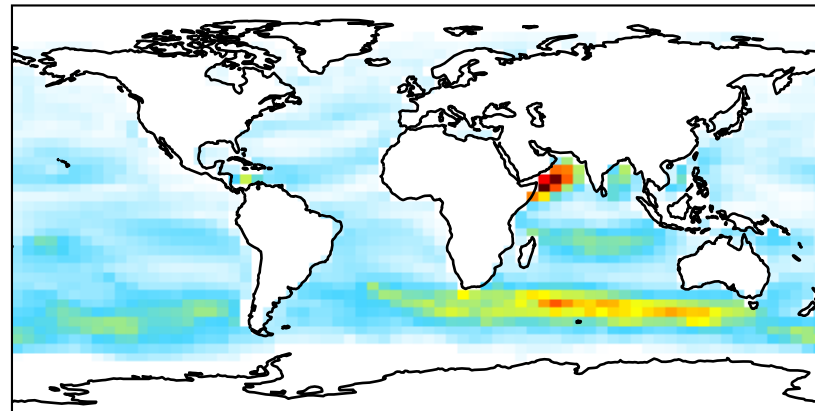


# EmisBrSALC\_Natural

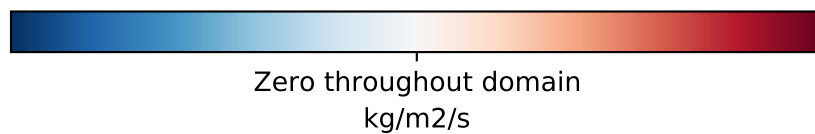
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



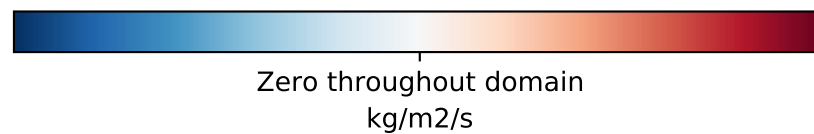
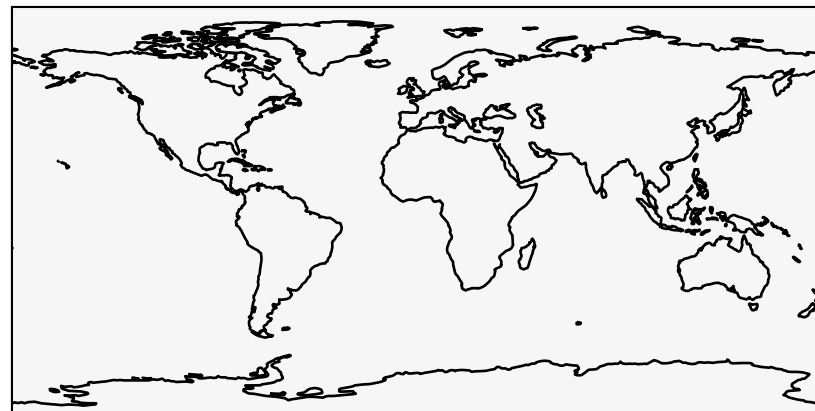
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



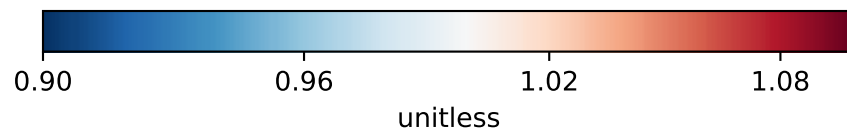
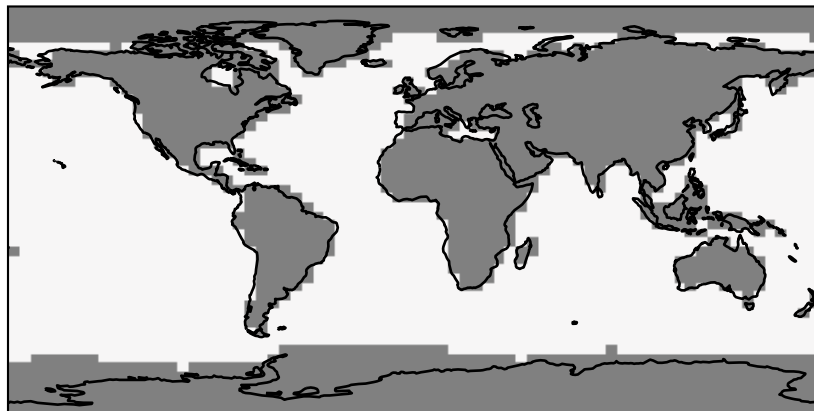
Difference  
Dev - Ref, Dynamic Range



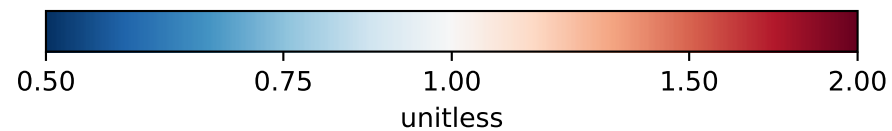
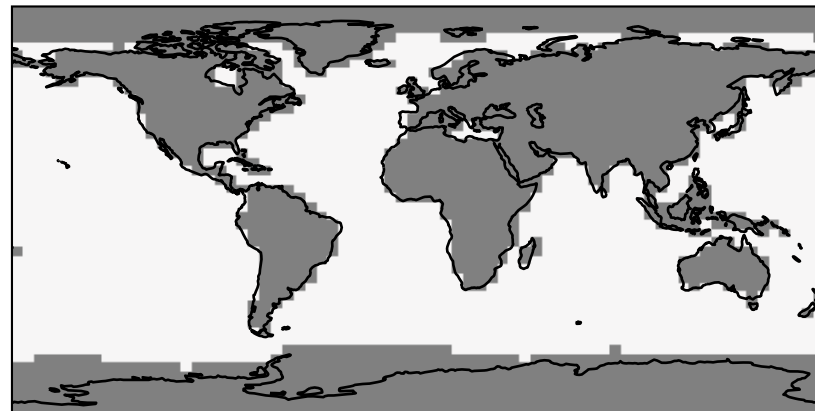
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



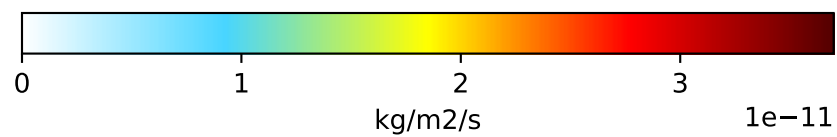
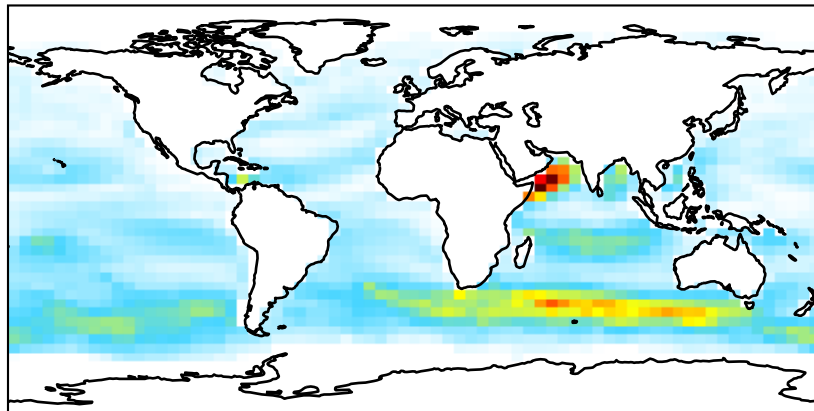
Ratio  
Dev/Ref, Fixed Range



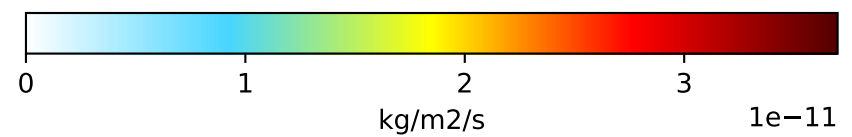
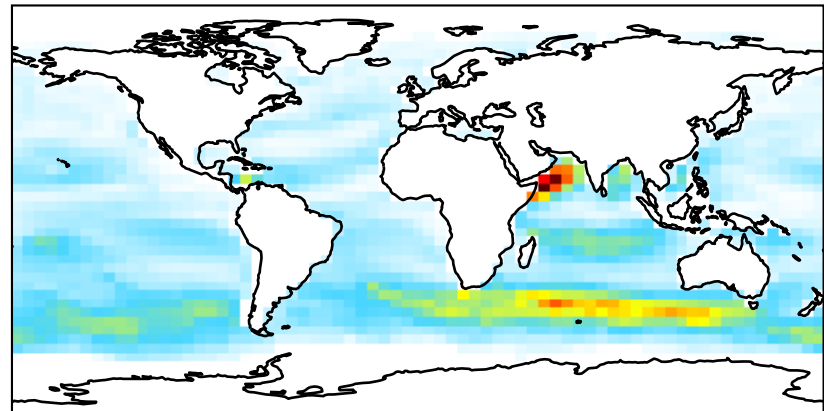


# EmisSALA\_Natural

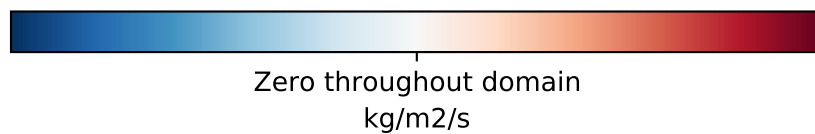
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



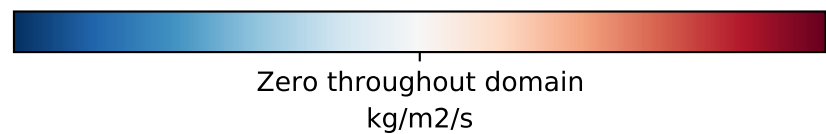
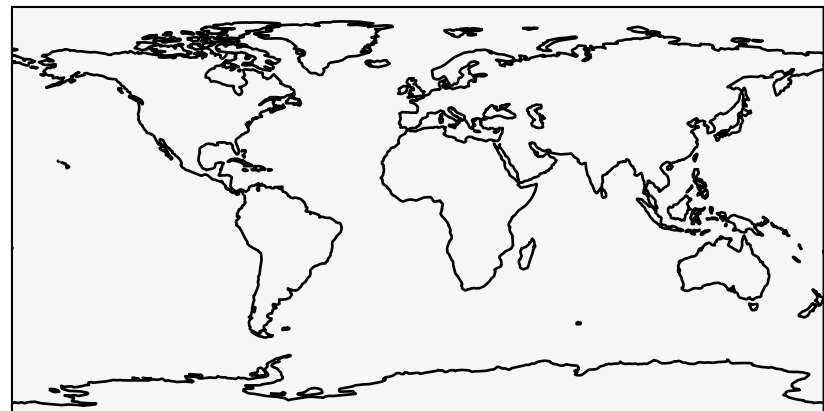
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



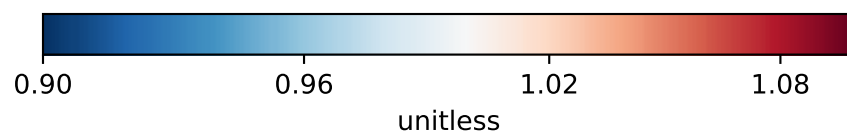
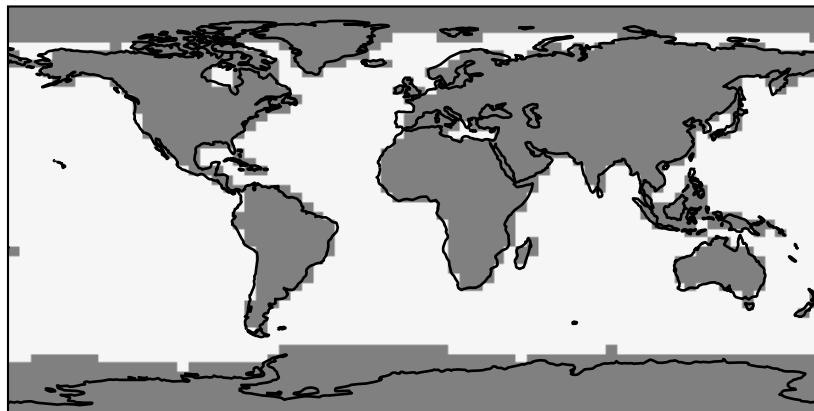
Difference  
Dev - Ref, Dynamic Range



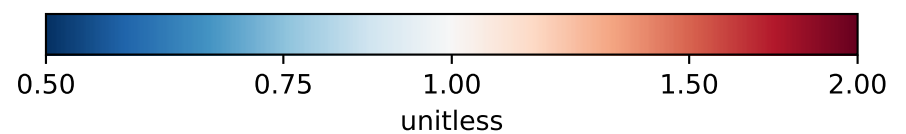
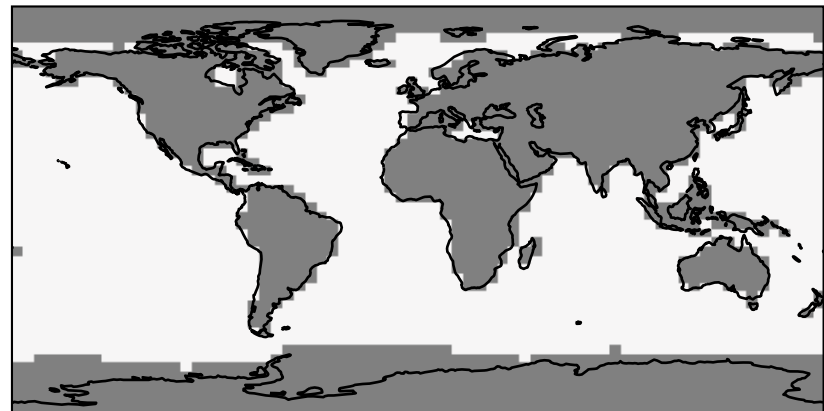
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

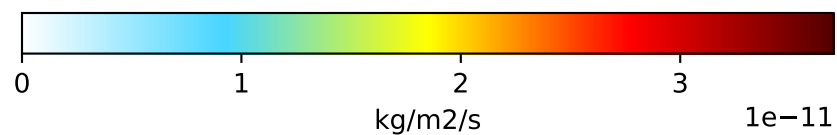
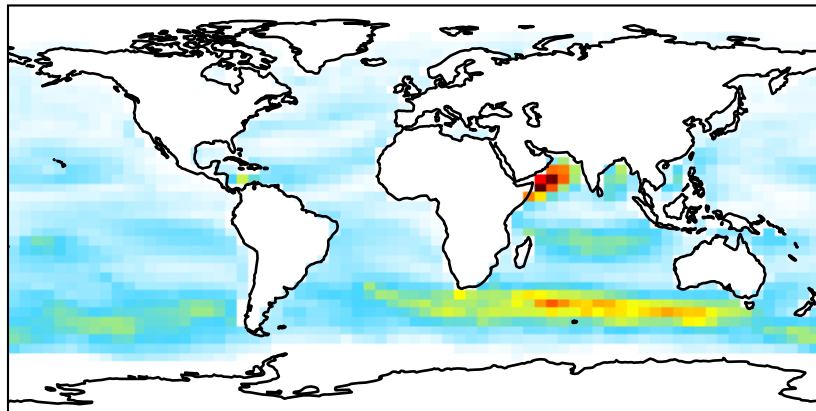


Ratio  
Dev/Ref, Fixed Range

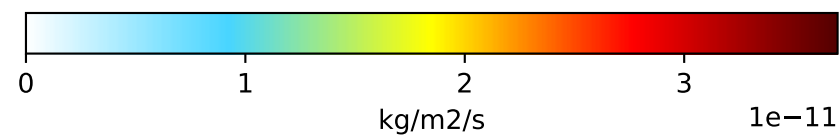
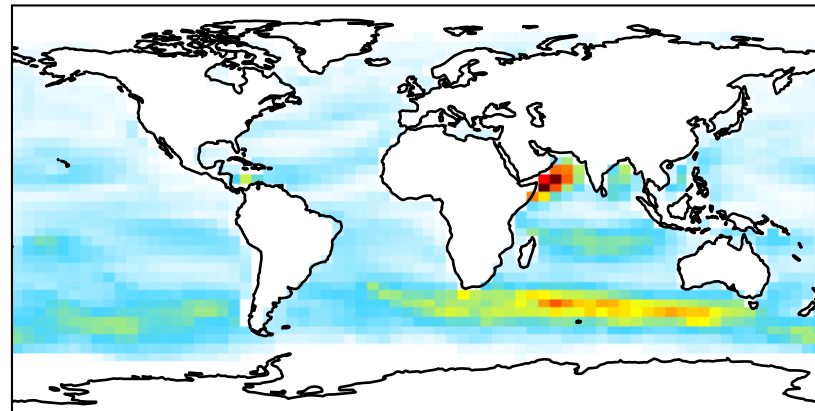


# EmisSALAAL\_Natural

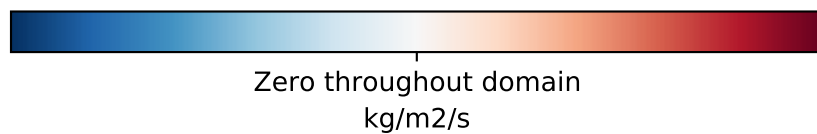
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



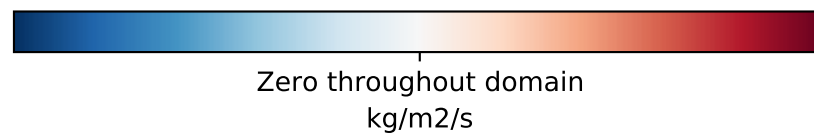
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



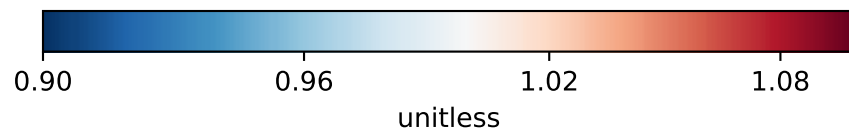
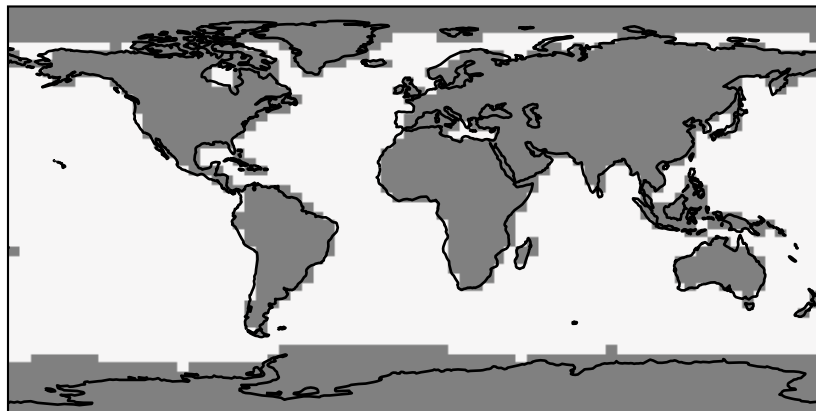
Difference  
Dev - Ref, Dynamic Range



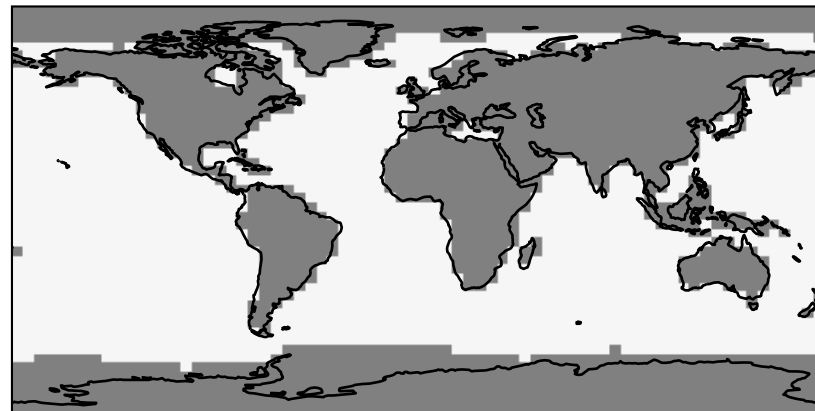
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

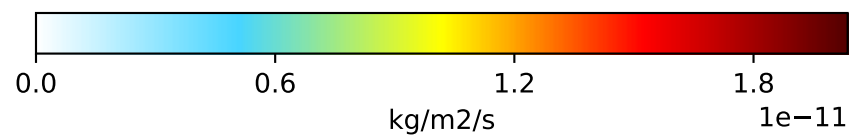
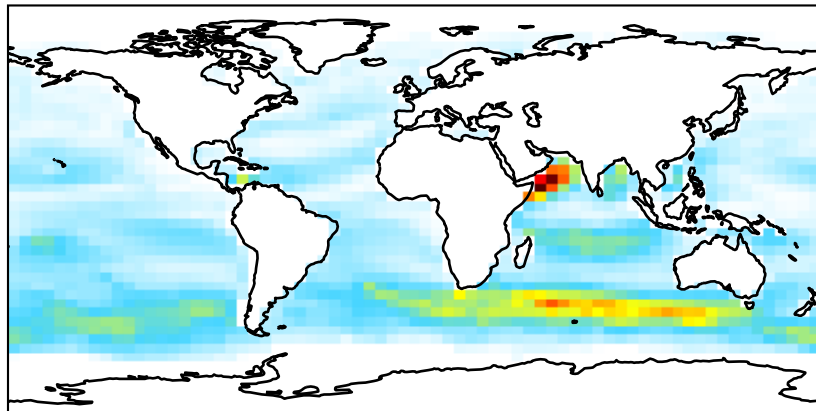


Ratio  
Dev/Ref, Fixed Range

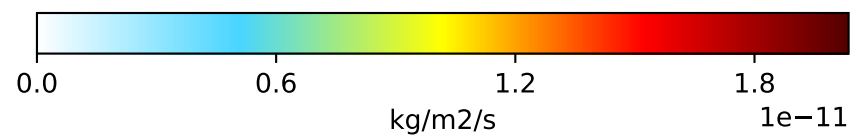
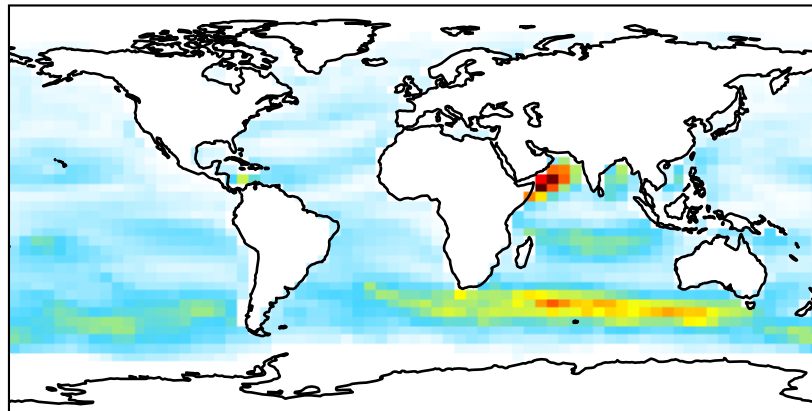


# EmisSALACL\_Natural

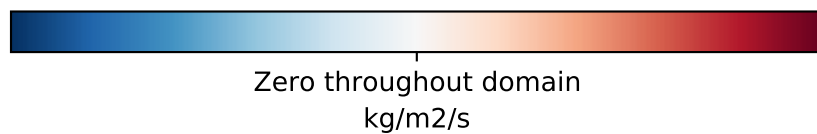
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



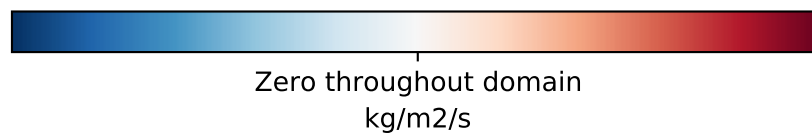
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



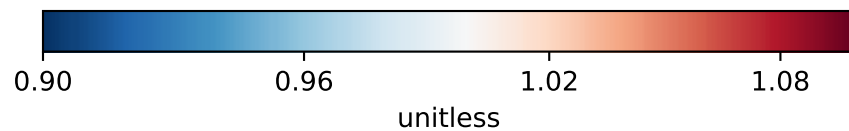
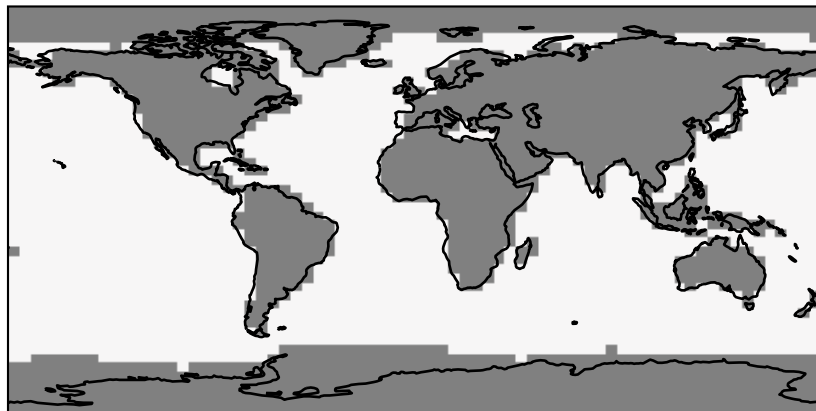
Difference  
Dev - Ref, Dynamic Range



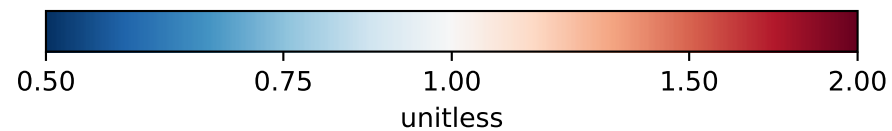
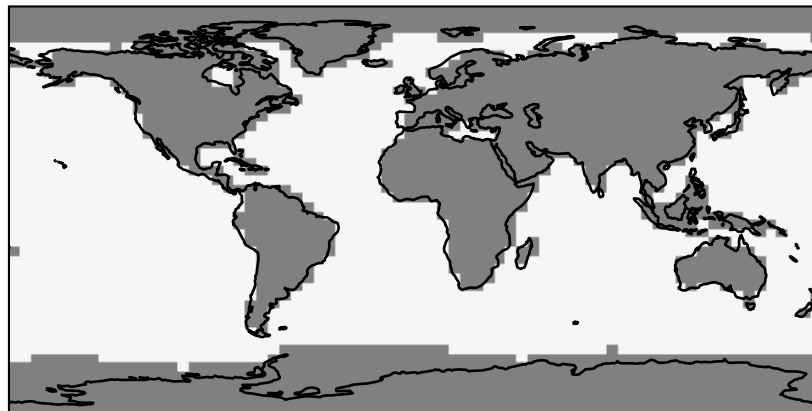
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

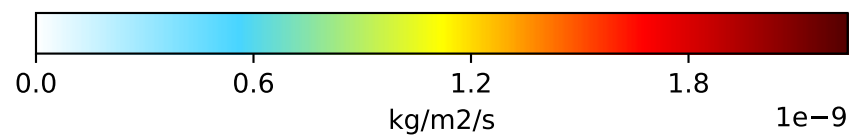
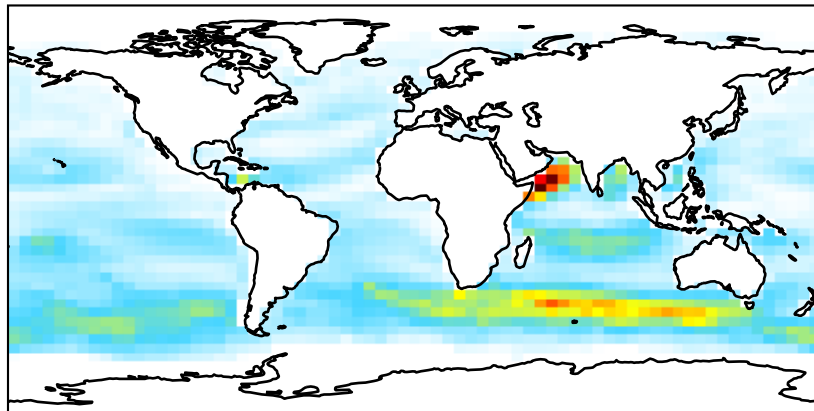


Ratio  
Dev/Ref, Fixed Range

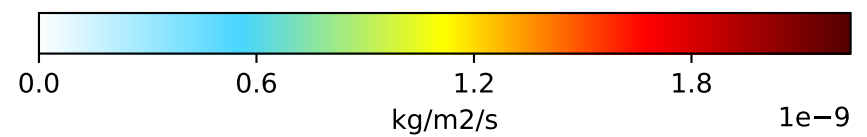
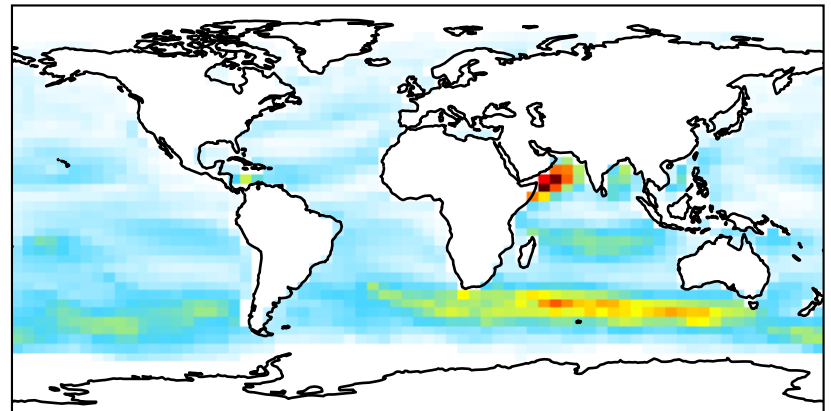


# EmisSALC\_Natural

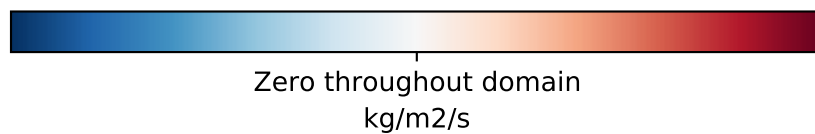
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



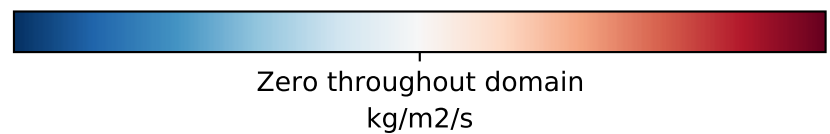
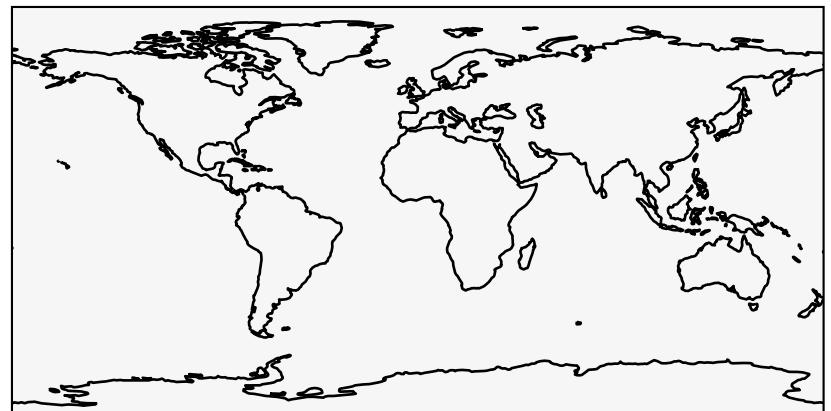
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



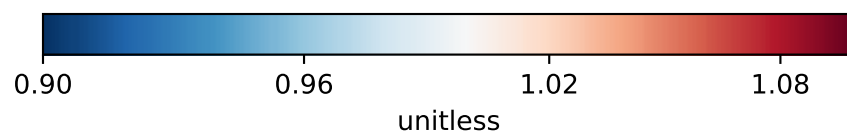
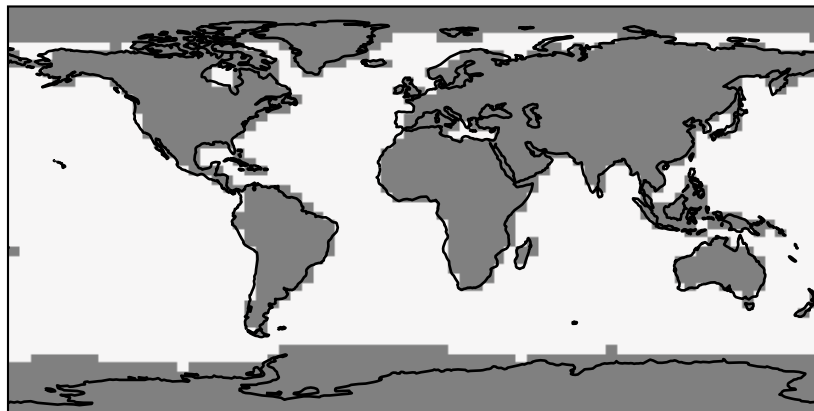
Difference  
Dev - Ref, Dynamic Range



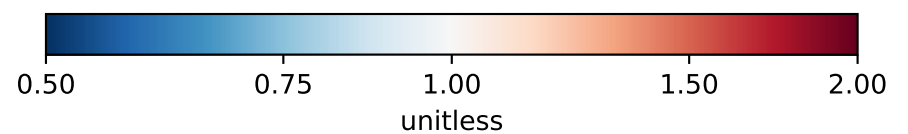
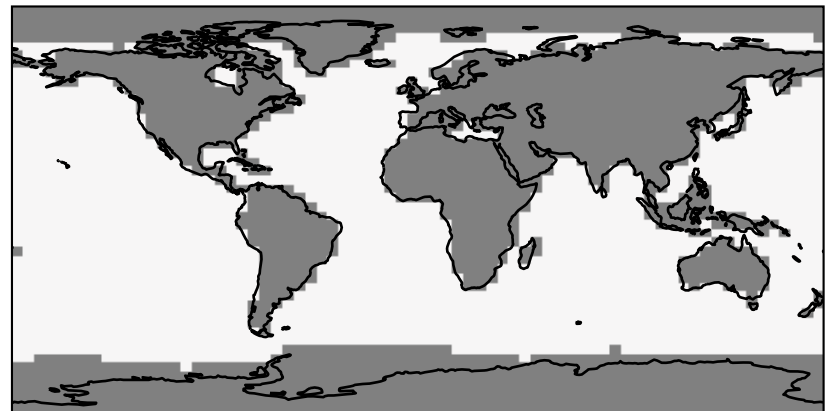
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

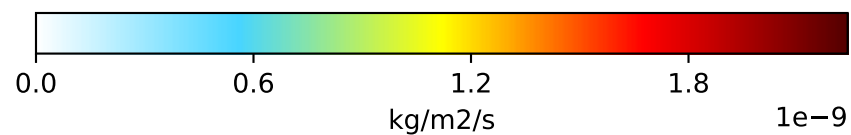
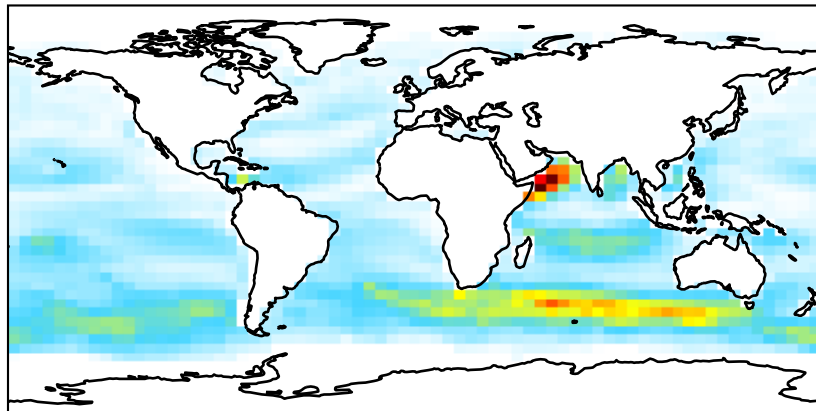


Ratio  
Dev/Ref, Fixed Range

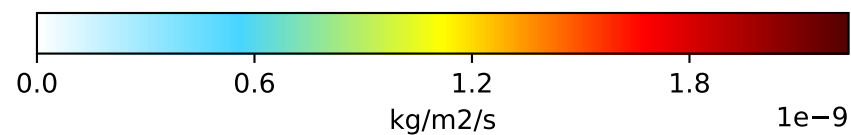
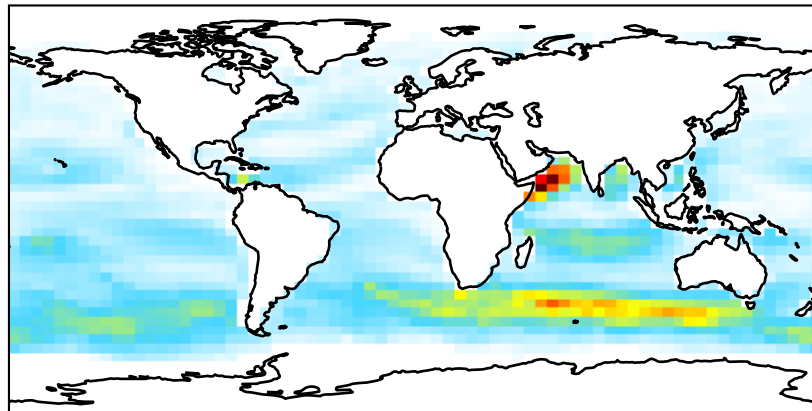


# EmisSALCAL\_Natural

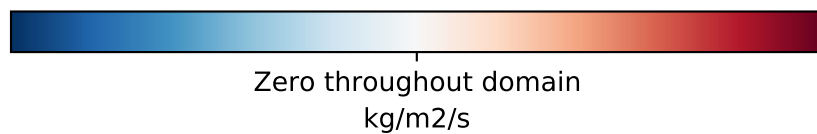
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



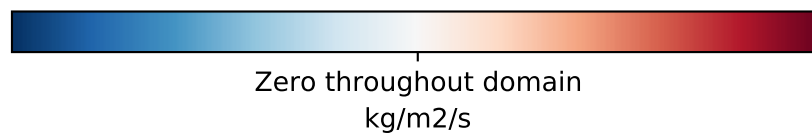
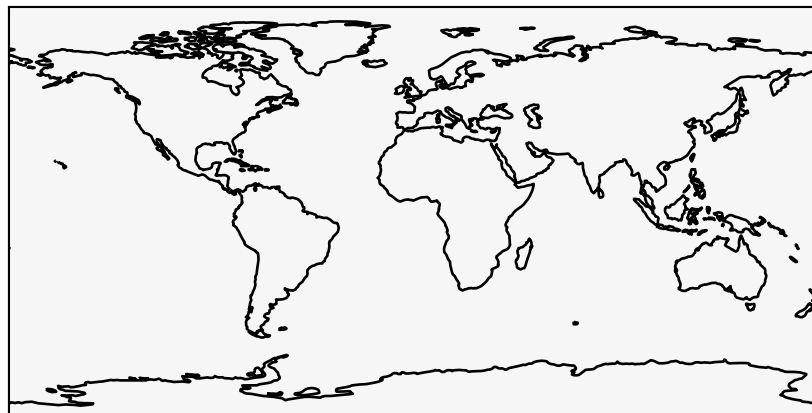
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



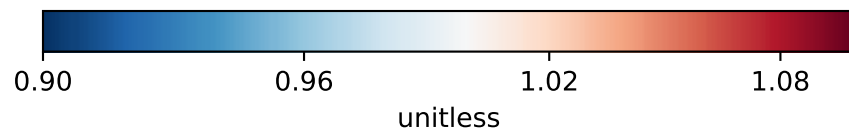
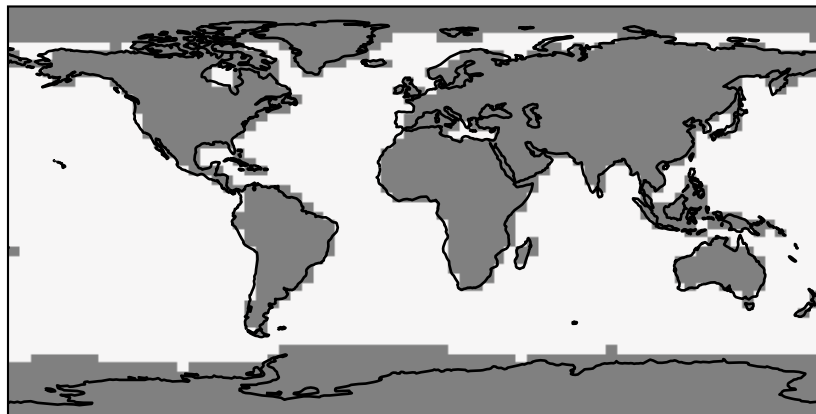
Difference  
Dev - Ref, Dynamic Range



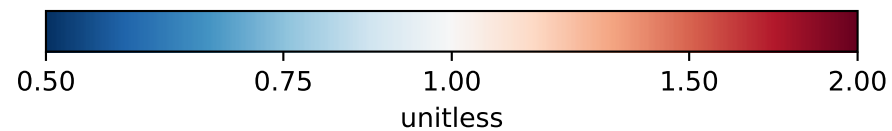
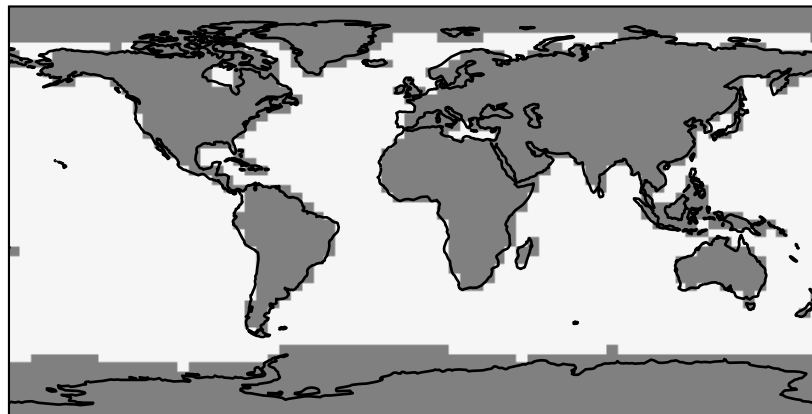
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



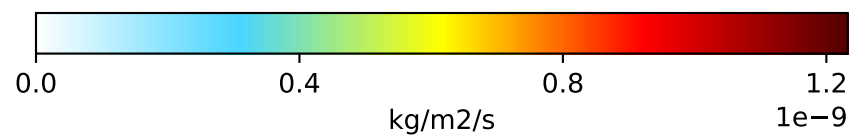
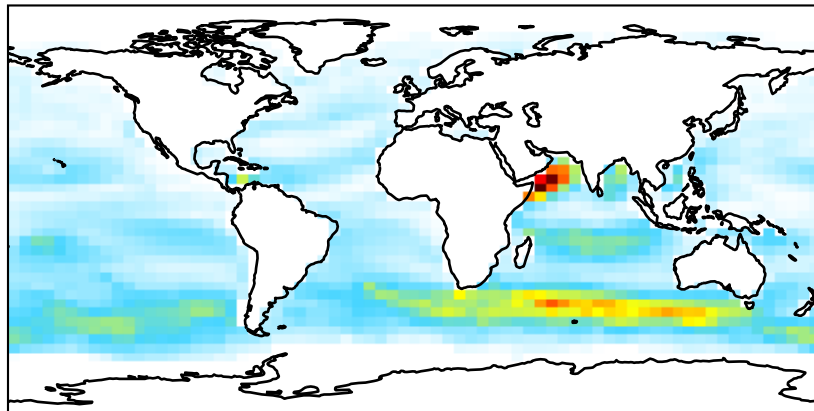
Ratio  
Dev/Ref, Fixed Range



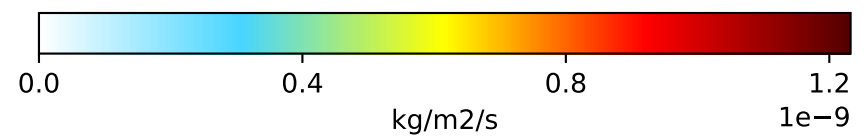
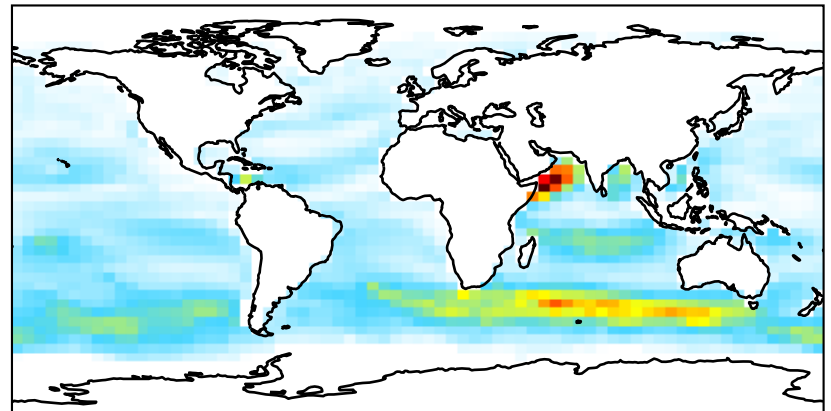


# EmisSALCCL\_Natural

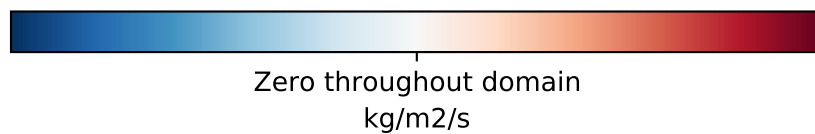
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



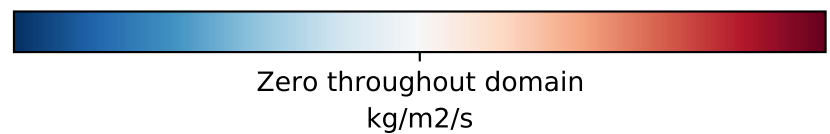
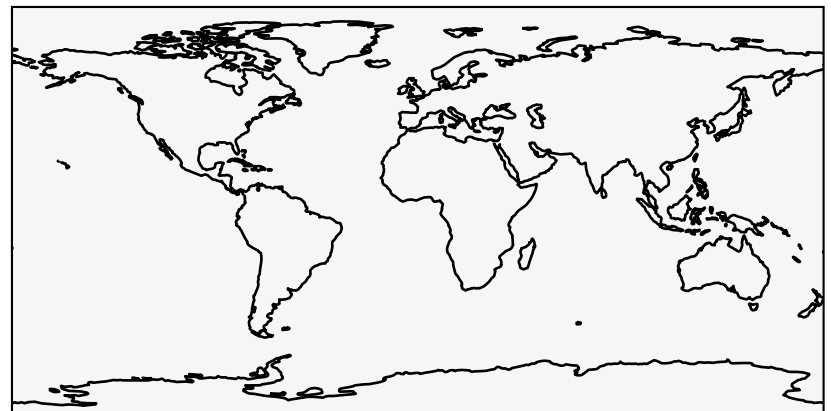
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



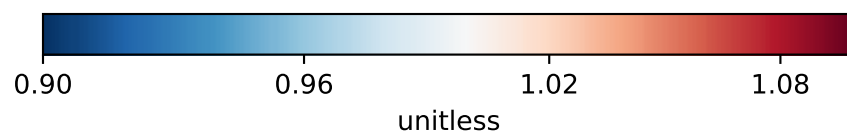
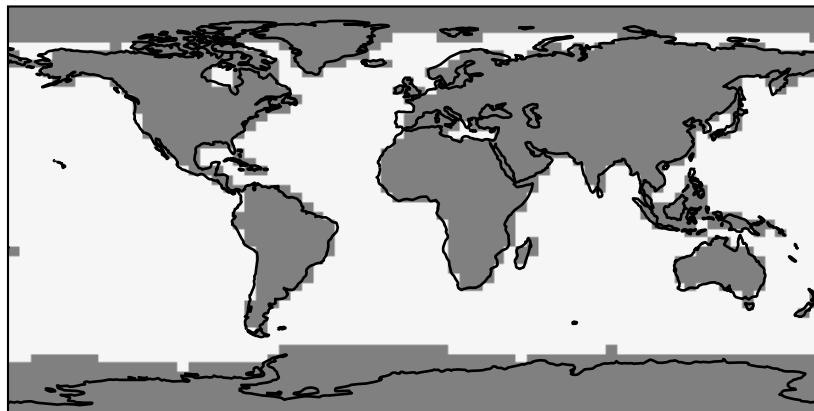
Difference  
Dev - Ref, Dynamic Range



Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

