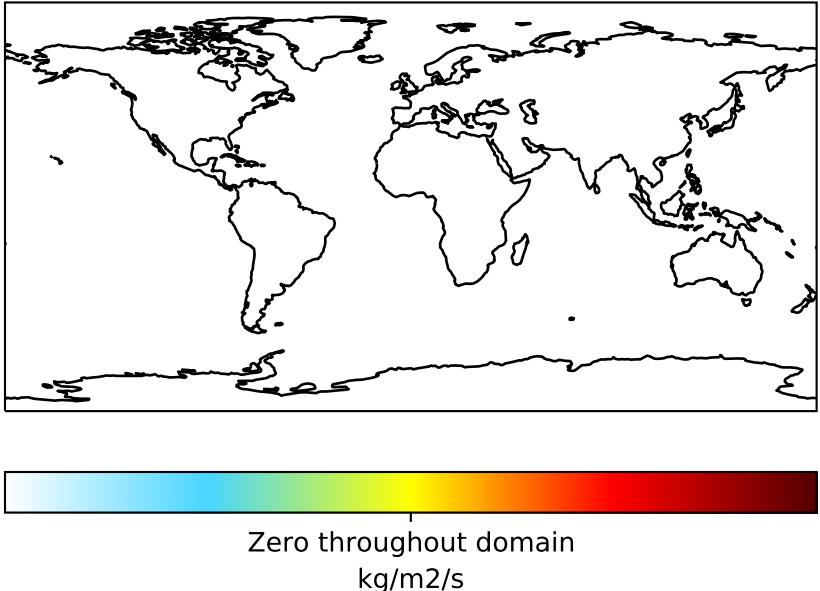
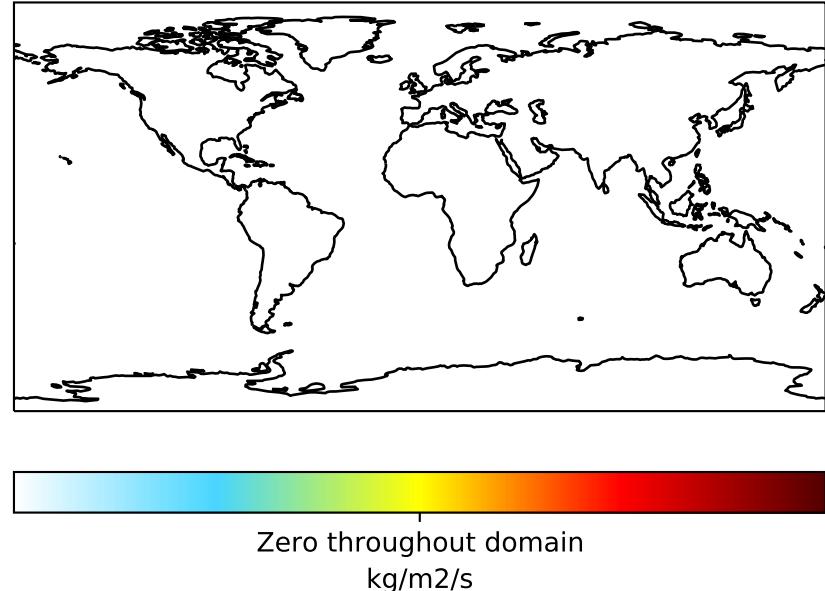


# EmisALD2\_Ship

GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



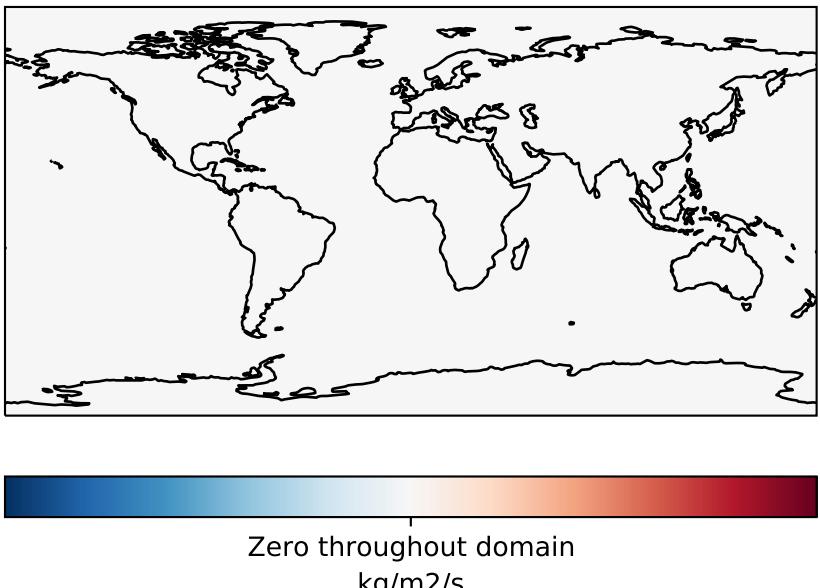
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



Zero throughout domain  
kg/m<sup>2</sup>/s

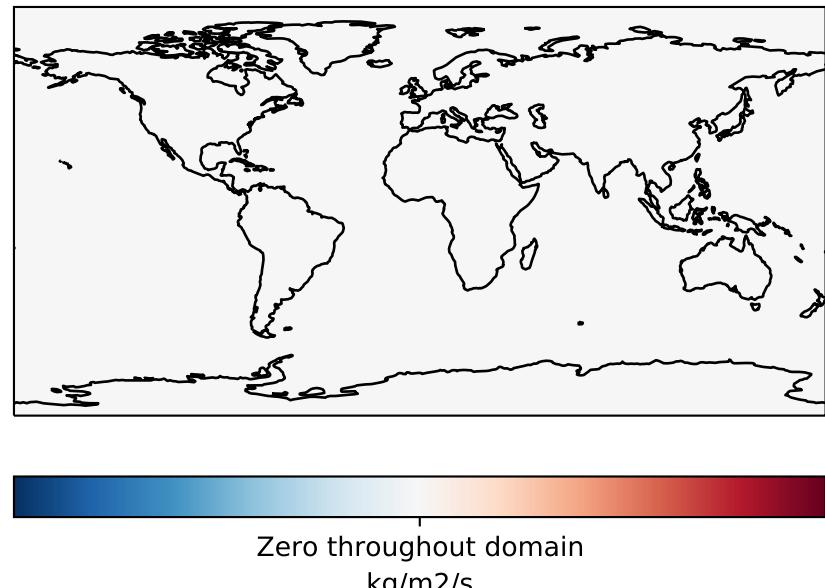
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Dynamic Range



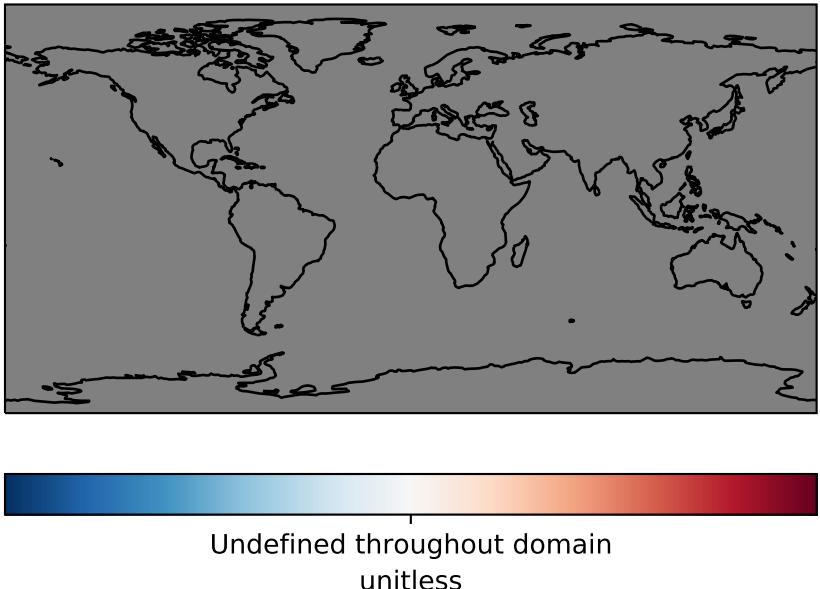
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



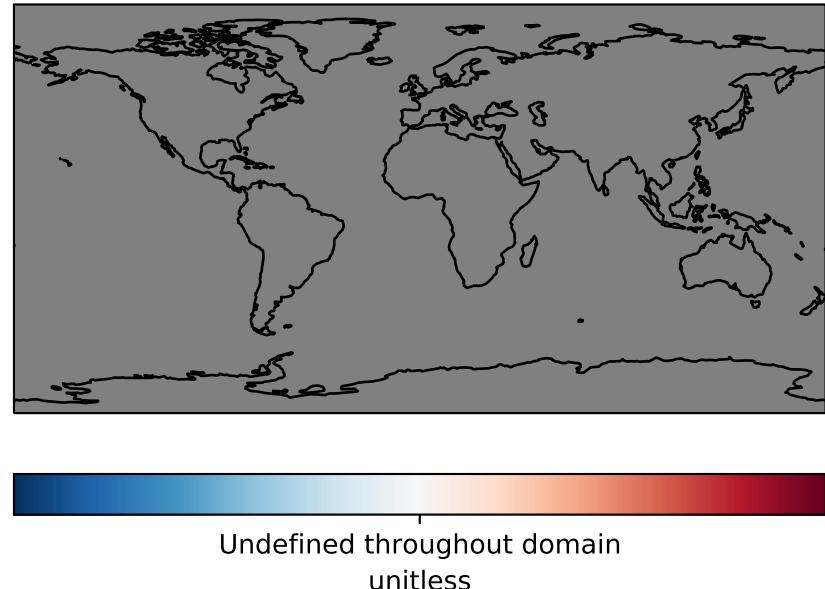
Zero throughout domain  
kg/m<sup>2</sup>/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

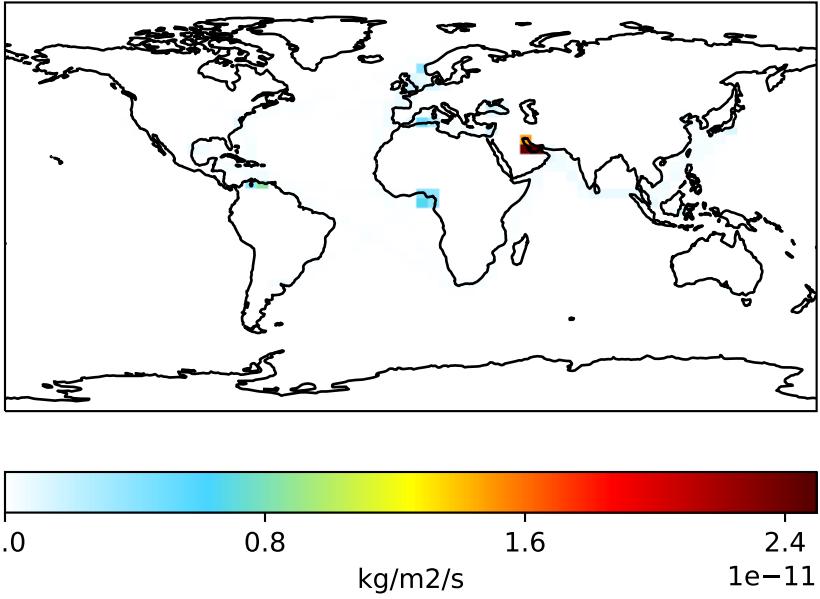
Ratio  
Dev/Ref, Fixed Range



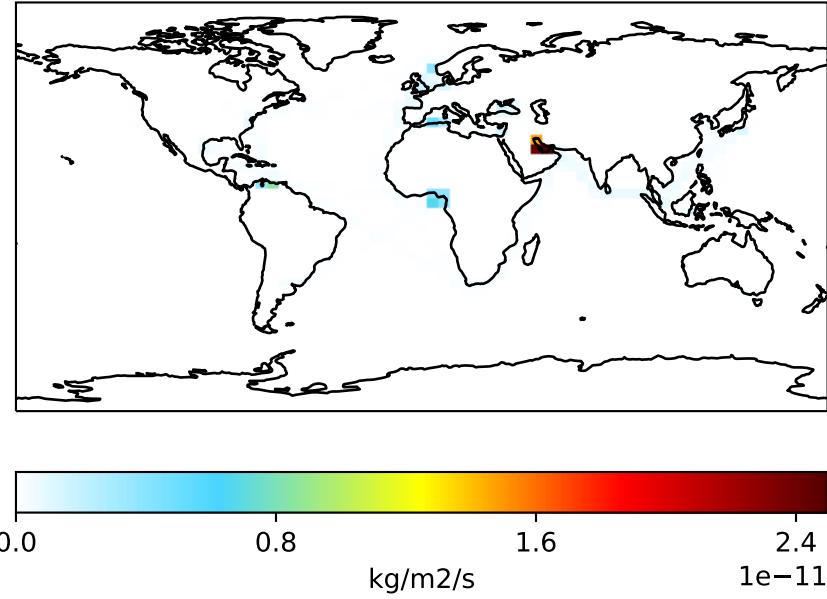
Undefined throughout domain  
unitless

# EmisALK4\_Ship

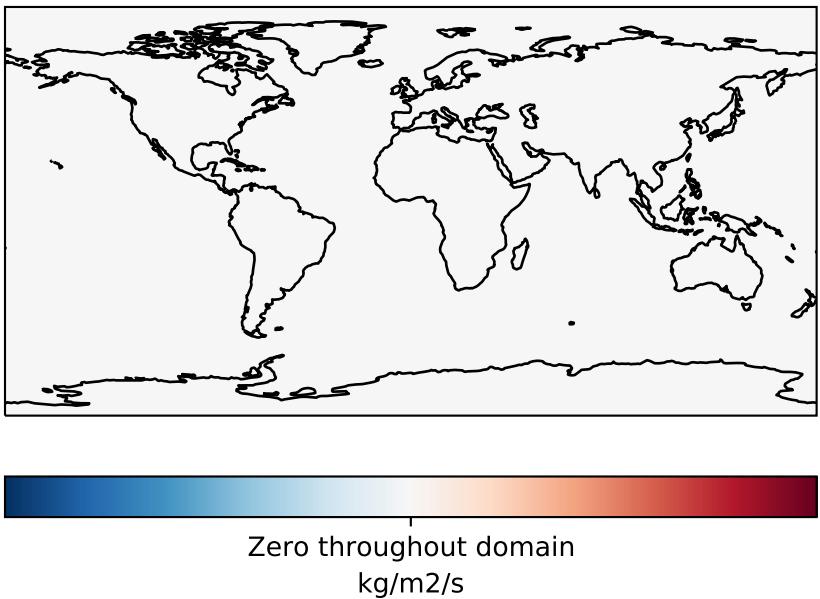
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



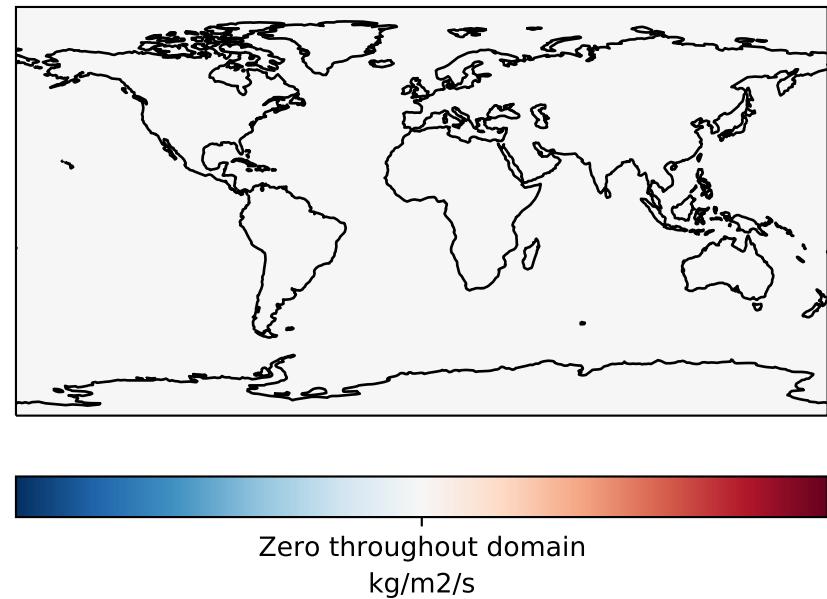
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



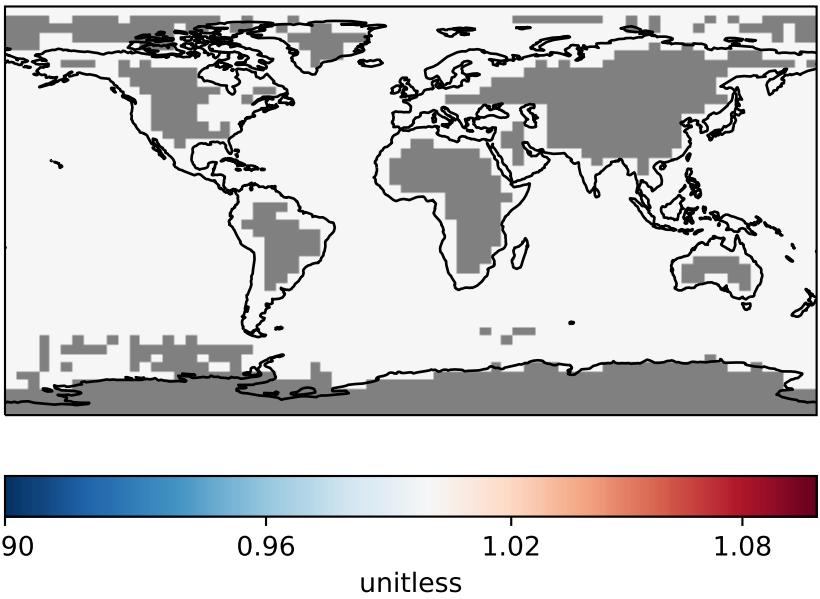
Difference  
Dev - Ref, Dynamic Range



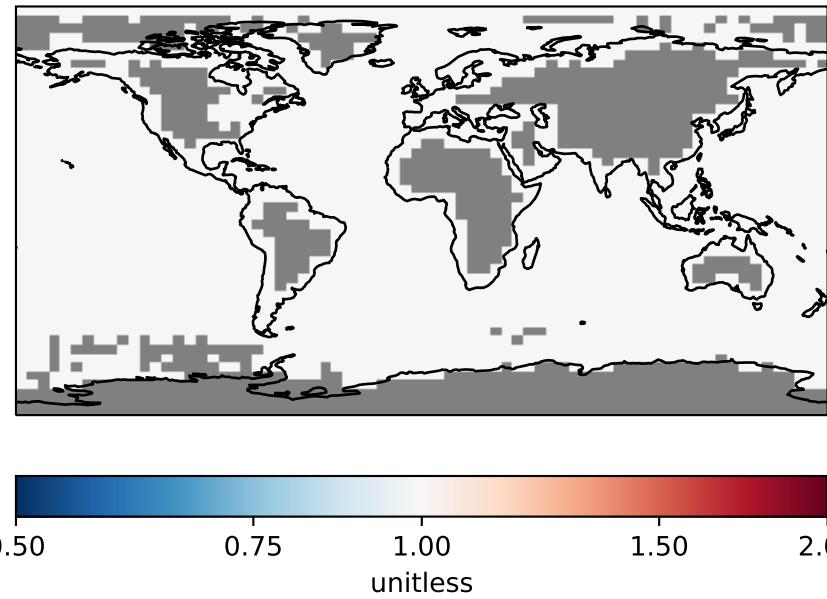
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

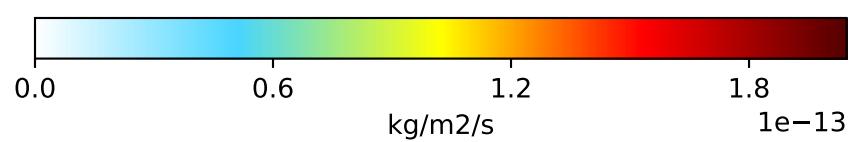
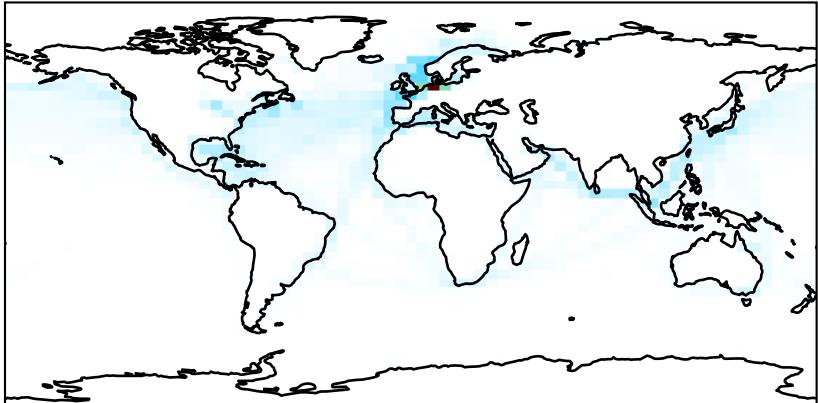


Ratio  
Dev/Ref, Fixed Range

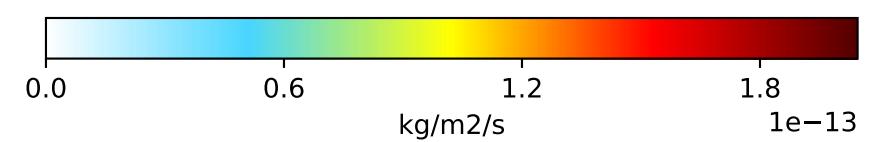
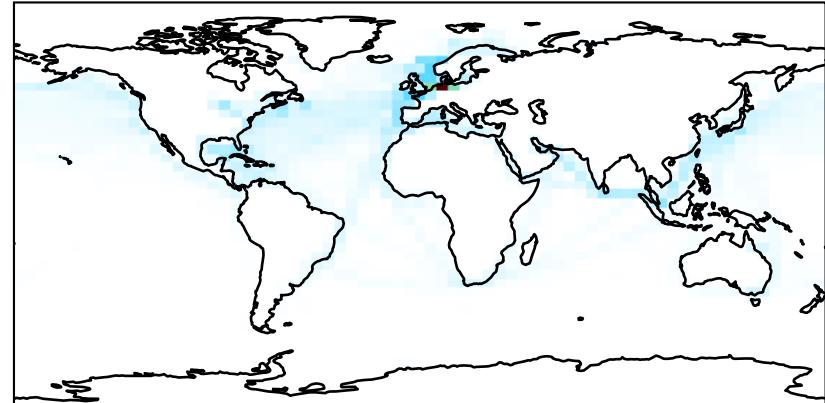


# EmisBCPI\_Ship

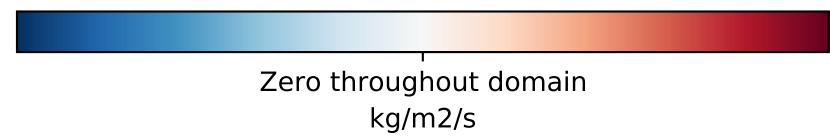
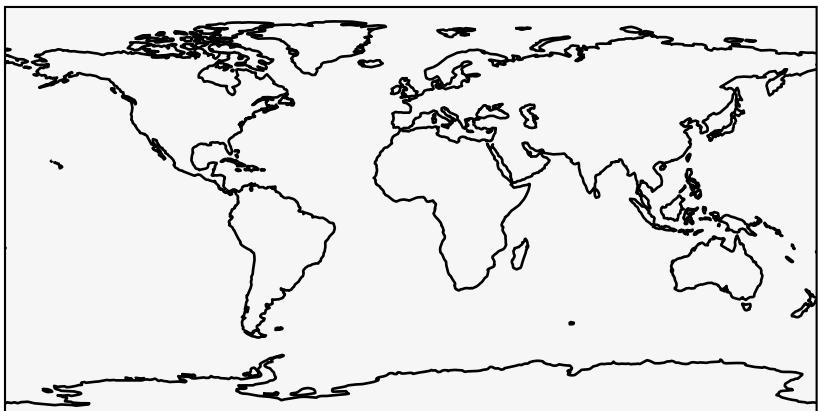
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



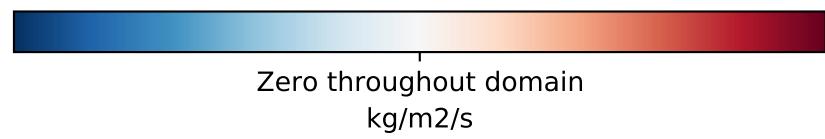
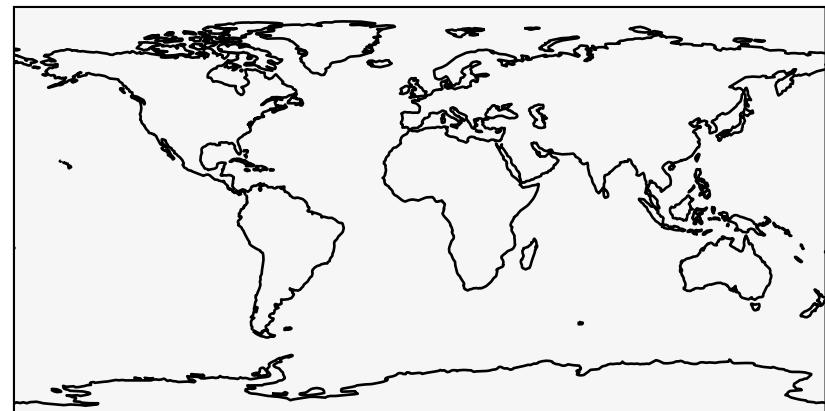
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



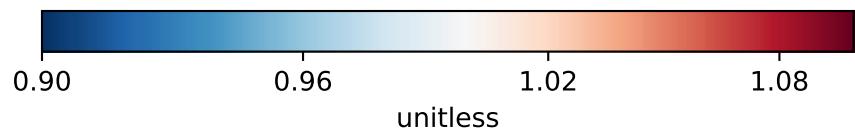
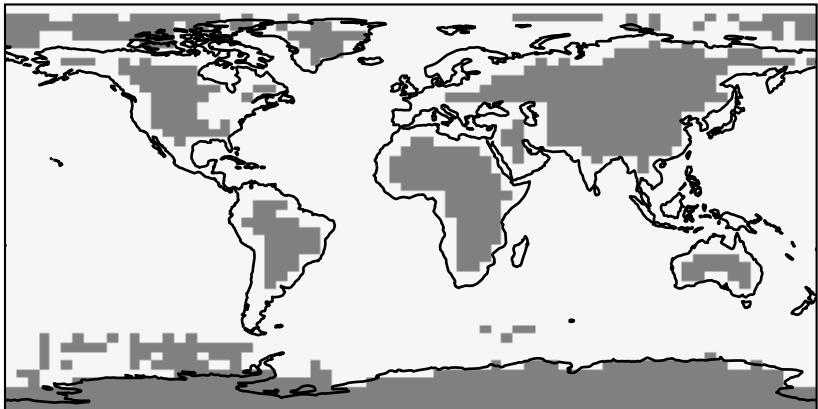
Difference  
Dev - Ref, Dynamic Range



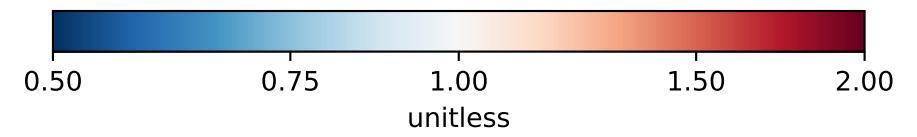
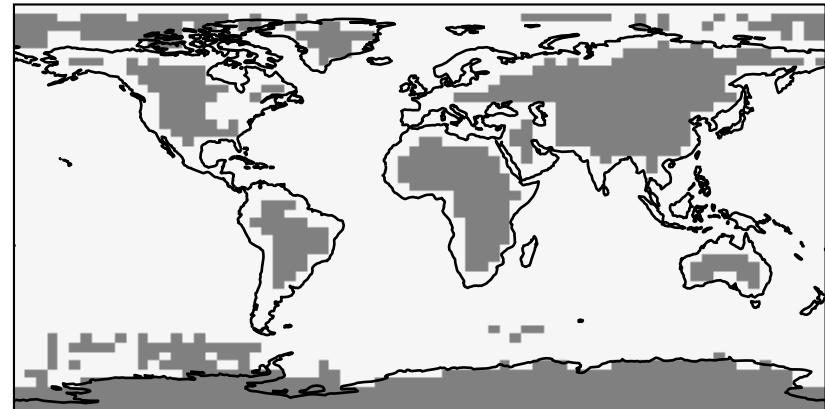
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

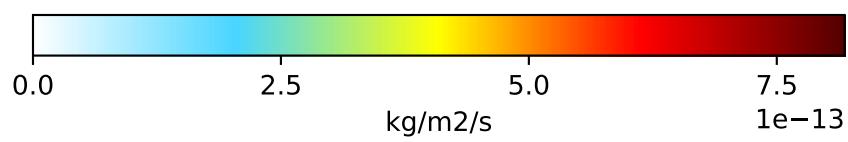
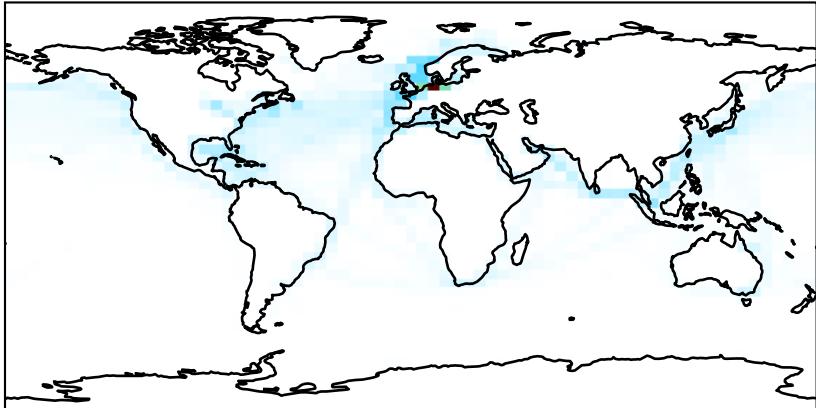


Ratio  
Dev/Ref, Fixed Range

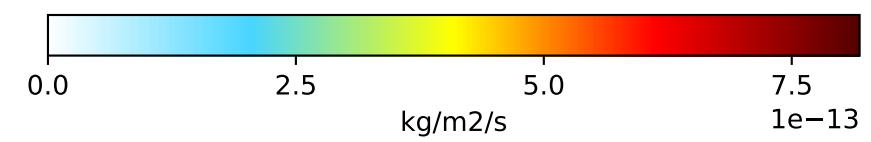
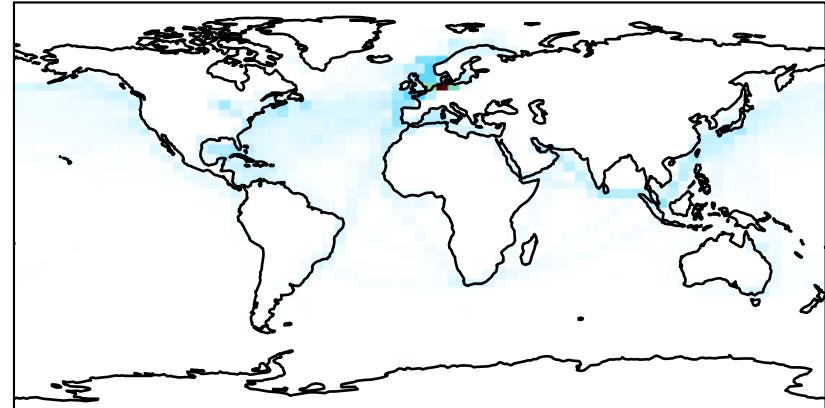


# EmisBCPO\_Ship

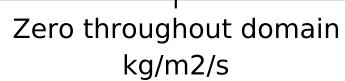
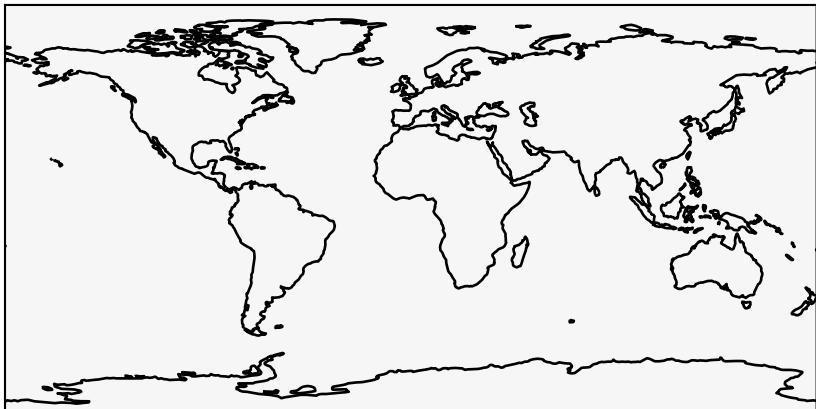
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



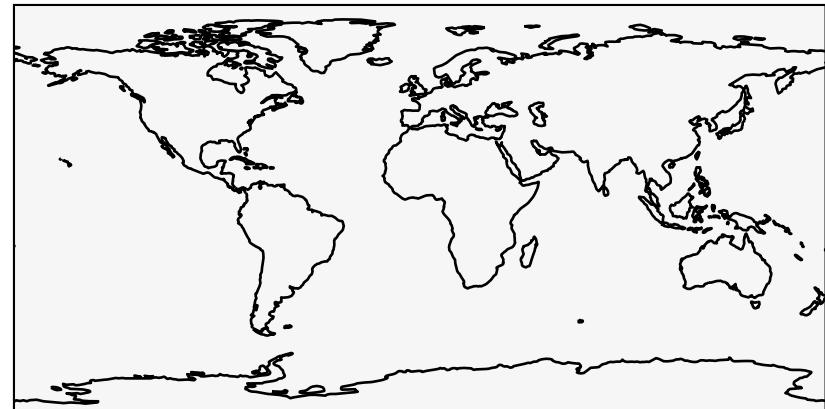
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



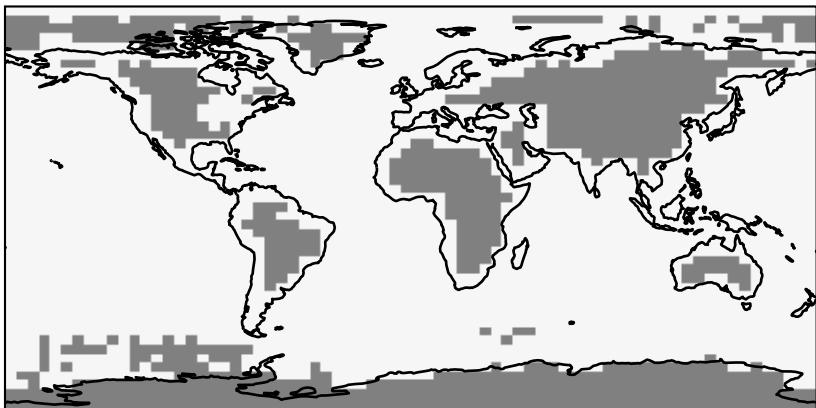
Difference  
Dev - Ref, Dynamic Range



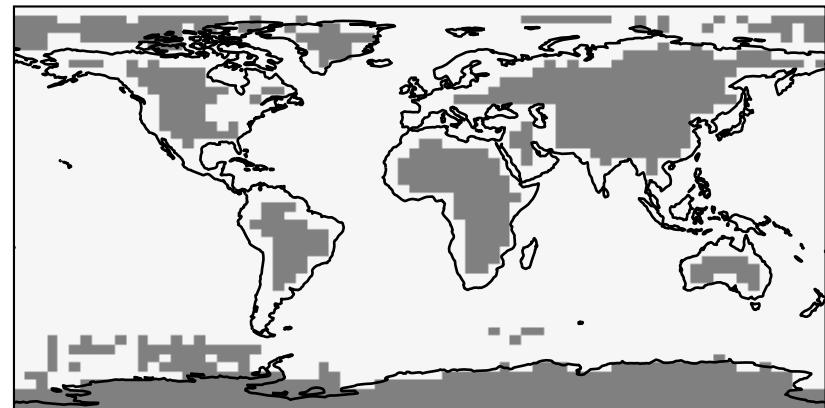
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

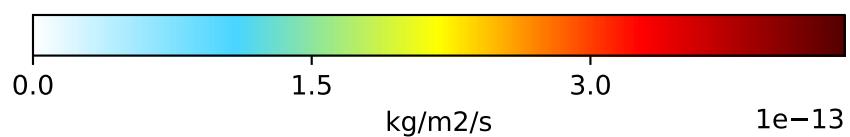
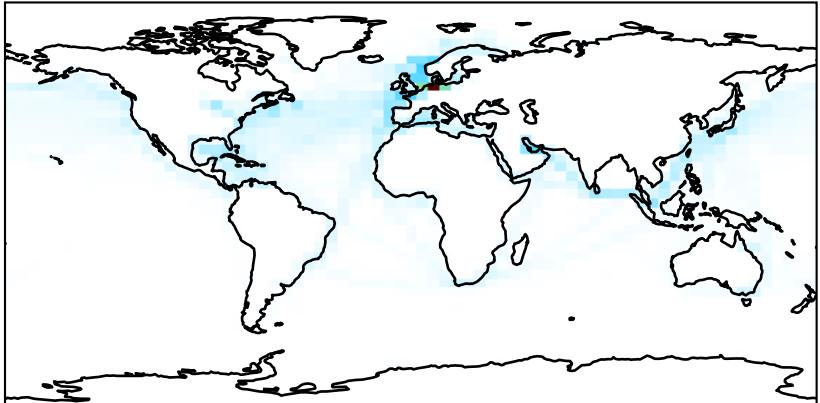


Ratio  
Dev/Ref, Fixed Range

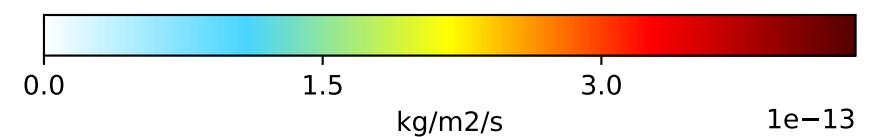
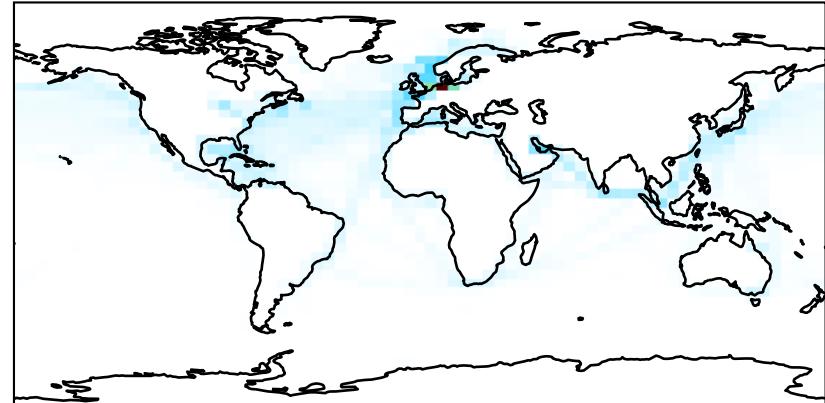


# EmisBENZ\_Ship

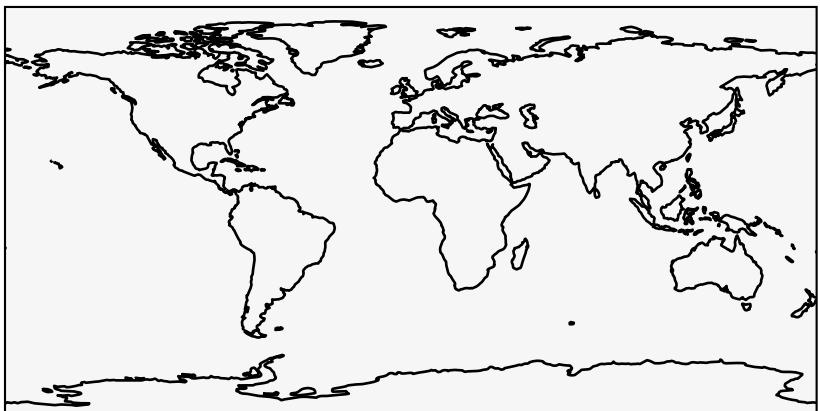
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0

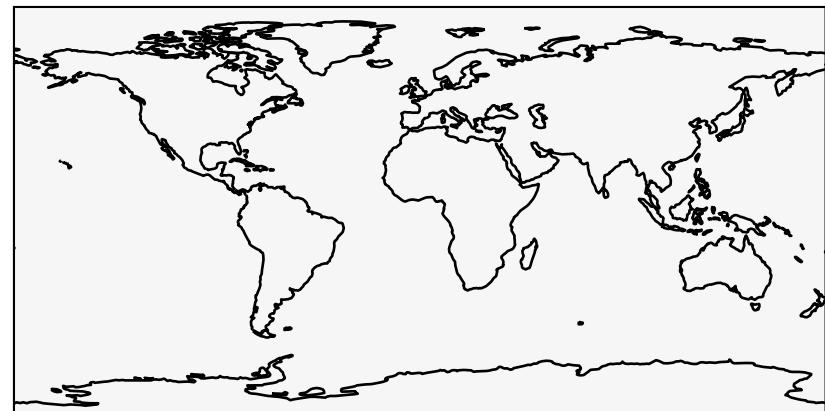


Difference  
Dev - Ref, Dynamic Range



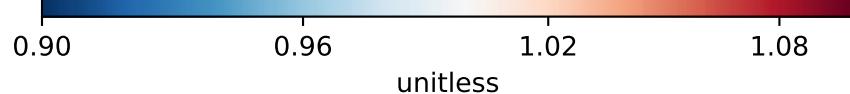
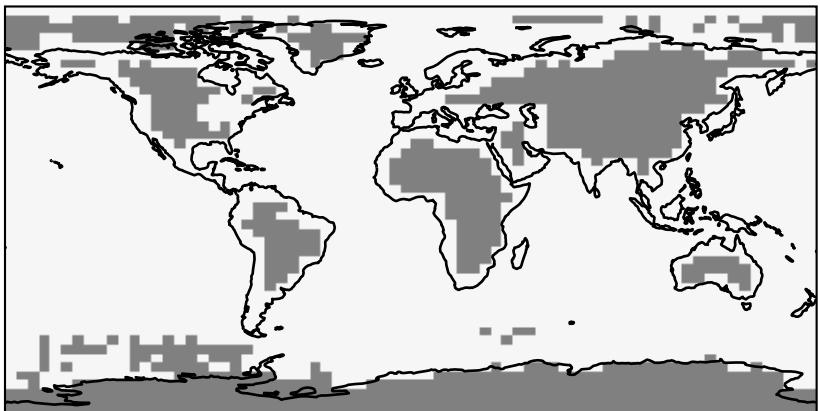
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%,95%]

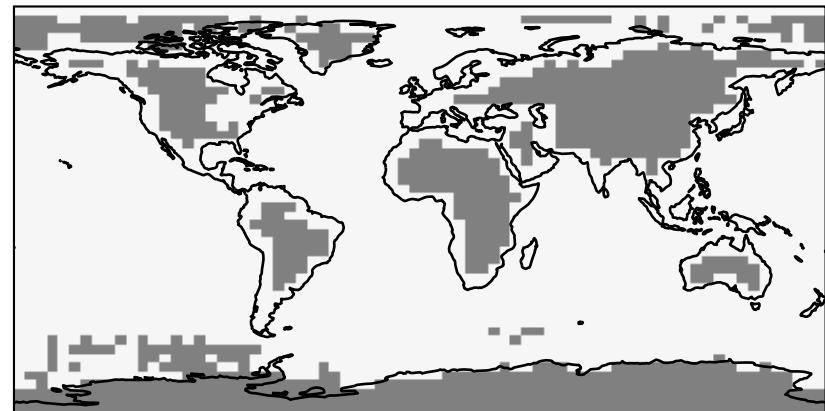


Zero throughout domain  
kg/m<sup>2</sup>/s

Ratio  
Dev/Ref, Dynamic Range

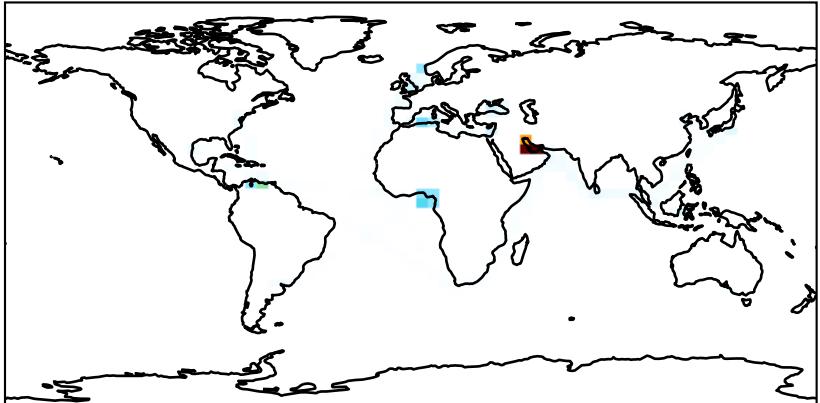


Ratio  
Dev/Ref, Fixed Range

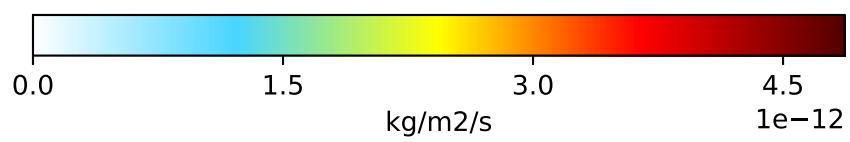
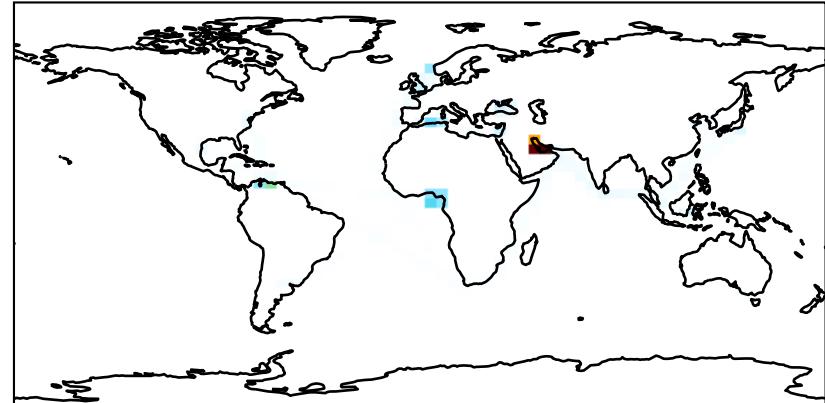


# EmisC2H6\_Ship

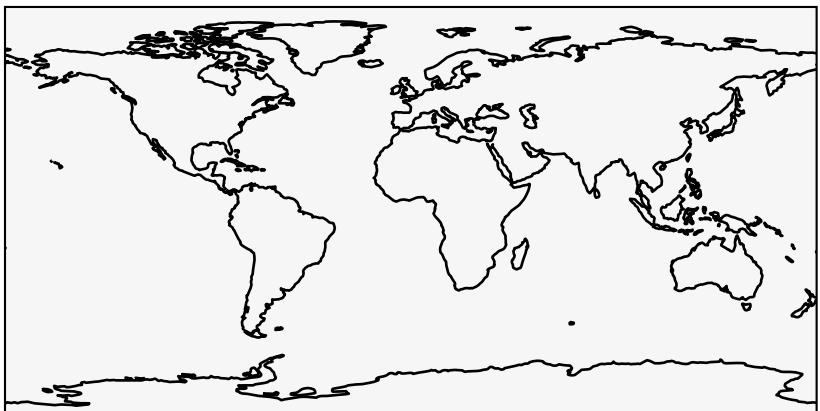
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0

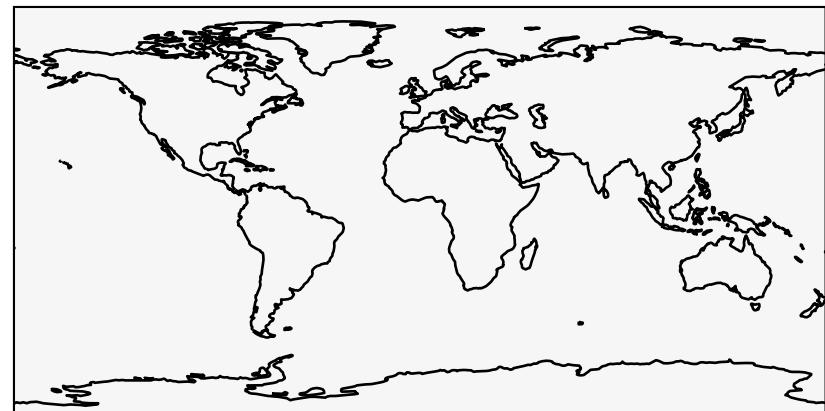


Difference  
Dev - Ref, Dynamic Range



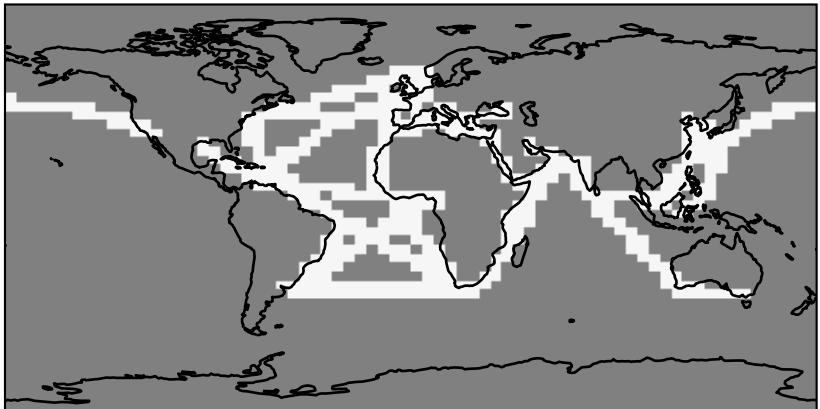
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



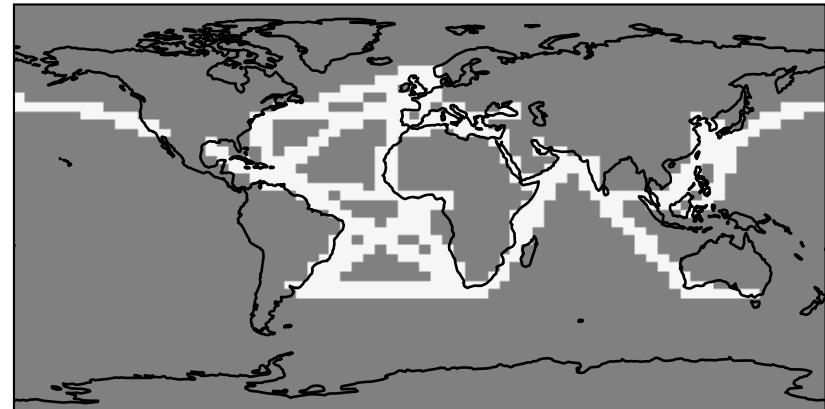
Zero throughout domain  
kg/m<sup>2</sup>/s

Ratio  
Dev/Ref, Dynamic Range



0.90 0.96 1.02 1.08  
unitless

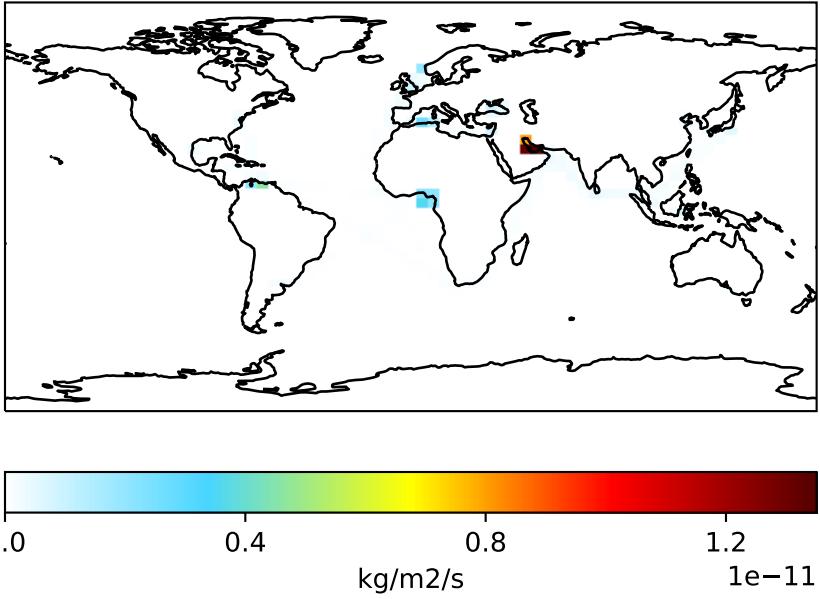
Ratio  
Dev/Ref, Fixed Range



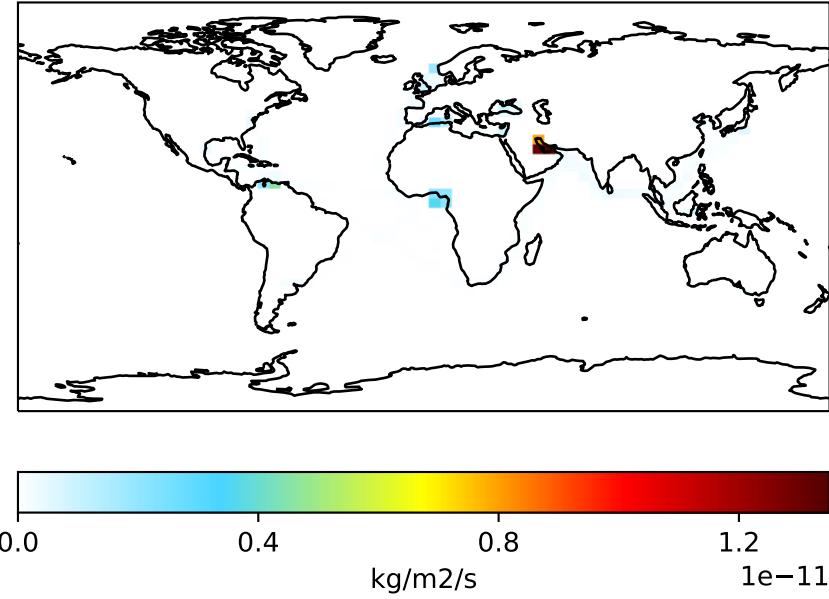
0.50 0.75 1.00 1.50 2.00  
unitless

# EmisC3H8\_Ship

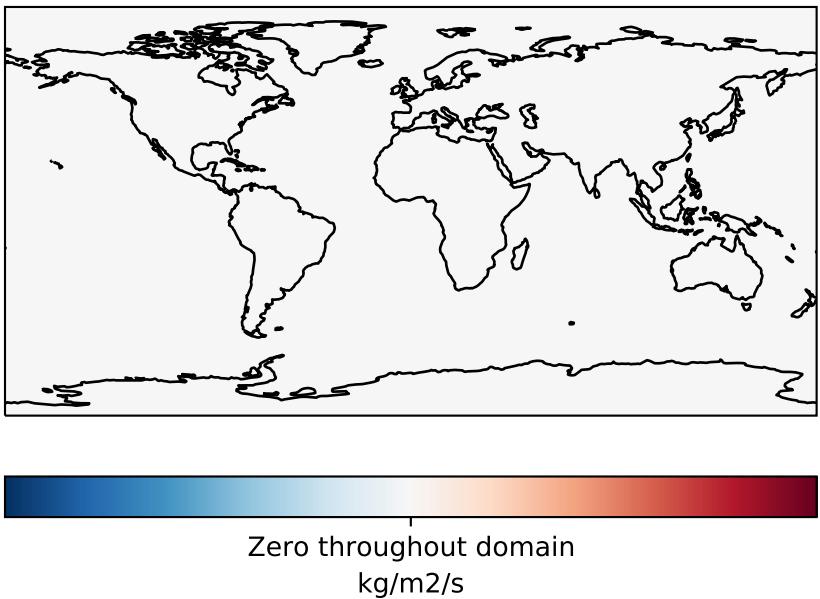
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



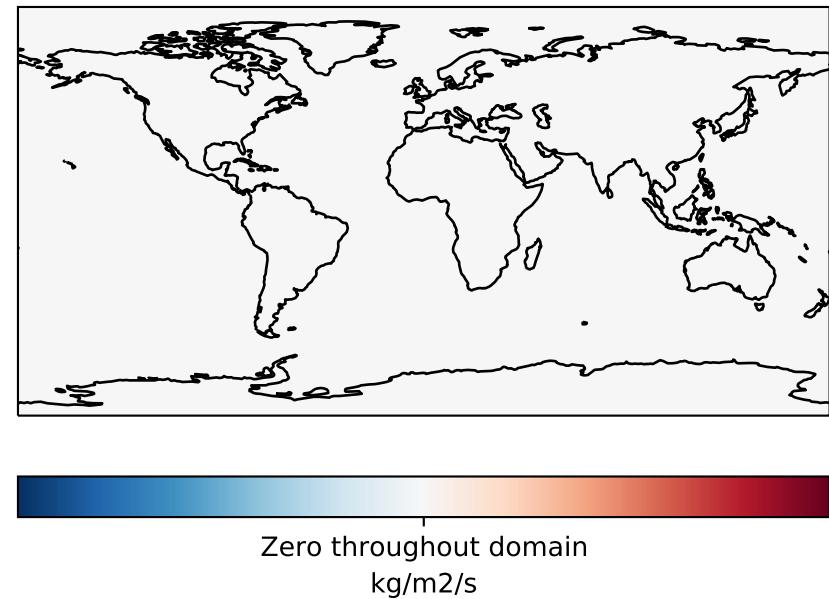
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



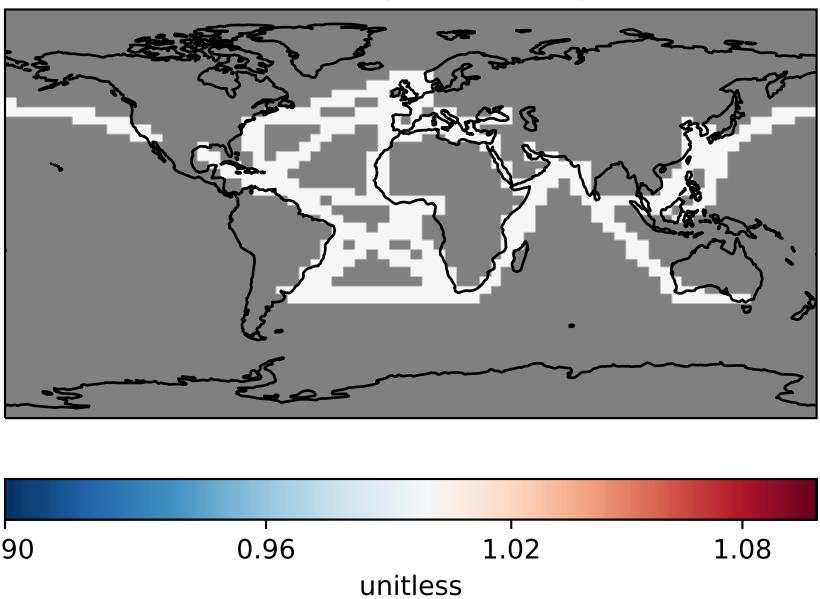
Difference  
Dev - Ref, Dynamic Range



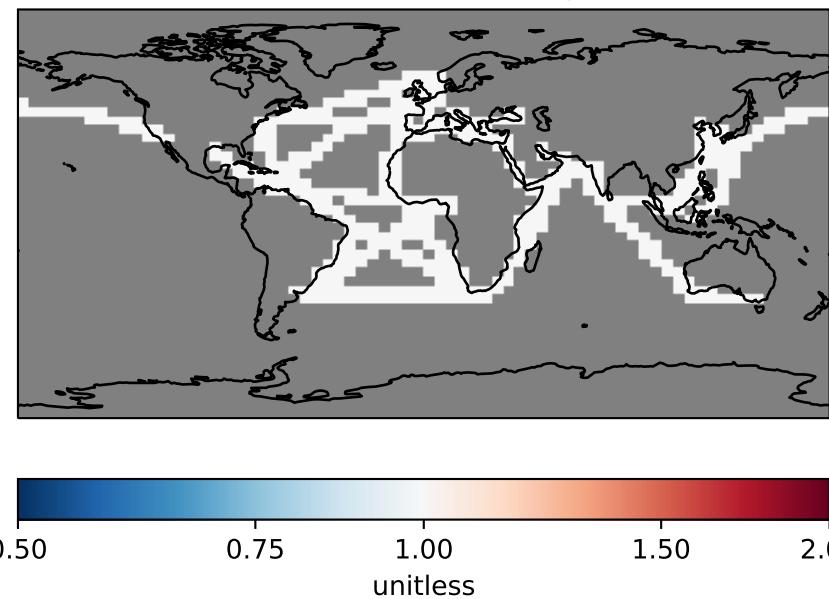
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

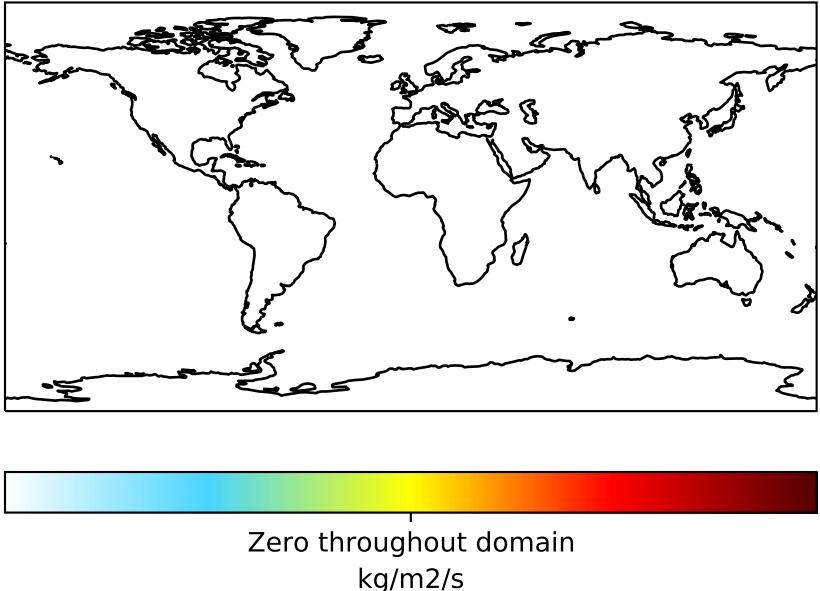


Ratio  
Dev/Ref, Fixed Range

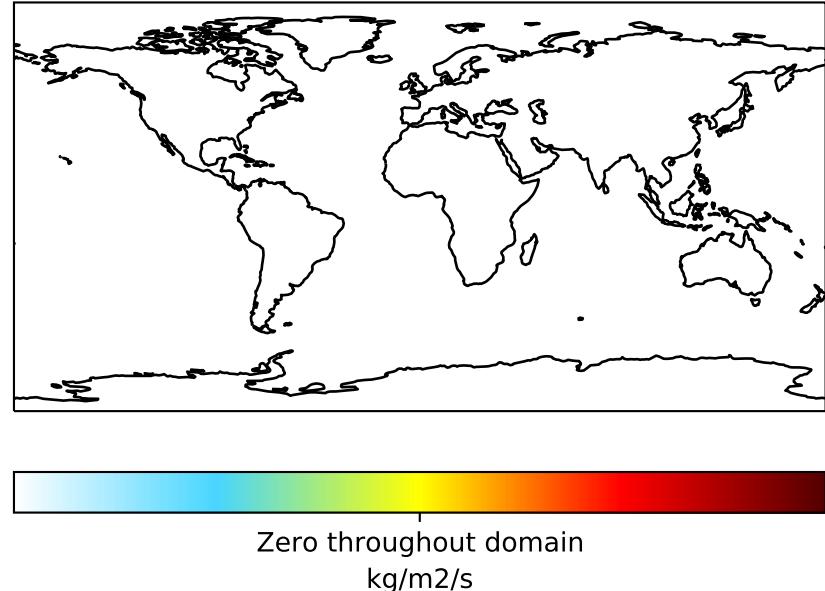


# EmisCH2O\_Ship

GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



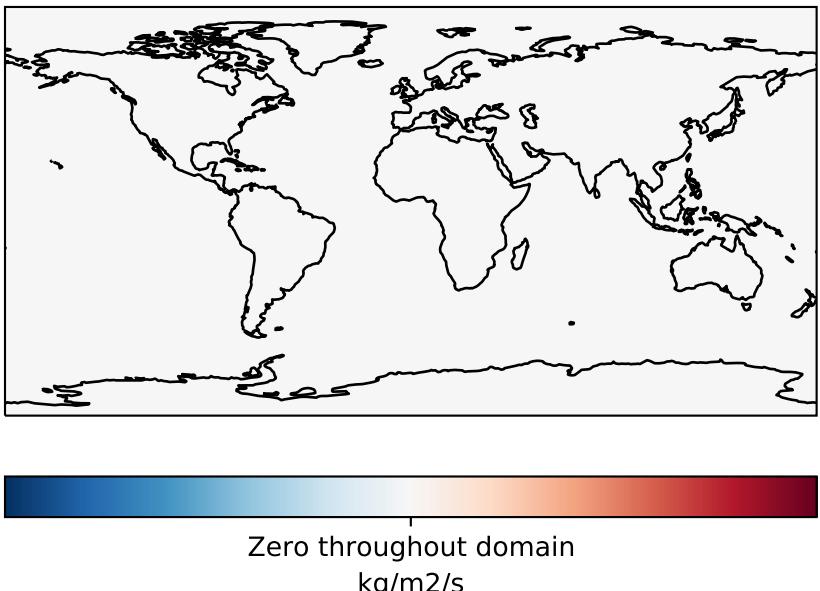
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



Zero throughout domain  
kg/m<sup>2</sup>/s

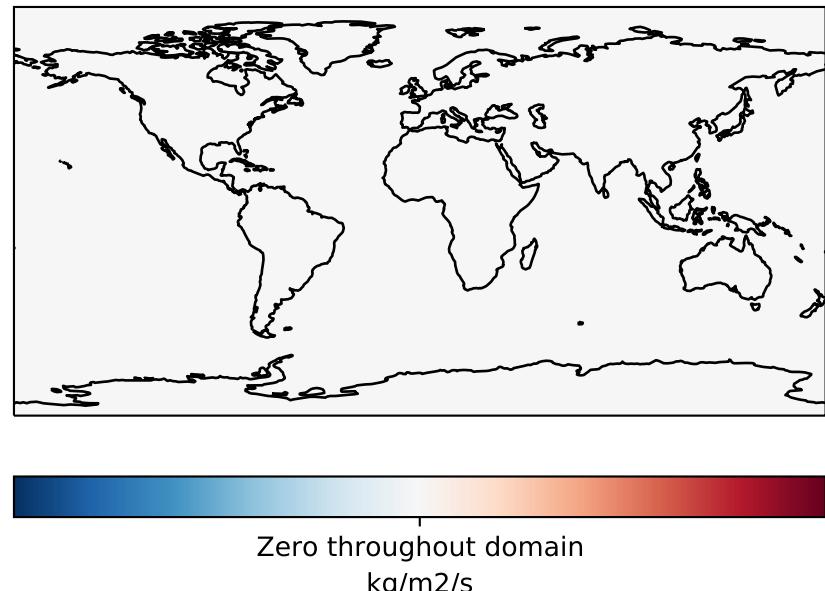
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Dynamic Range



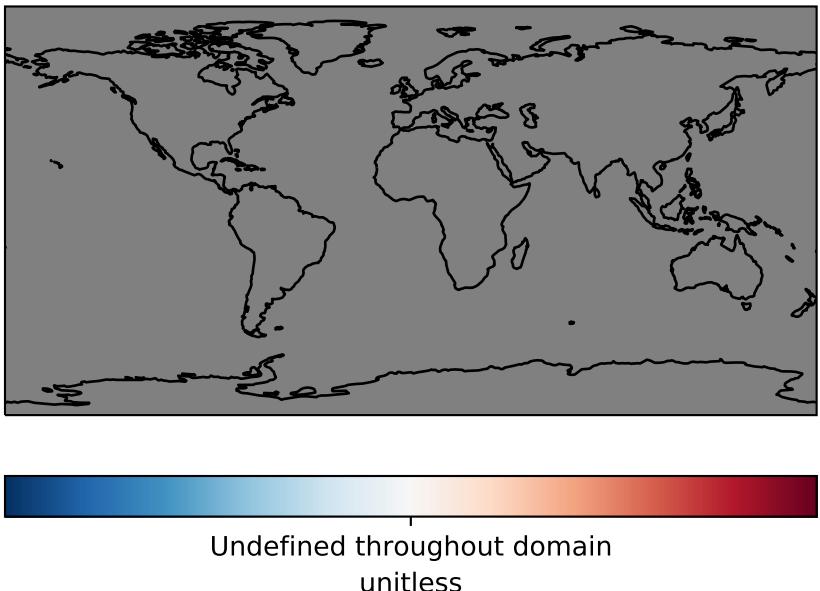
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



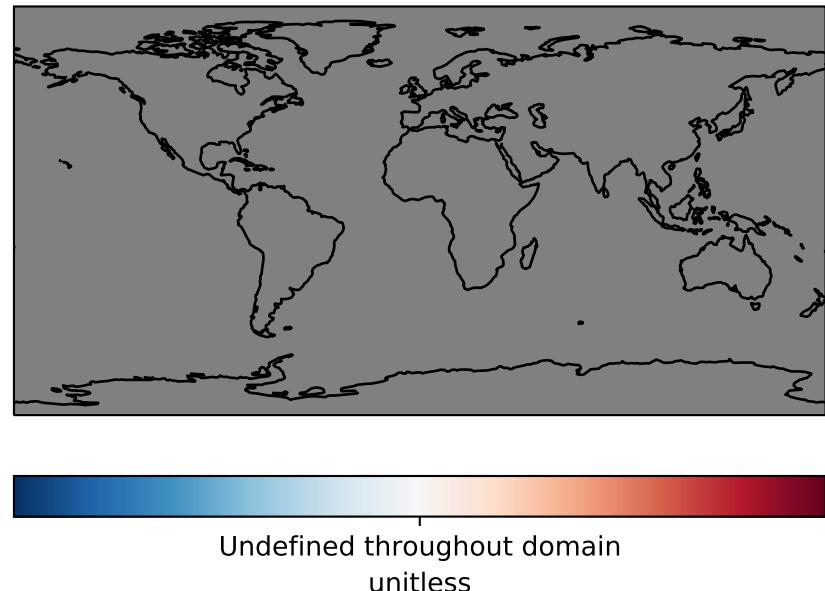
Zero throughout domain  
kg/m<sup>2</sup>/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

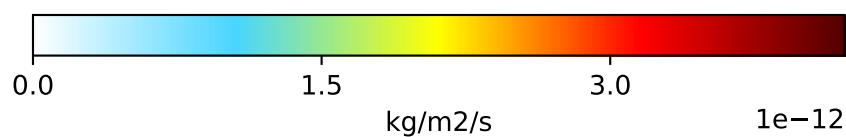
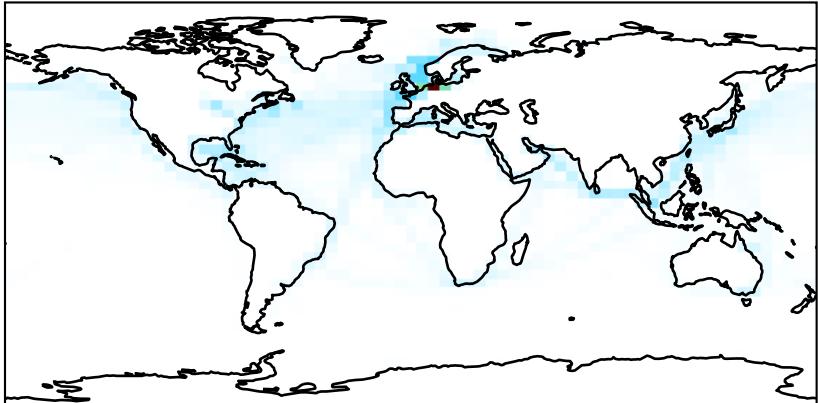
Ratio  
Dev/Ref, Fixed Range



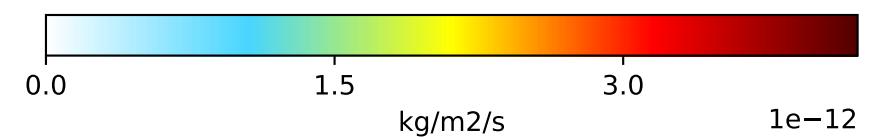
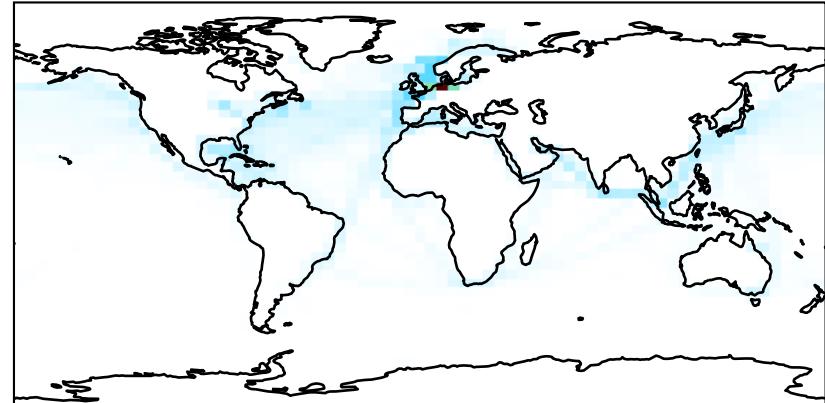
Undefined throughout domain  
unitless

# EmisCO\_Ship

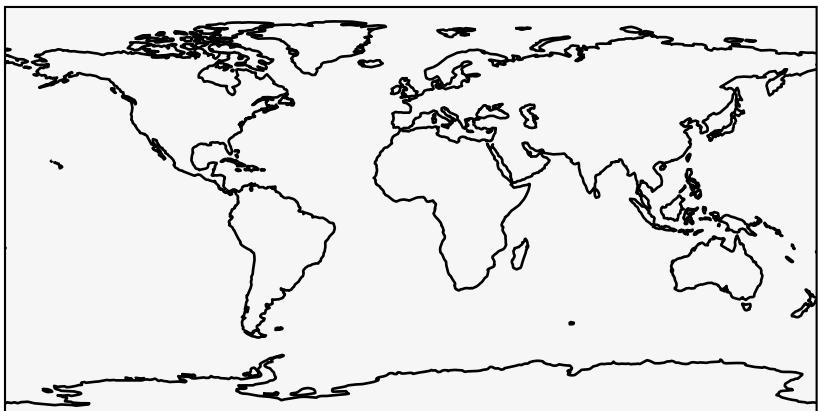
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



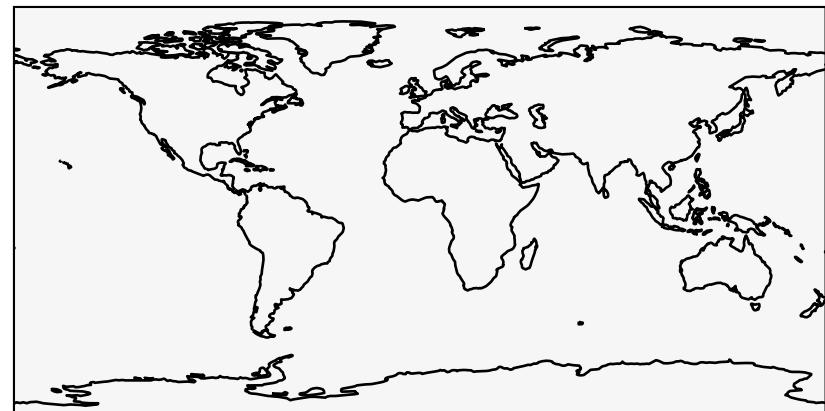
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



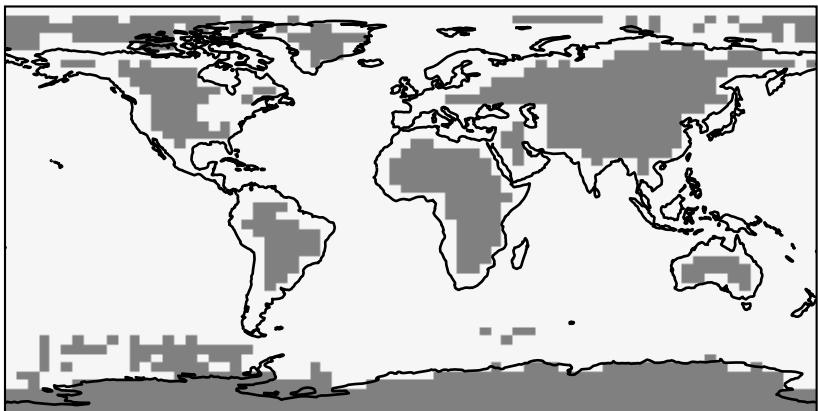
Difference  
Dev - Ref, Dynamic Range



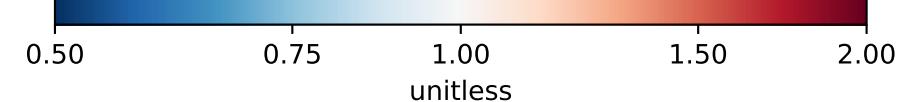
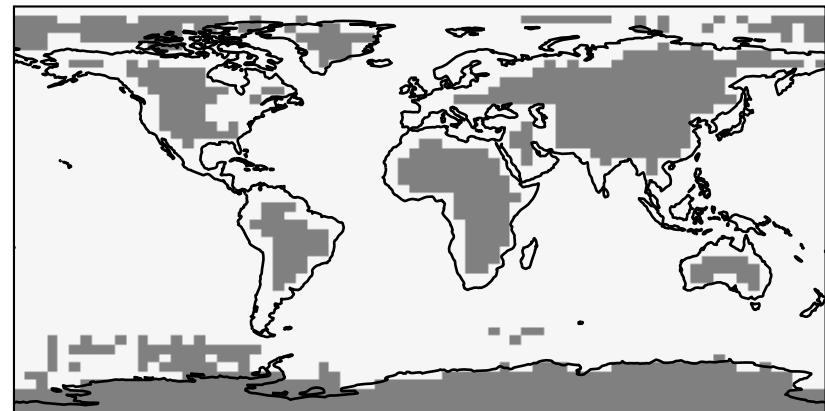
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

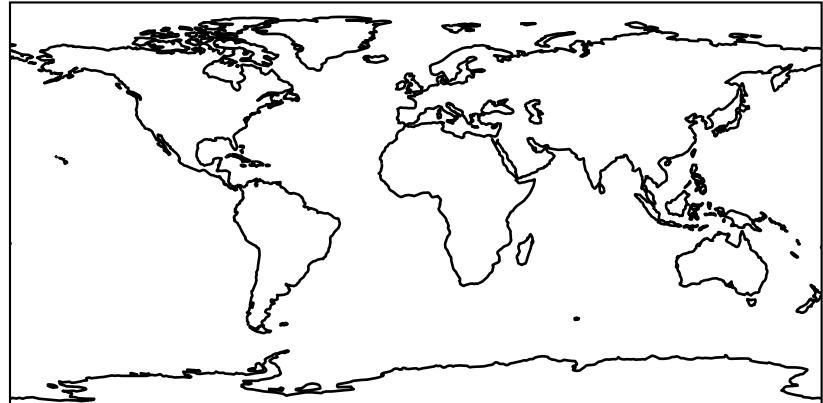


Ratio  
Dev/Ref, Fixed Range



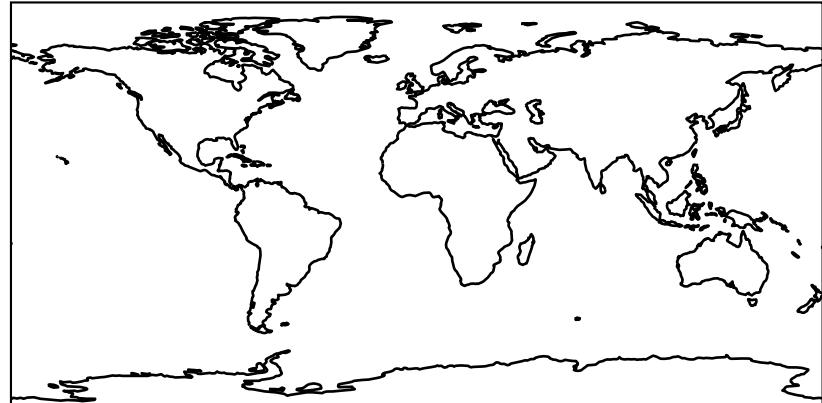
# EmisEOH\_Ship

GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



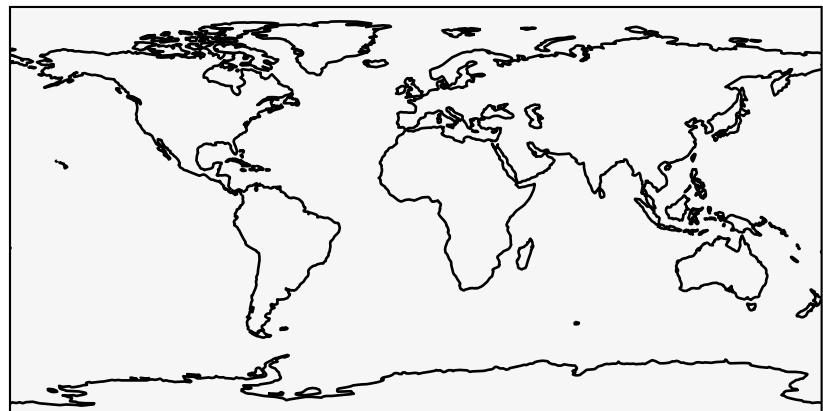
Zero throughout domain  
kg/m<sup>2</sup>/s

GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



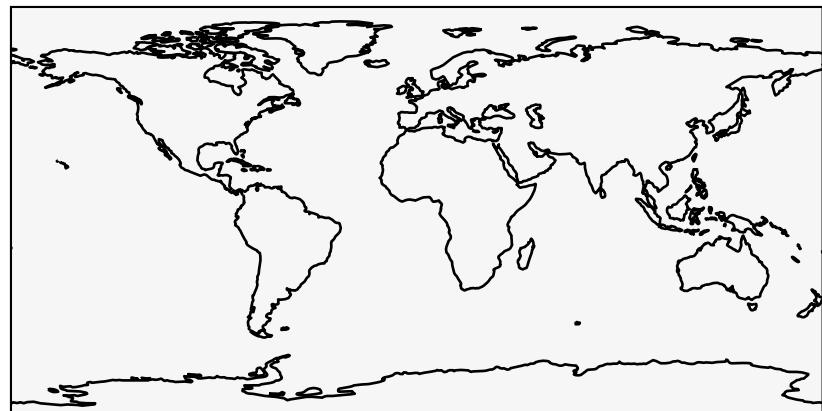
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Dynamic Range



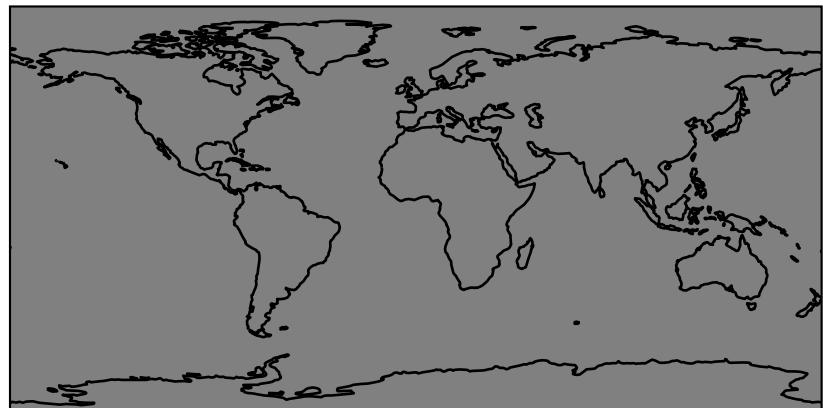
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



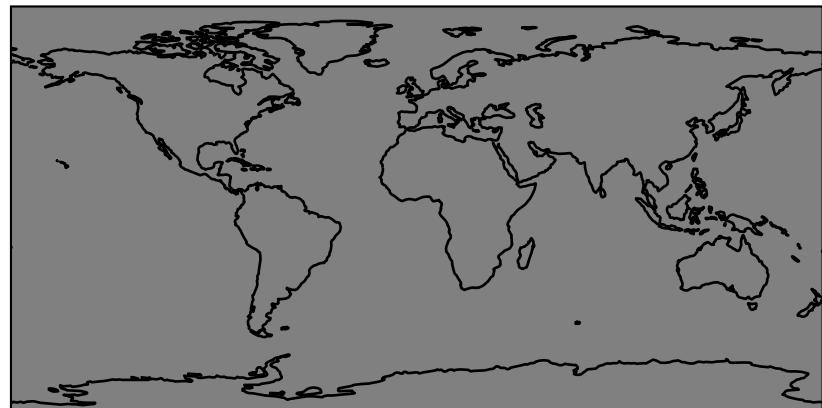
Zero throughout domain  
kg/m<sup>2</sup>/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

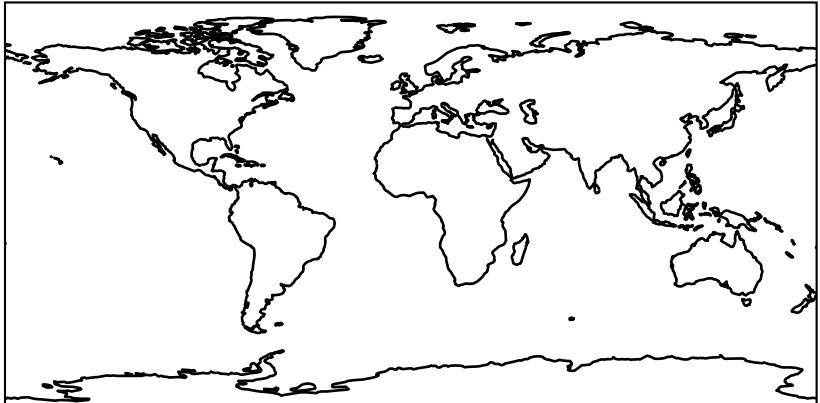
Ratio  
Dev/Ref, Fixed Range



Undefined throughout domain  
unitless

# EmisHCOOH\_Ship

GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



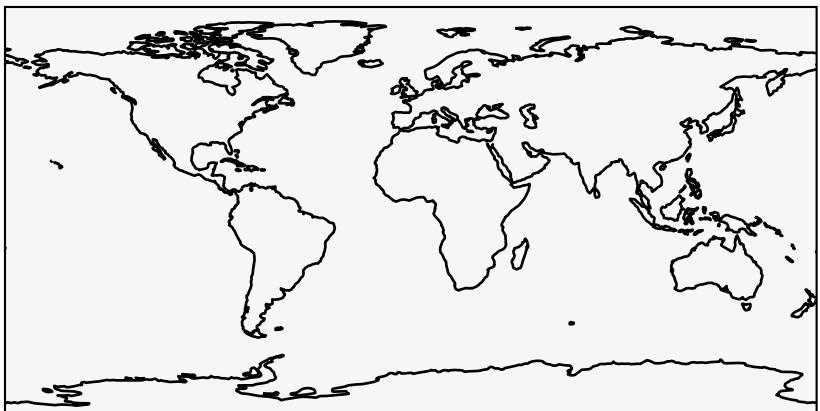
Zero throughout domain  
kg/m<sup>2</sup>/s

GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



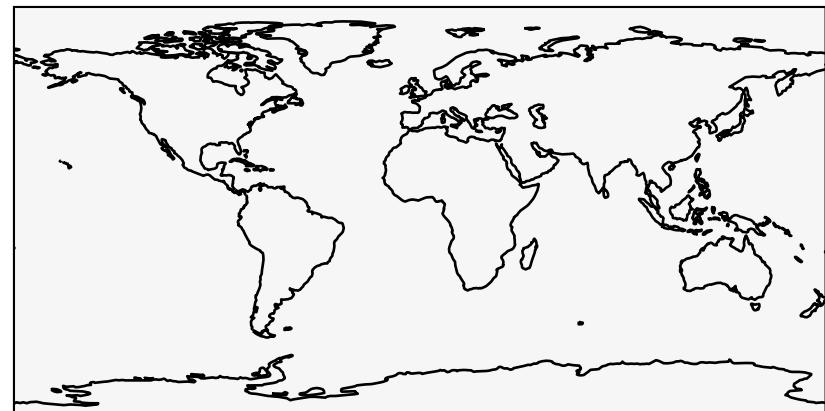
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Dynamic Range



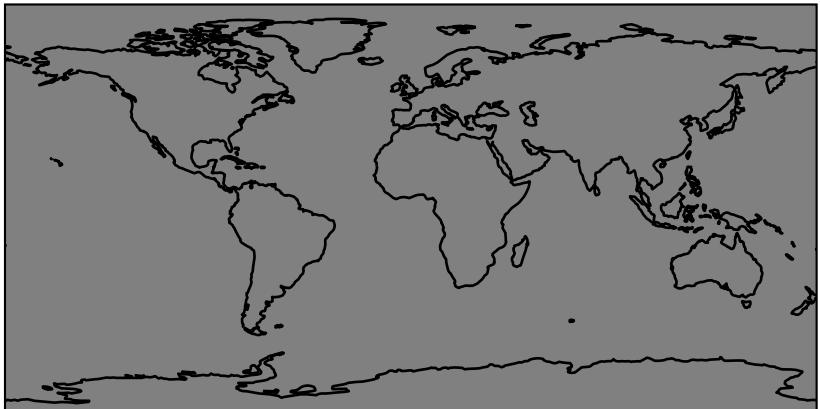
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



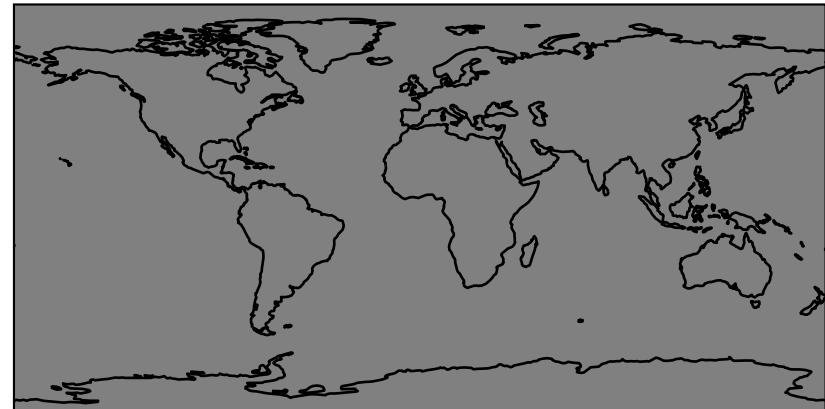
Zero throughout domain  
kg/m<sup>2</sup>/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

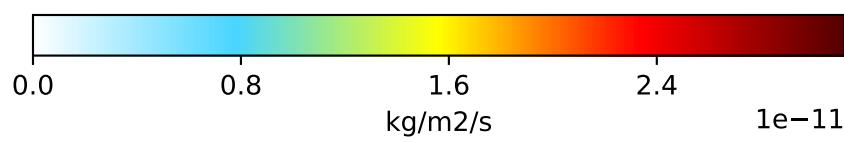
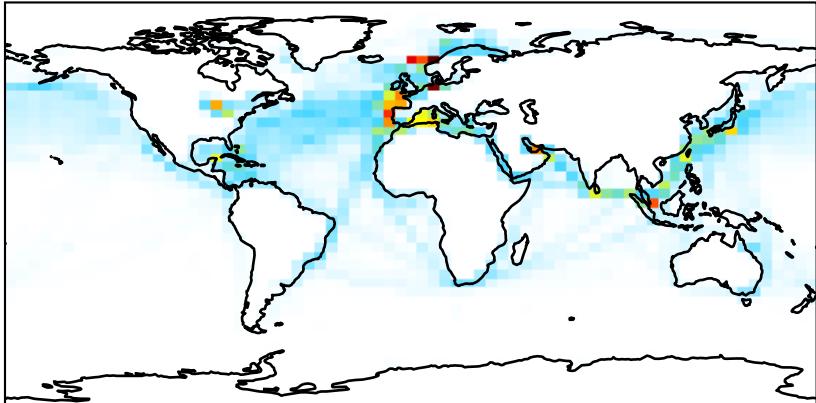
Ratio  
Dev/Ref, Fixed Range



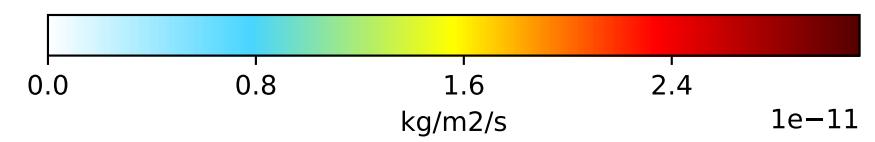
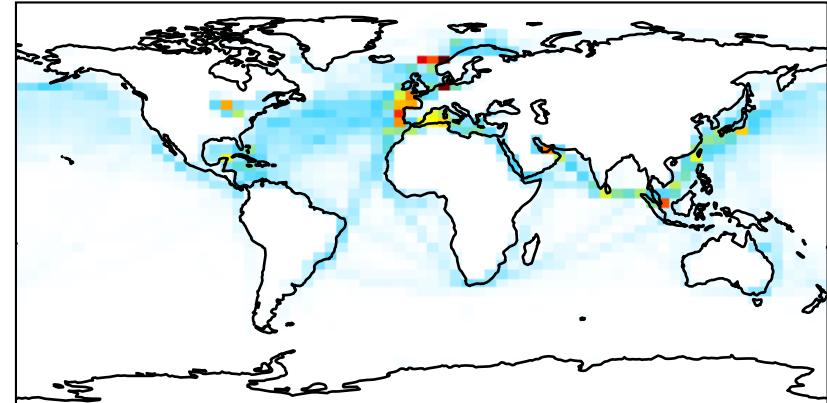
Undefined throughout domain  
unitless

# EmisHNO3\_Ship

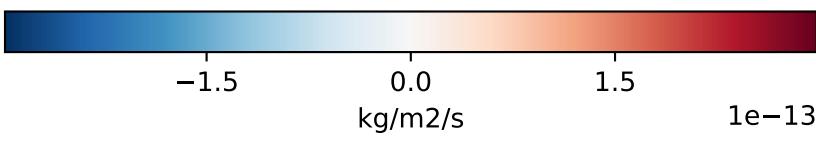
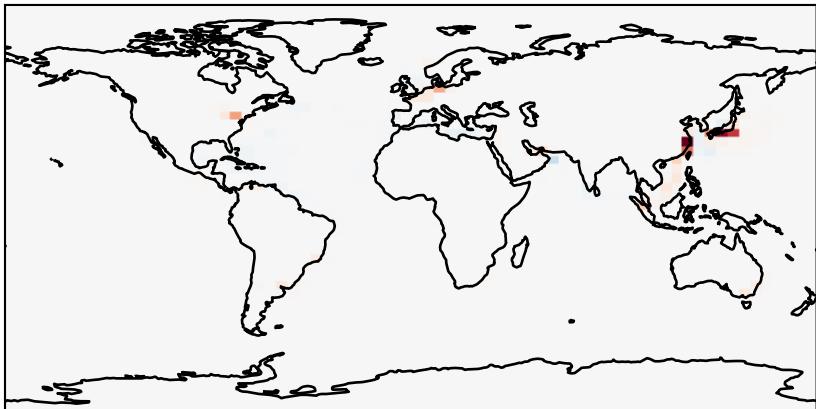
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



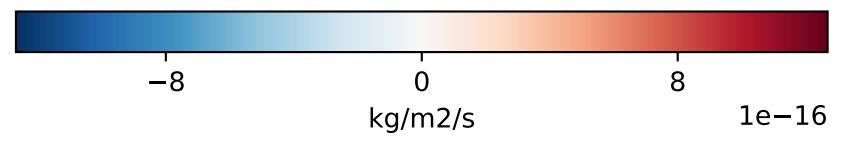
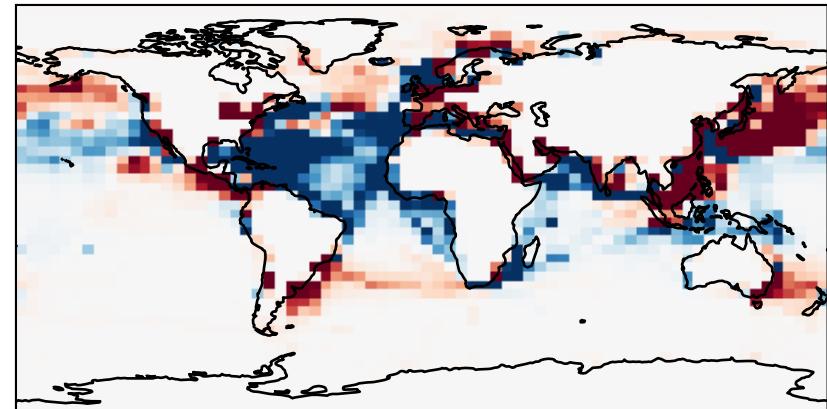
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



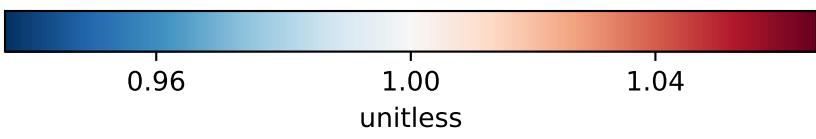
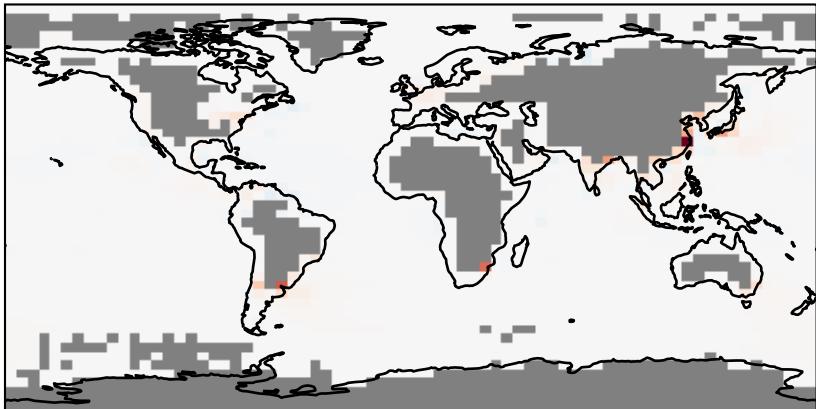
Difference  
Dev - Ref, Dynamic Range



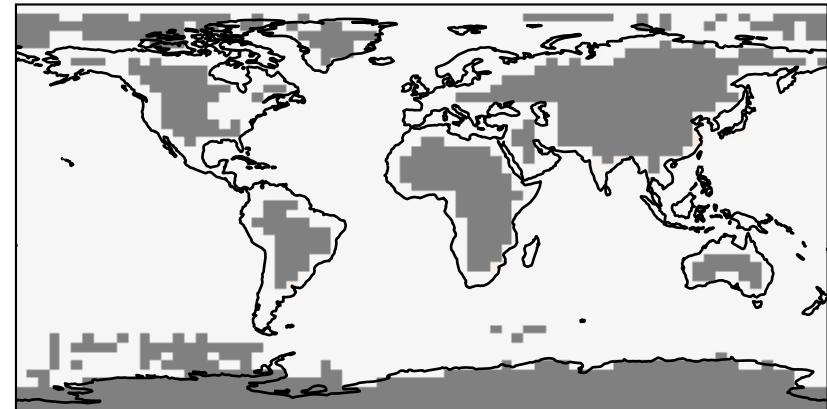
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

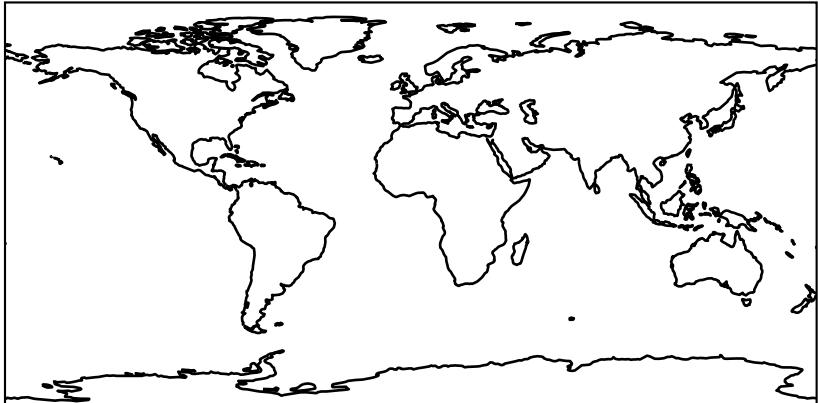


Ratio  
Dev/Ref, Fixed Range



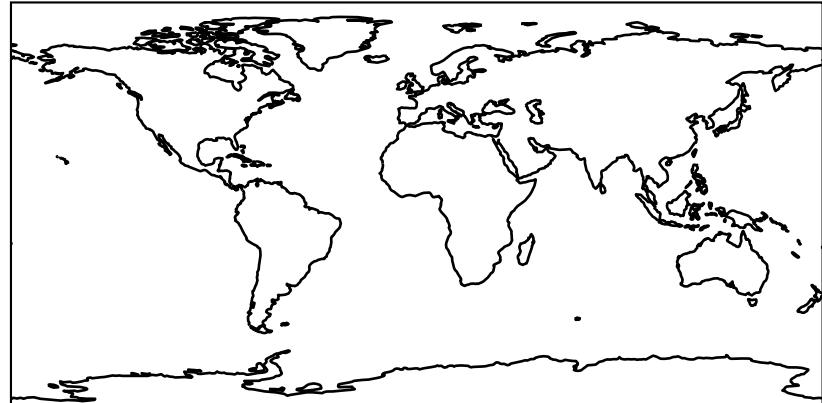
# EmisMEK\_Ship

GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



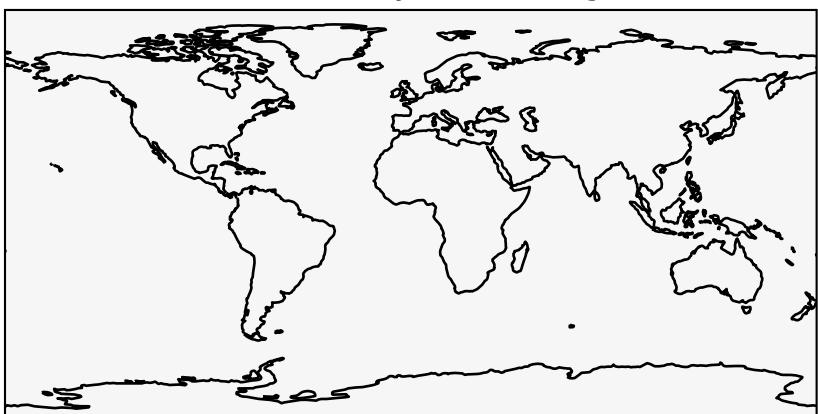
Zero throughout domain  
kg/m<sup>2</sup>/s

GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



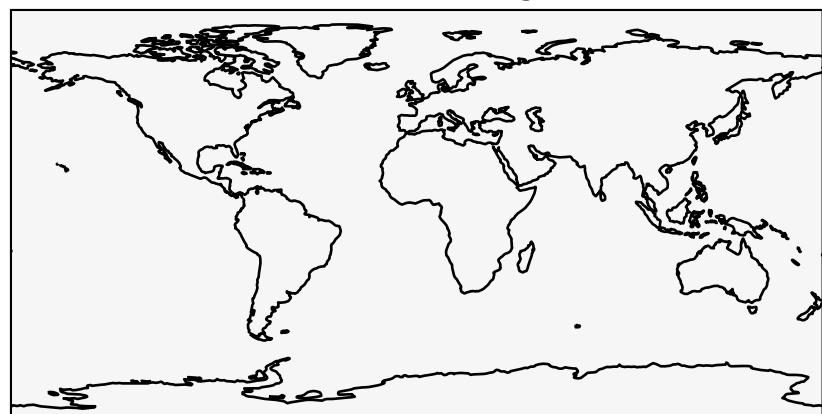
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Dynamic Range



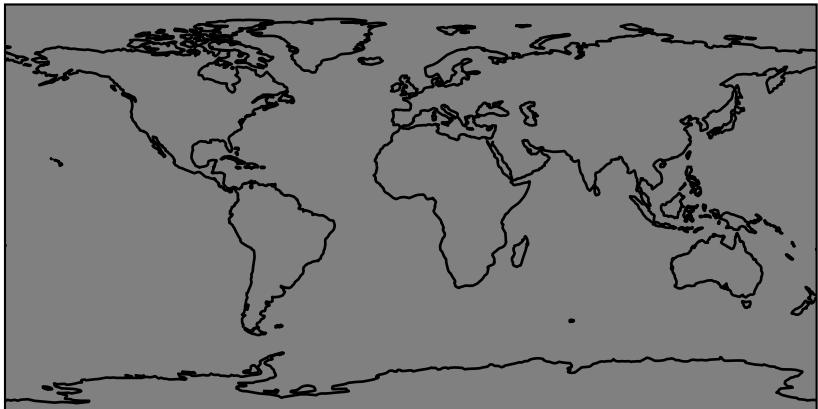
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



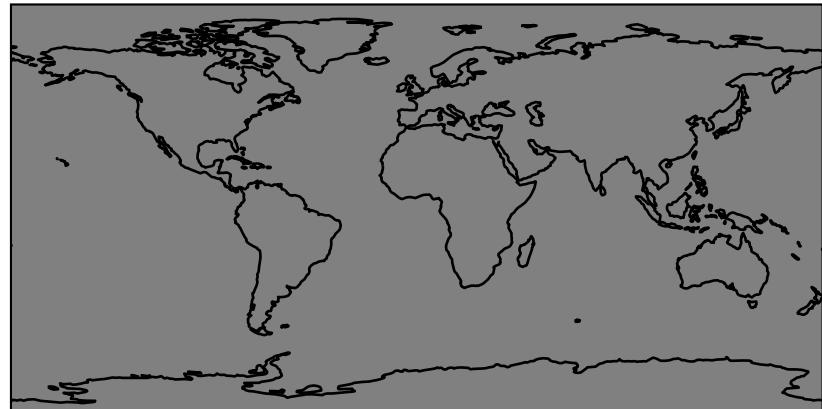
Zero throughout domain  
kg/m<sup>2</sup>/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

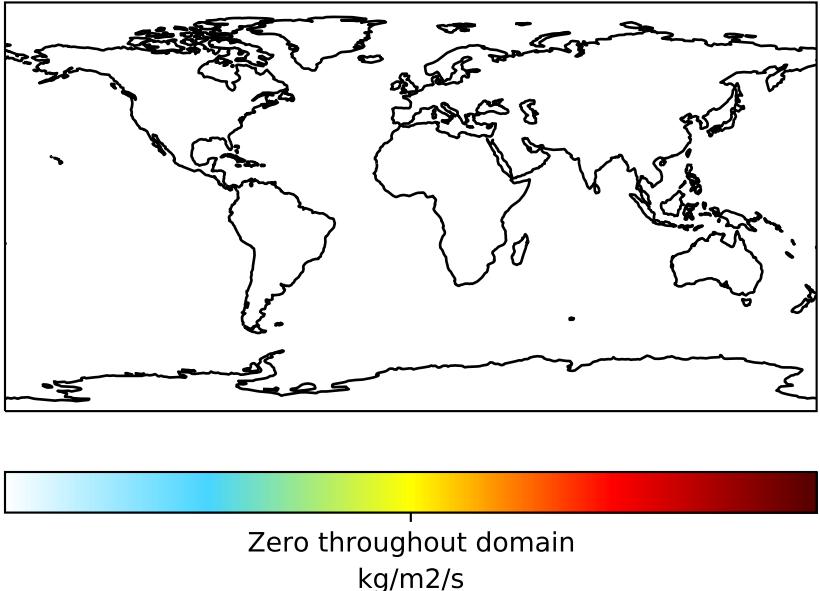
Ratio  
Dev/Ref, Fixed Range



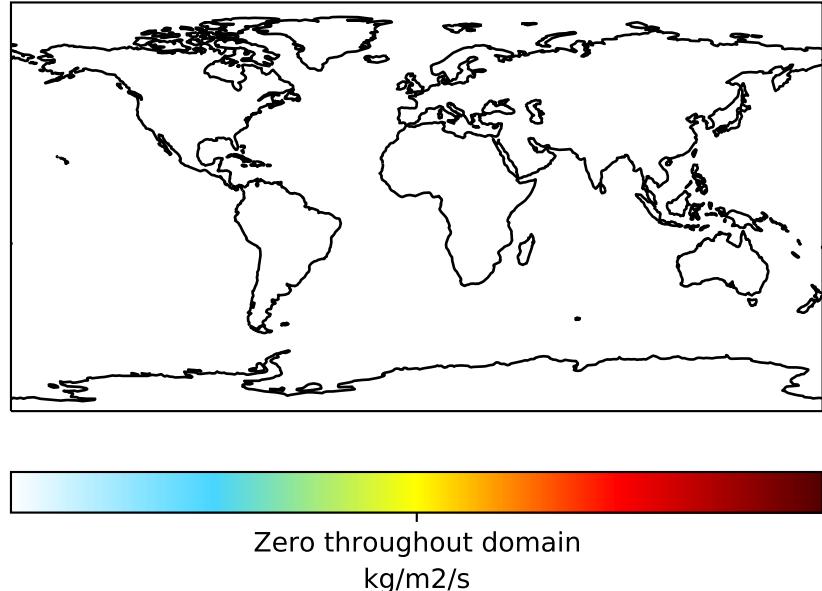
Undefined throughout domain  
unitless

# EmisMOH\_Ship

GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



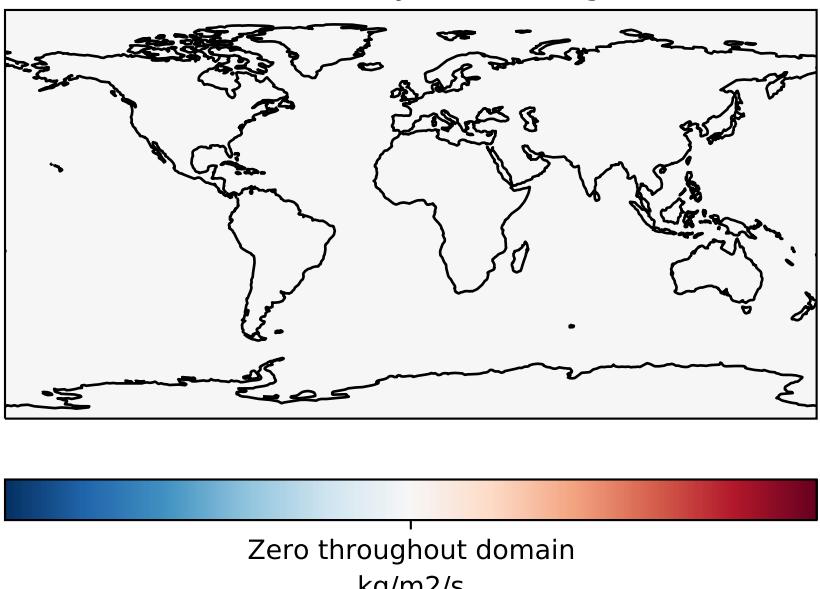
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



Zero throughout domain  
kg/m<sup>2</sup>/s

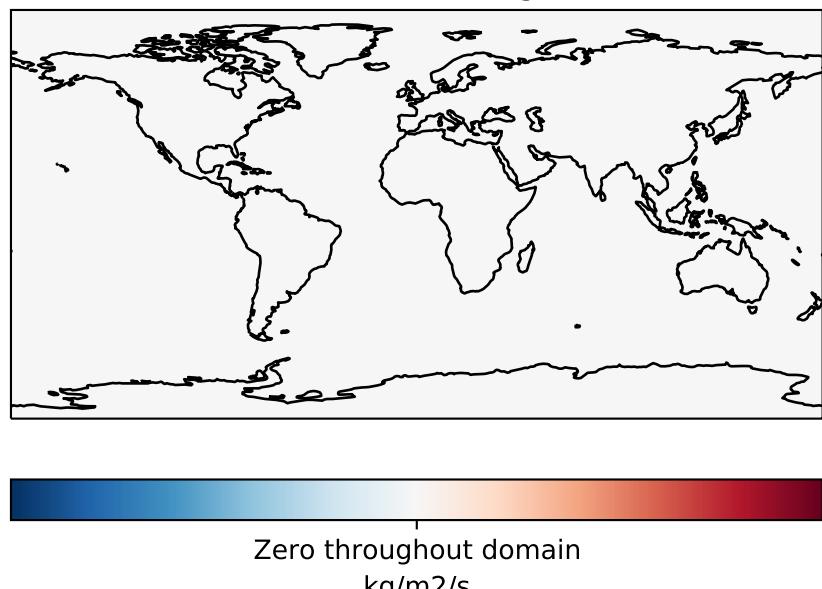
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Dynamic Range



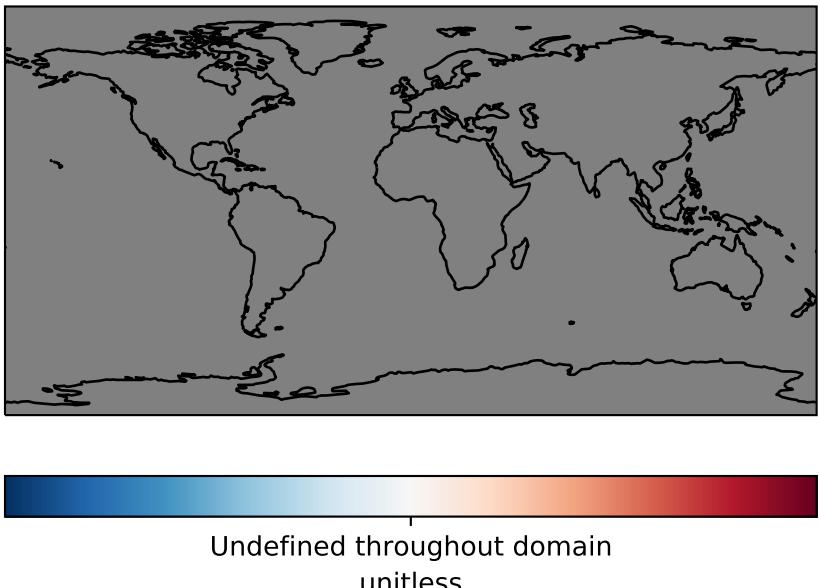
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



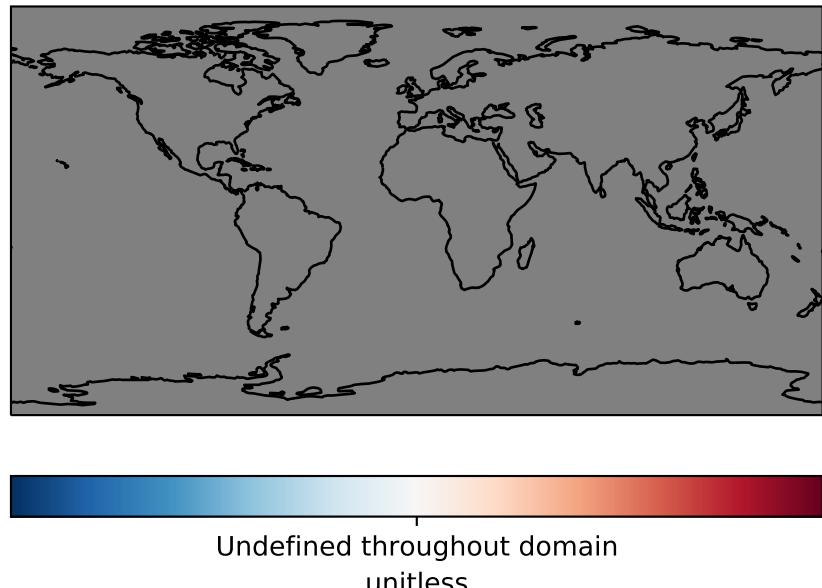
Zero throughout domain  
kg/m<sup>2</sup>/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

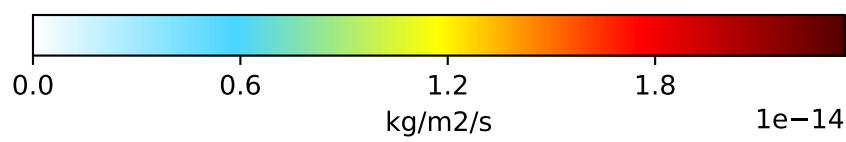
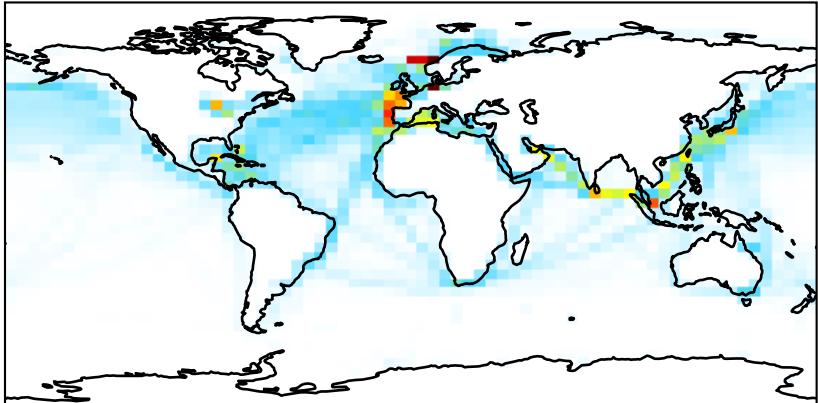
Ratio  
Dev/Ref, Fixed Range



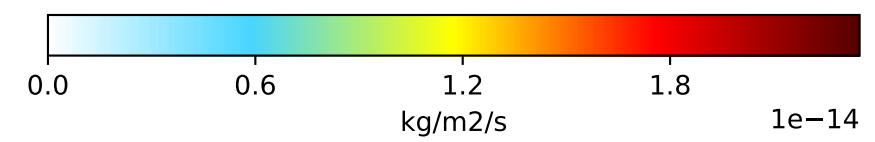
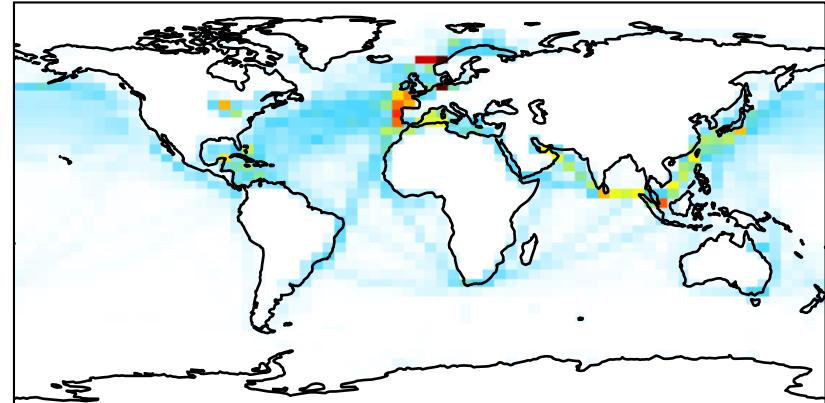
Undefined throughout domain  
unitless

# EmisNH3\_Ship

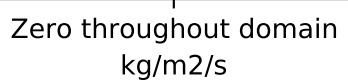
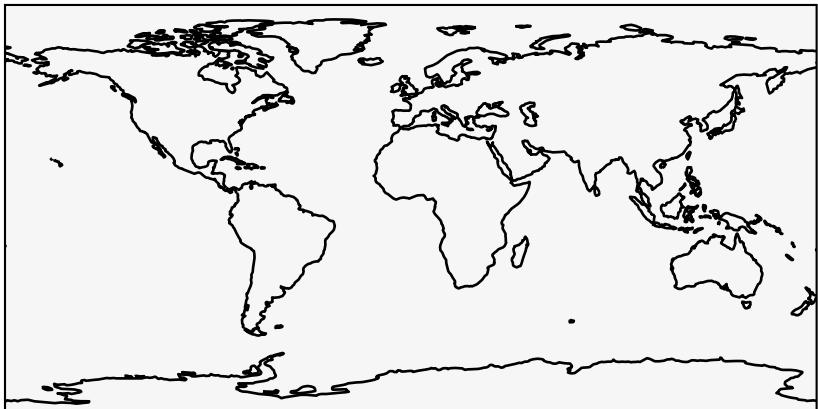
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



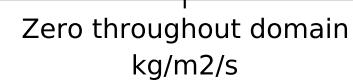
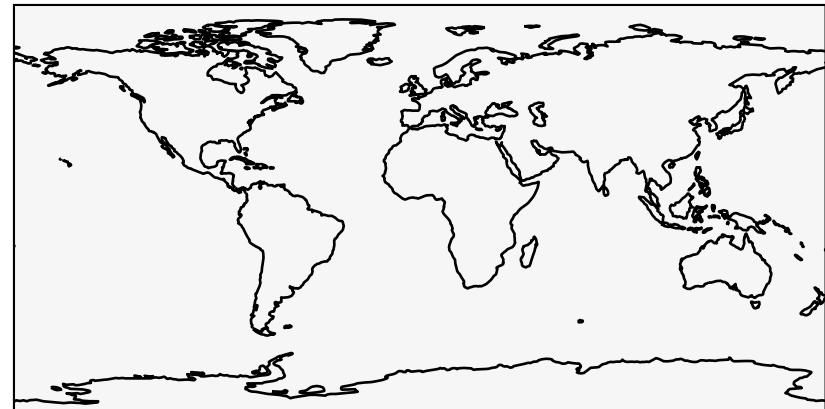
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



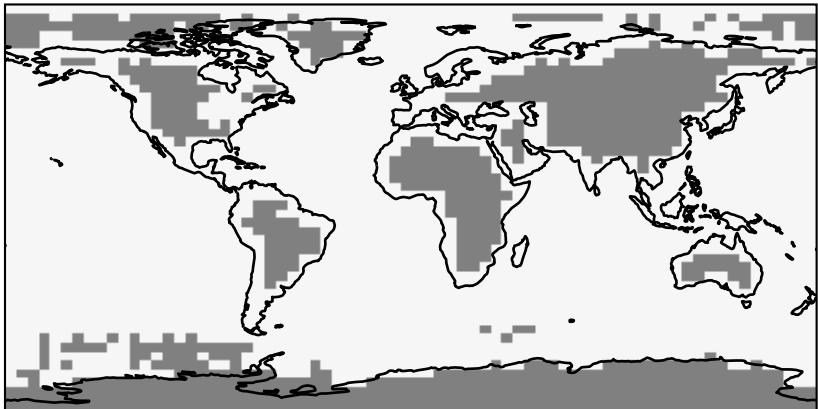
Difference  
Dev - Ref, Dynamic Range



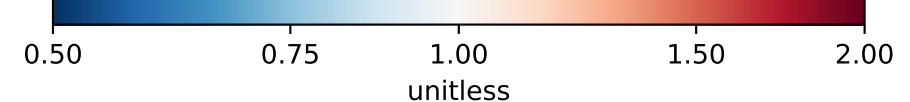
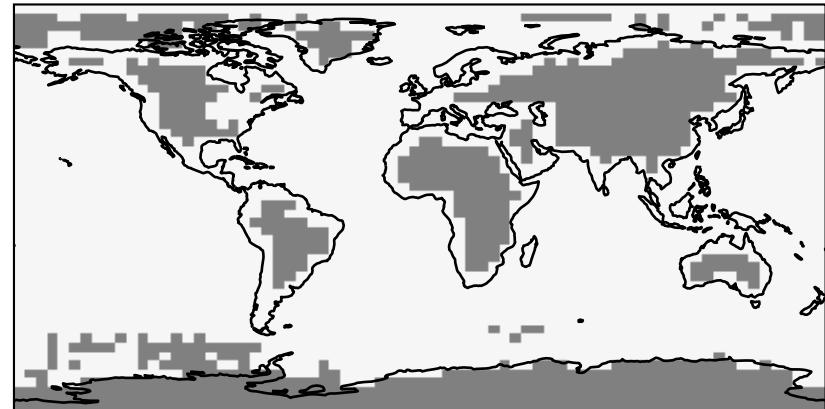
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

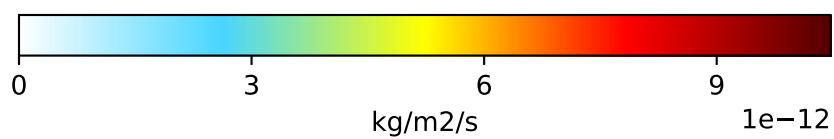
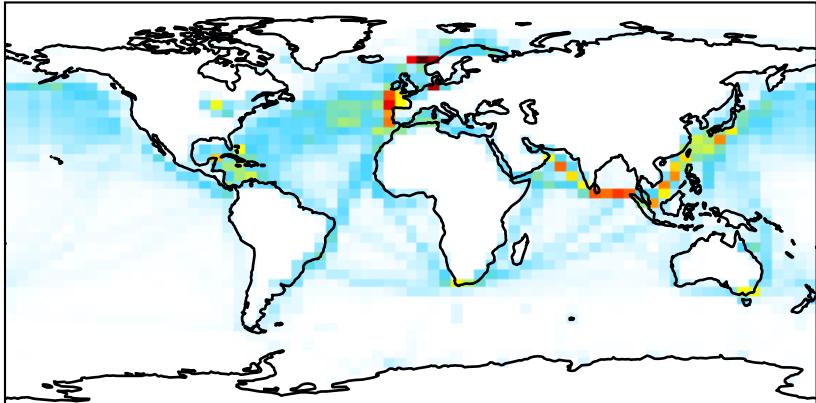


Ratio  
Dev/Ref, Fixed Range

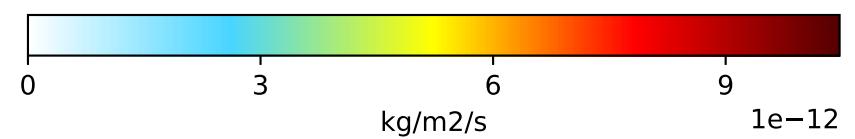
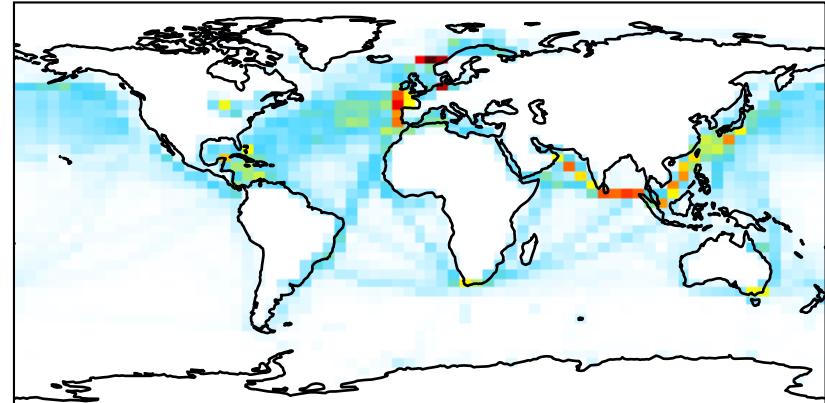


# EmisNO2\_Ship

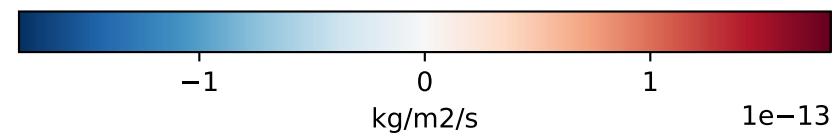
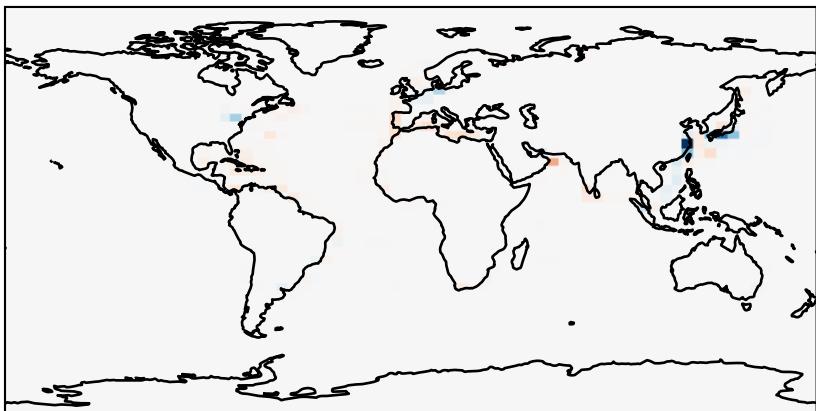
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



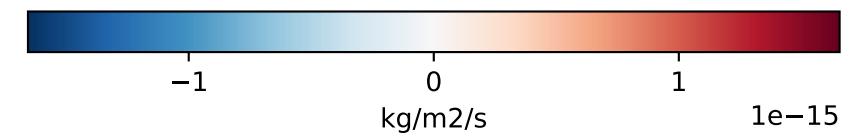
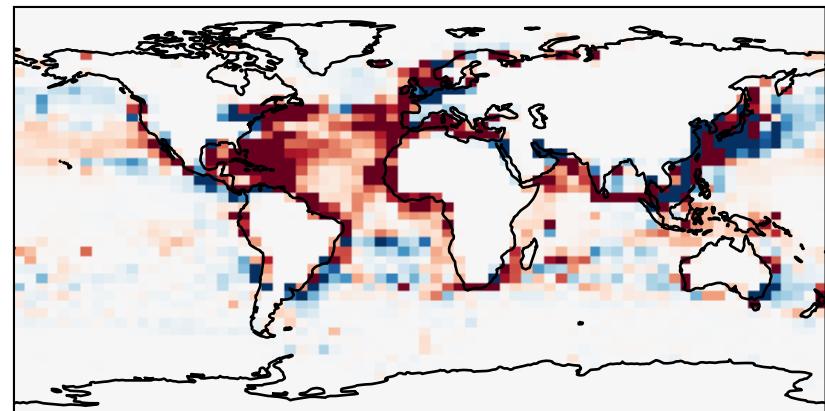
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



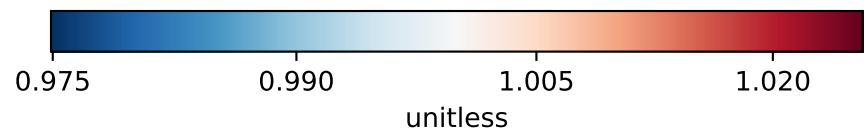
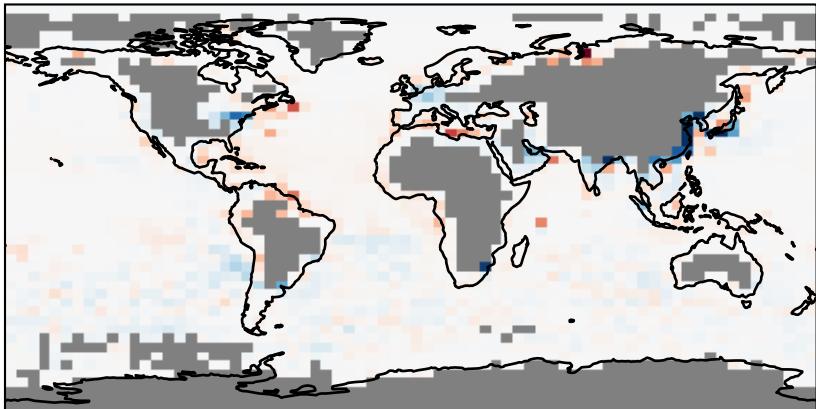
Difference  
Dev - Ref, Dynamic Range



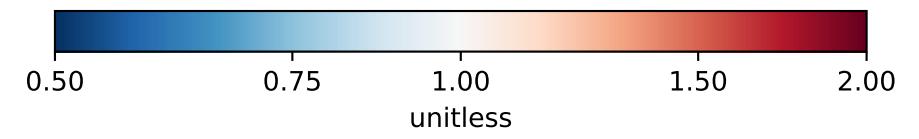
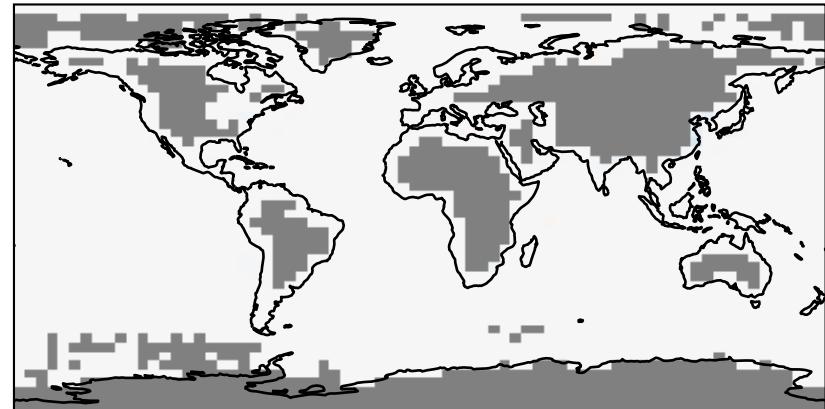
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

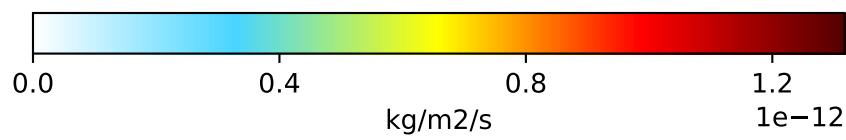
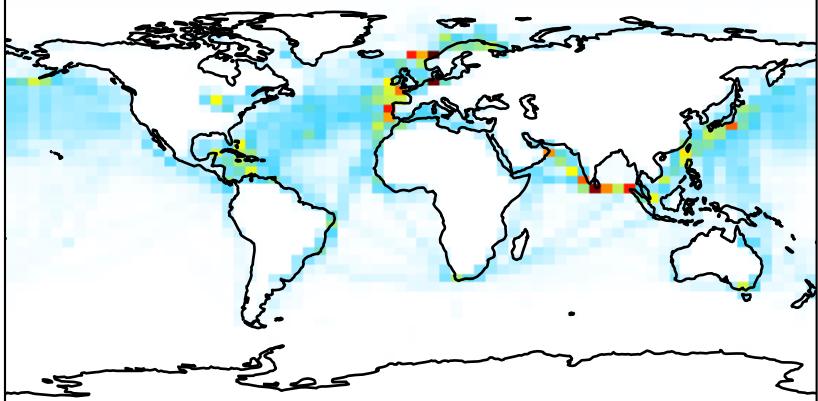


Ratio  
Dev/Ref, Fixed Range

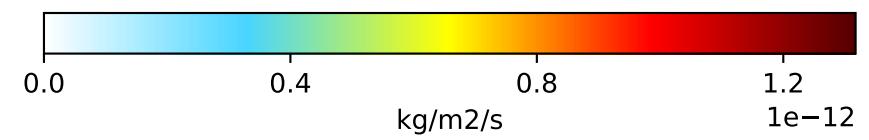
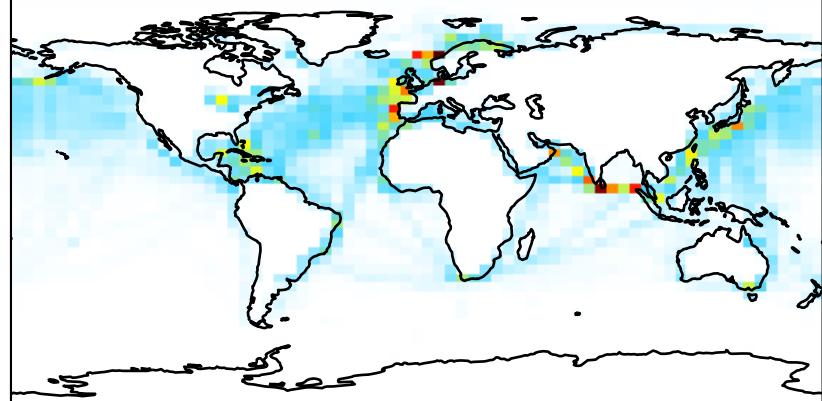


# EmisNO\_Ship

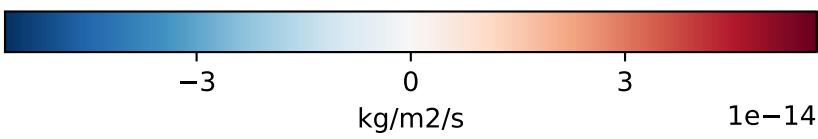
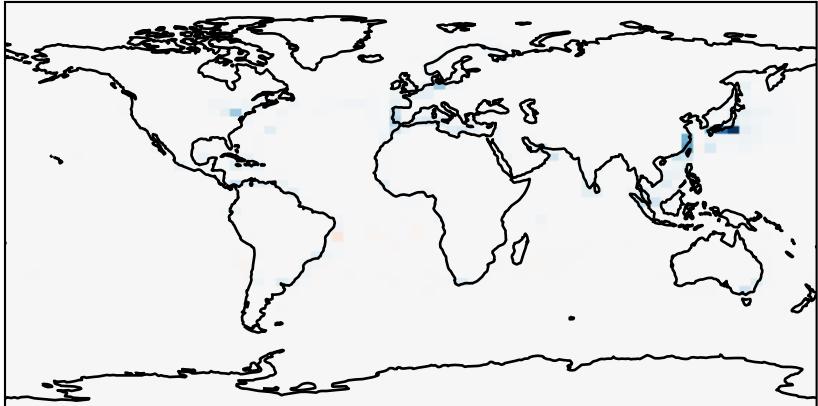
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



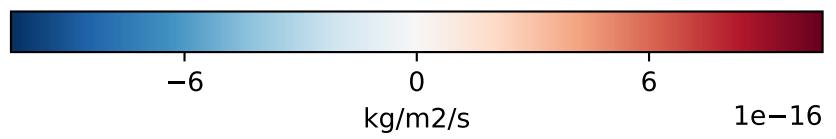
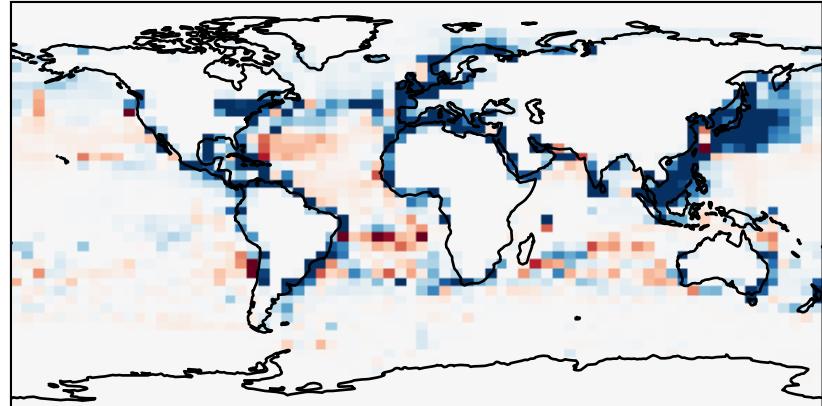
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



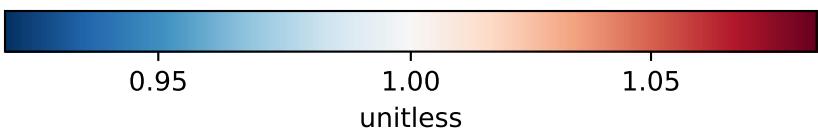
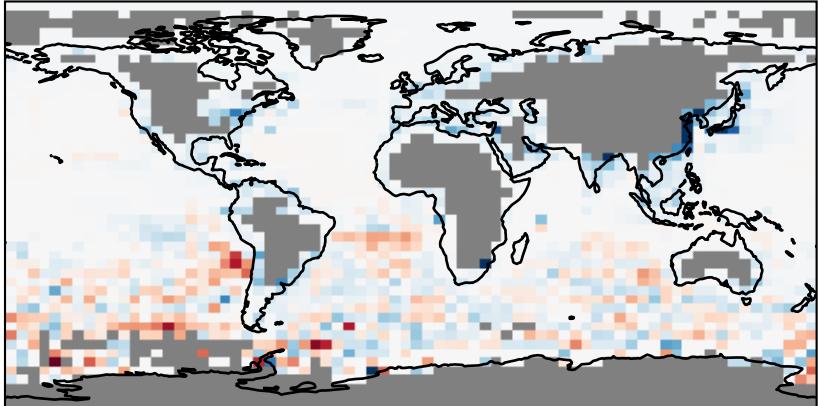
Difference  
Dev - Ref, Dynamic Range



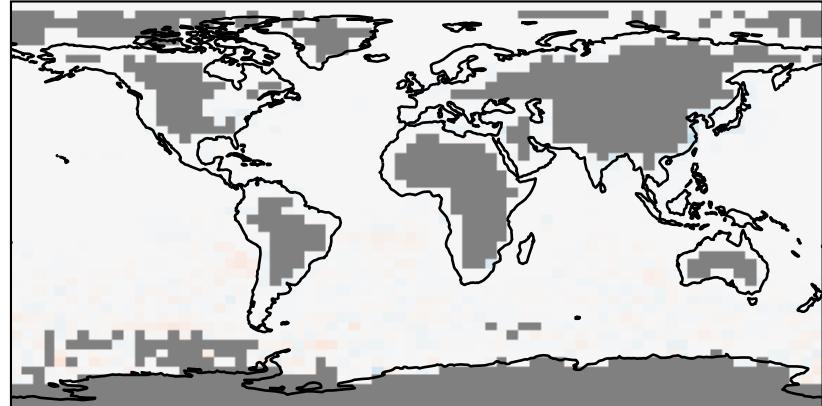
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

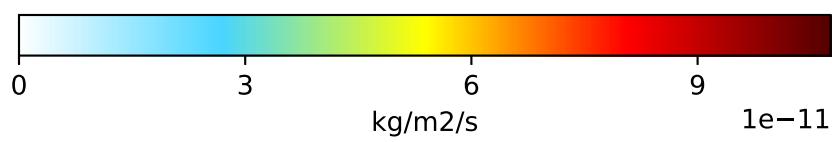
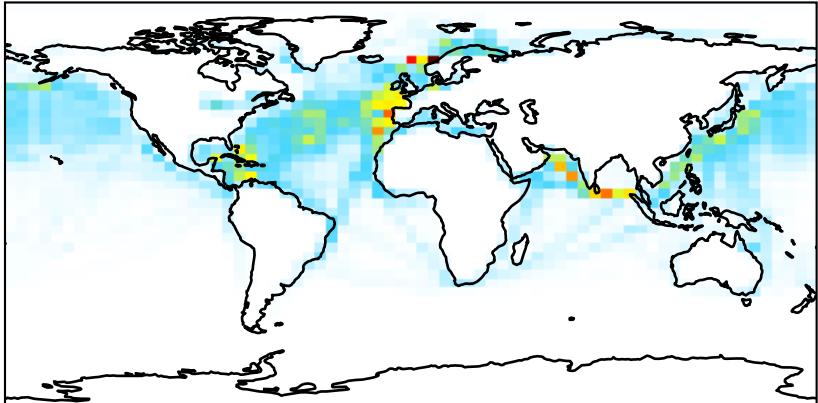


Ratio  
Dev/Ref, Fixed Range

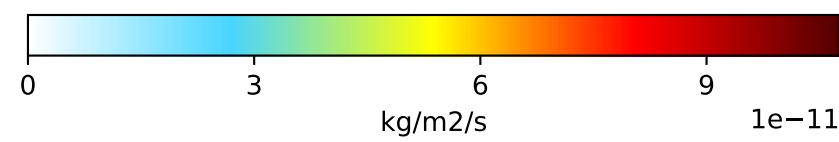
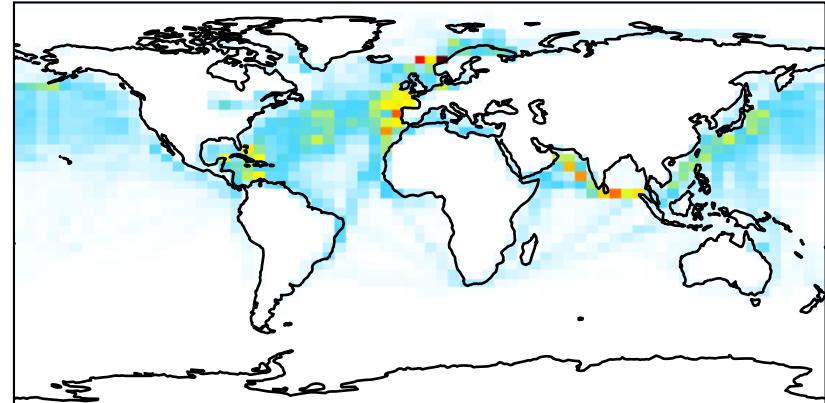


# EmisO3\_Ship

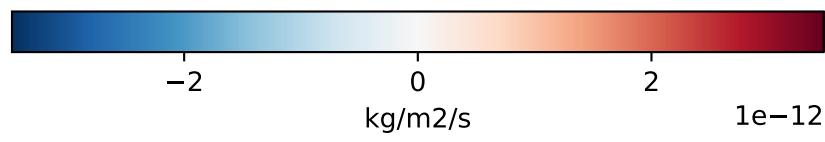
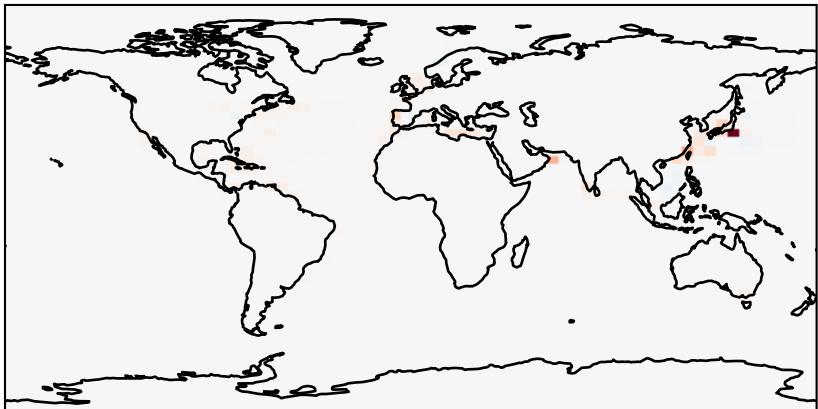
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



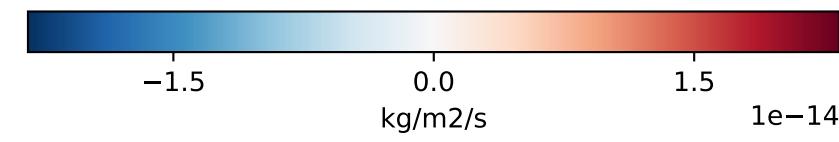
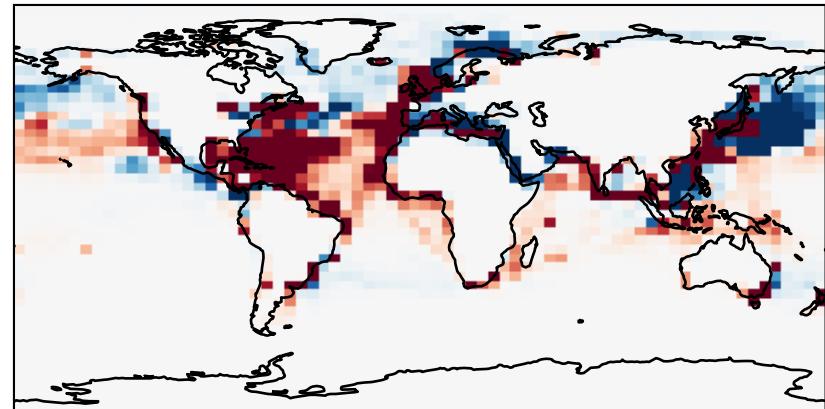
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



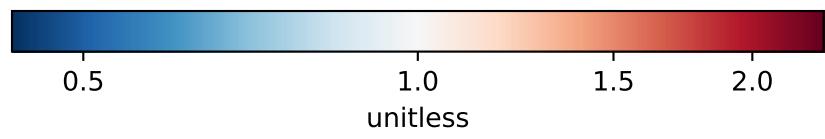
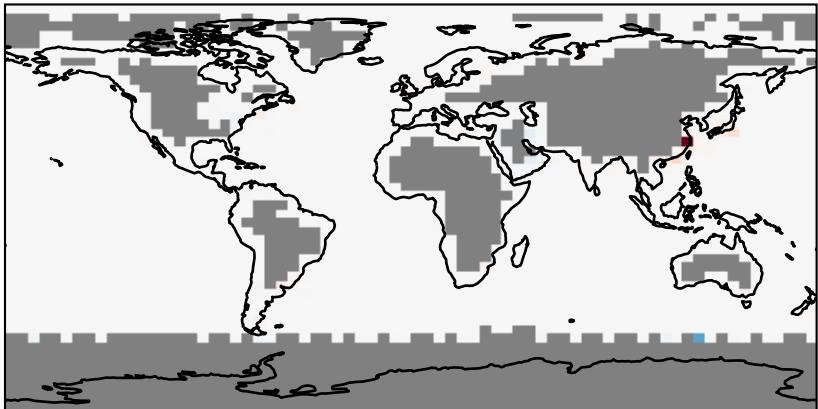
Difference  
Dev - Ref, Dynamic Range



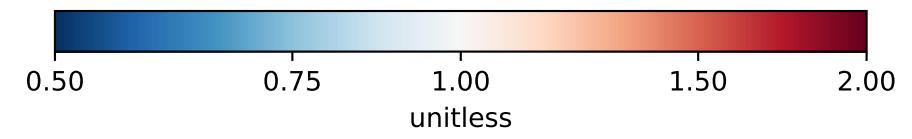
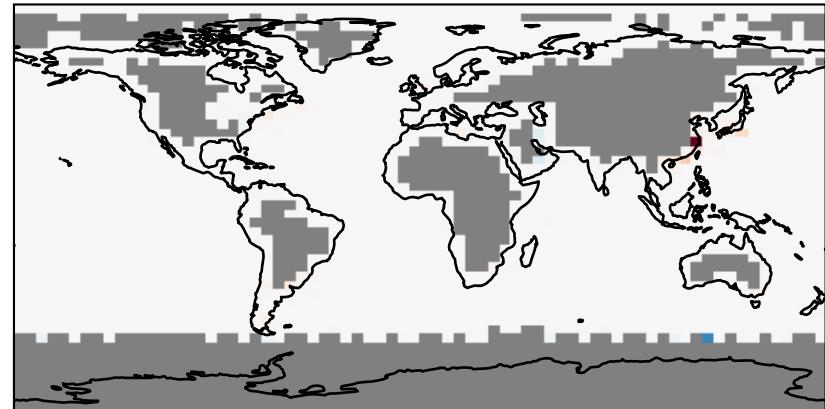
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

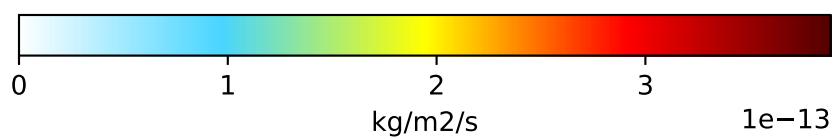
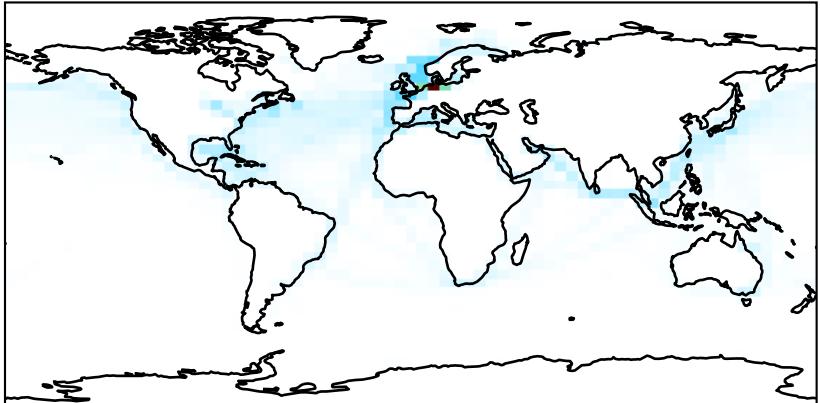


Ratio  
Dev/Ref, Fixed Range

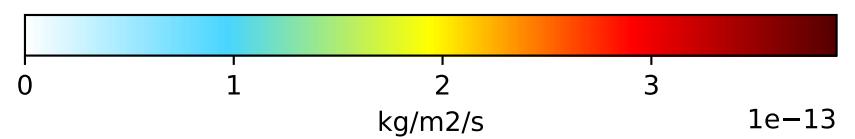
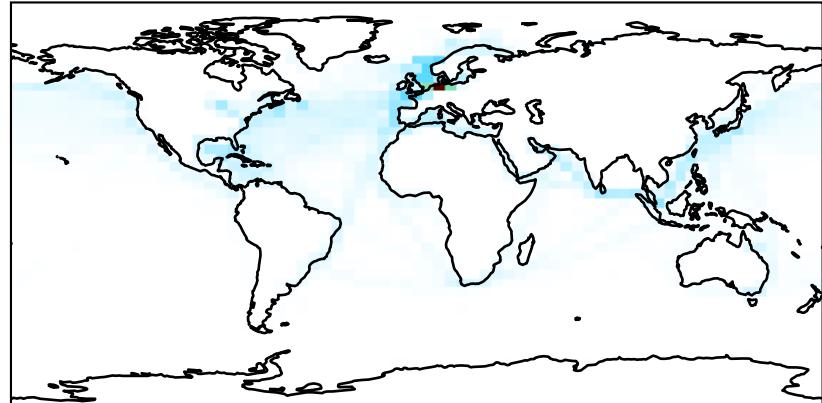


# EmisOCPI\_Ship

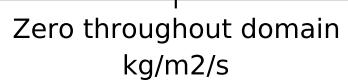
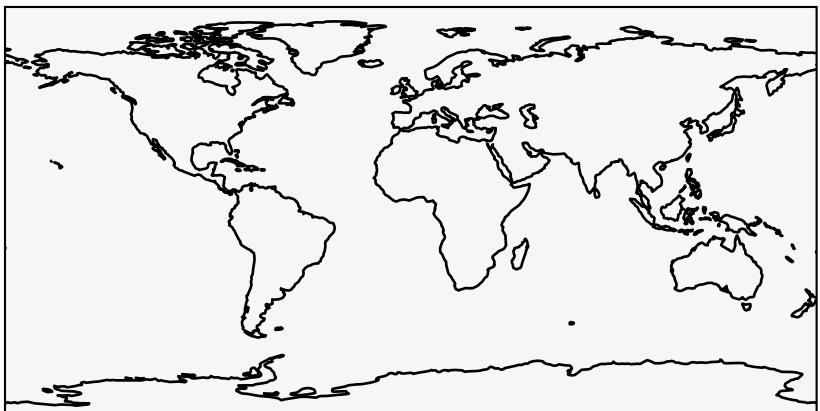
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



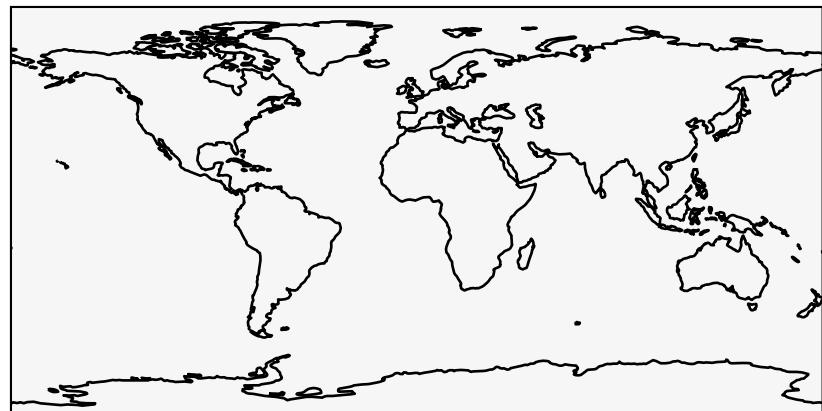
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



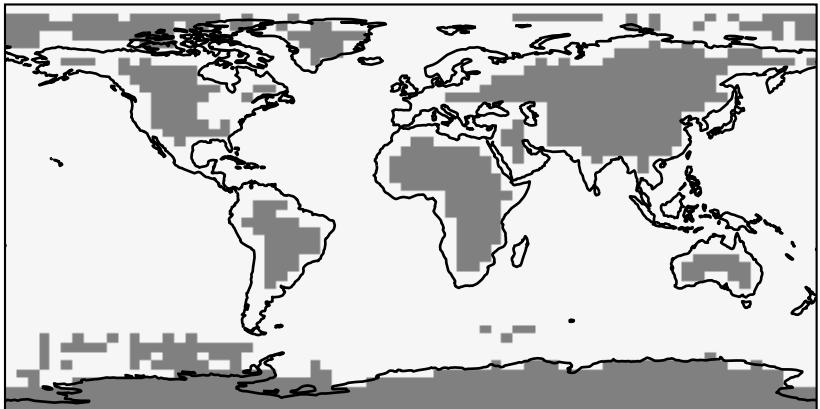
Difference  
Dev - Ref, Dynamic Range



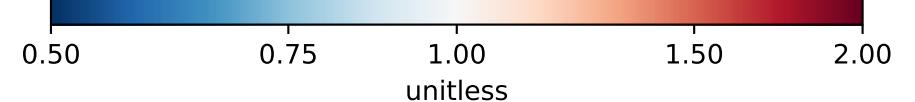
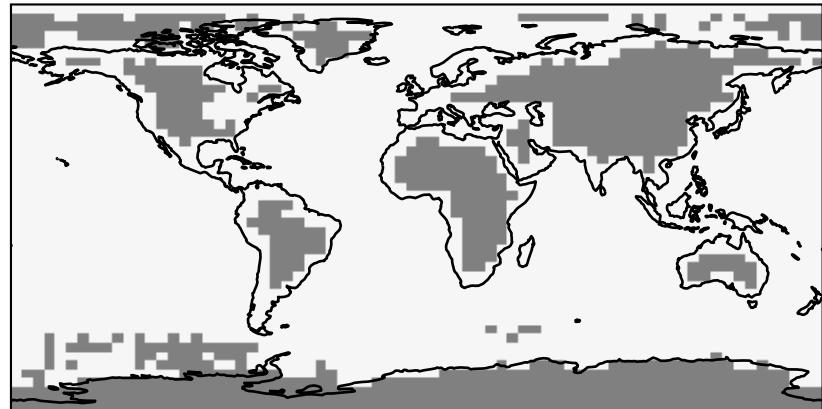
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

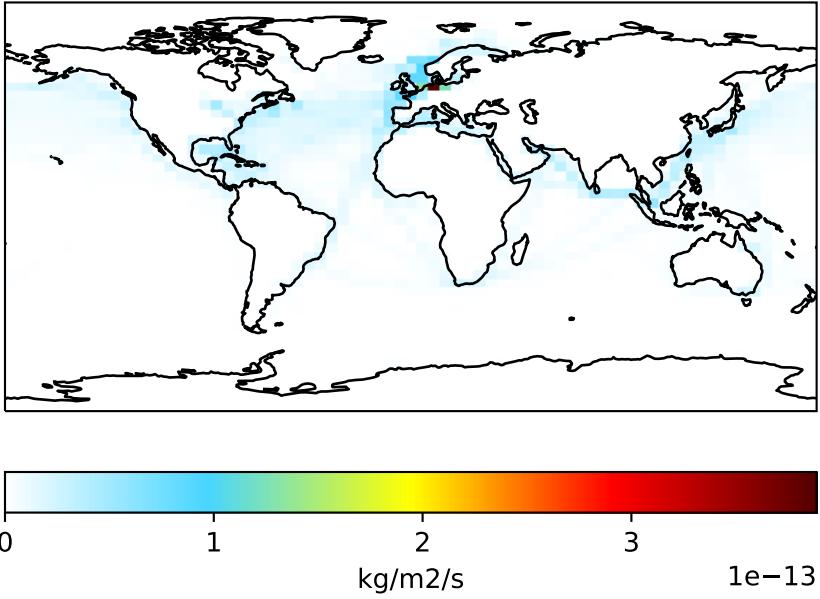


Ratio  
Dev/Ref, Fixed Range

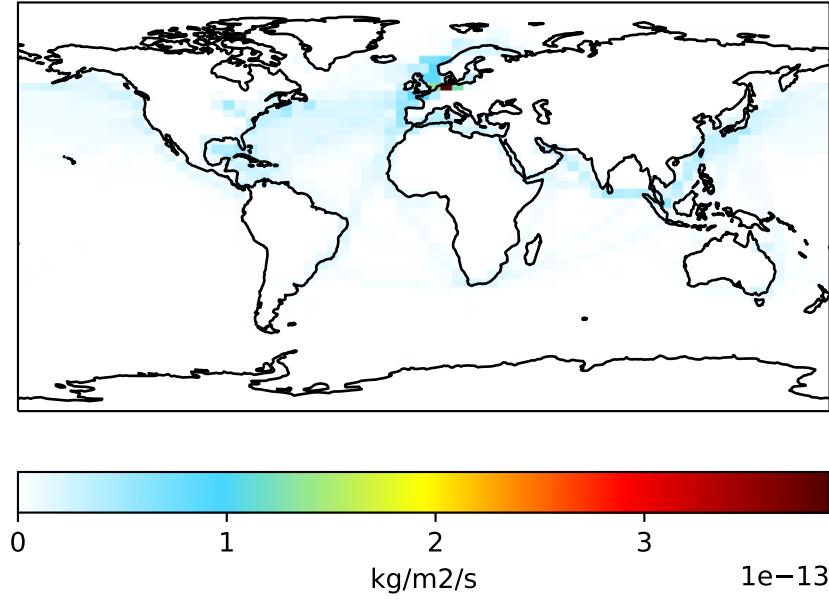


# EmisOCPO\_Ship

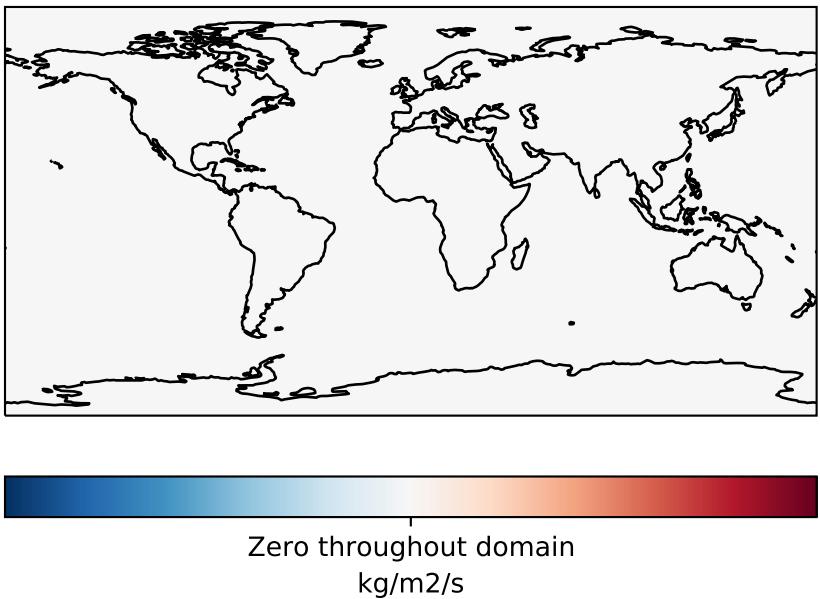
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



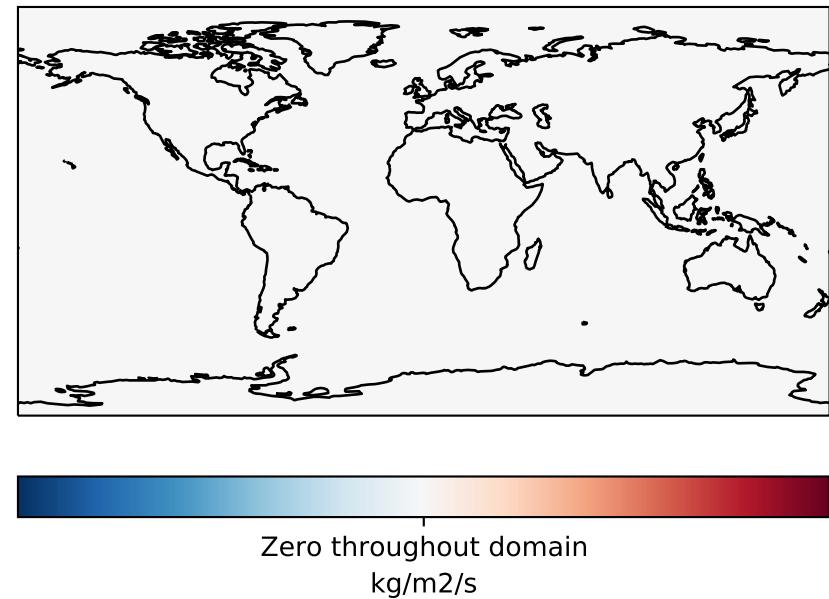
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



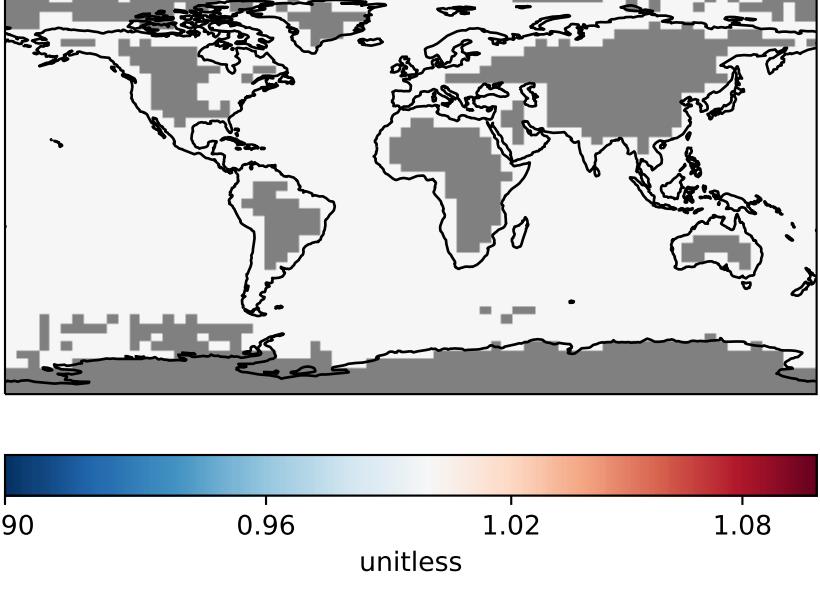
Difference  
Dev - Ref, Dynamic Range



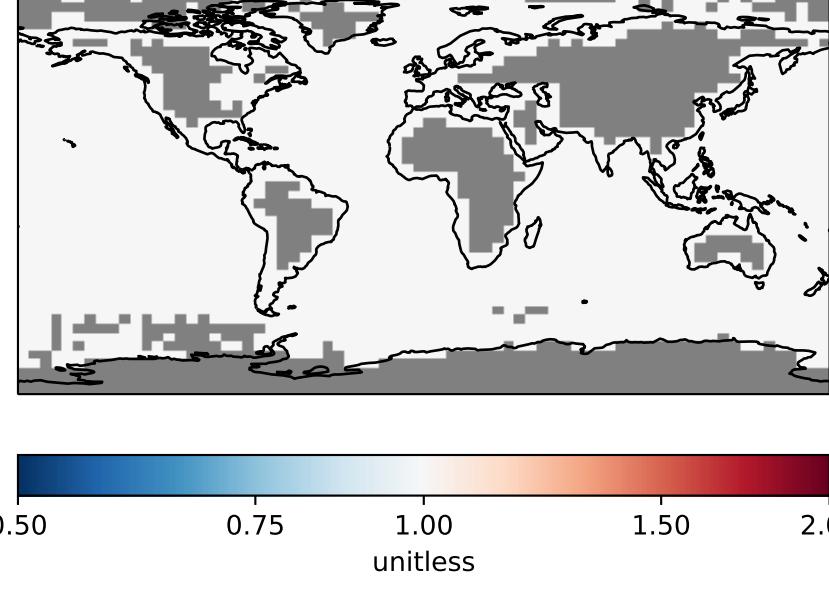
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

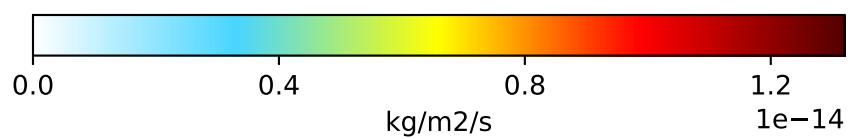
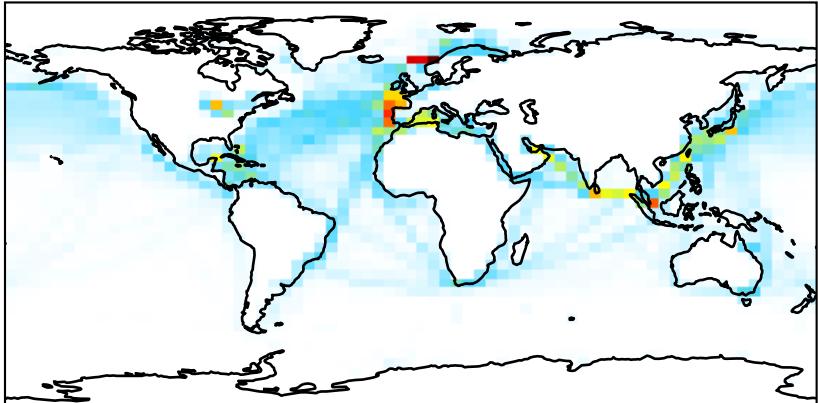


Ratio  
Dev/Ref, Fixed Range

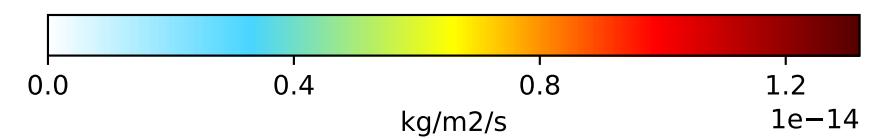
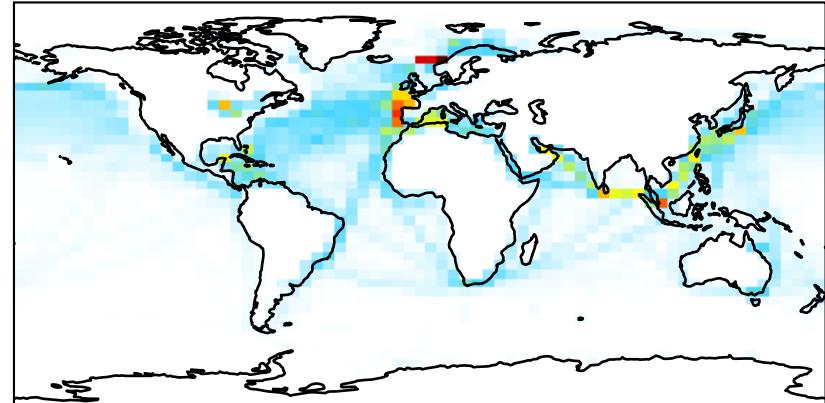


# EmispFe\_Ship

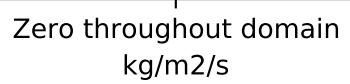
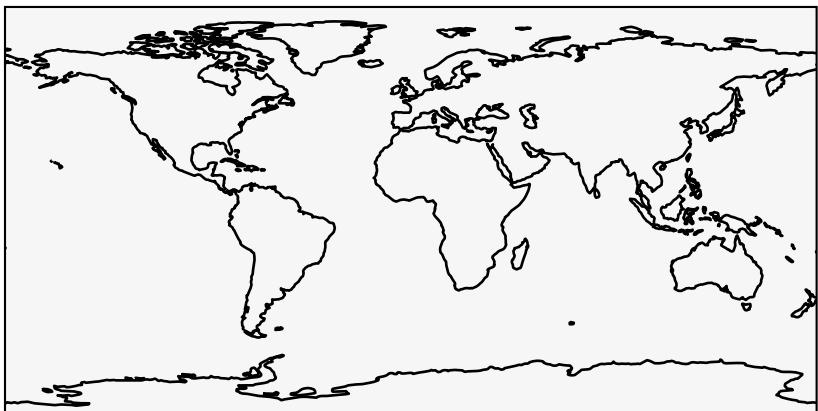
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



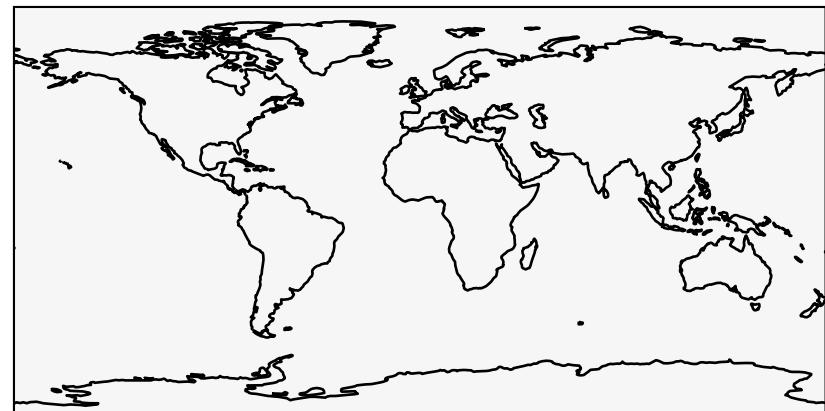
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



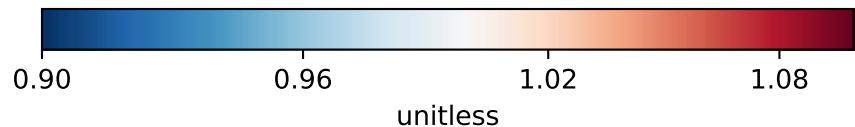
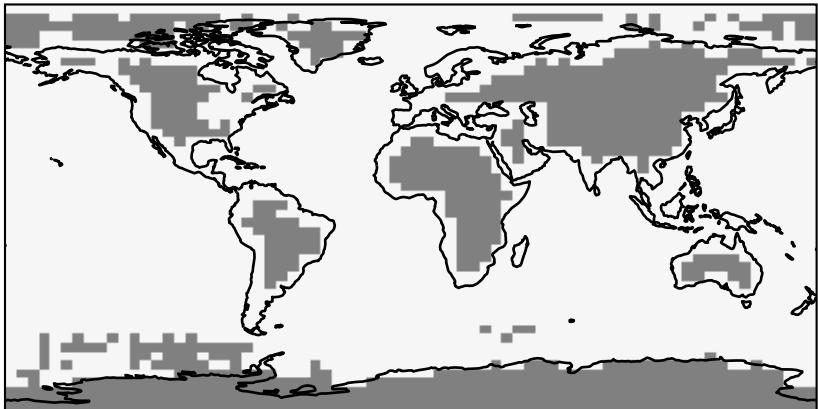
Difference  
Dev - Ref, Dynamic Range



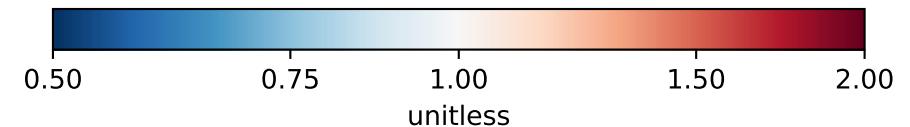
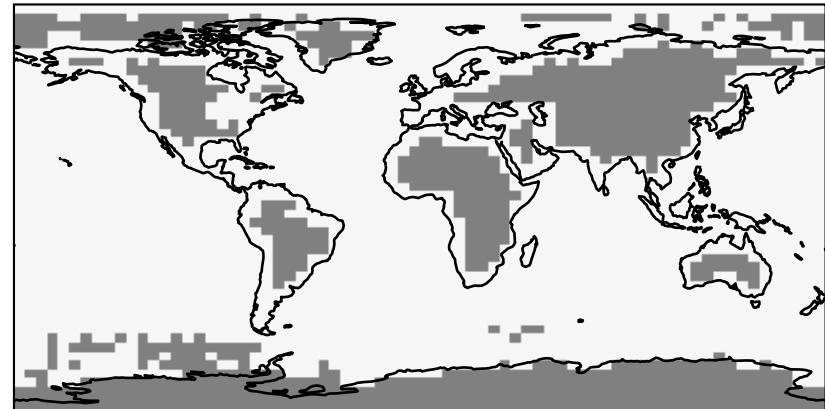
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

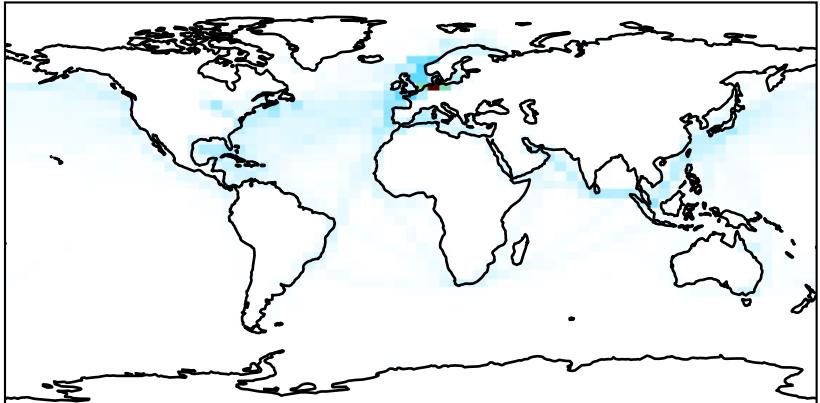


Ratio  
Dev/Ref, Fixed Range

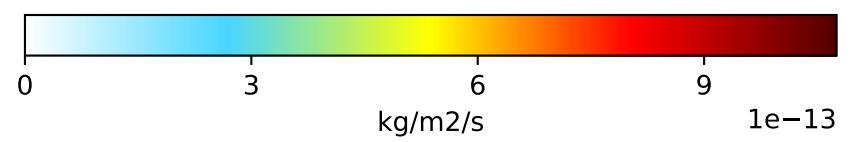
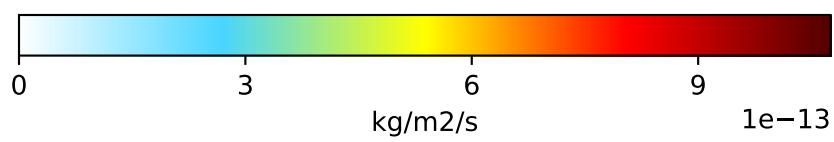
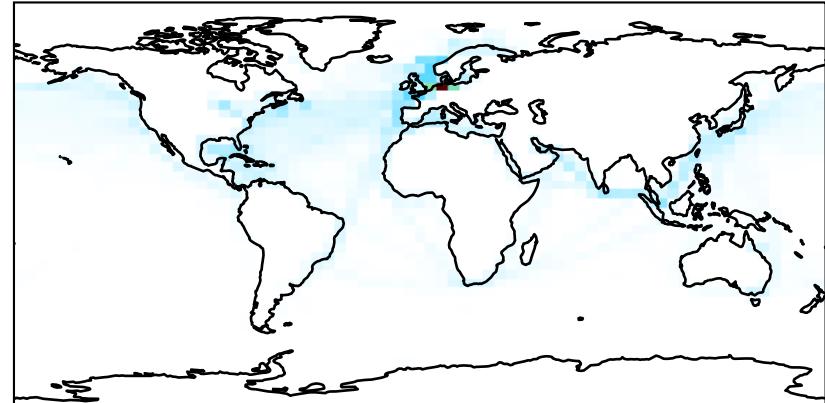


# EmisPRPE\_Ship

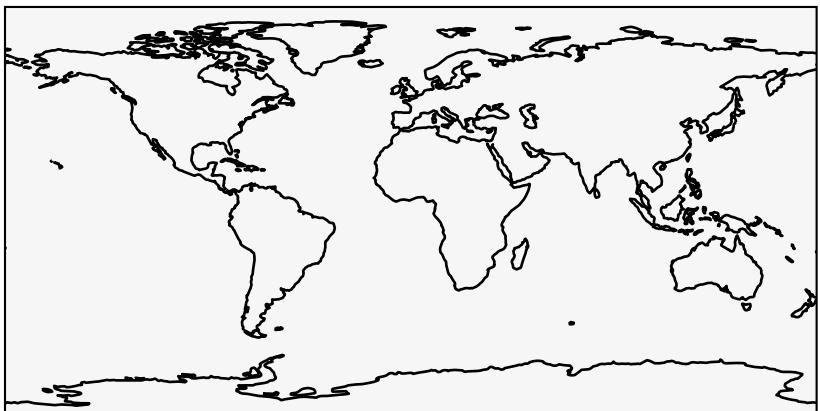
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0

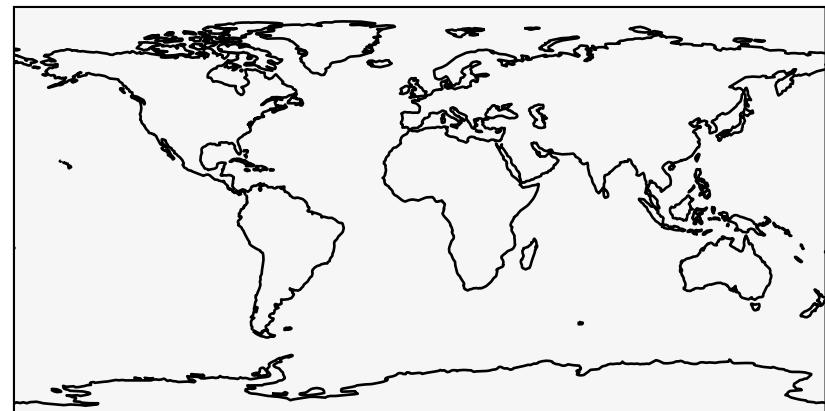


Difference  
Dev - Ref, Dynamic Range



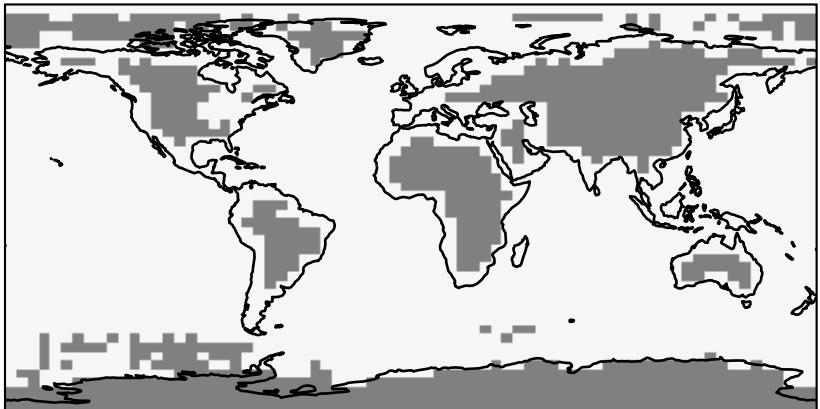
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



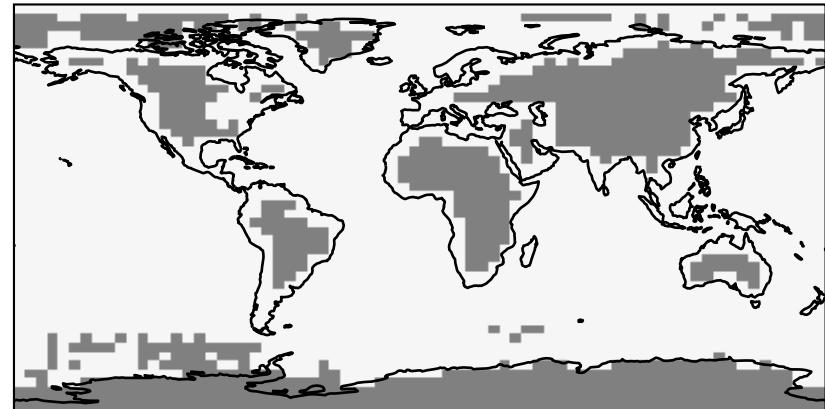
Zero throughout domain  
kg/m<sup>2</sup>/s

Ratio  
Dev/Ref, Dynamic Range



0.90 0.96 1.02 1.08  
unitless

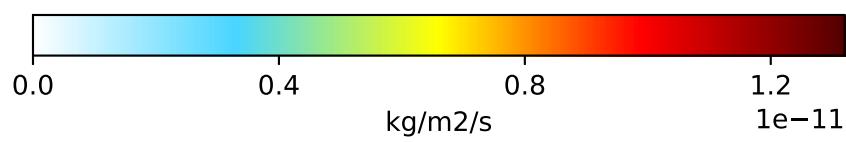
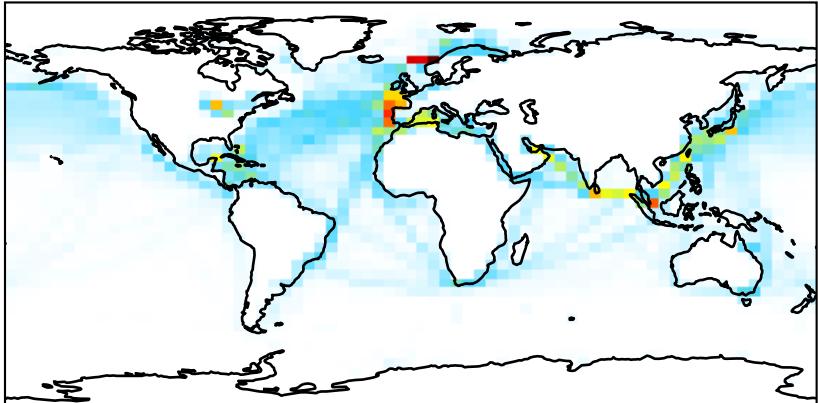
Ratio  
Dev/Ref, Fixed Range



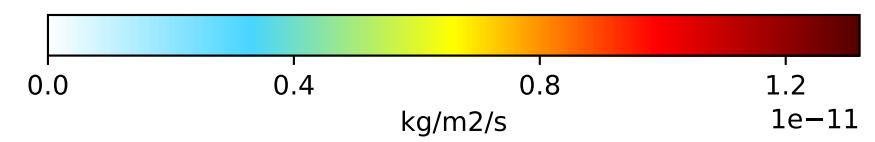
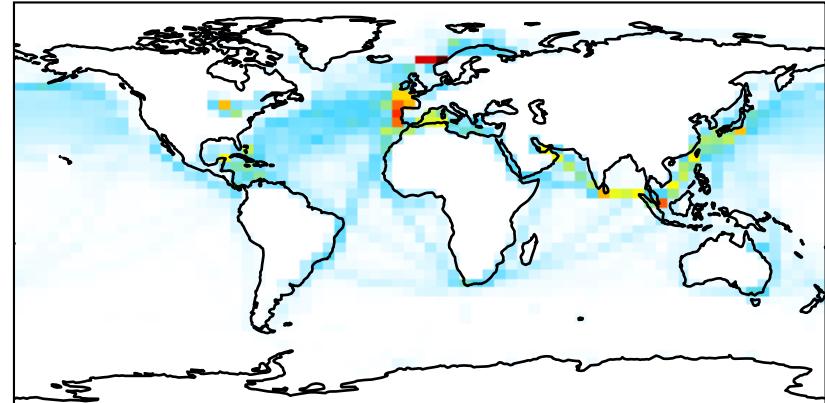
0.50 0.75 1.00 1.50 2.00  
unitless

# EmisSO2\_Ship

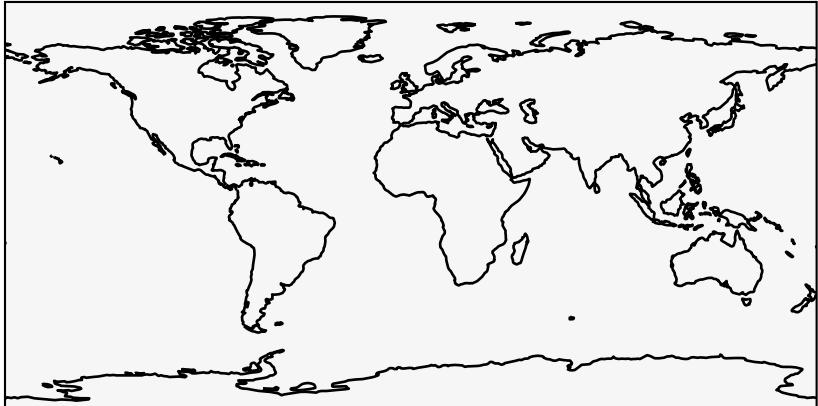
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0

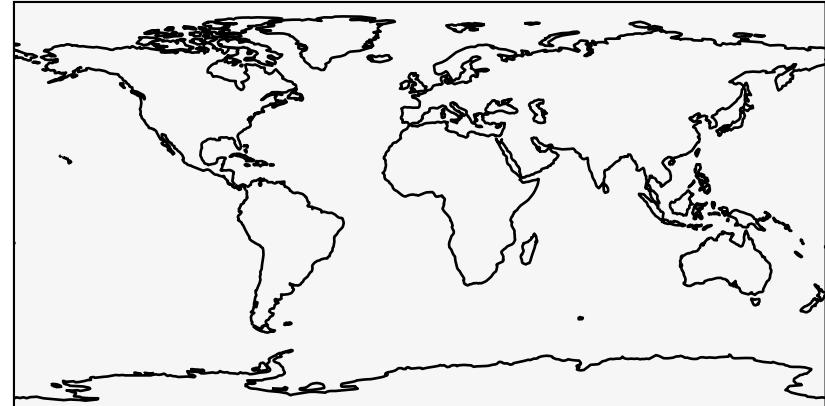


Difference  
Dev - Ref, Dynamic Range



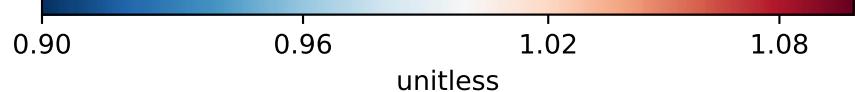
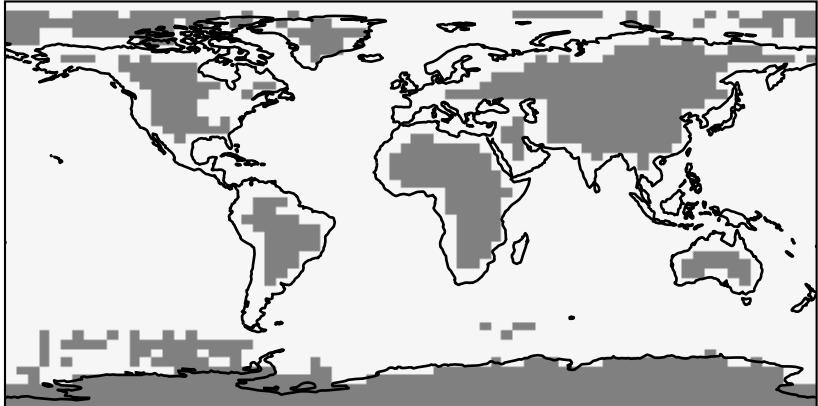
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%,95%]

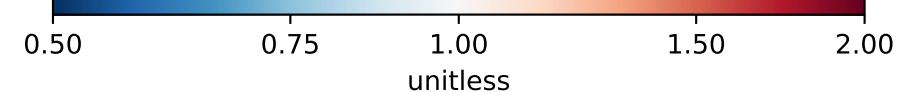
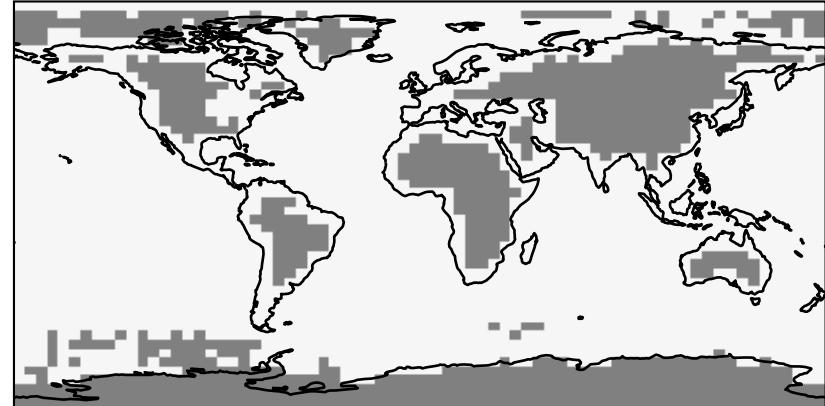


Zero throughout domain  
kg/m<sup>2</sup>/s

Ratio  
Dev/Ref, Dynamic Range

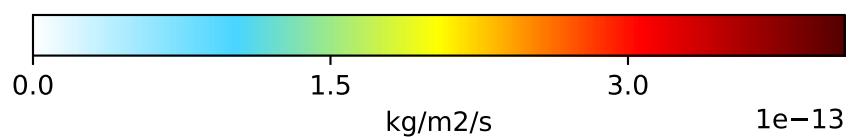
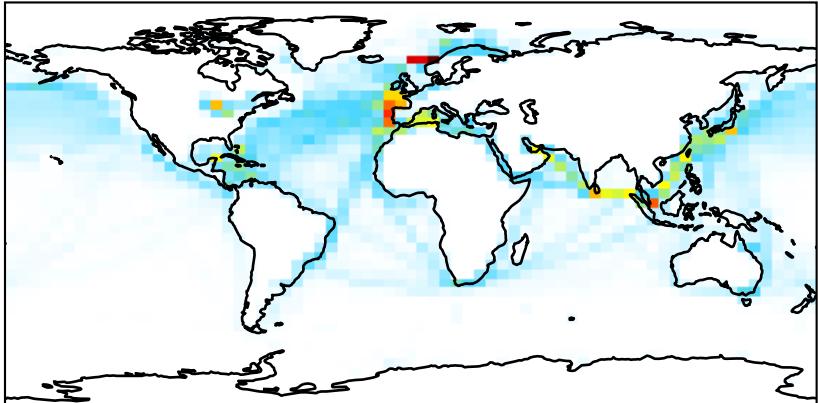


Ratio  
Dev/Ref, Fixed Range

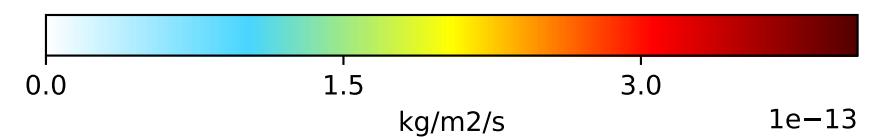
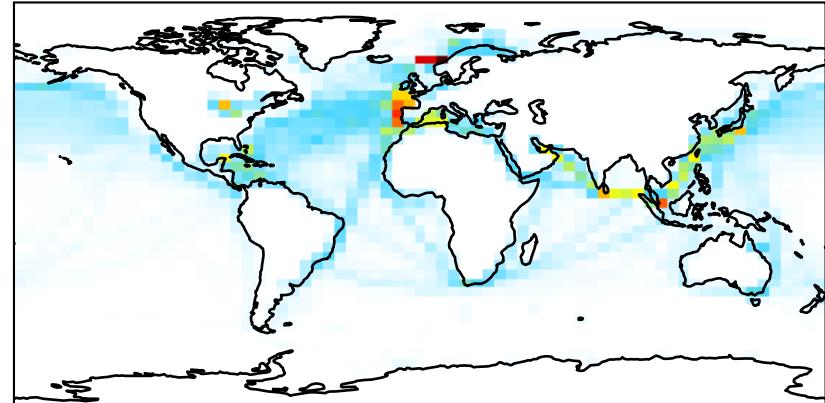


# EmisSO4\_Ship

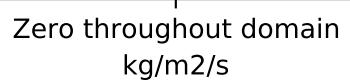
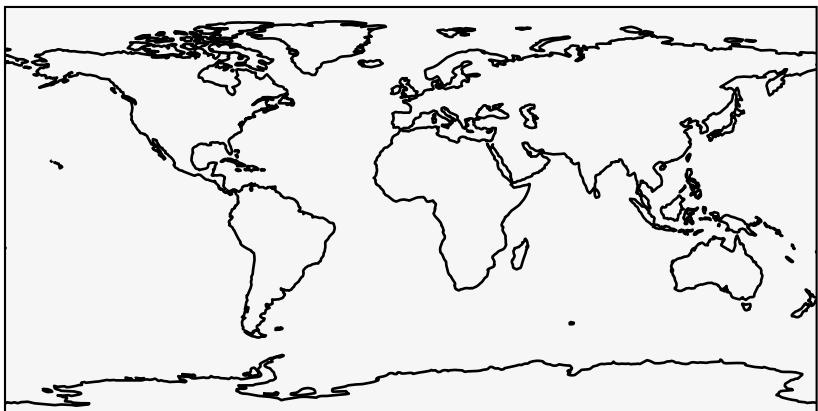
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



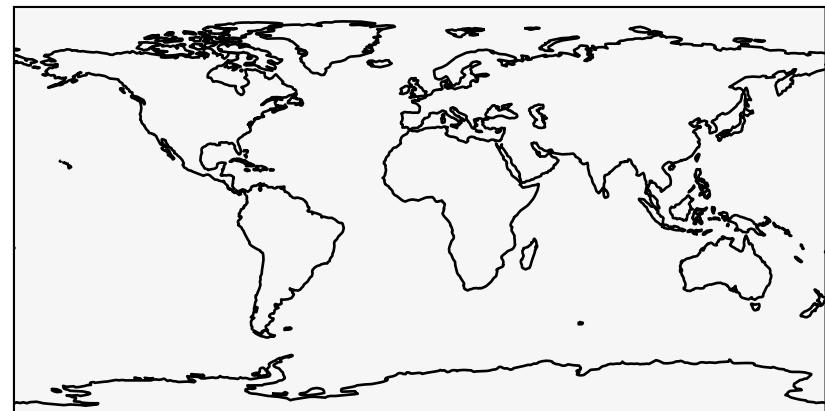
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



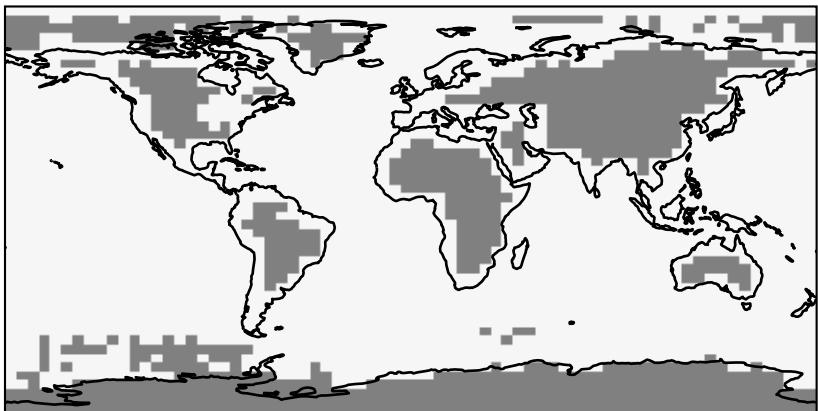
Difference  
Dev - Ref, Dynamic Range



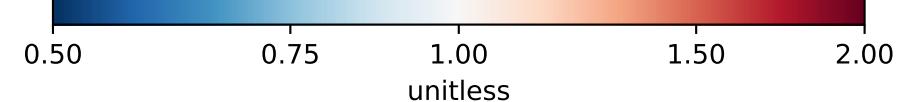
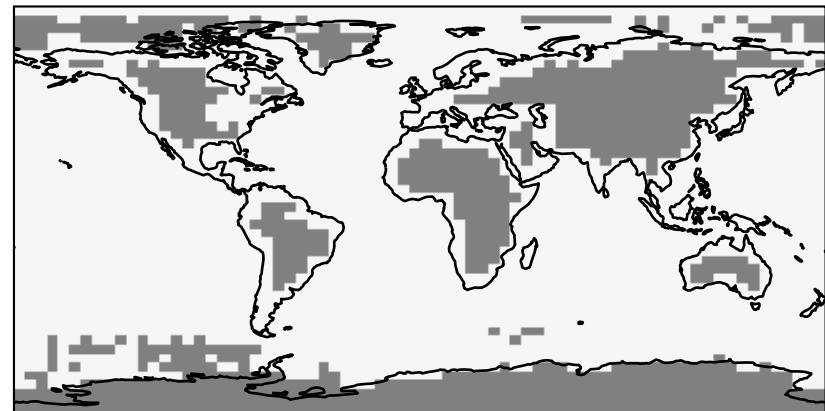
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

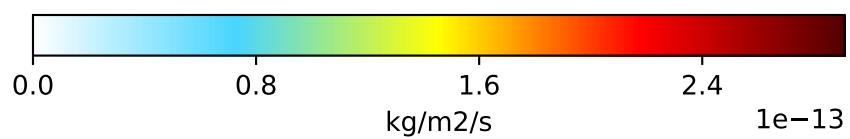
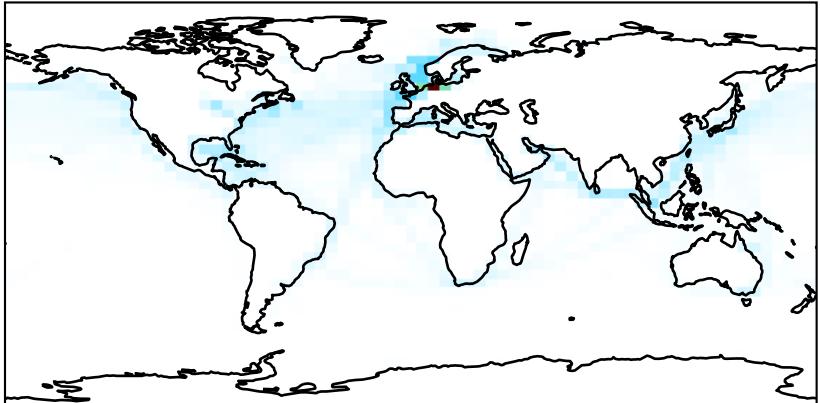


Ratio  
Dev/Ref, Fixed Range

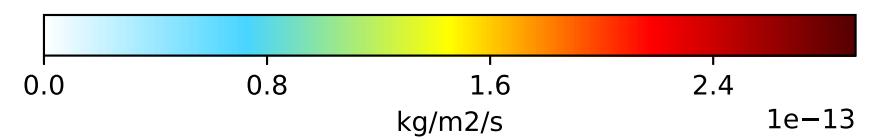
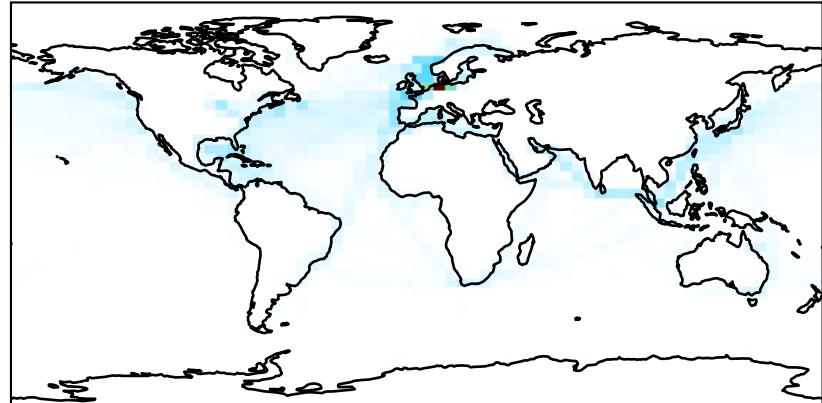


# EmisSOAP\_Ship

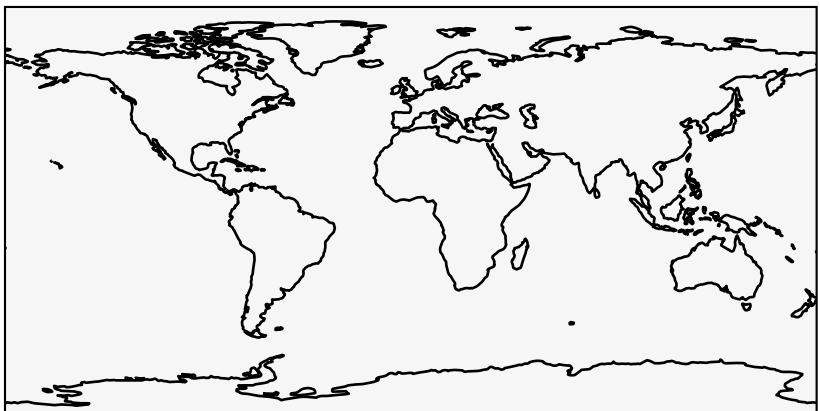
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0

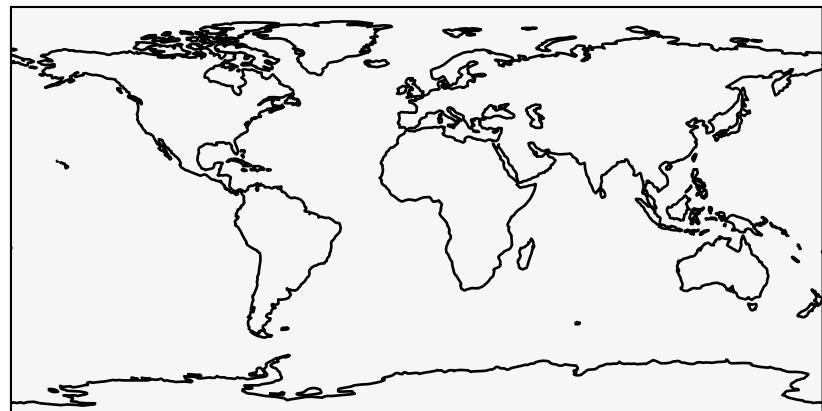


Difference  
Dev - Ref, Dynamic Range



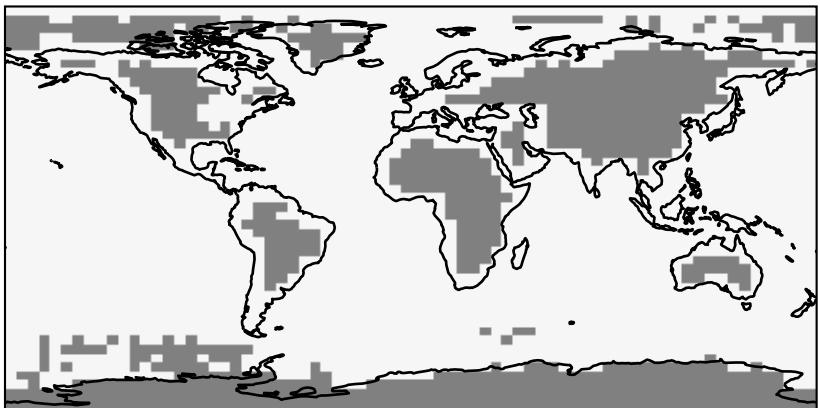
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



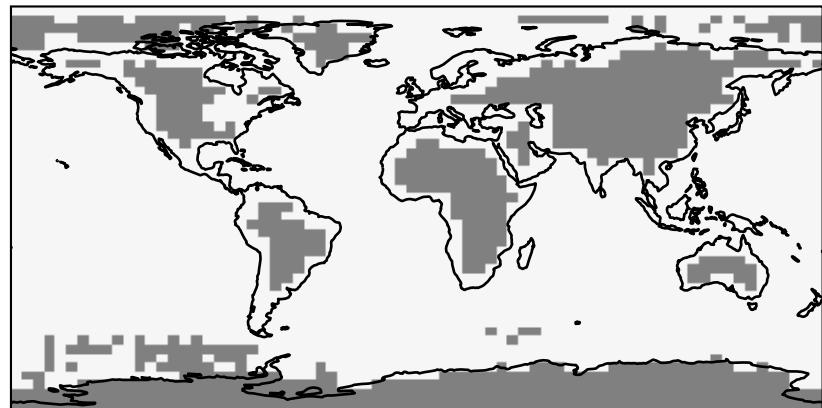
Zero throughout domain  
kg/m<sup>2</sup>/s

Ratio  
Dev/Ref, Dynamic Range



0.90 0.96 1.02 1.08  
unitless

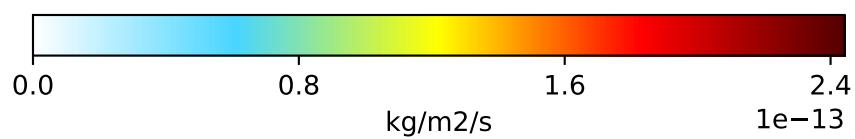
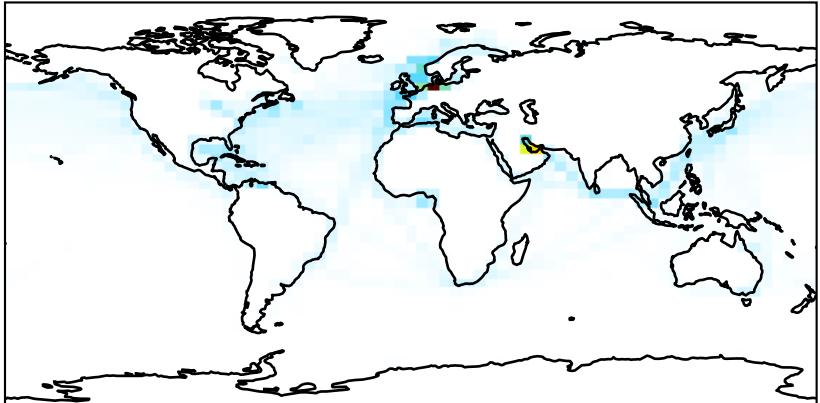
Ratio  
Dev/Ref, Fixed Range



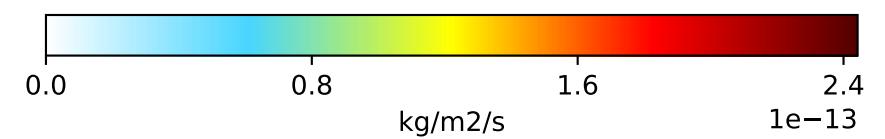
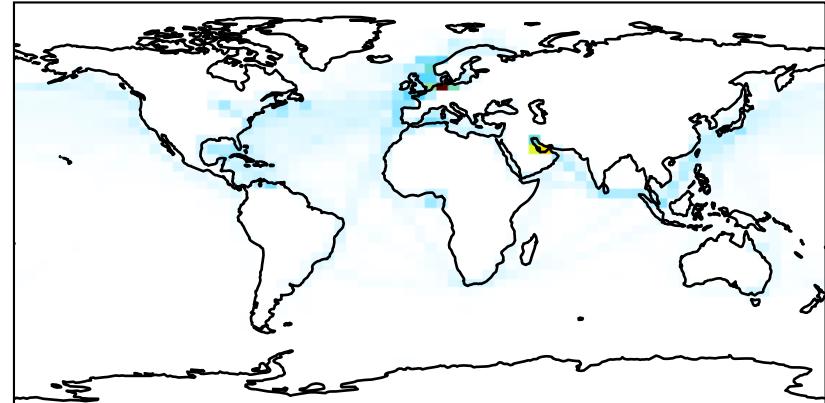
0.50 0.75 1.00 1.50 2.00  
unitless

# EmisTOLU\_Ship

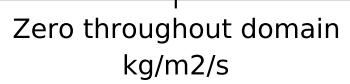
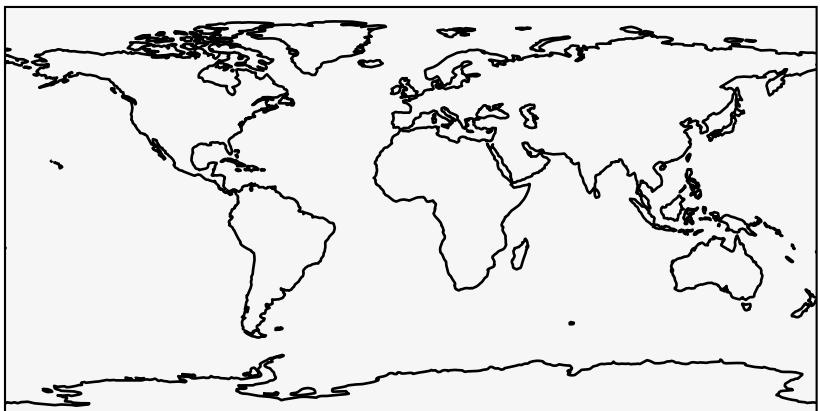
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



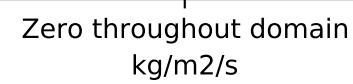
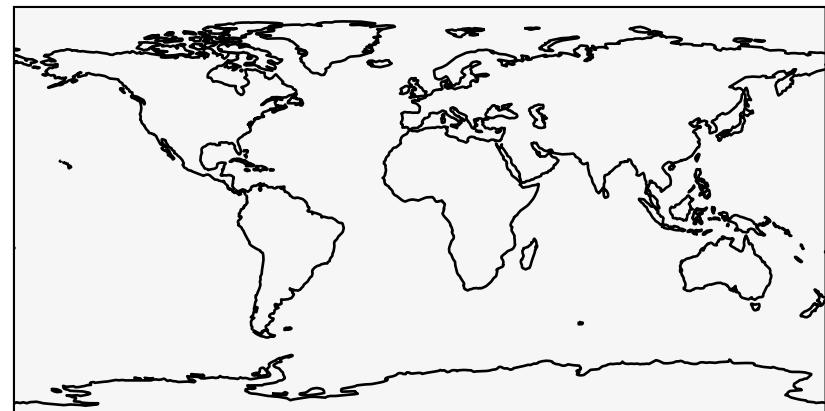
GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0



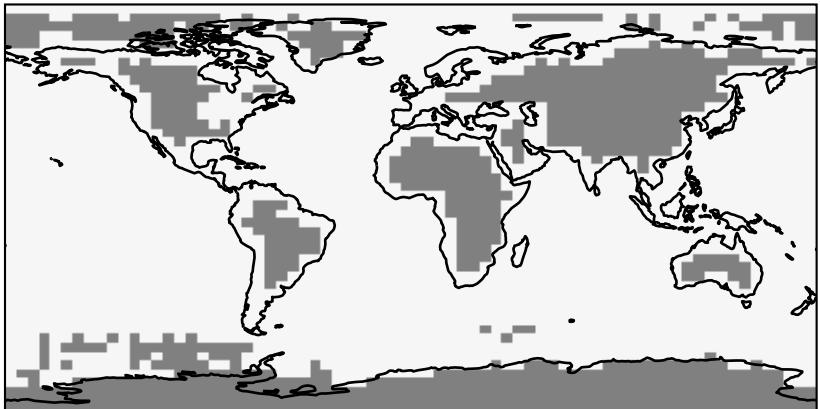
Difference  
Dev - Ref, Dynamic Range



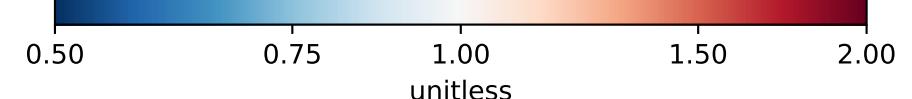
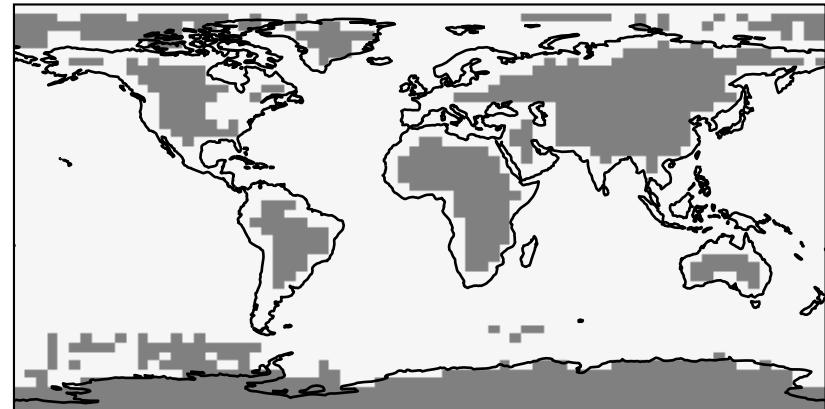
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

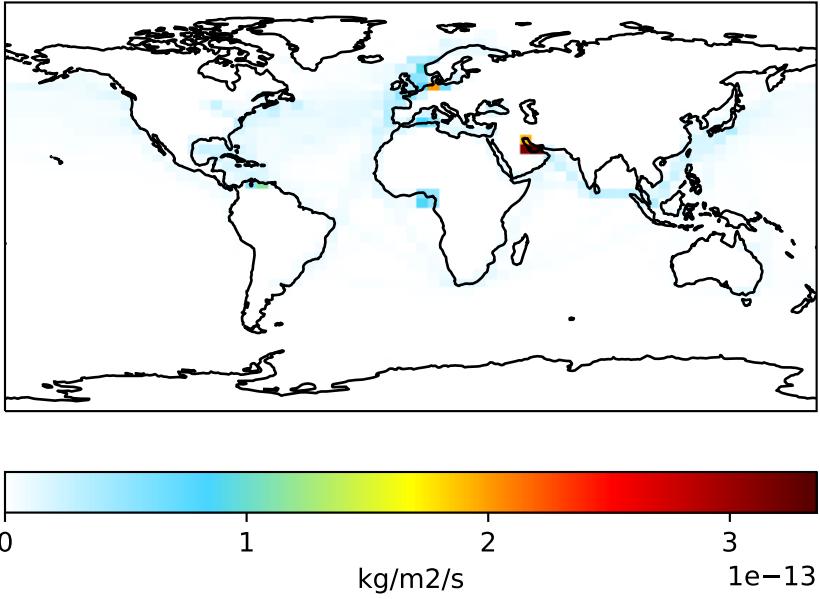


Ratio  
Dev/Ref, Fixed Range

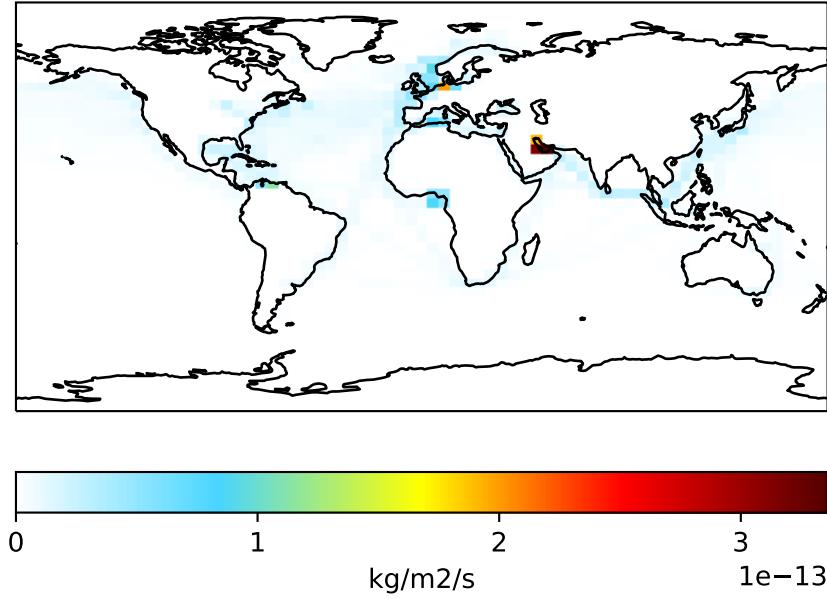


# EmisXYLE\_Ship

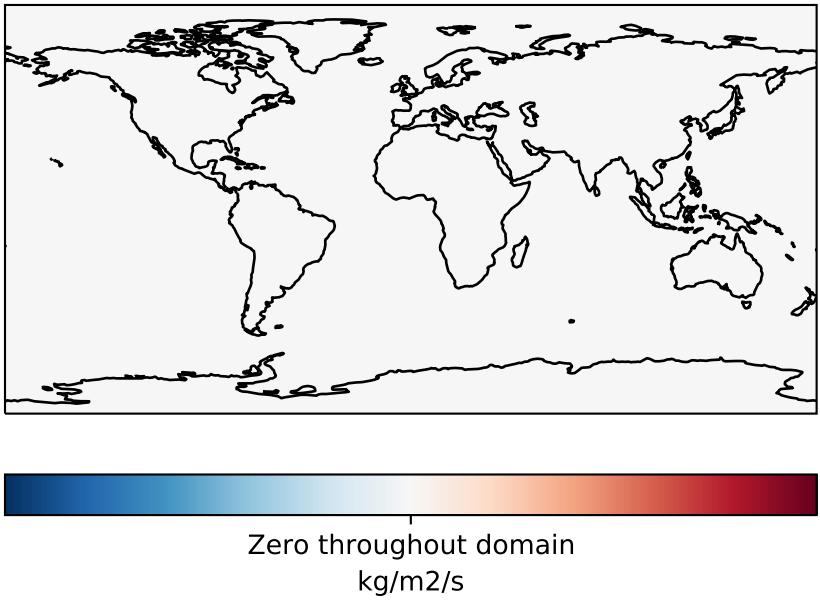
GCC\_13.0.0-rc.2 (Ref)  
4.0x5.0



GCC\_13.1.0-beta.1 (Dev)  
4.0x5.0

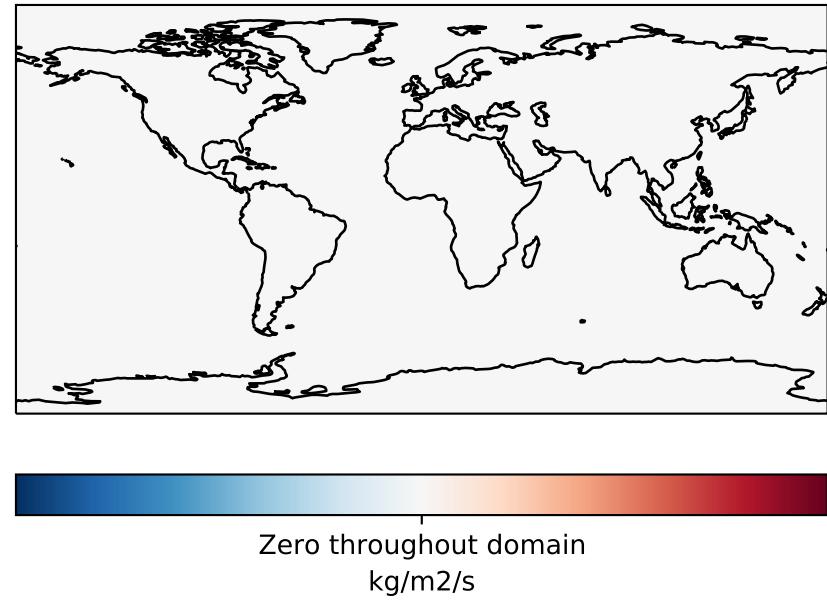


Difference  
Dev - Ref, Dynamic Range

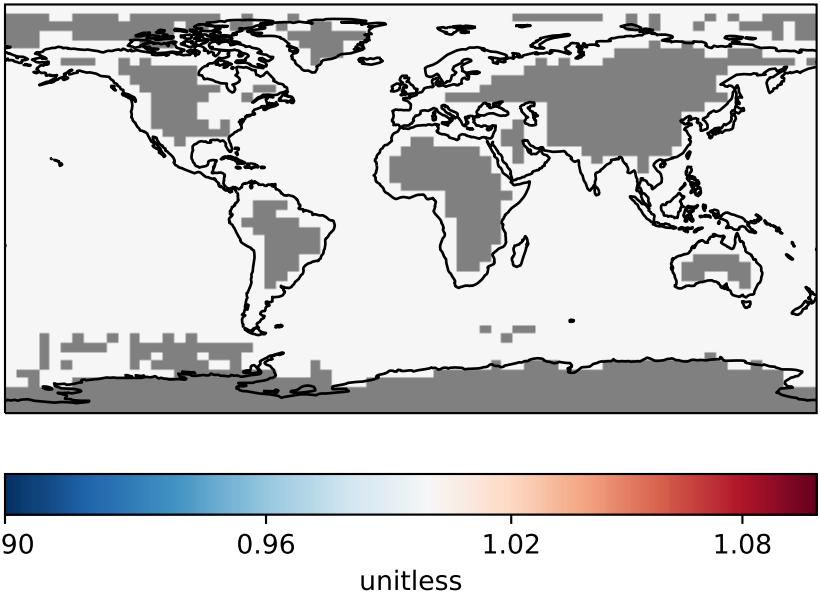


Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%,95%]

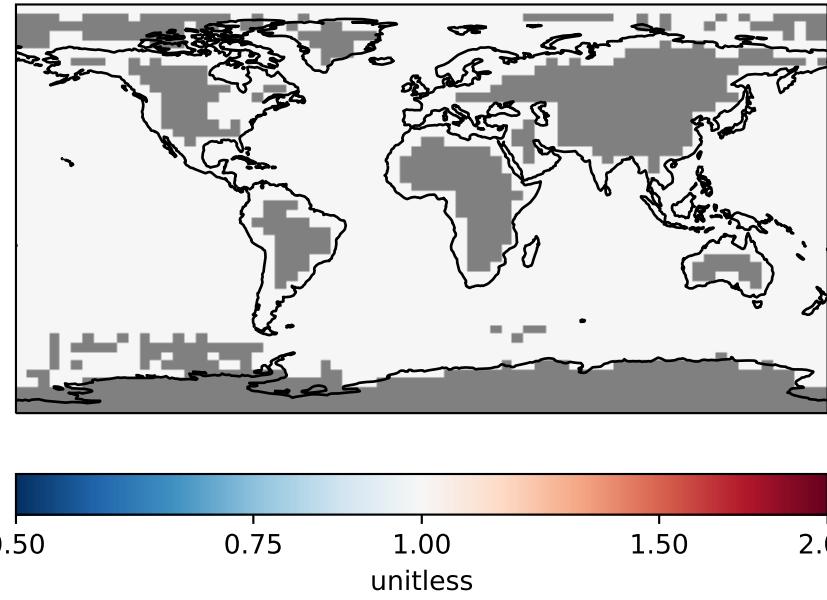


Ratio  
Dev/Ref, Dynamic Range



0.90 0.96 1.02 1.08  
unitless

Ratio  
Dev/Ref, Fixed Range



0.50 0.75 1.00 1.50 2.00  
unitless