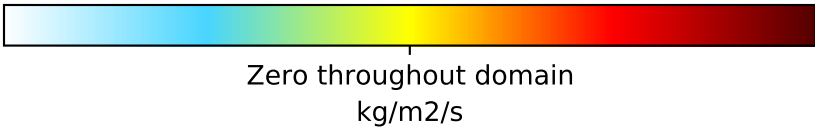
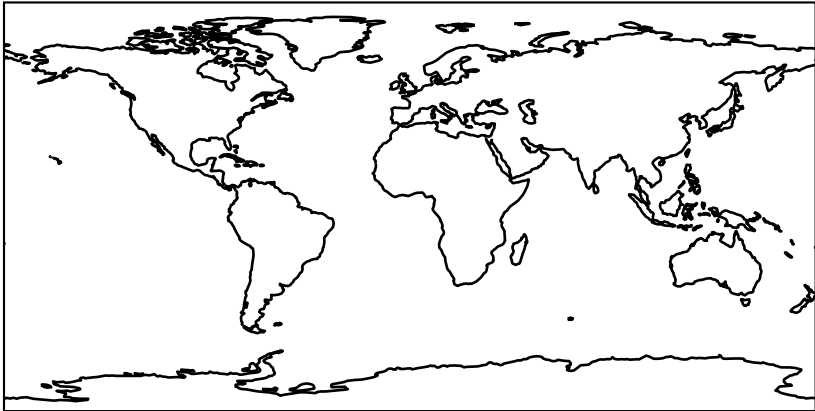
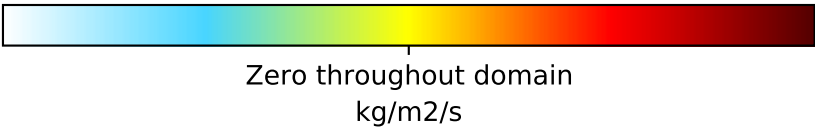
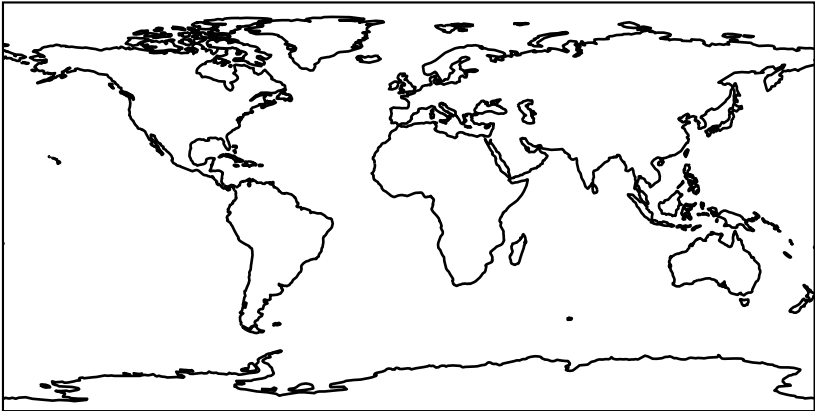


EmisALD2_Ship

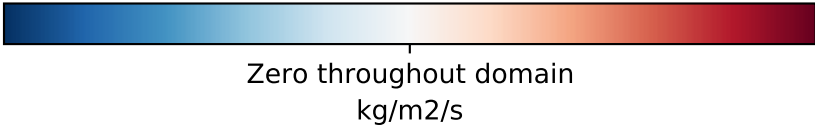
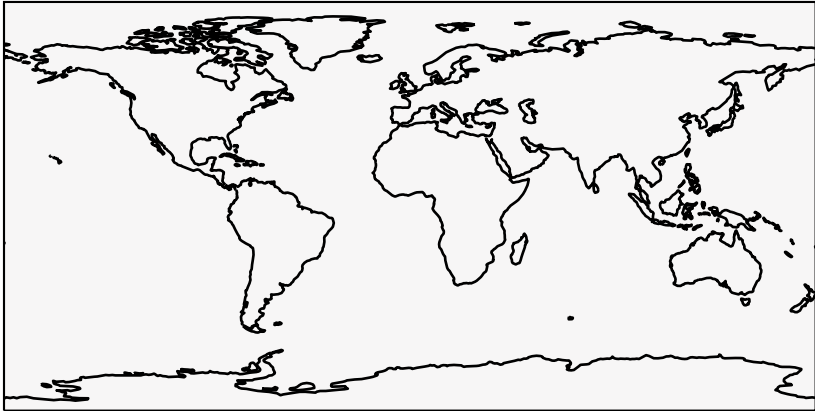
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



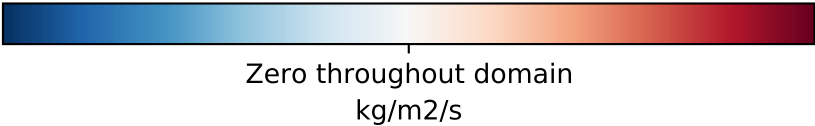
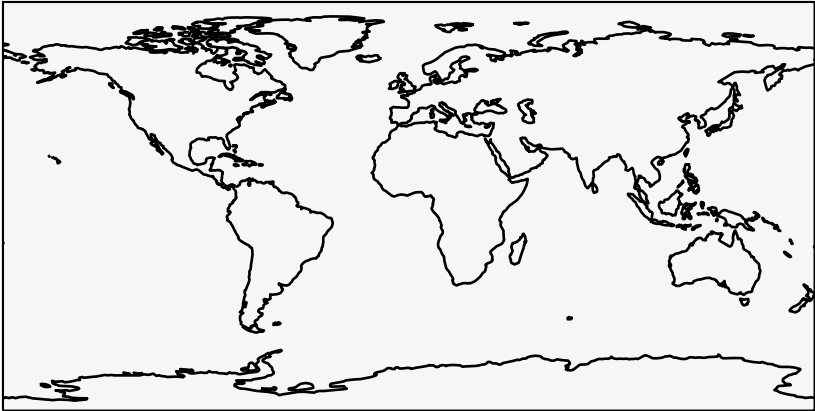
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



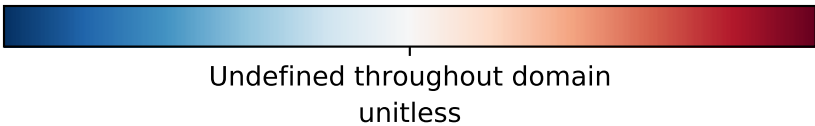
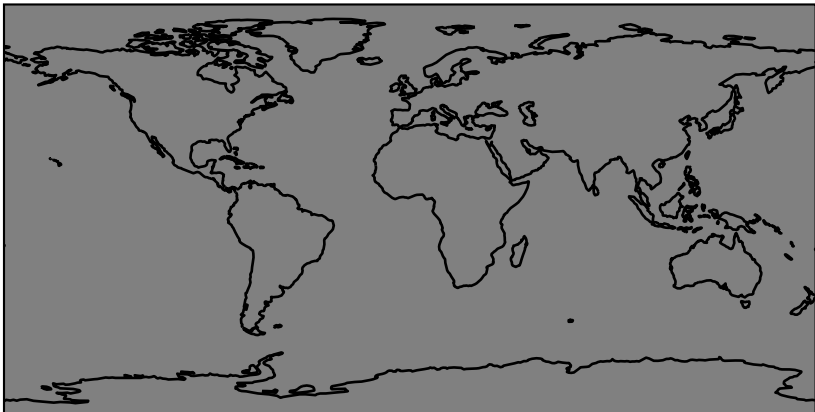
Difference
Dev - Ref, Dynamic Range



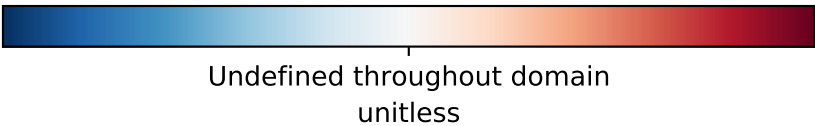
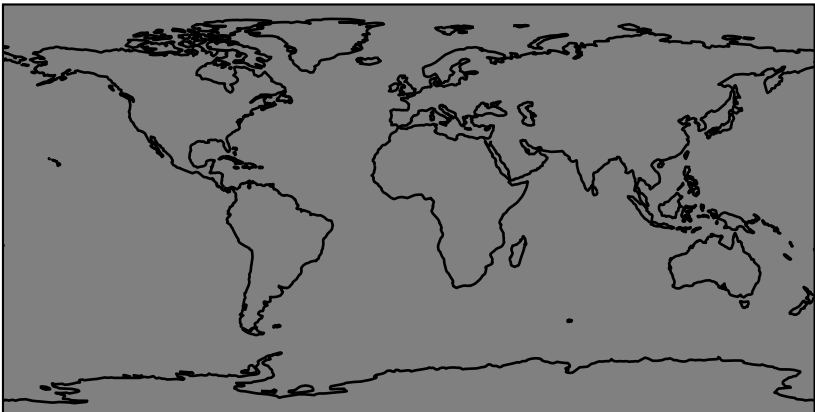
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

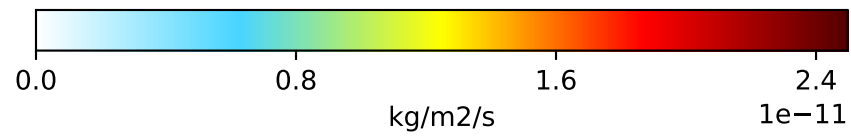


Ratio
Dev/Ref, Fixed Range

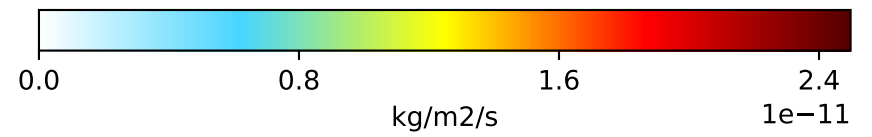
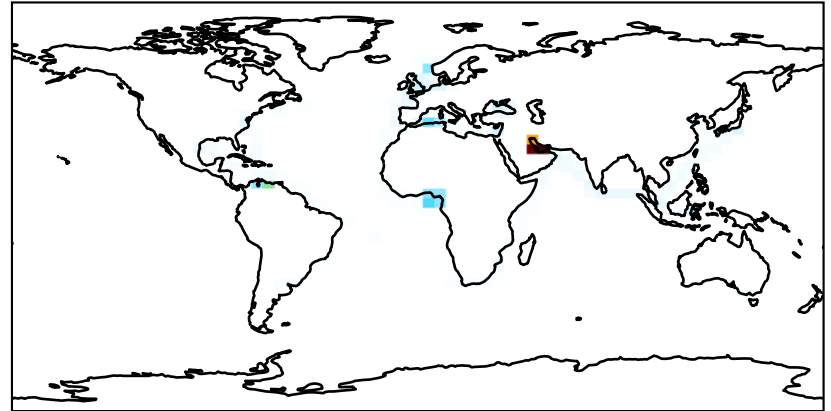


EmisALK4_Ship

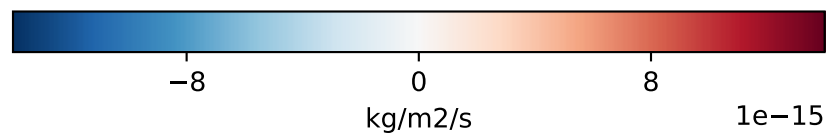
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



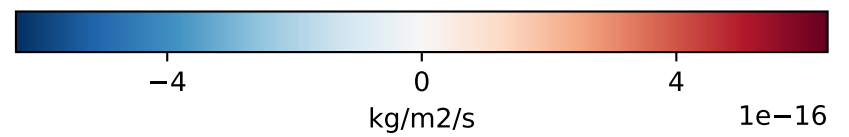
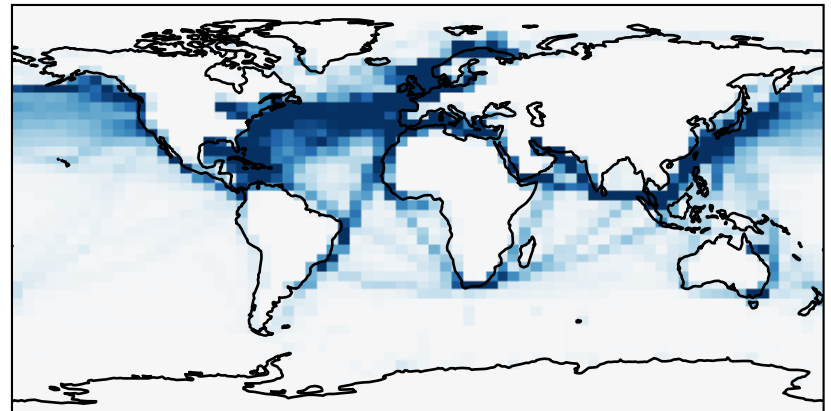
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



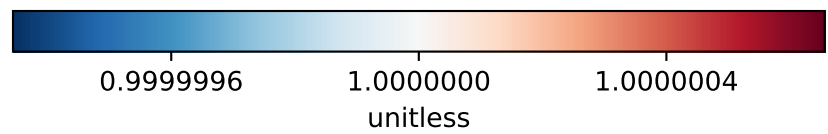
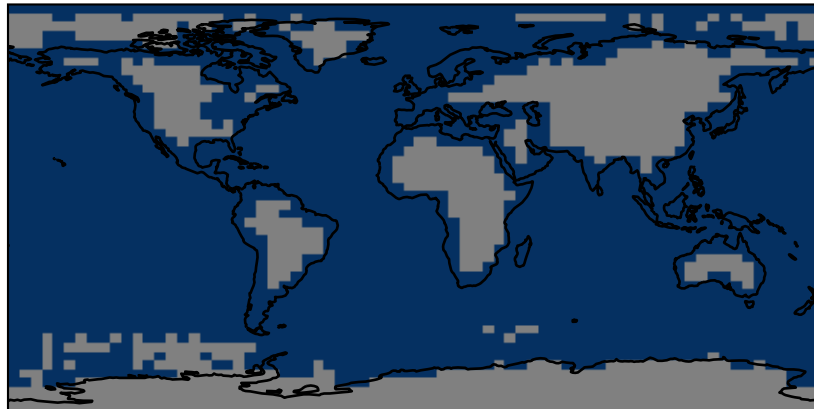
Difference
Dev - Ref, Dynamic Range



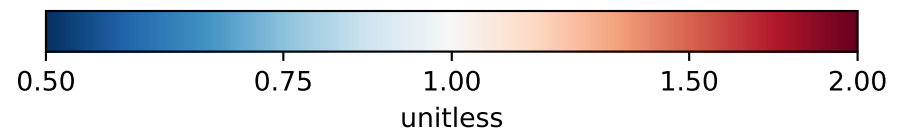
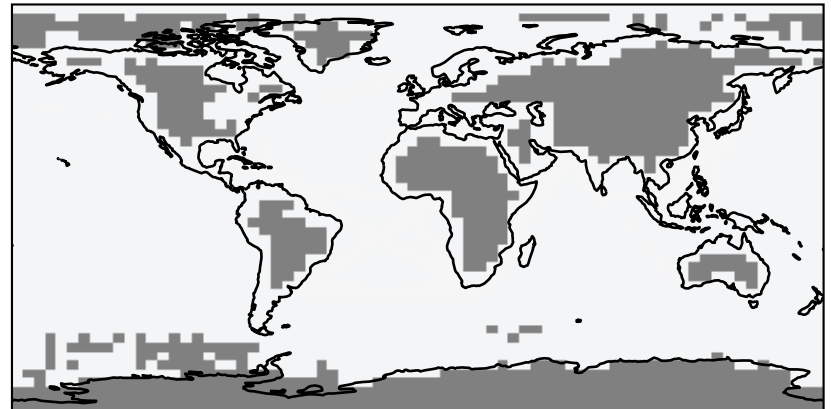
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

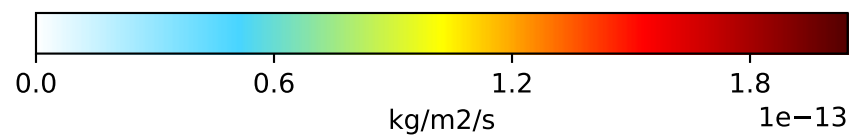
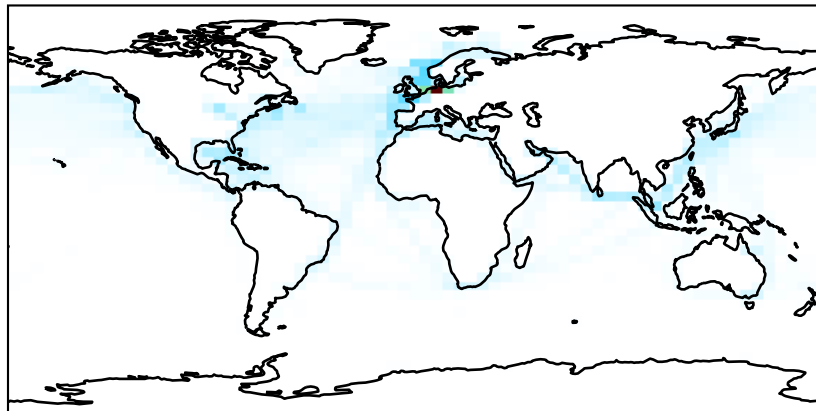


Ratio
Dev/Ref, Fixed Range

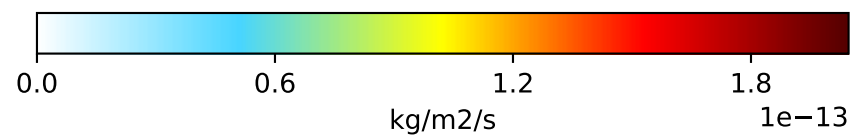
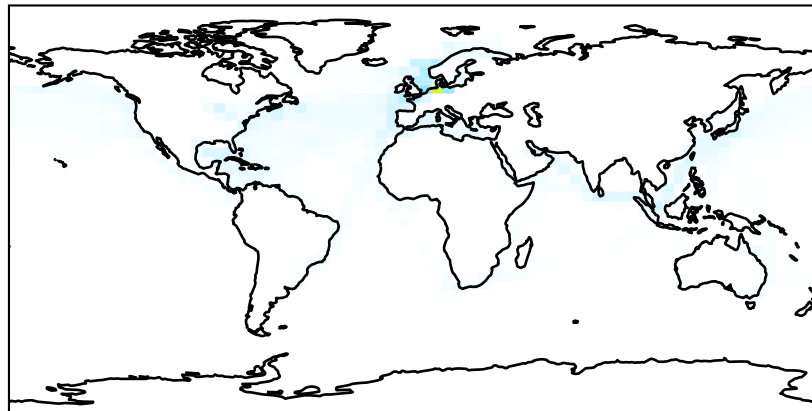


EmisBCPI_Ship

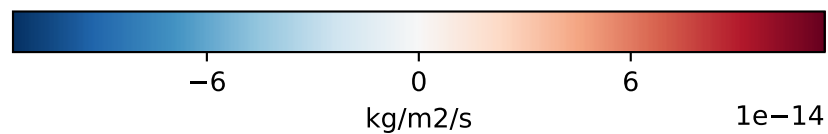
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



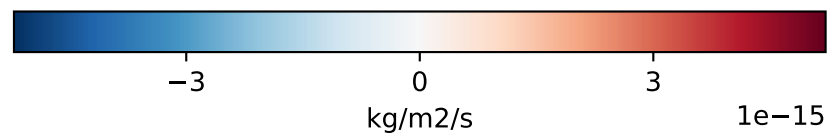
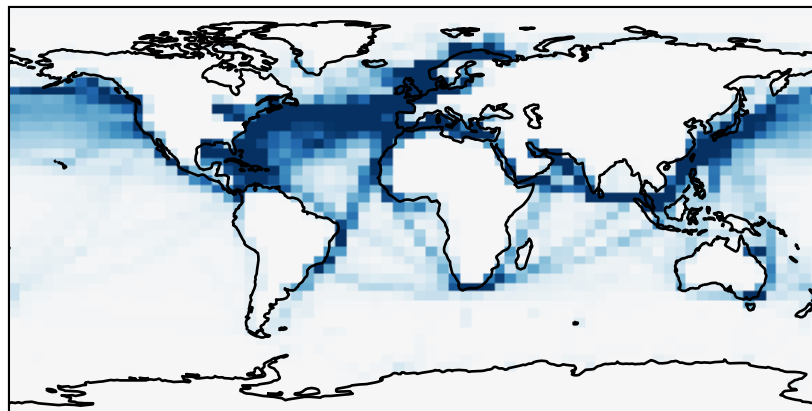
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



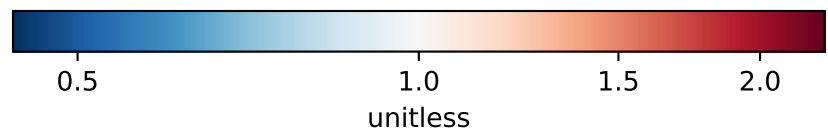
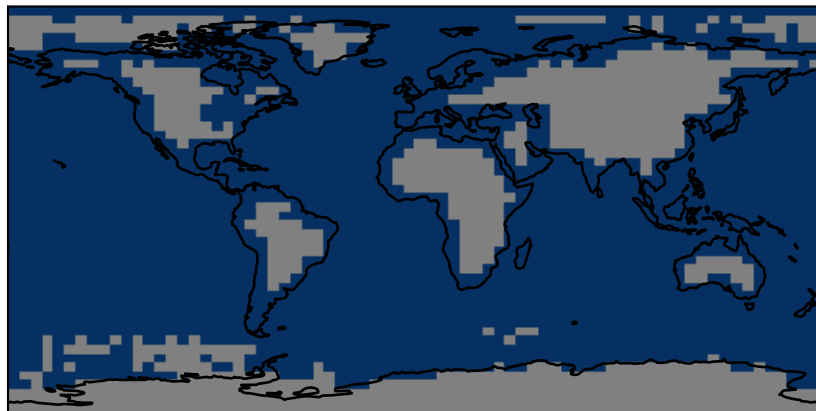
Difference
Dev - Ref, Dynamic Range



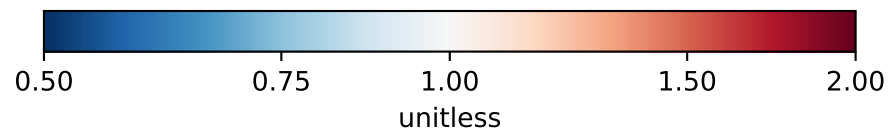
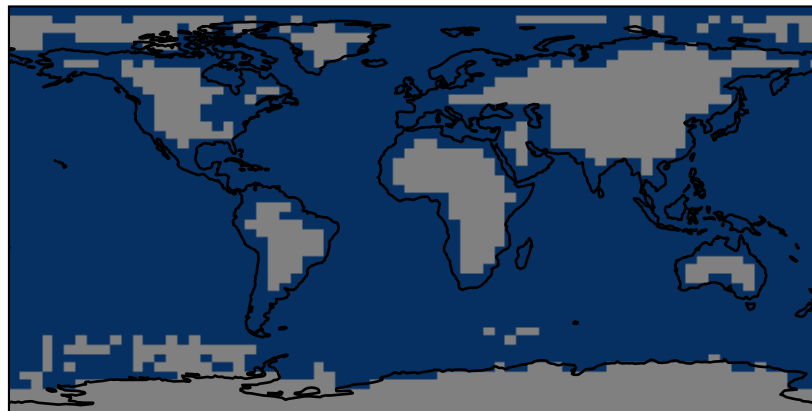
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

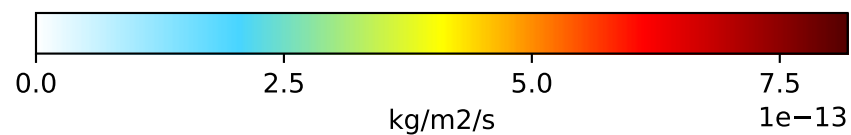
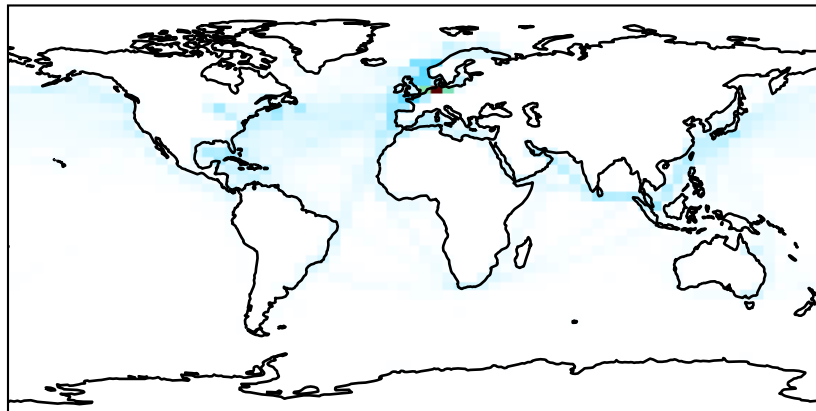


Ratio
Dev/Ref, Fixed Range

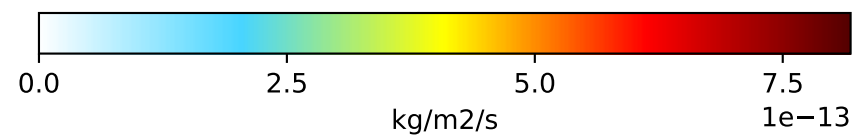
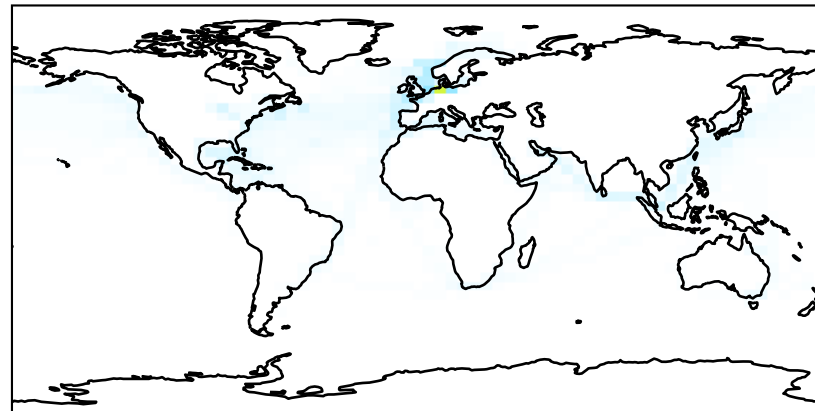


EmisBCPO_Ship

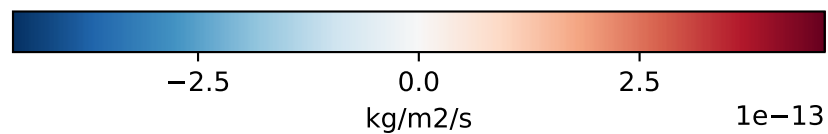
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



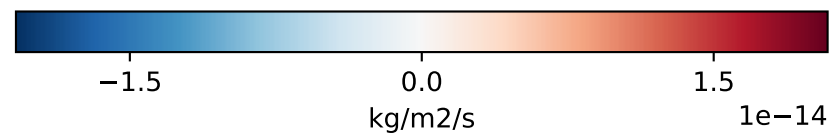
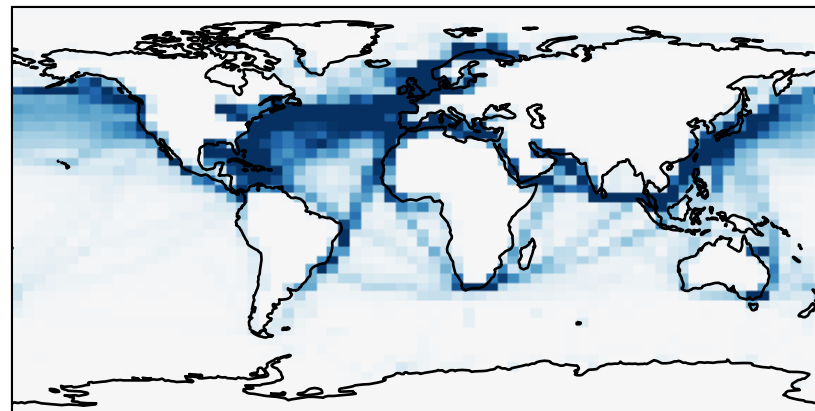
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



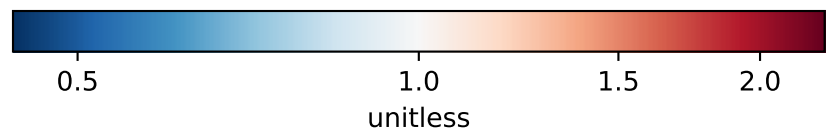
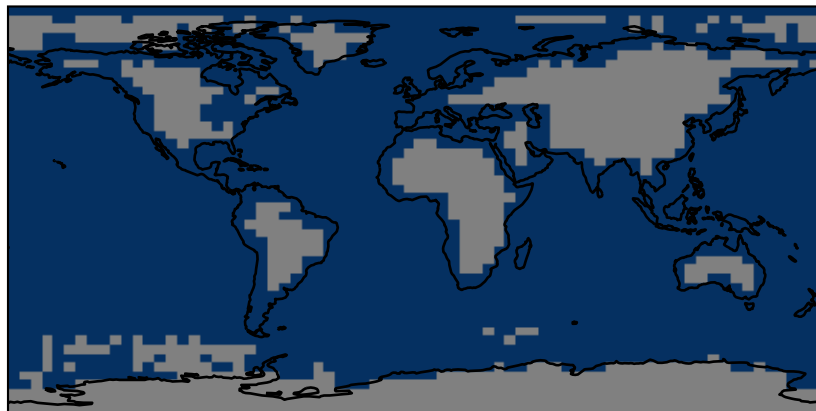
Difference
Dev - Ref, Dynamic Range



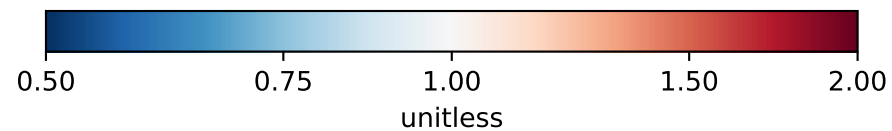
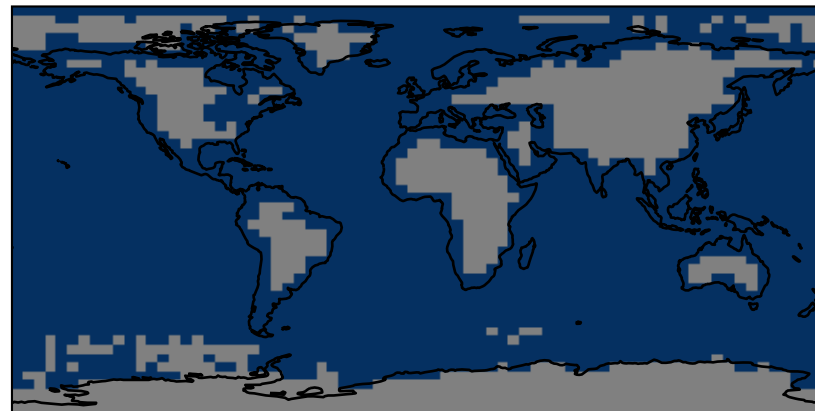
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

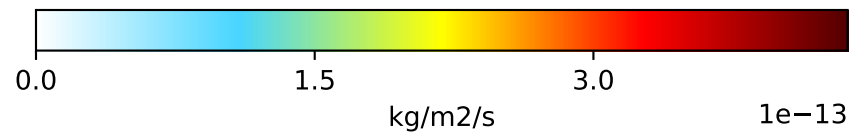
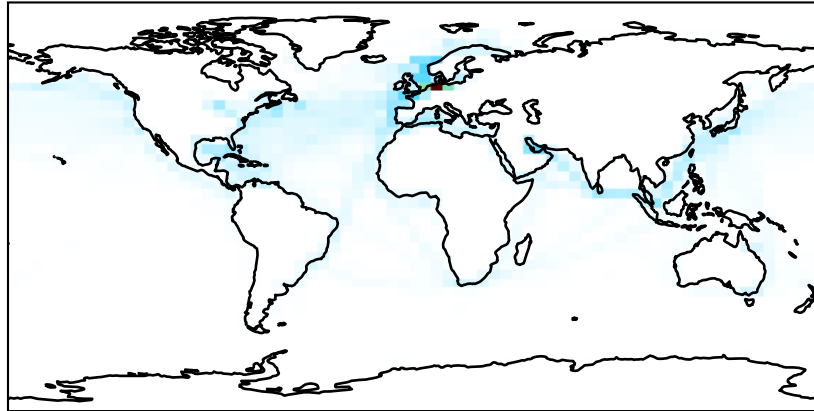


Ratio
Dev/Ref, Fixed Range

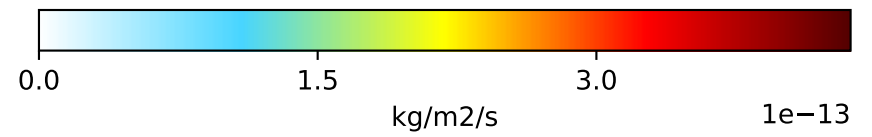
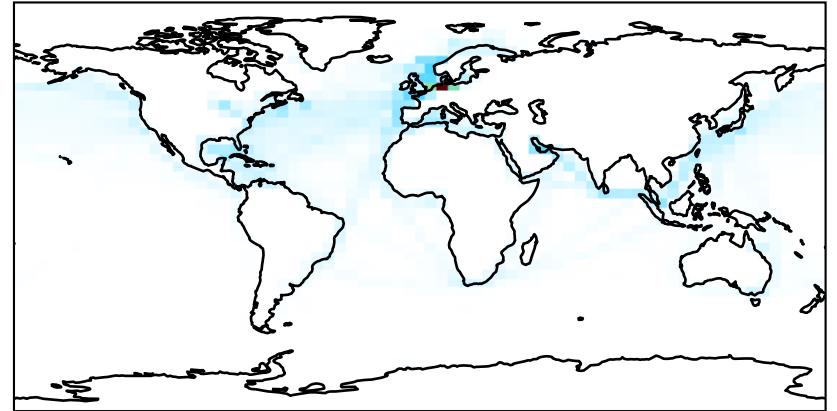


EmisBENZ_Ship

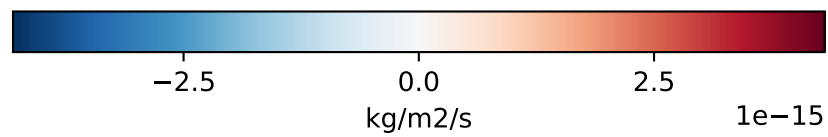
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



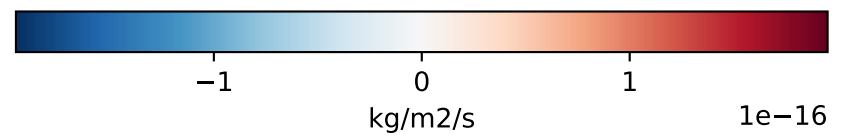
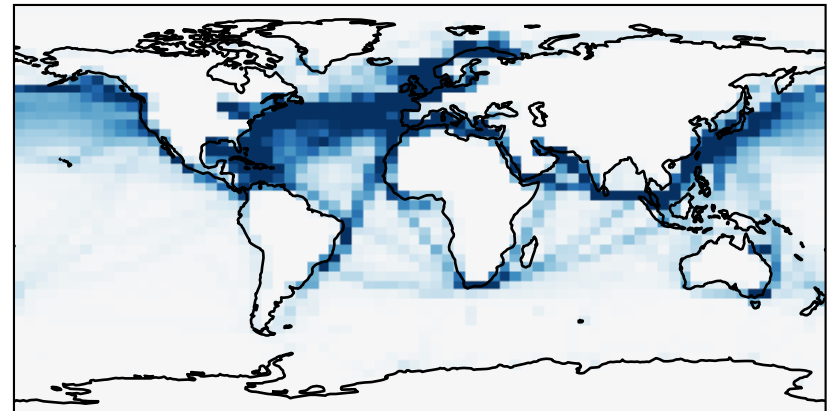
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



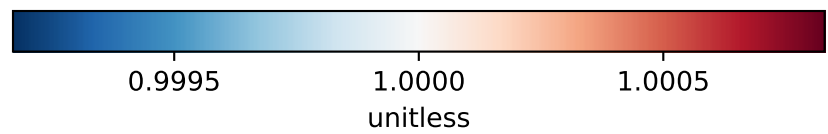
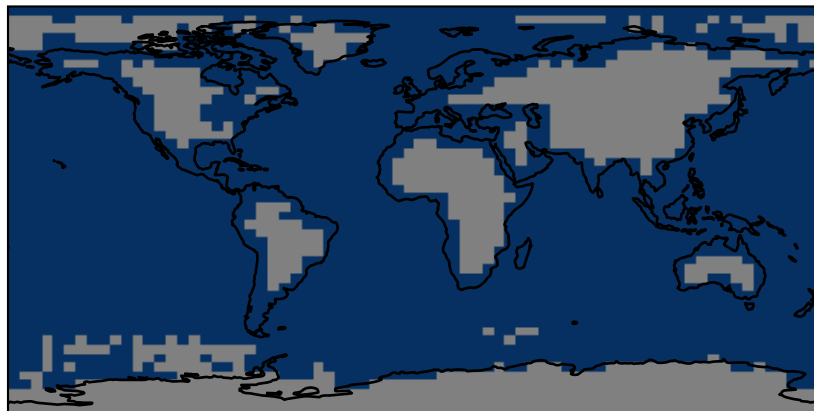
Difference
Dev - Ref, Dynamic Range



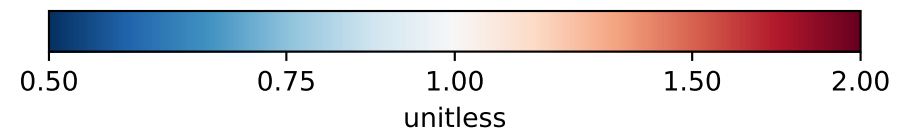
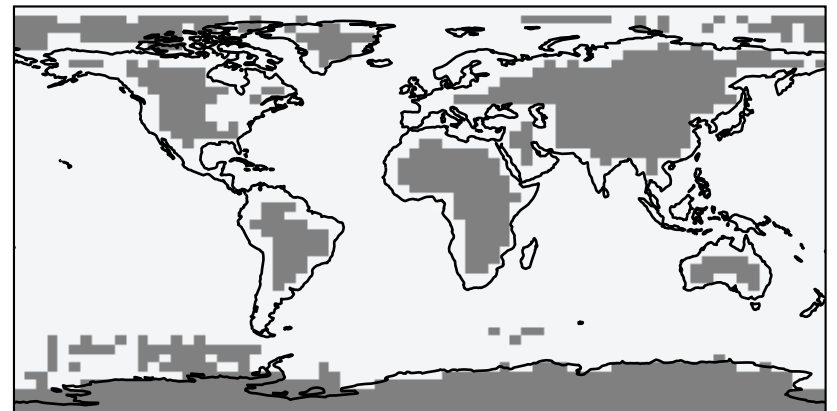
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

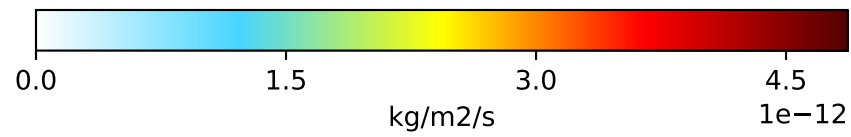


Ratio
Dev/Ref, Fixed Range

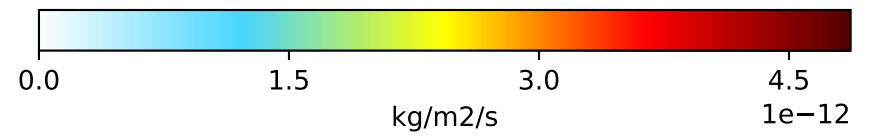


EmisC2H6_Ship

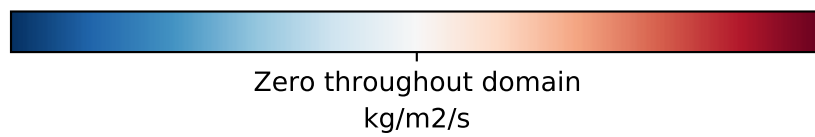
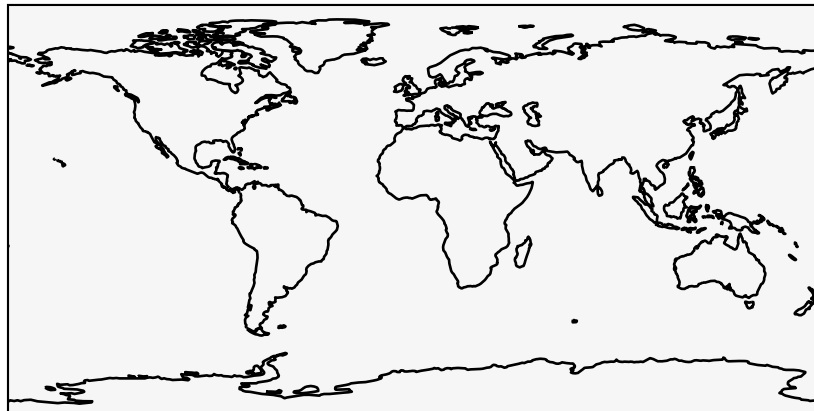
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



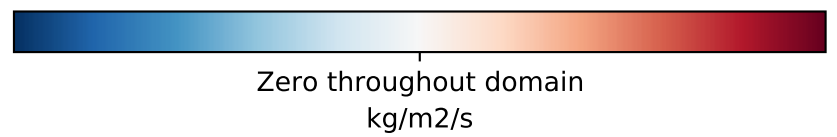
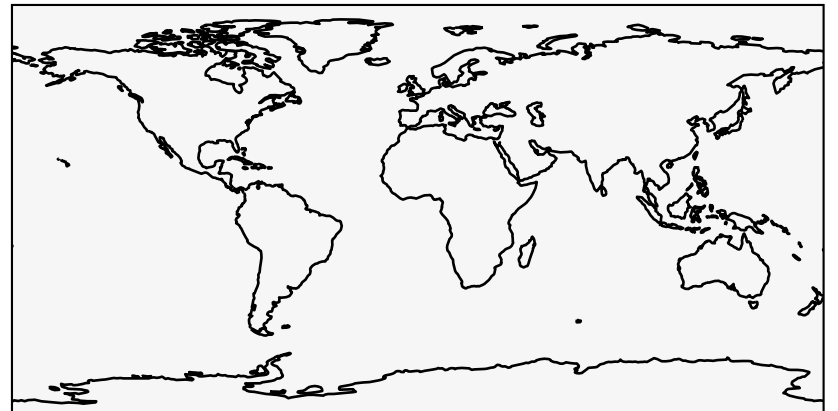
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



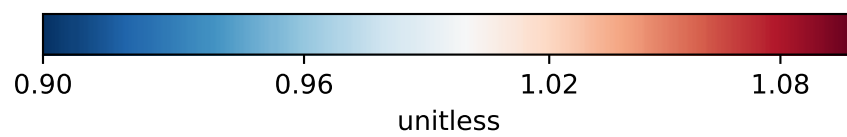
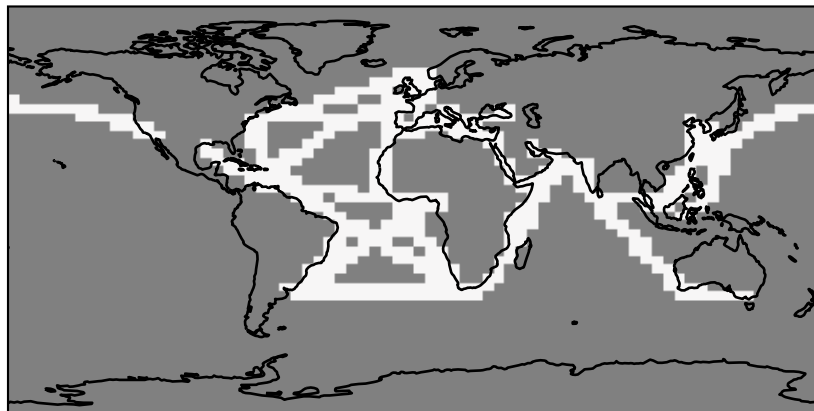
Difference
Dev - Ref, Dynamic Range



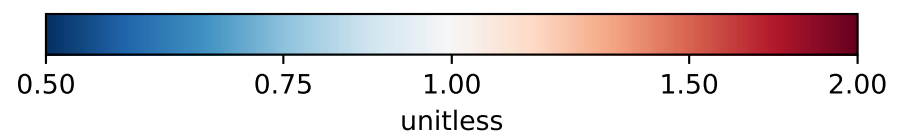
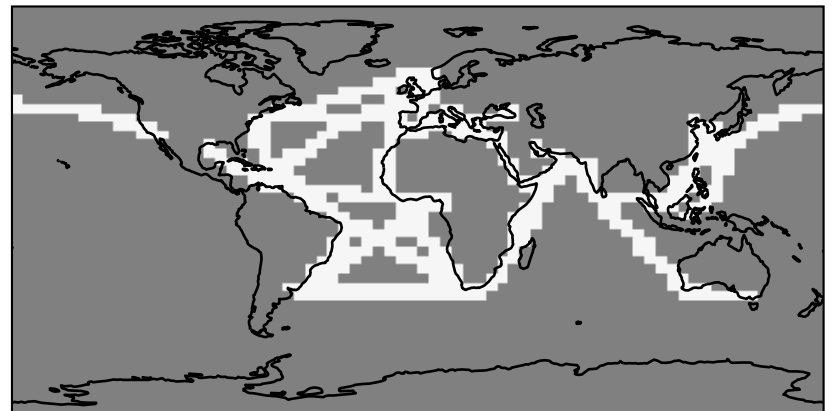
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

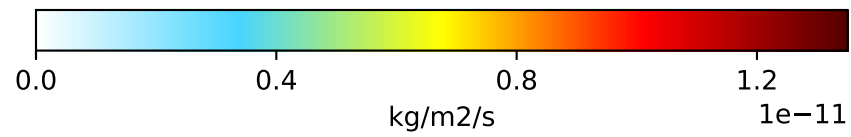


Ratio
Dev/Ref, Fixed Range

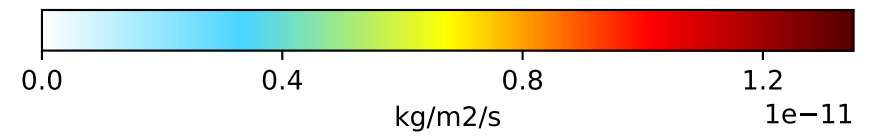
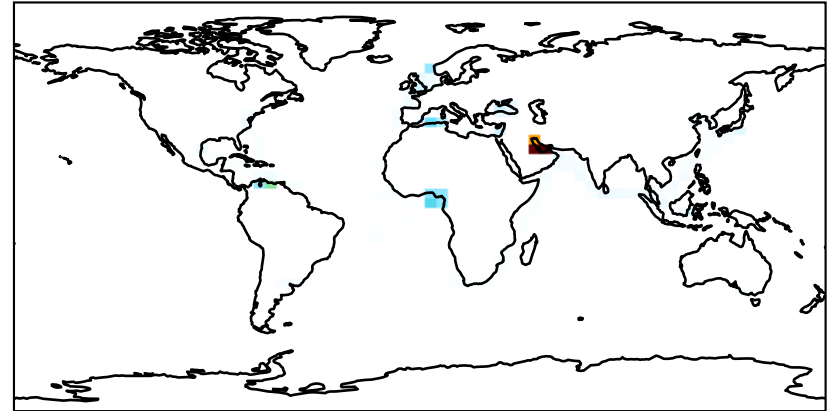


EmisC3H8_Ship

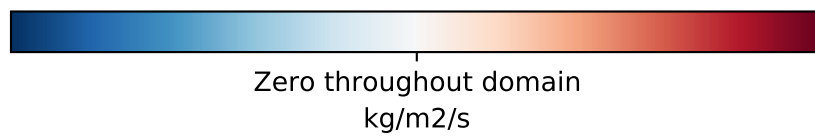
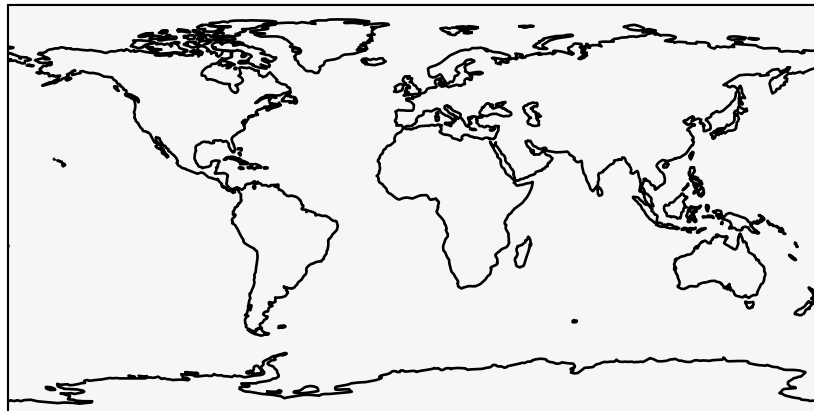
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



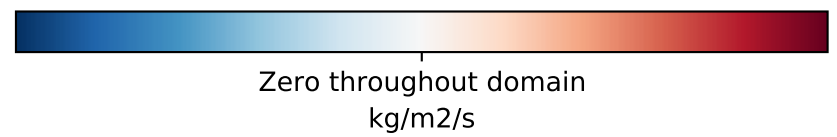
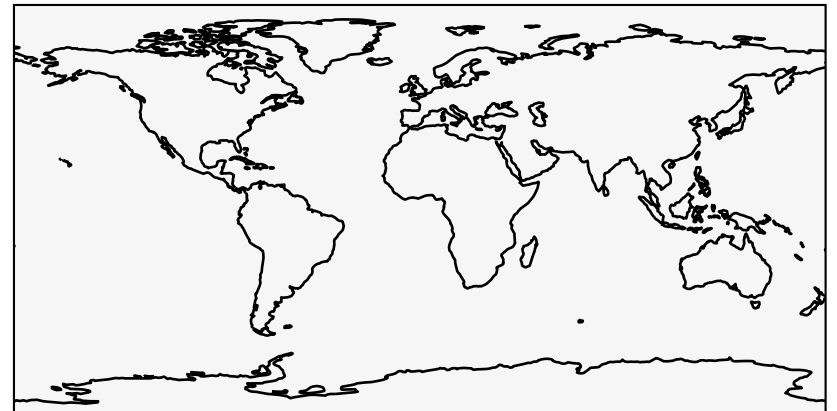
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



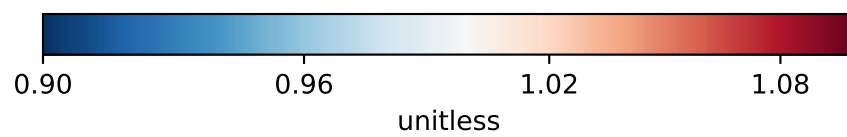
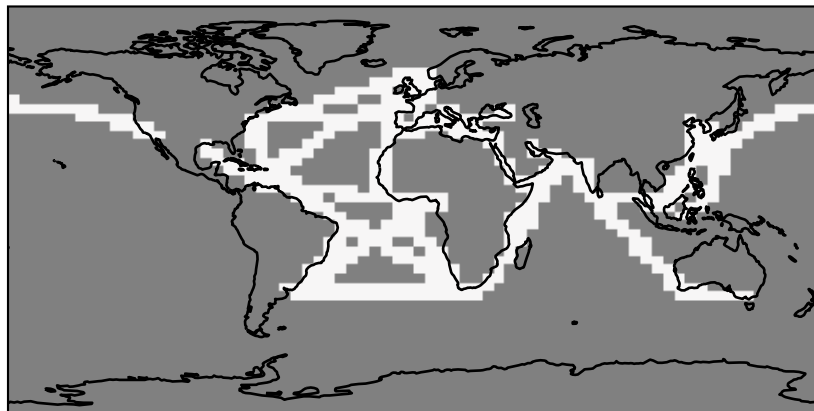
Difference
Dev - Ref, Dynamic Range



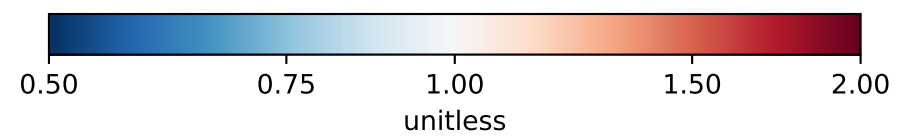
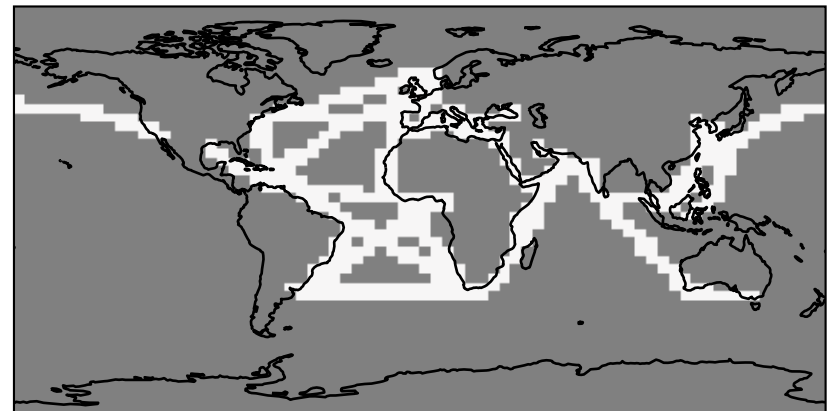
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

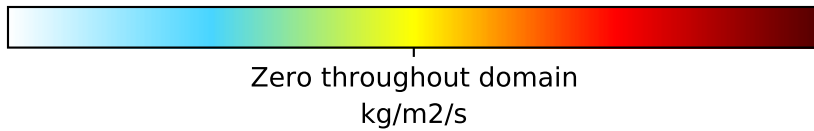
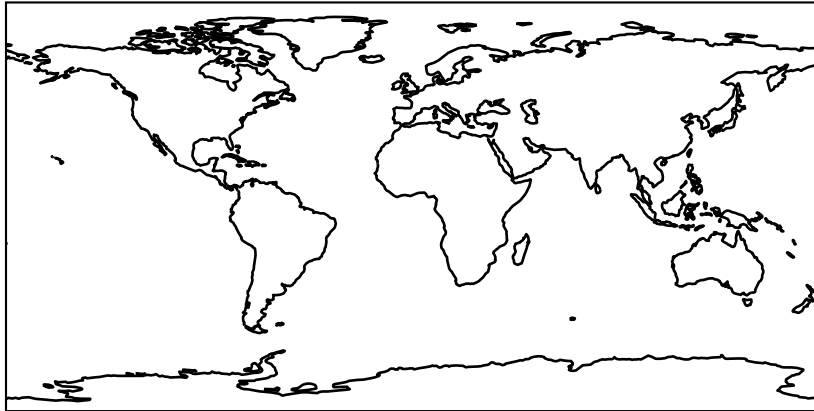


Ratio
Dev/Ref, Fixed Range

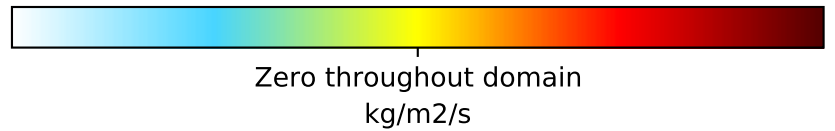
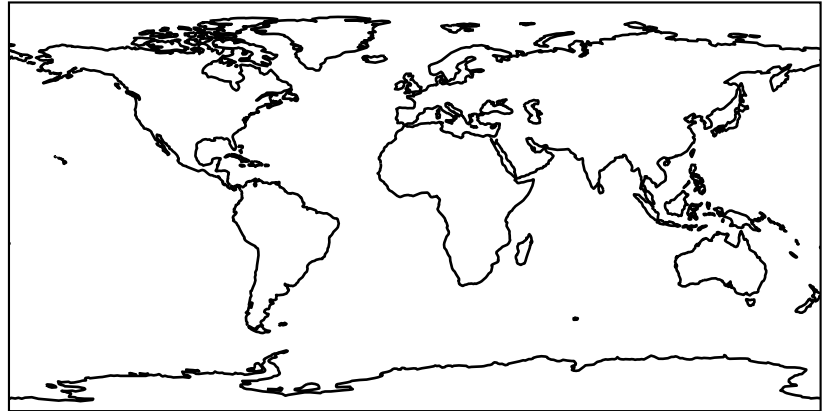


EmisCH2O_Ship

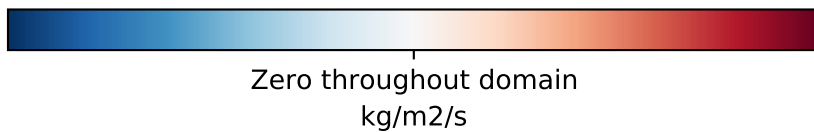
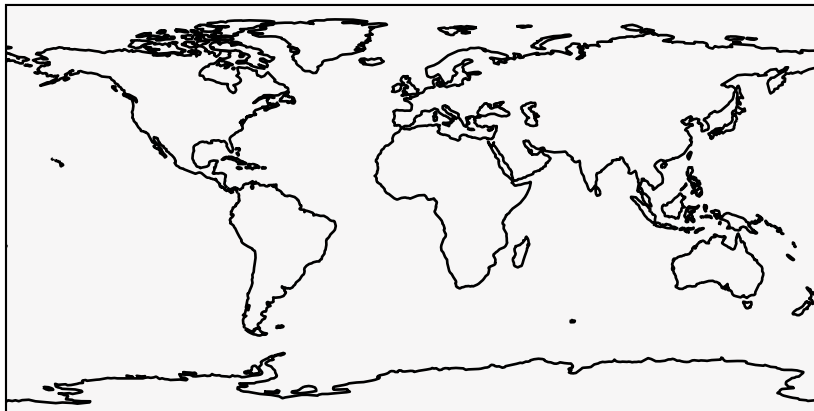
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



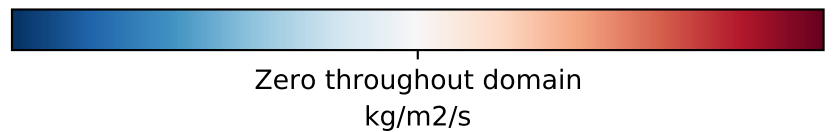
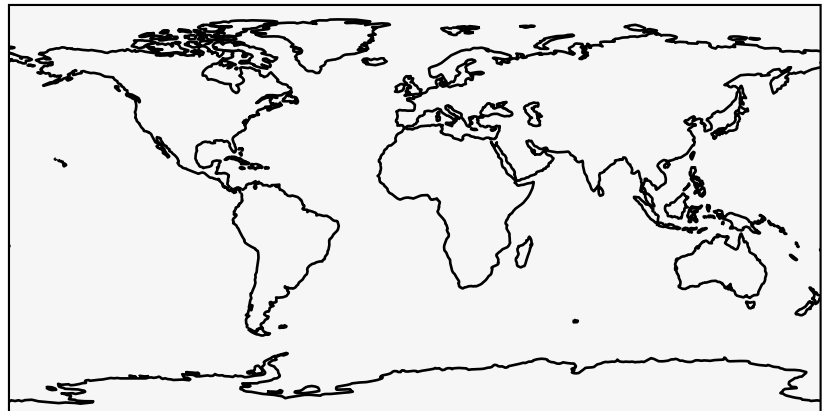
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



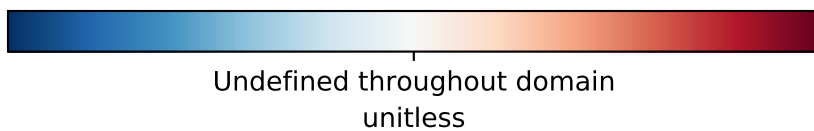
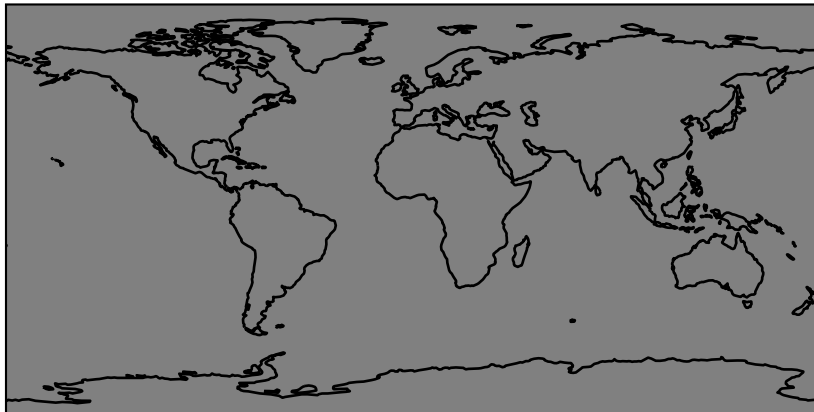
Difference
Dev - Ref, Dynamic Range



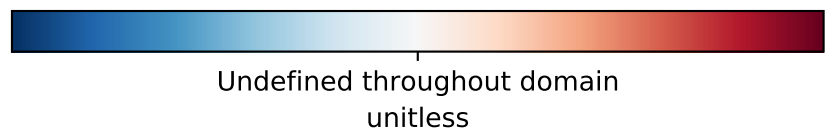
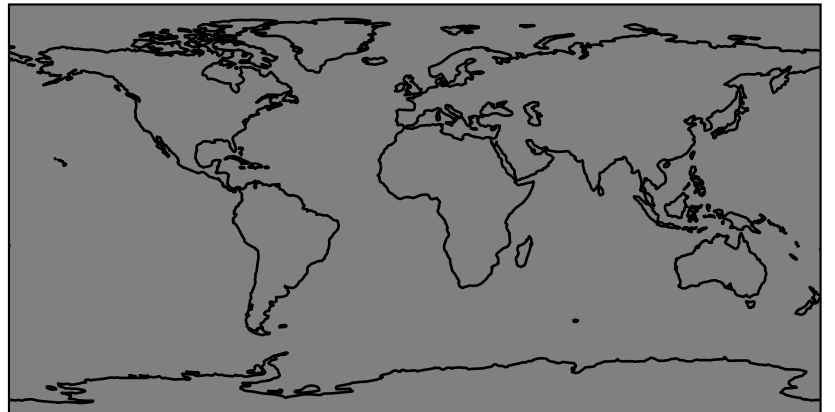
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

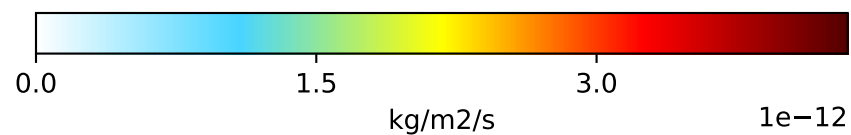
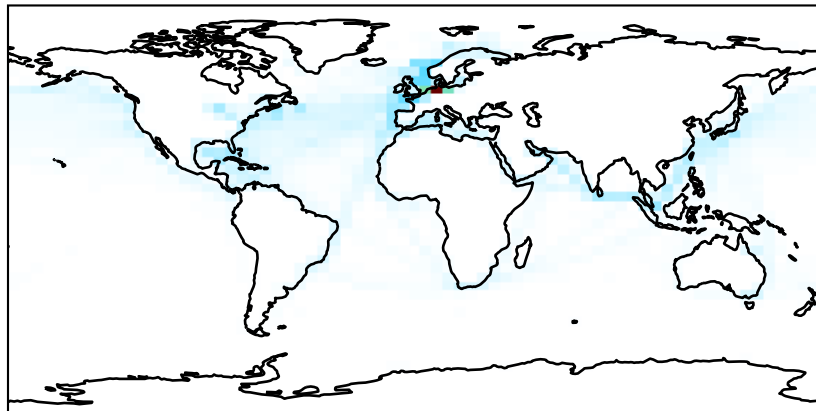


Ratio
Dev/Ref, Fixed Range

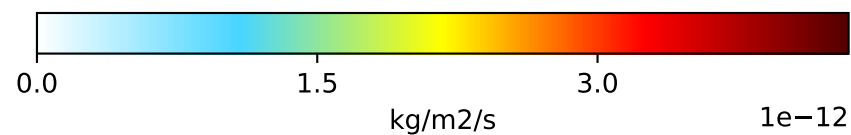
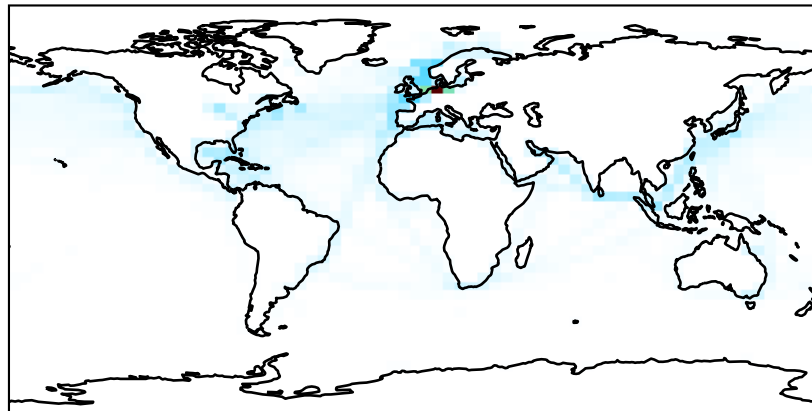


EmisCO_Ship

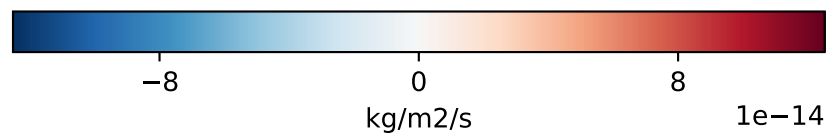
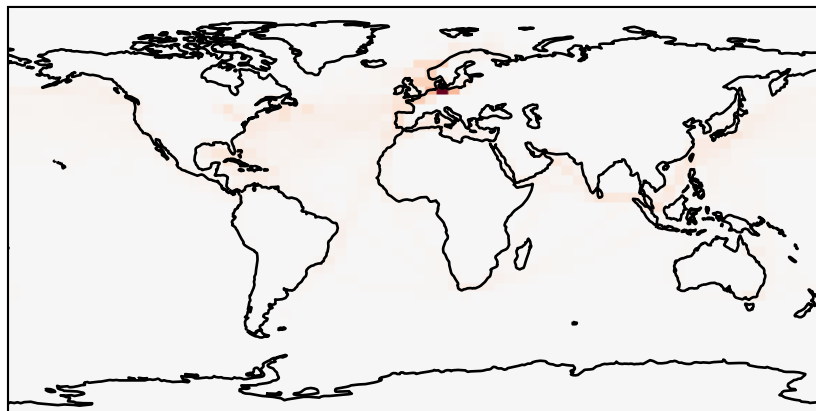
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



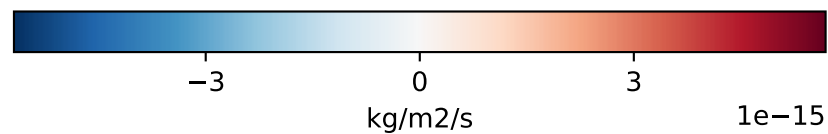
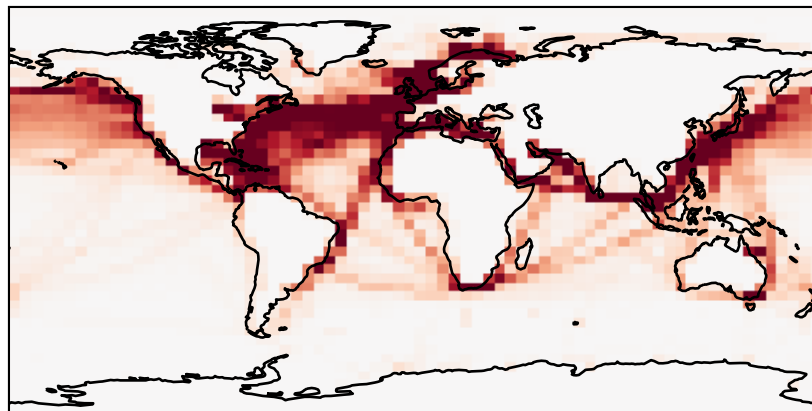
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



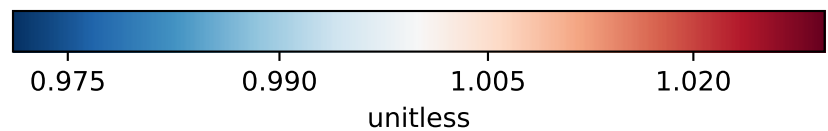
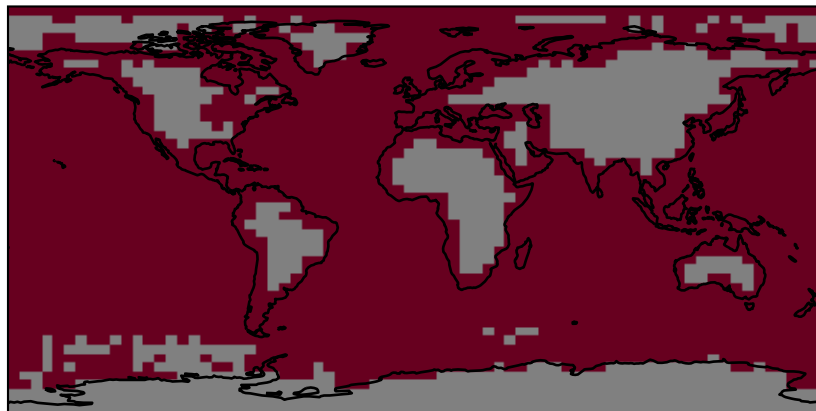
Difference
Dev - Ref, Dynamic Range



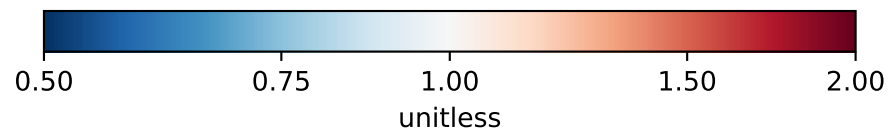
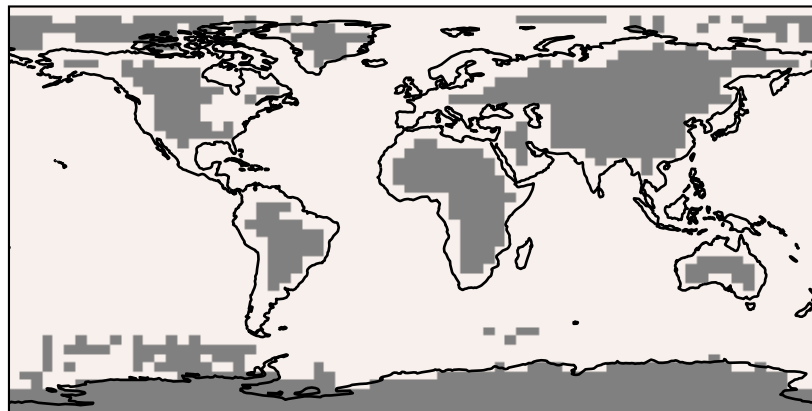
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

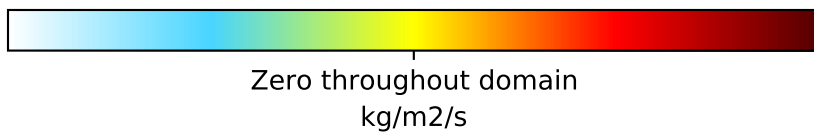


Ratio
Dev/Ref, Fixed Range

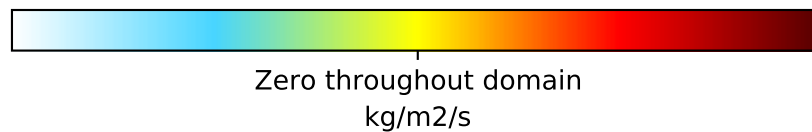
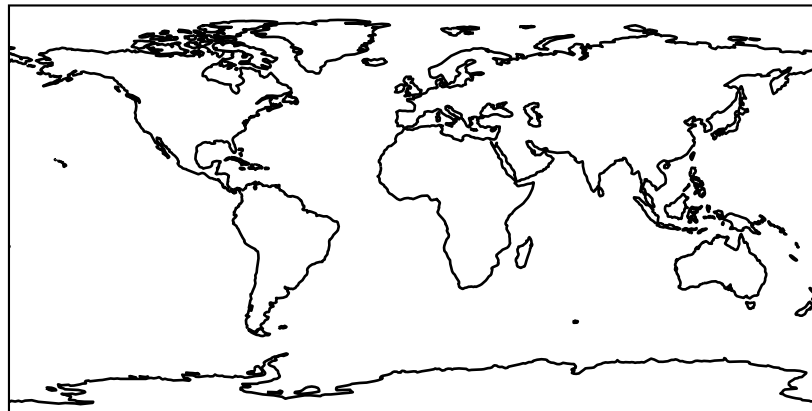


EmisEOH_Ship

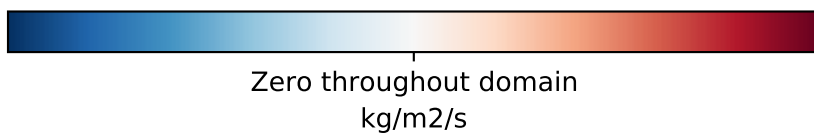
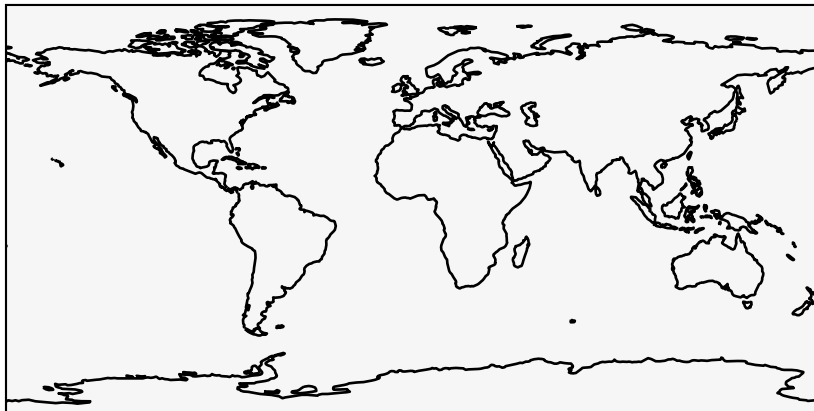
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



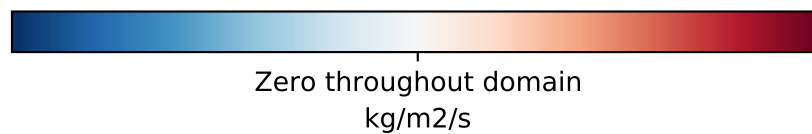
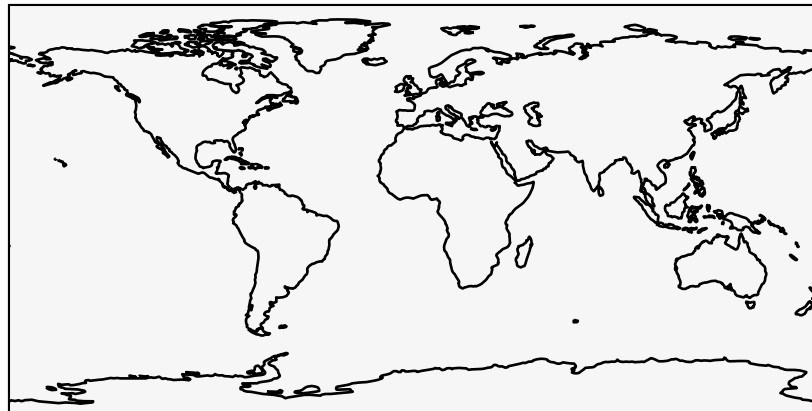
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



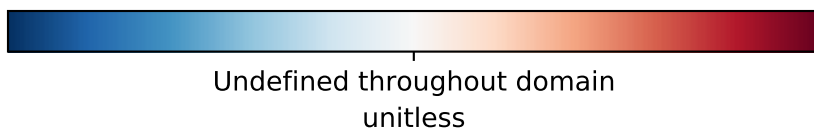
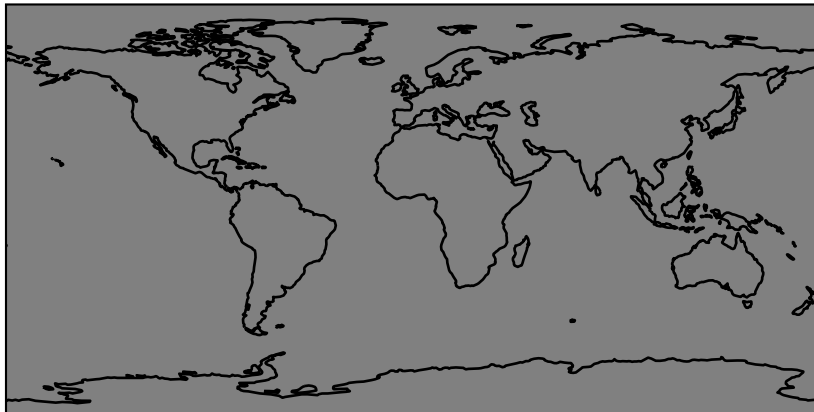
Difference
Dev - Ref, Dynamic Range



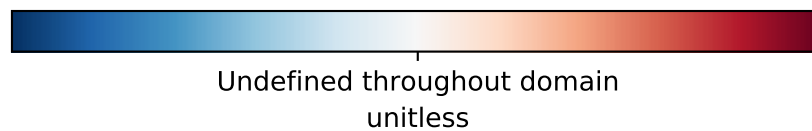
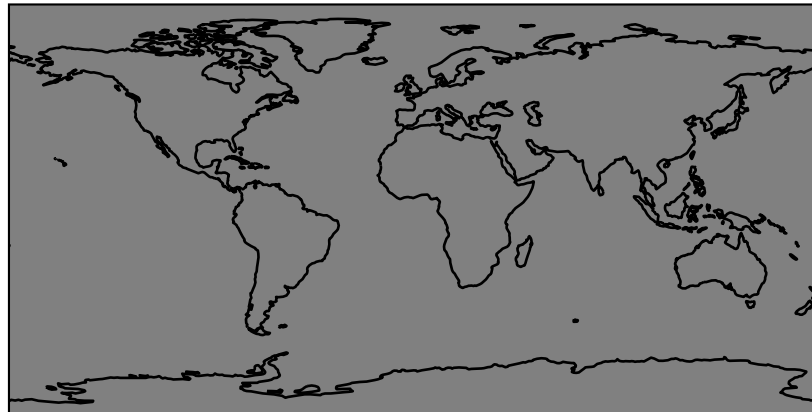
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

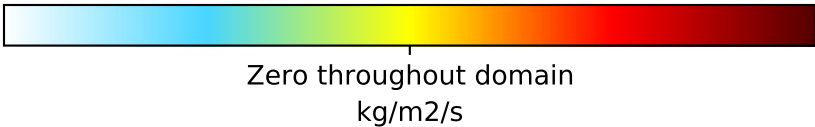


Ratio
Dev/Ref, Fixed Range

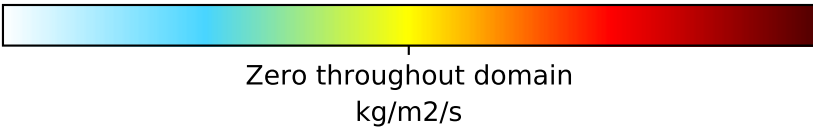


EmisHCOOH_Ship

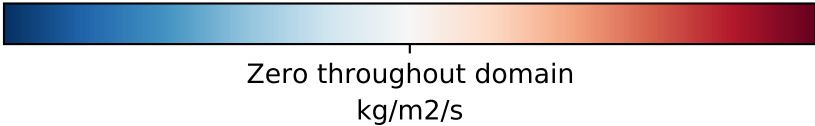
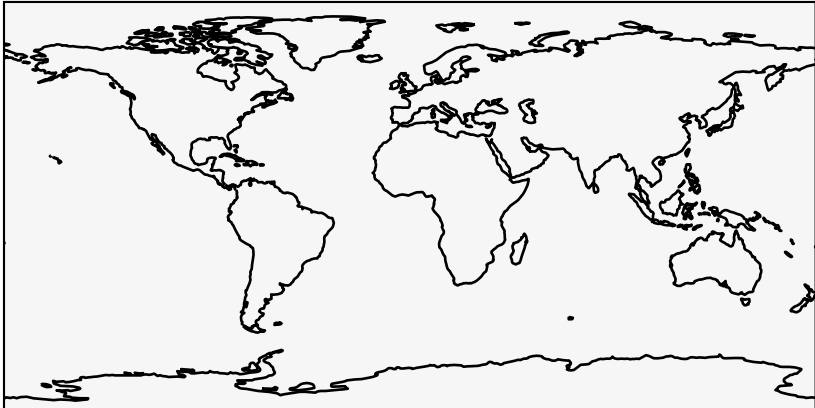
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



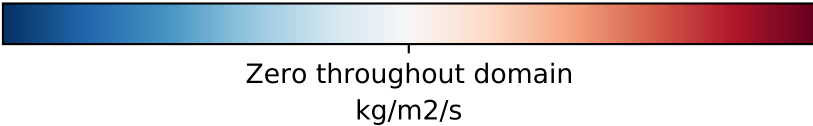
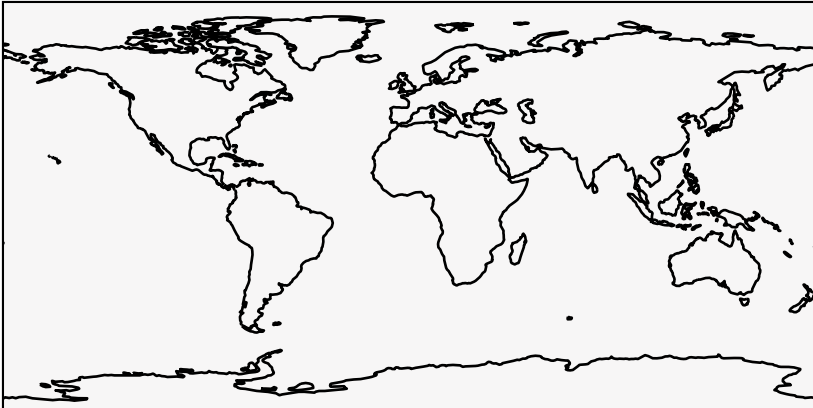
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



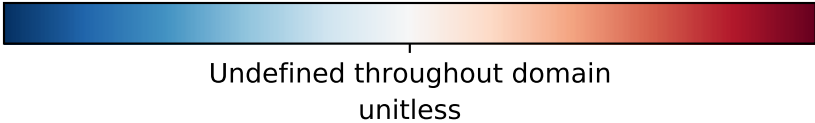
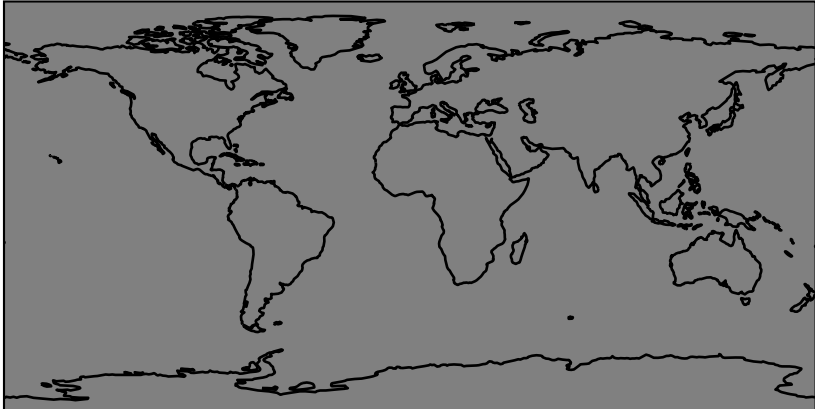
Difference
Dev - Ref, Dynamic Range



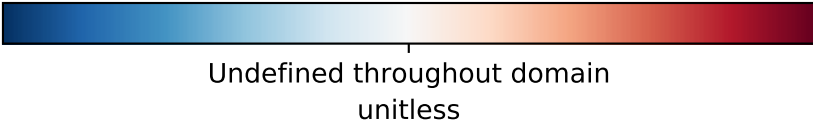
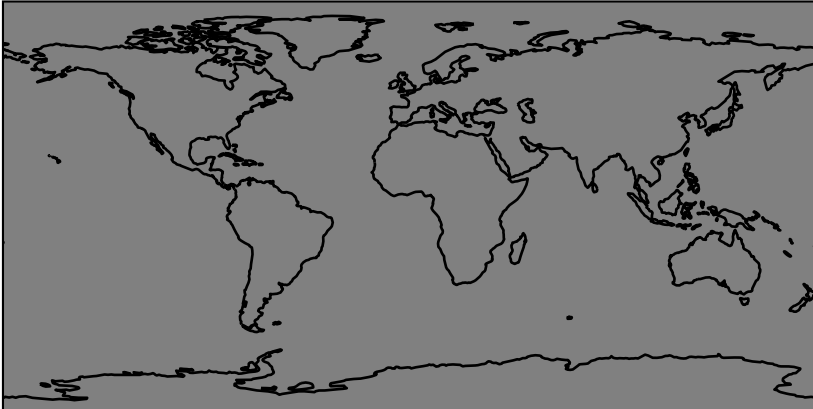
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

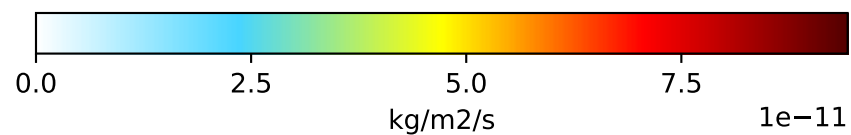
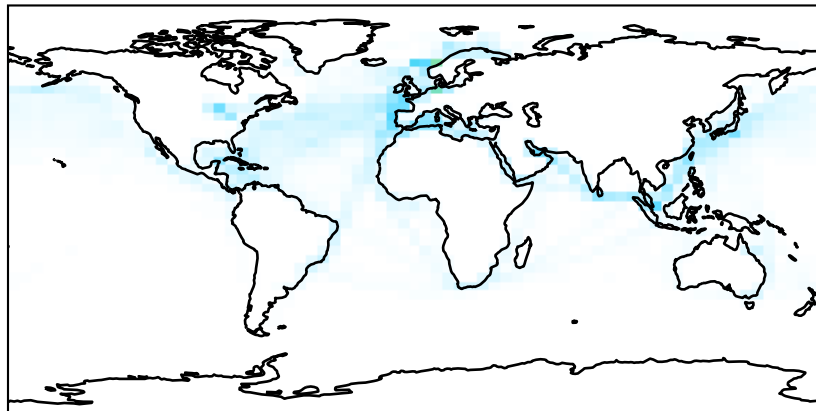


Ratio
Dev/Ref, Fixed Range

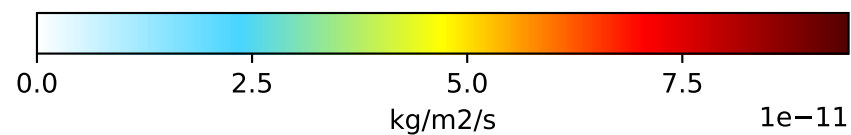
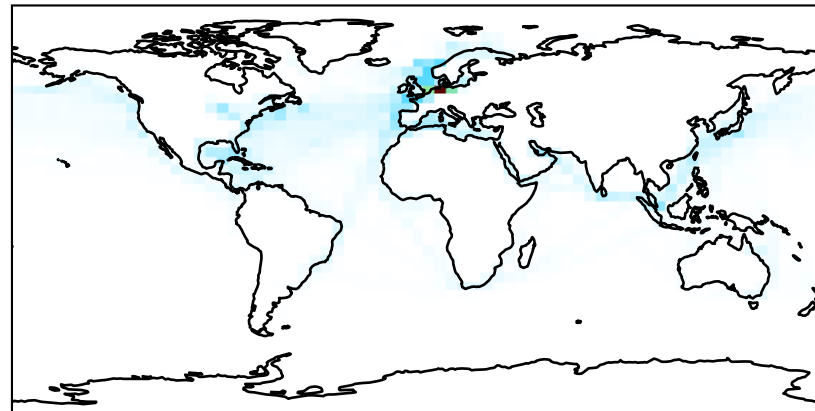


EmisHNO3_Ship

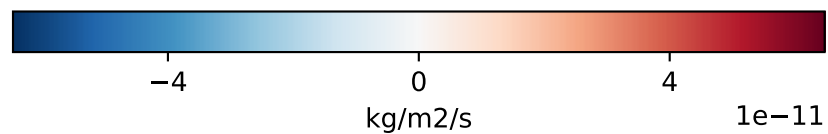
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



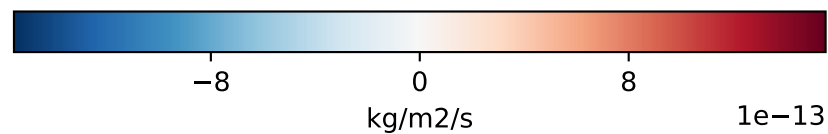
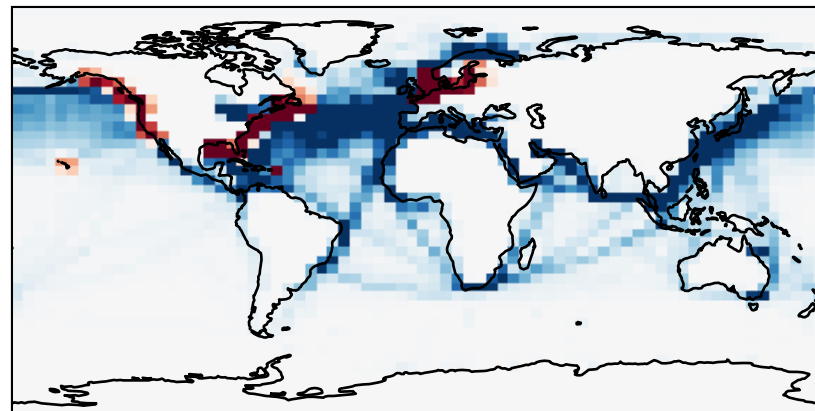
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



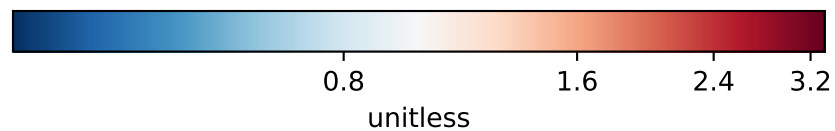
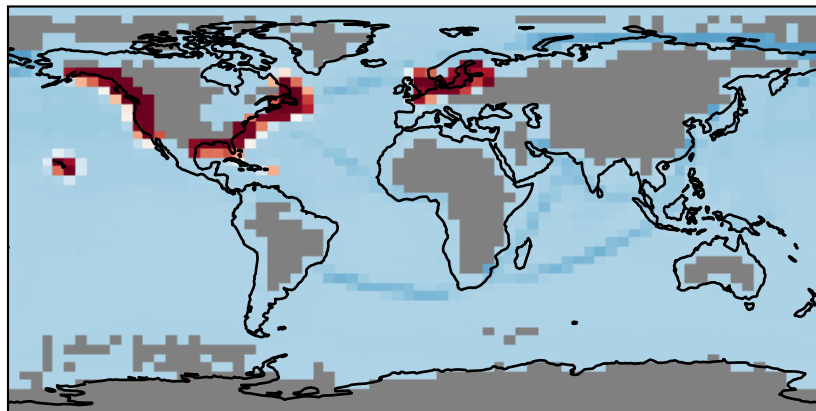
Difference
Dev - Ref, Dynamic Range



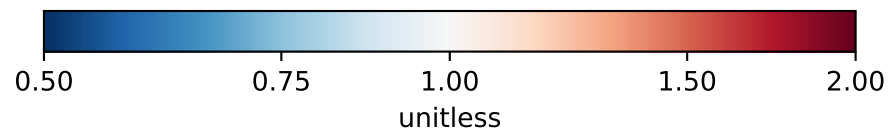
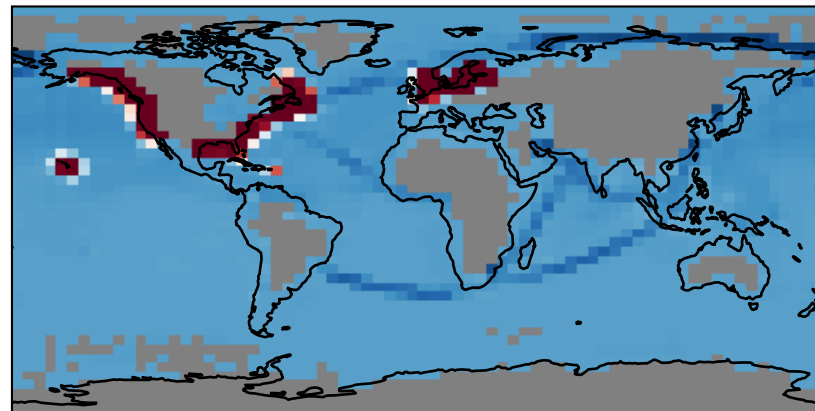
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

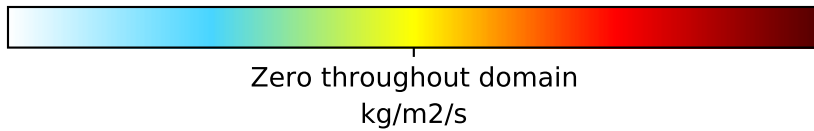
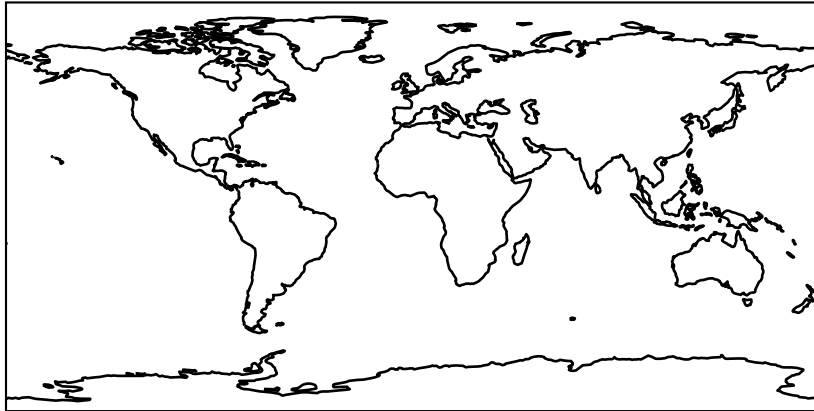


Ratio
Dev/Ref, Fixed Range

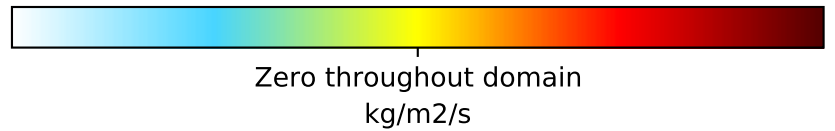
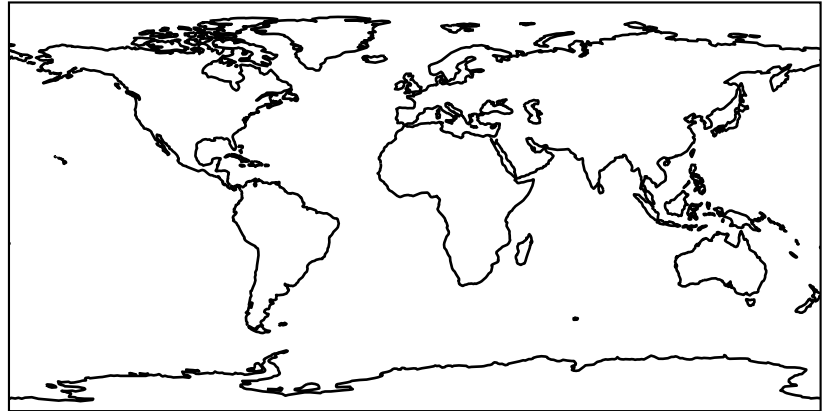


EmisMEK_Ship

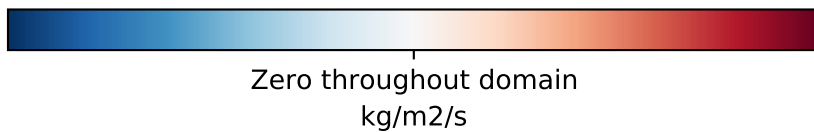
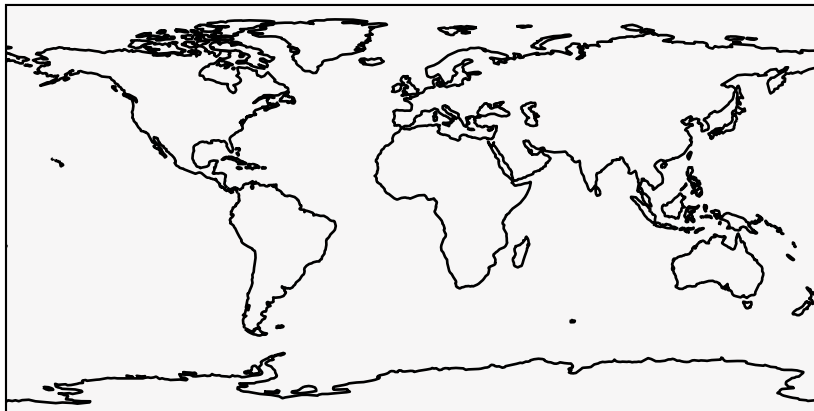
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



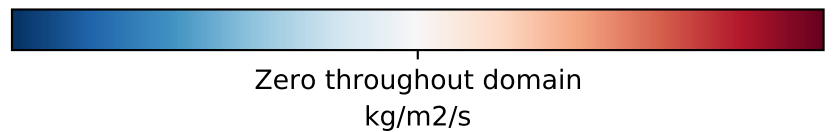
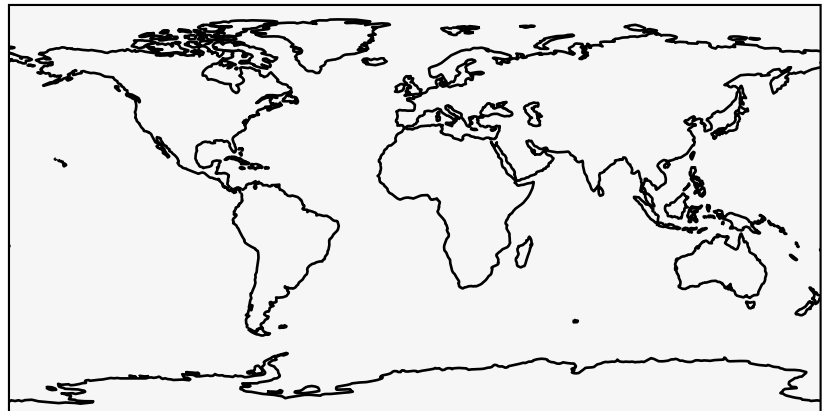
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



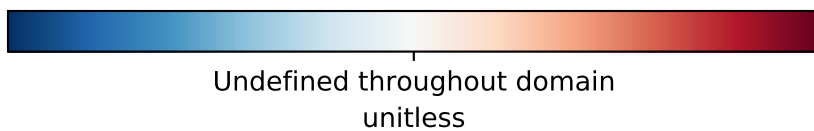
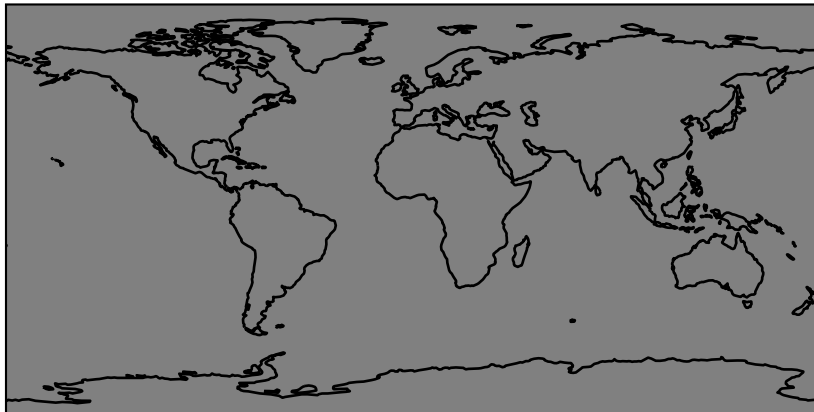
Difference
Dev - Ref, Dynamic Range



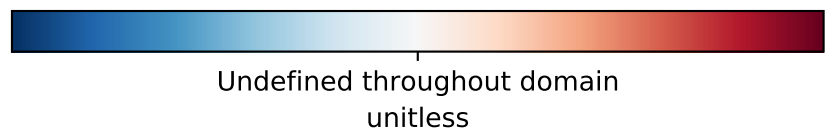
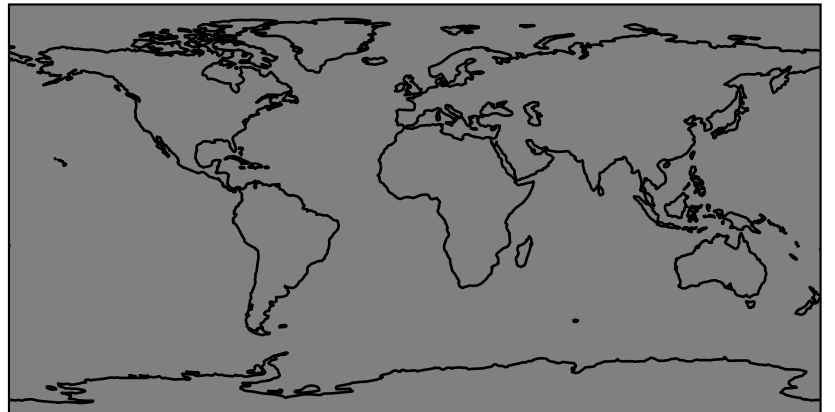
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

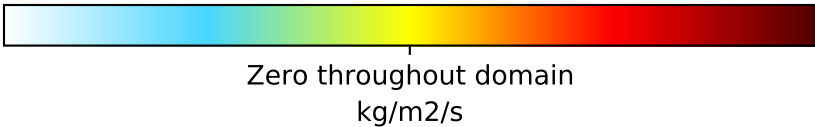


Ratio
Dev/Ref, Fixed Range

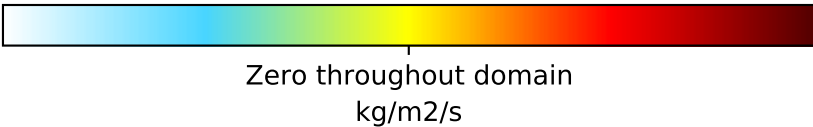


EmisMOH_Ship

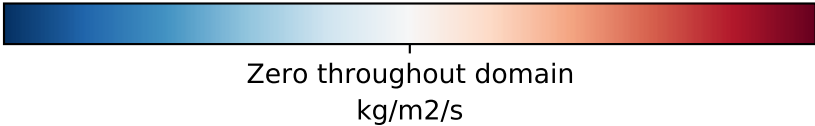
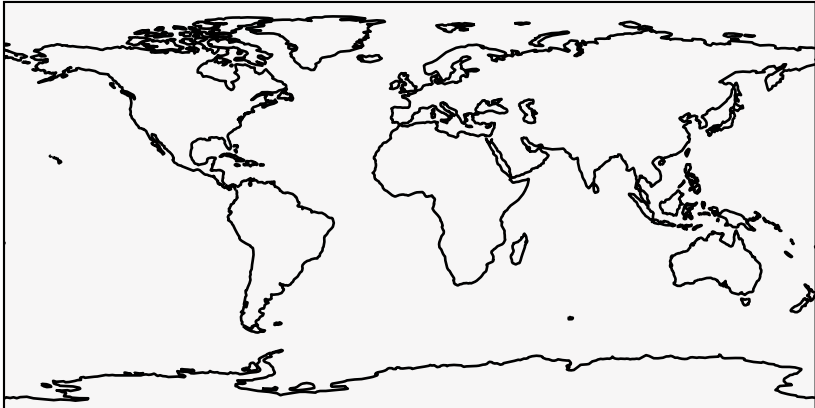
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



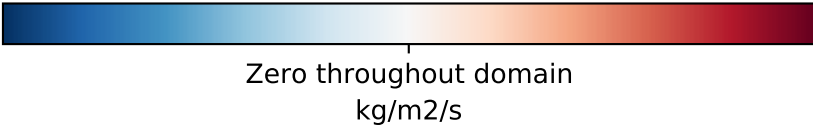
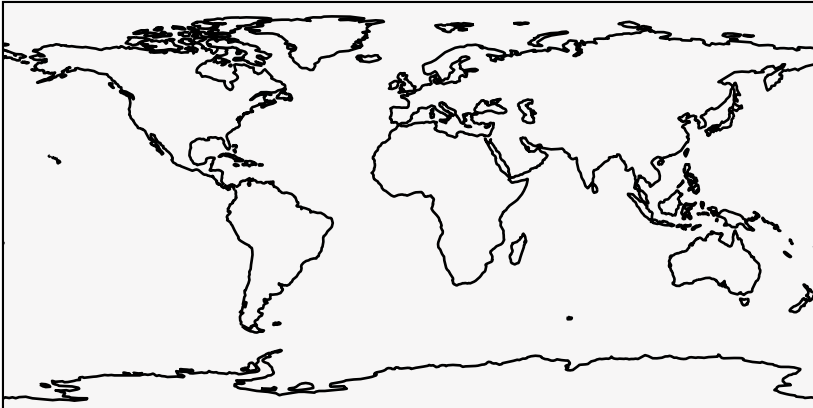
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



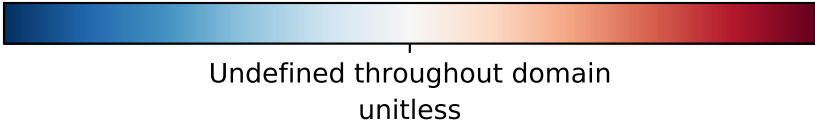
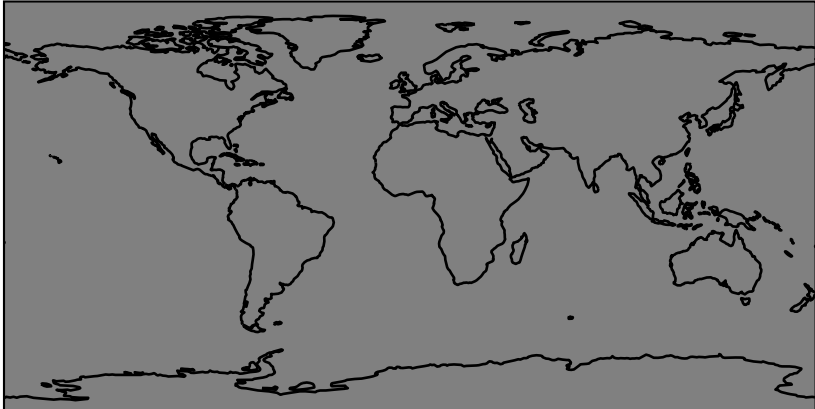
Difference
Dev - Ref, Dynamic Range



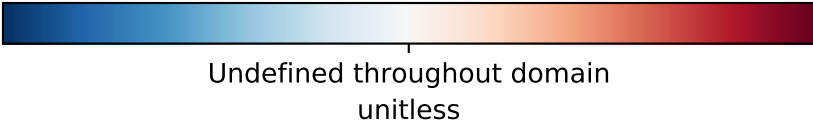
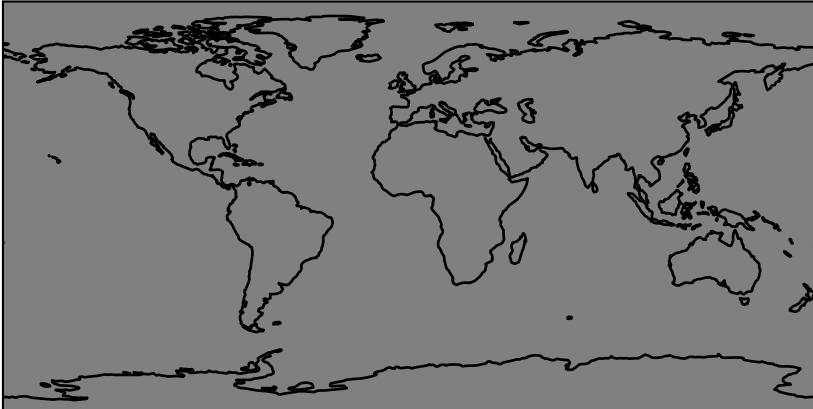
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

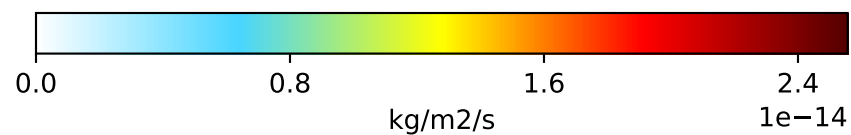
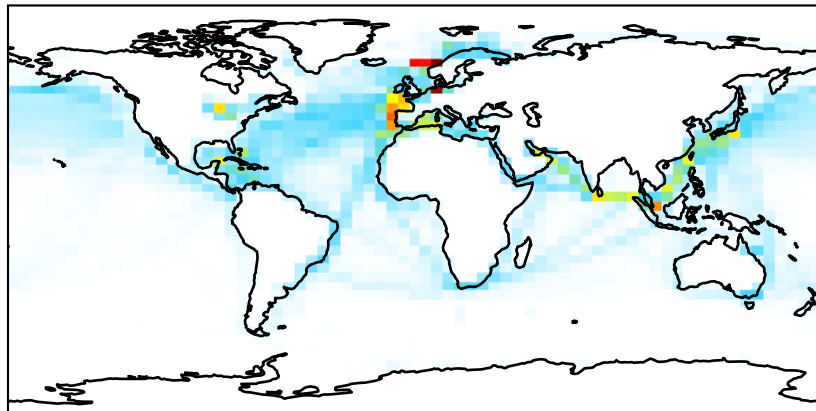


Ratio
Dev/Ref, Fixed Range

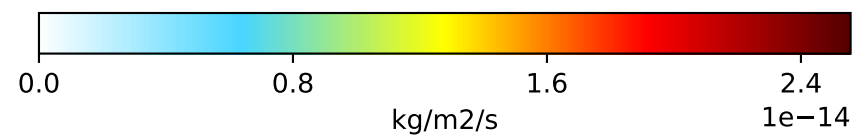
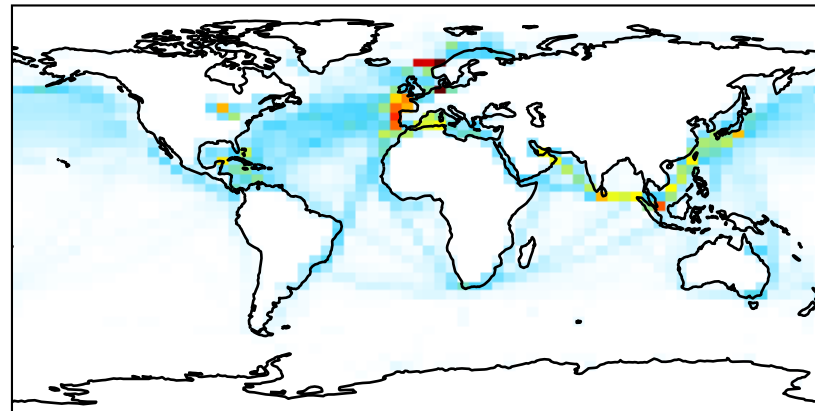


EmisNH3_Ship

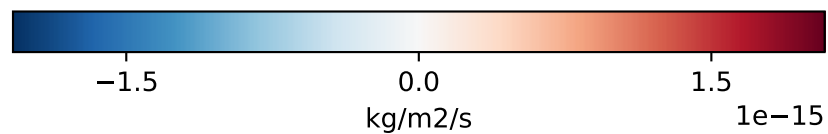
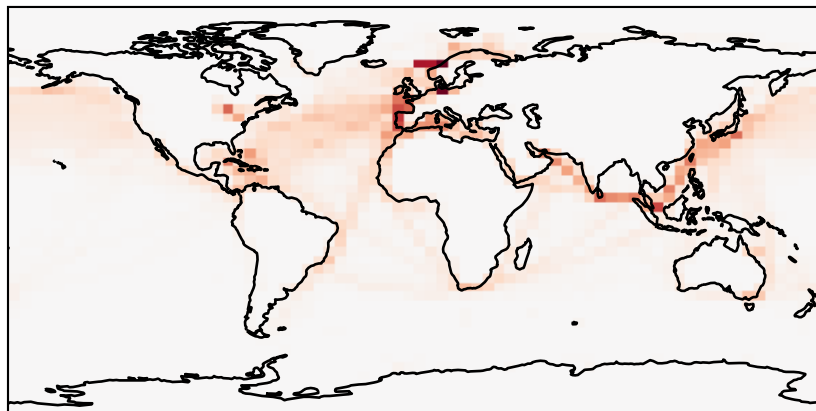
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



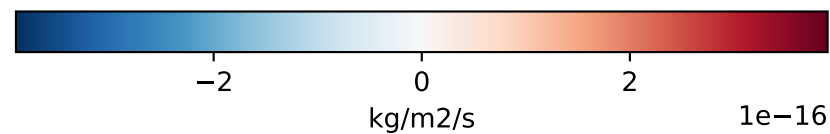
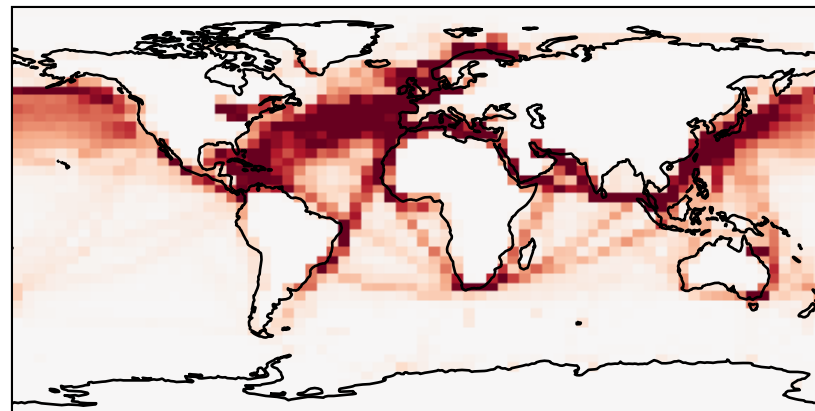
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



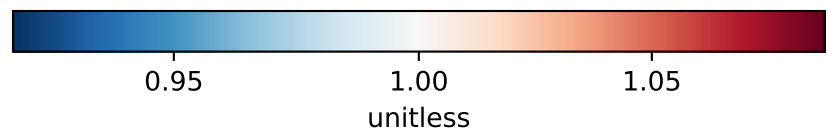
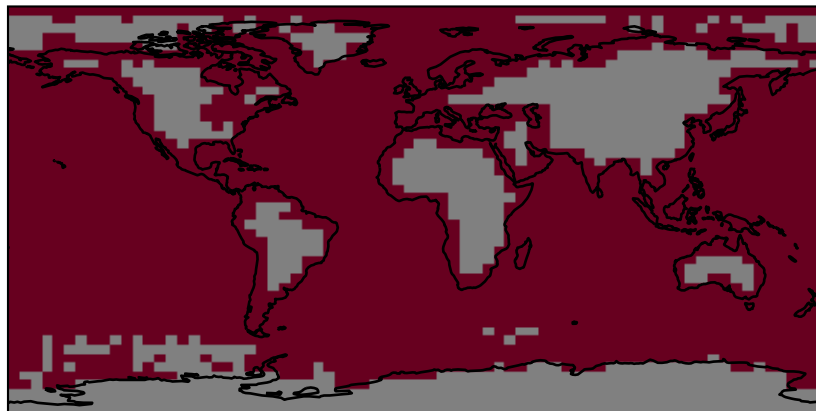
Difference
Dev - Ref, Dynamic Range



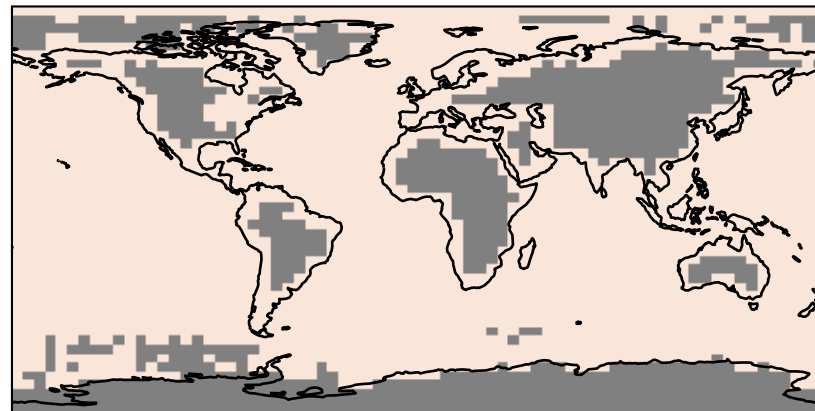
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

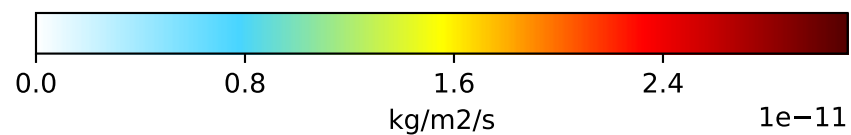
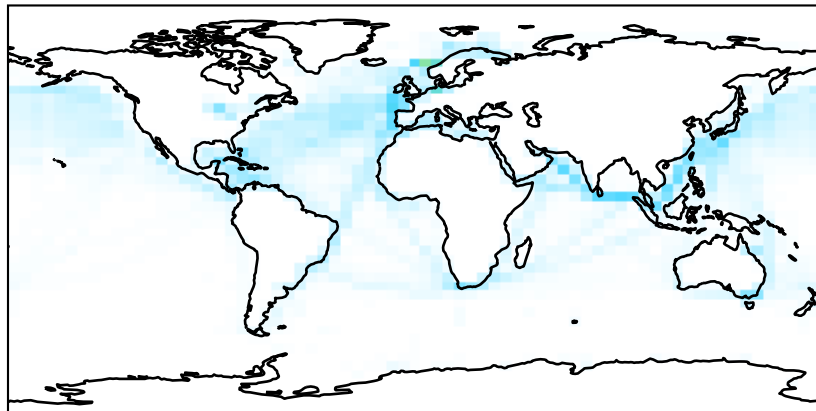


Ratio
Dev/Ref, Fixed Range

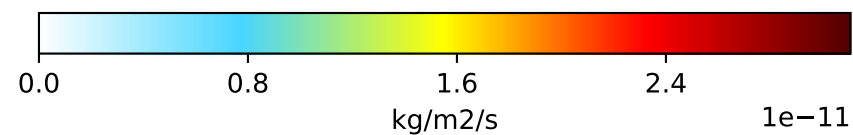
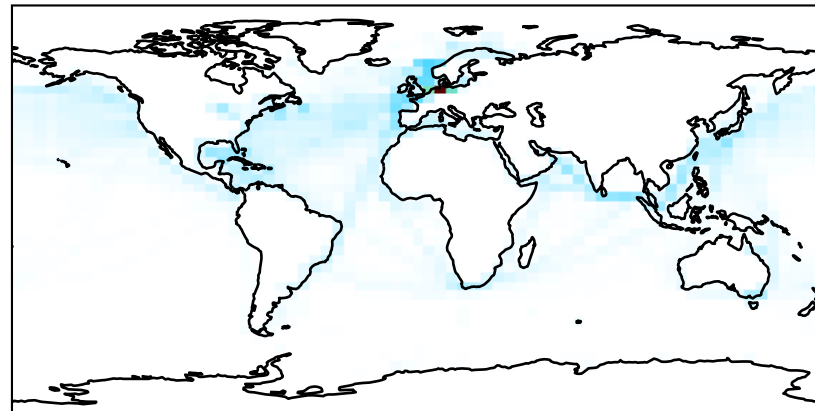


EmisNO2_Ship

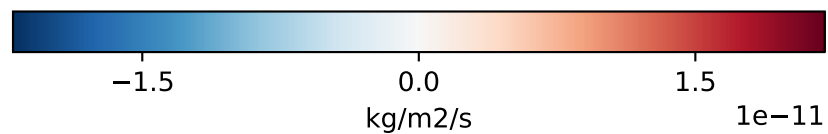
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



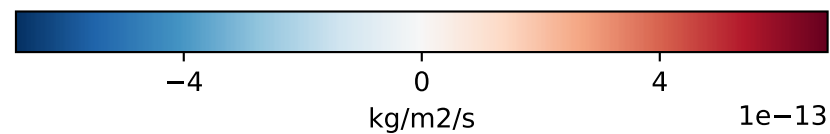
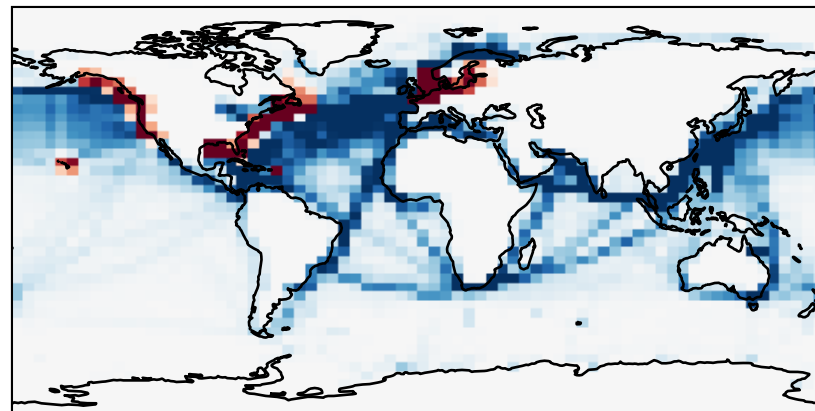
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



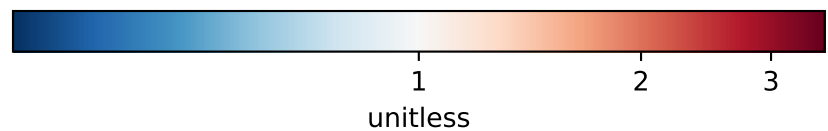
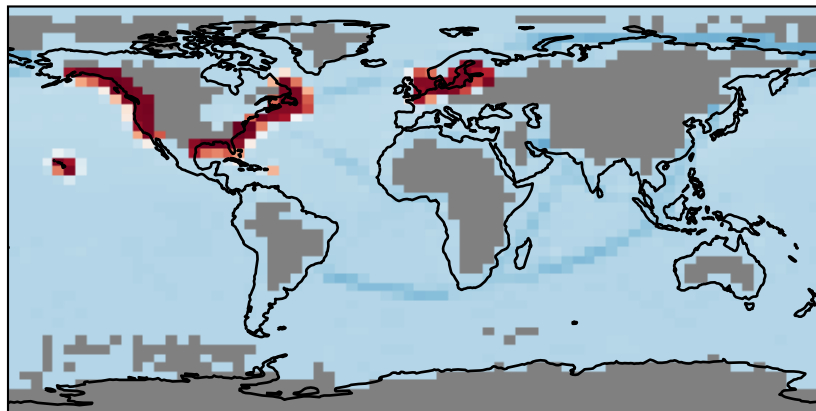
Difference
Dev - Ref, Dynamic Range



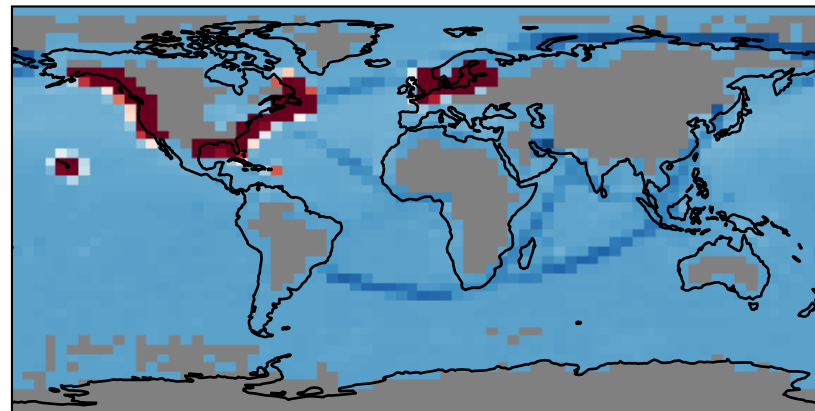
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

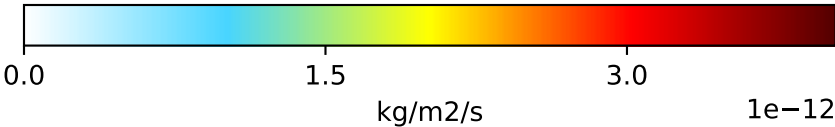
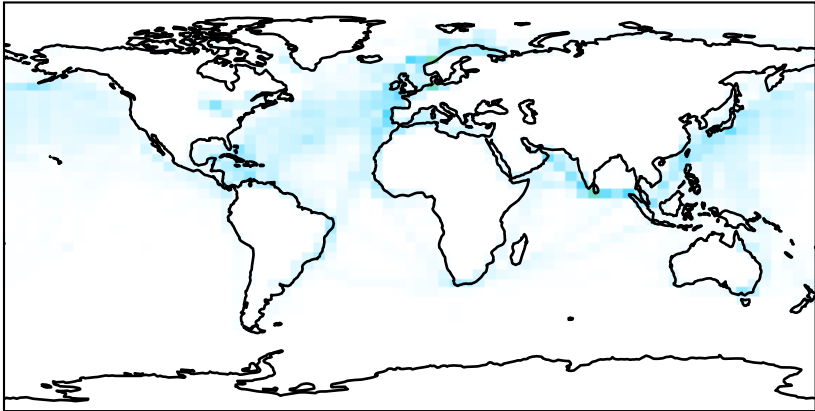


Ratio
Dev/Ref, Fixed Range

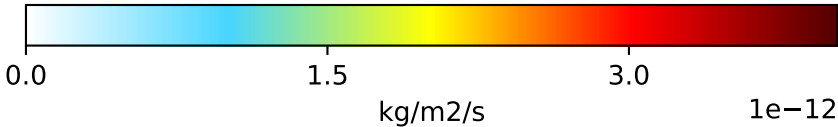
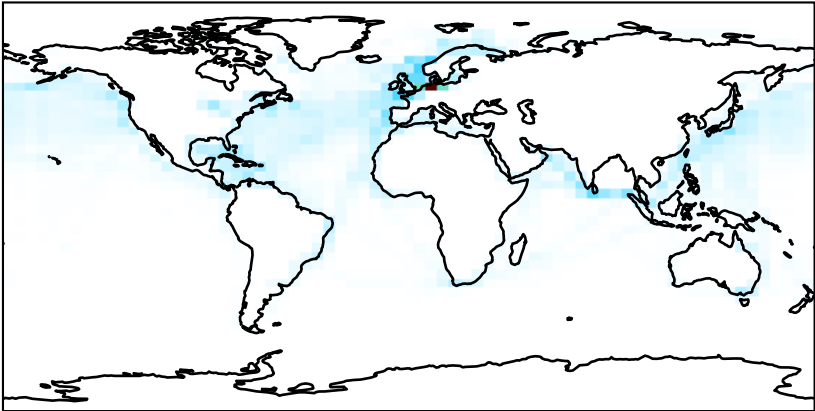


EmisNO_Ship

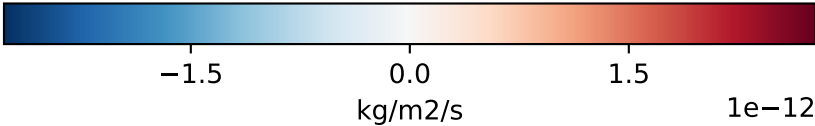
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



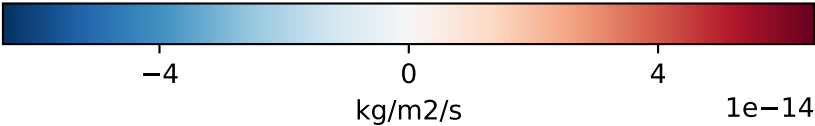
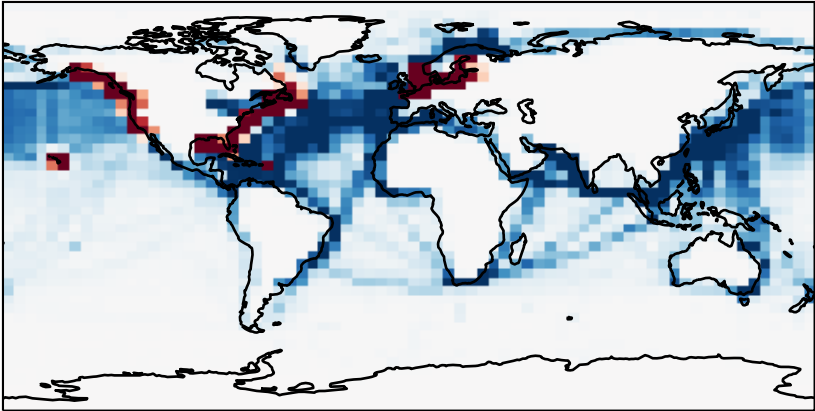
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



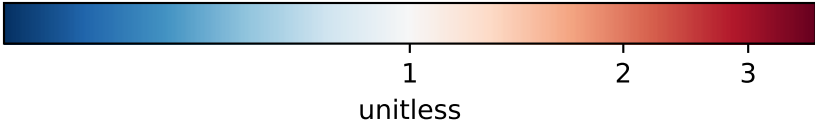
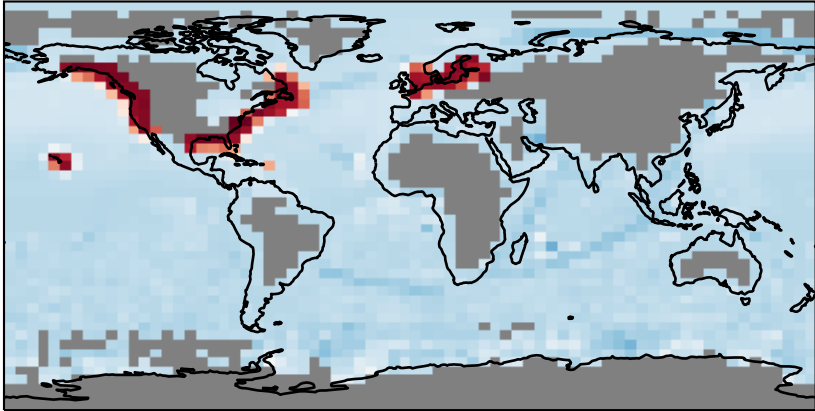
Difference
Dev - Ref, Dynamic Range



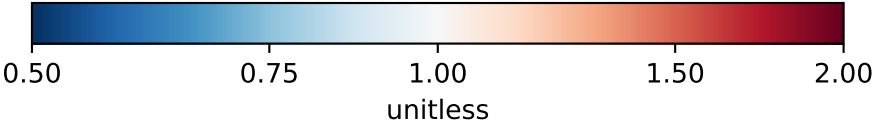
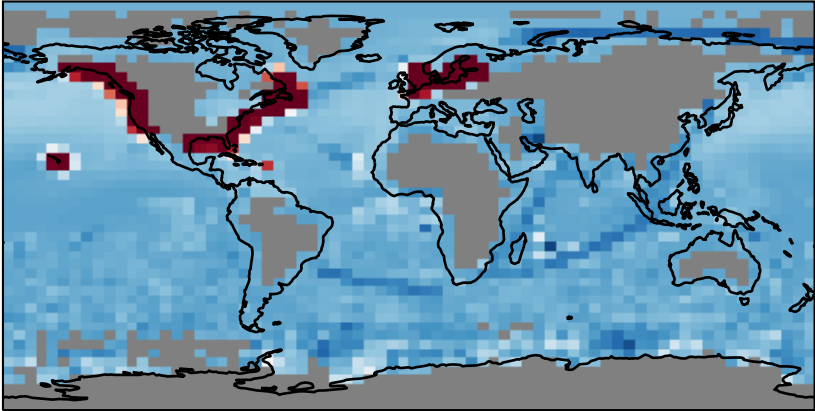
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

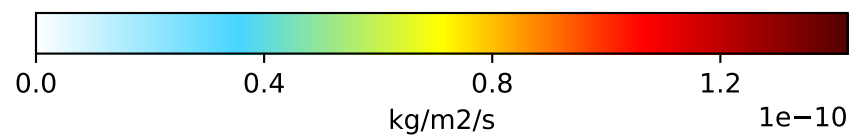
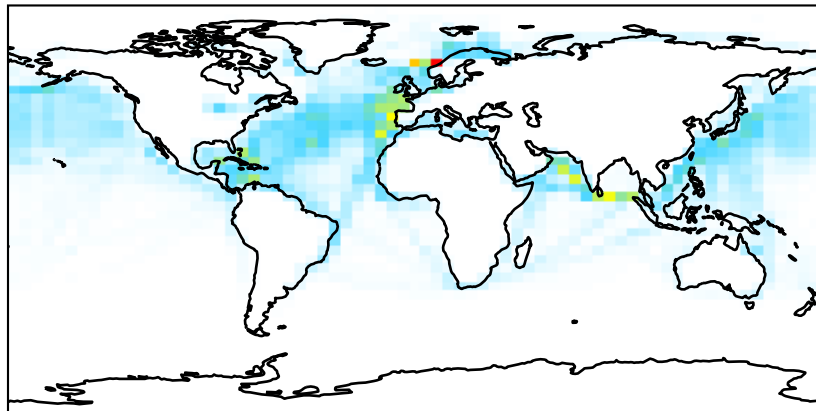


Ratio
Dev/Ref, Fixed Range

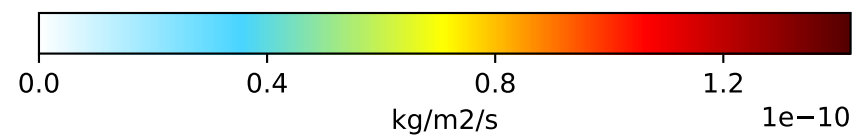
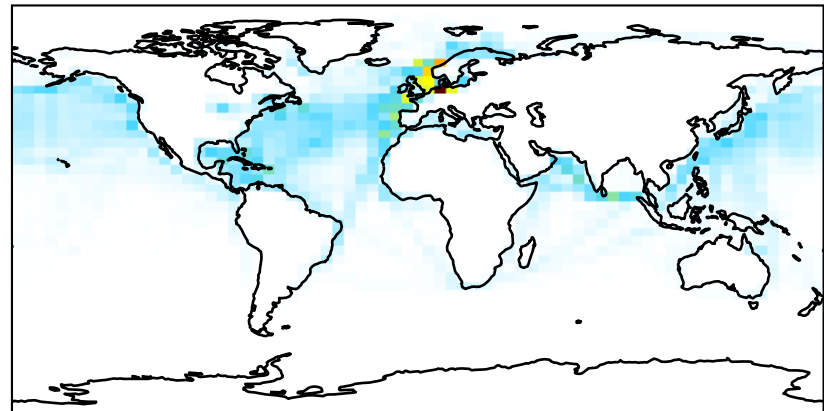


EmisO3_Ship

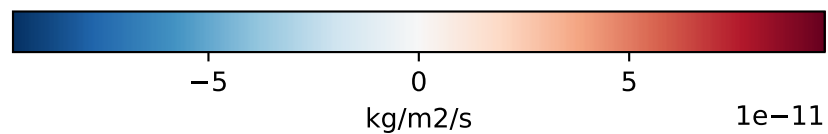
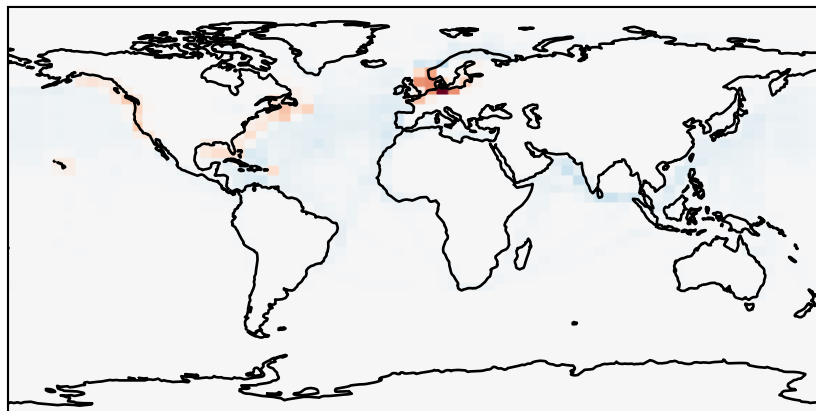
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



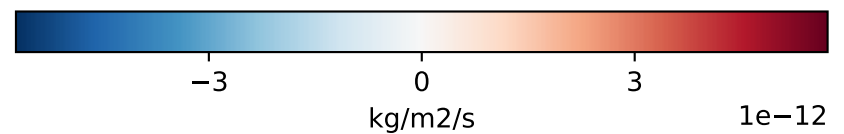
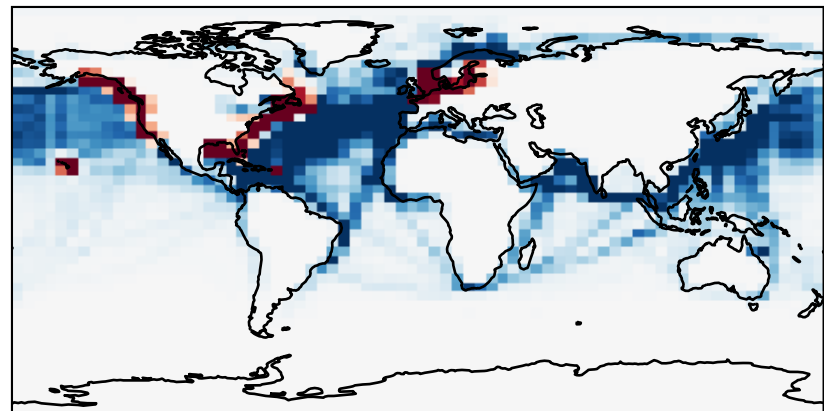
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



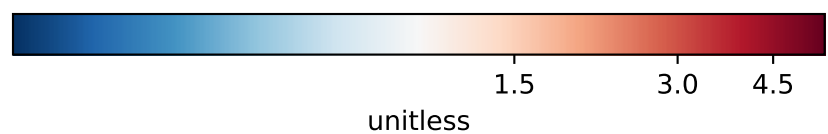
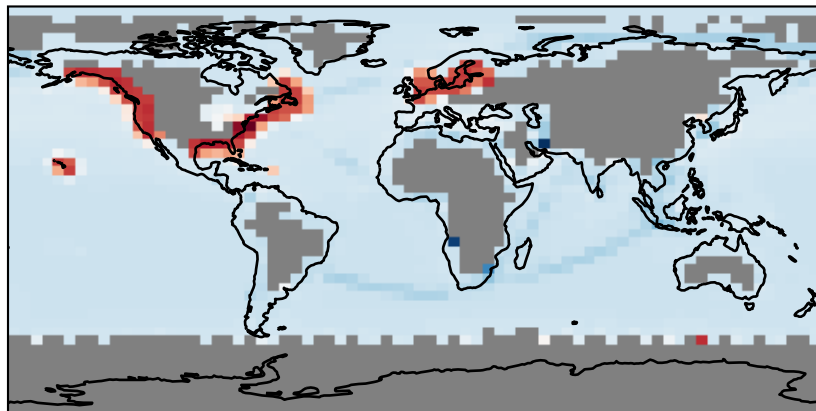
Difference
Dev - Ref, Dynamic Range



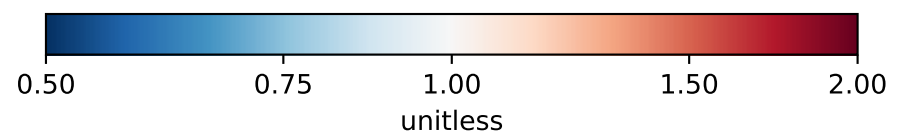
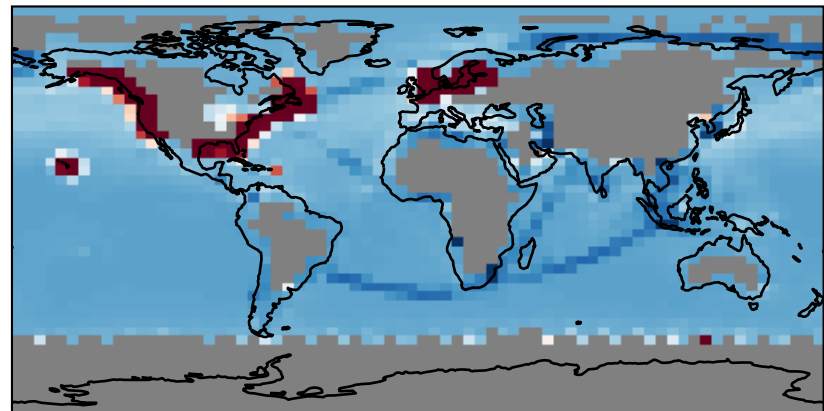
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

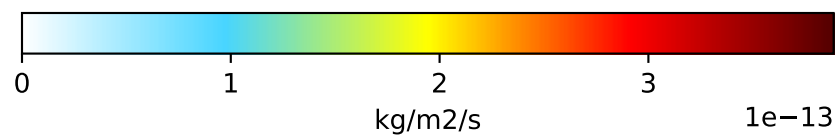
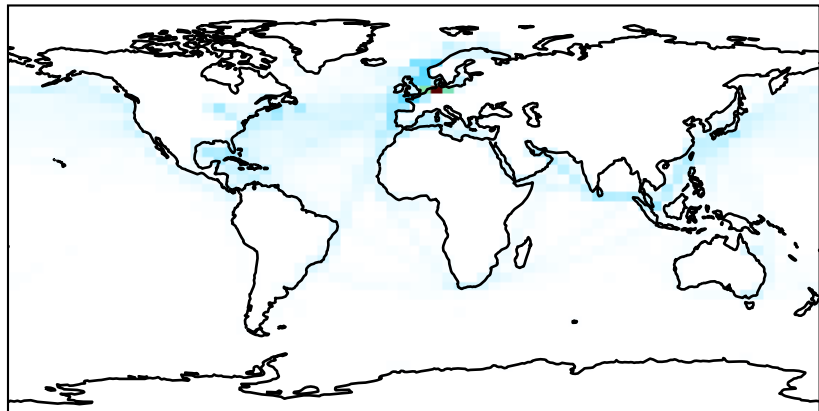


Ratio
Dev/Ref, Fixed Range

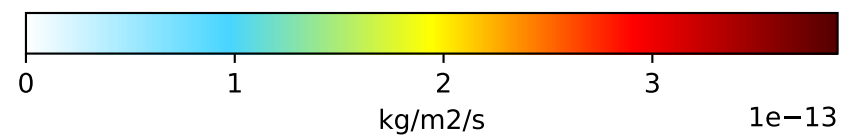
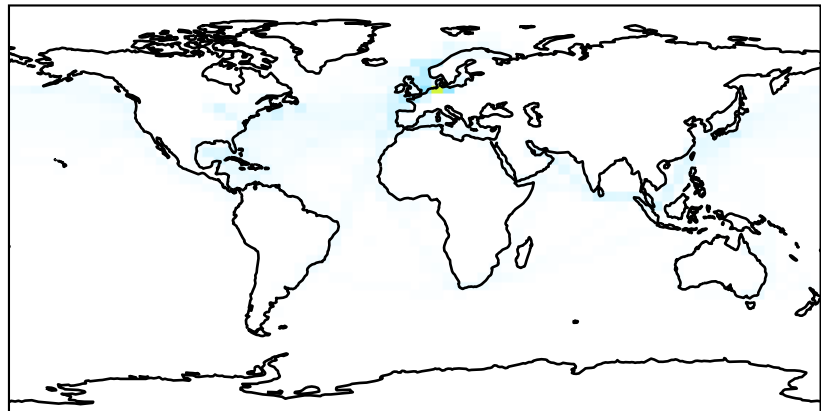


EmisOCPI_Ship

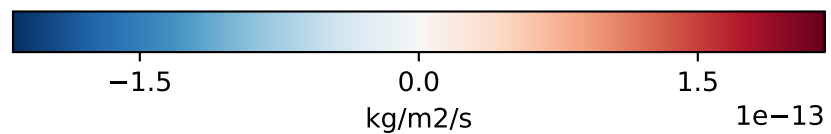
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



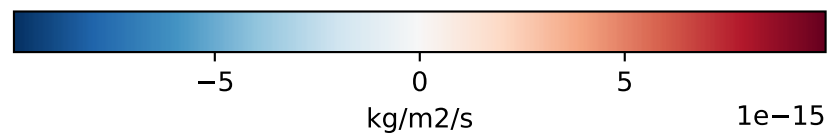
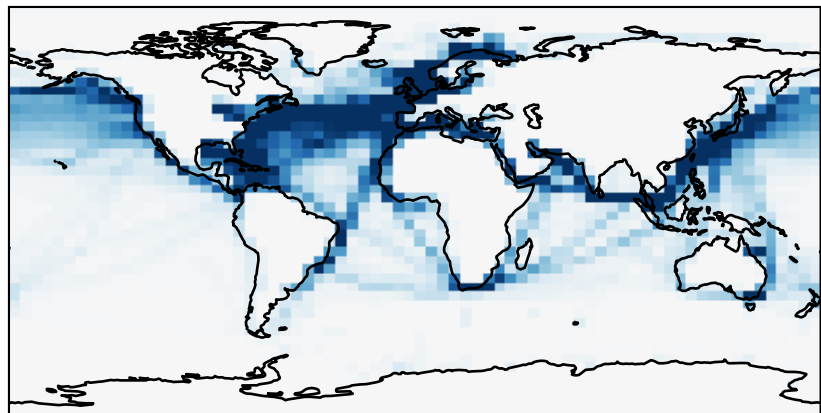
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



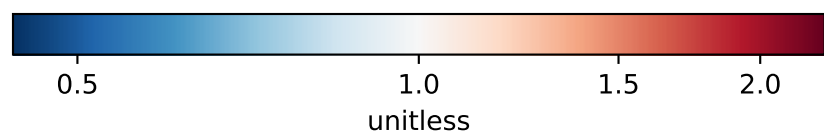
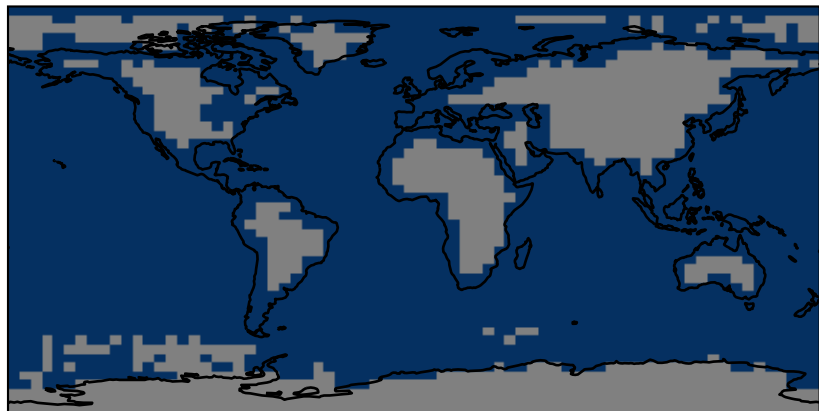
Difference
Dev - Ref, Dynamic Range



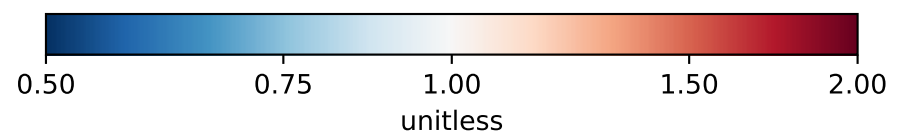
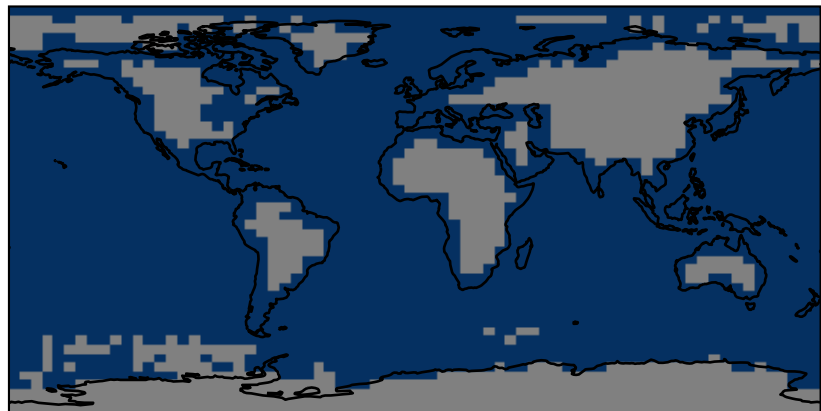
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

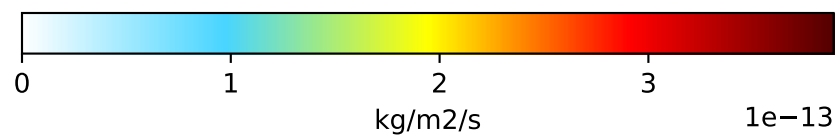
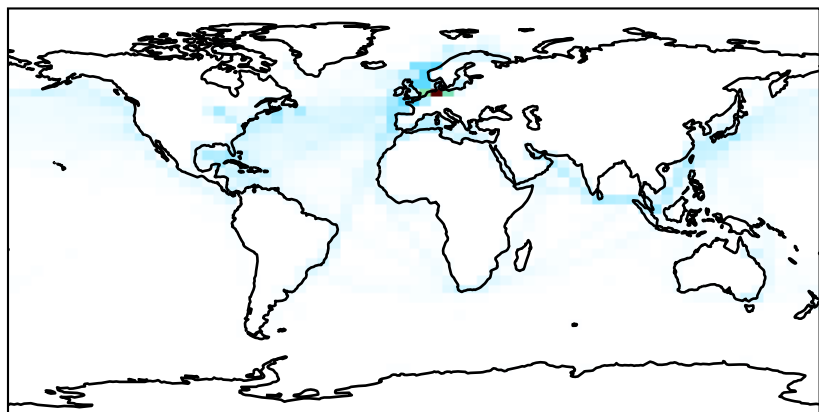


Ratio
Dev/Ref, Fixed Range

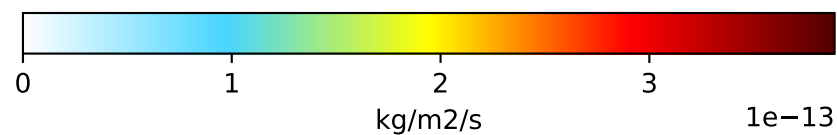
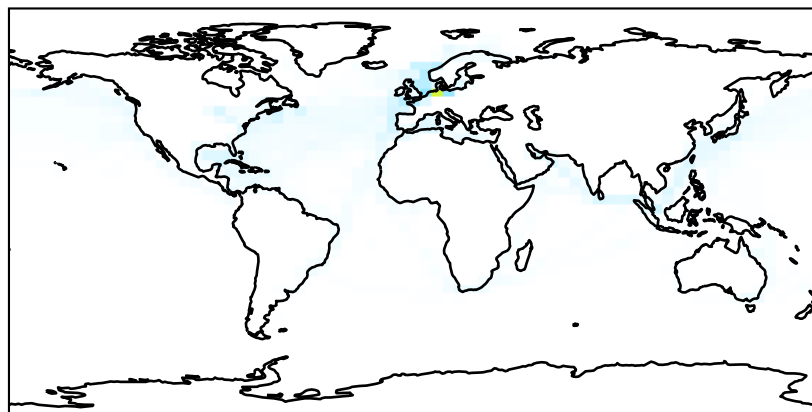


EmisOCPO_Ship

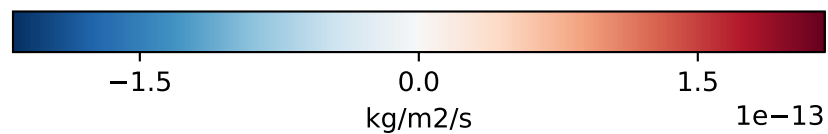
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



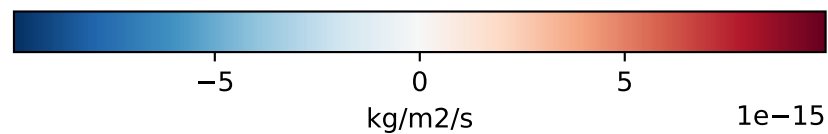
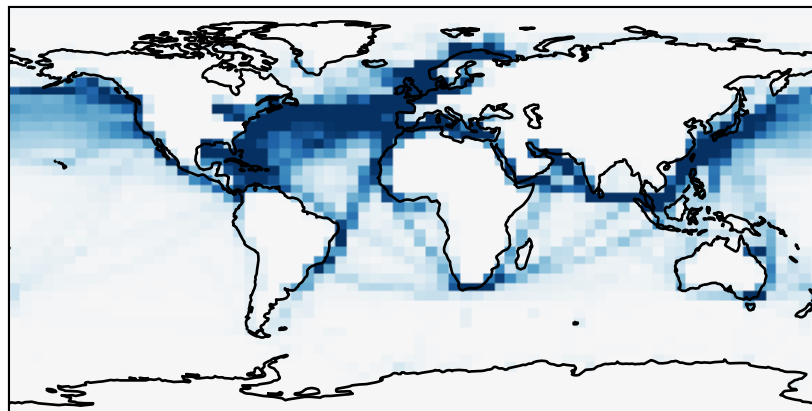
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



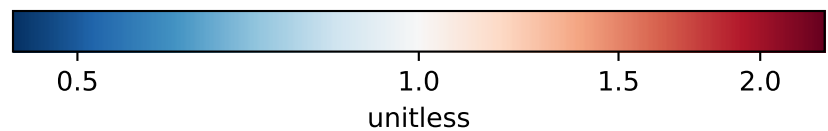
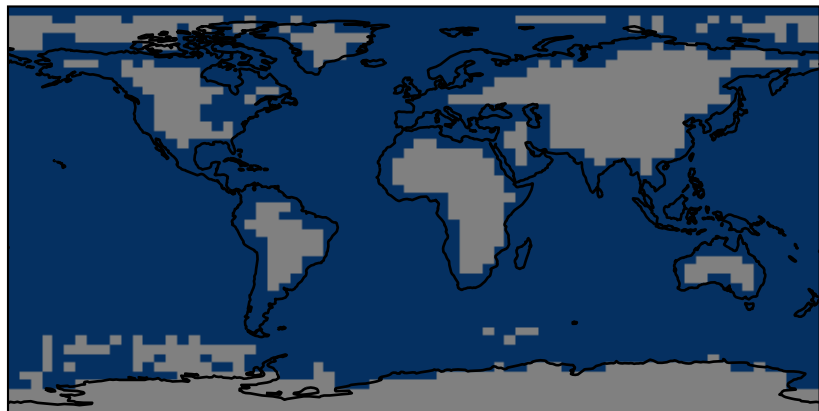
Difference
Dev - Ref, Dynamic Range



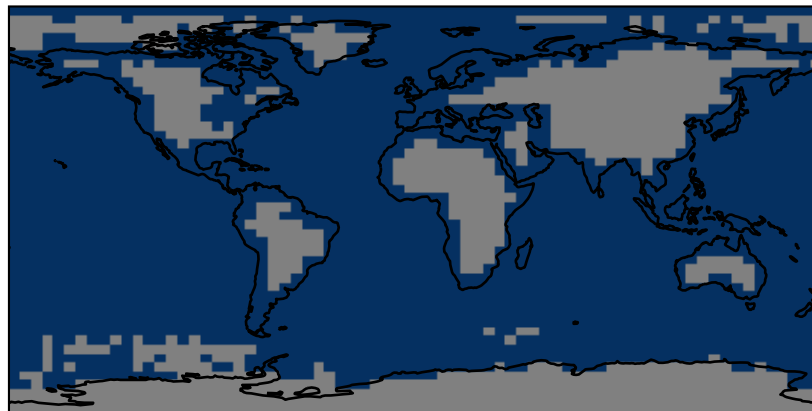
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

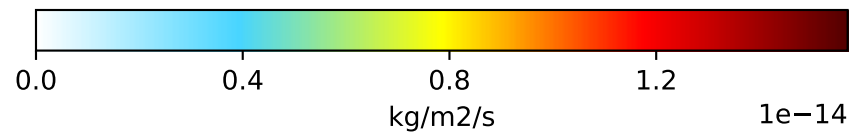
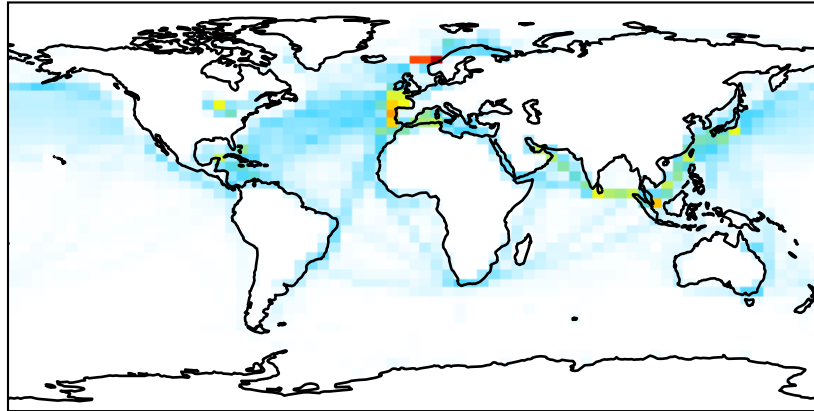


Ratio
Dev/Ref, Fixed Range

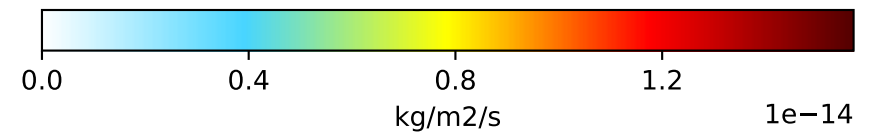
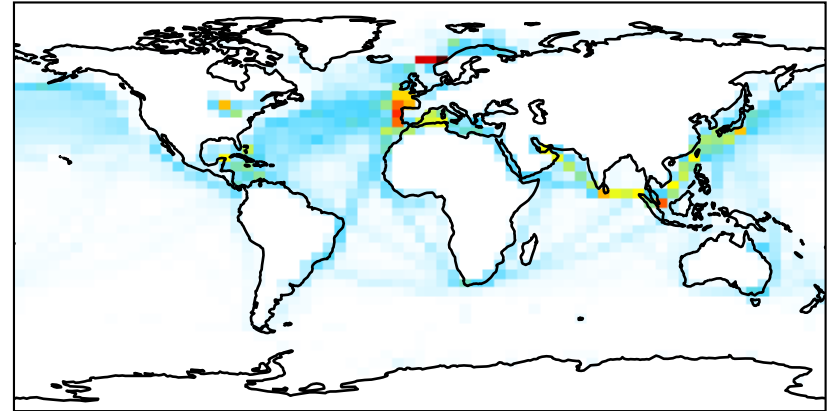


EmispFe_Ship

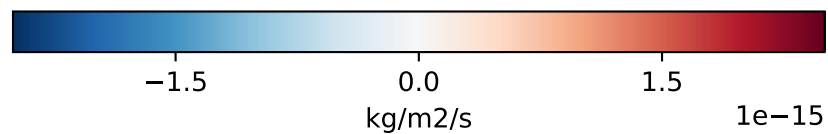
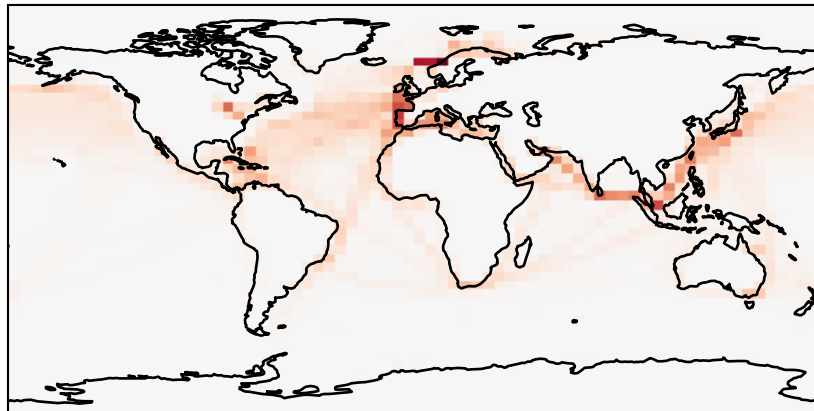
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



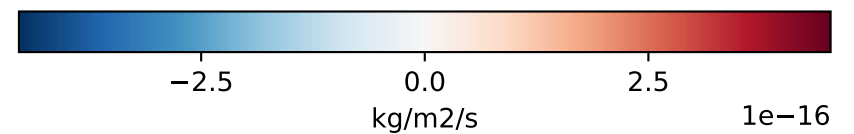
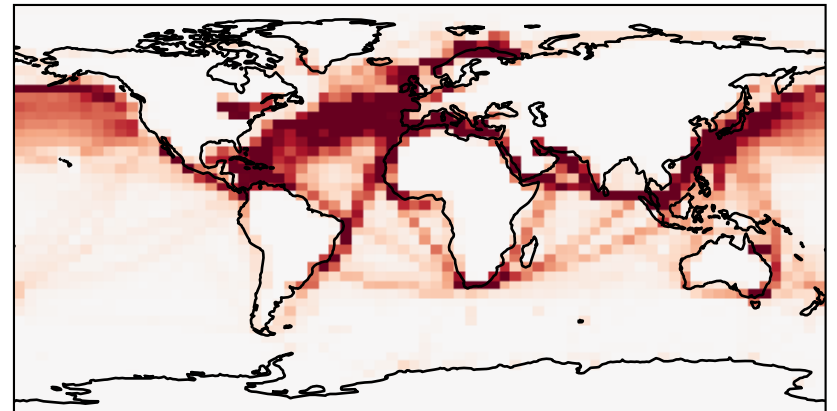
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



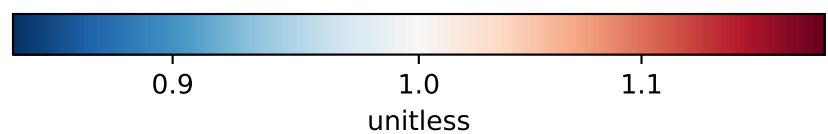
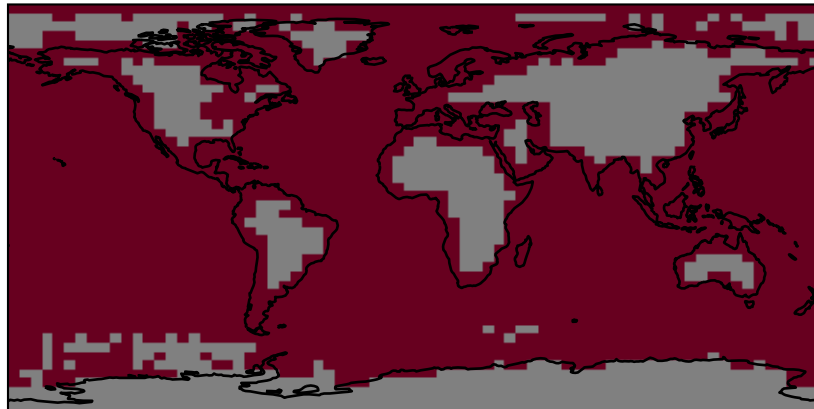
Difference
Dev - Ref, Dynamic Range



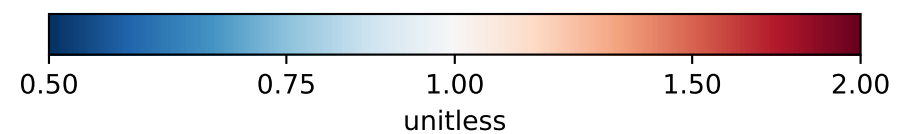
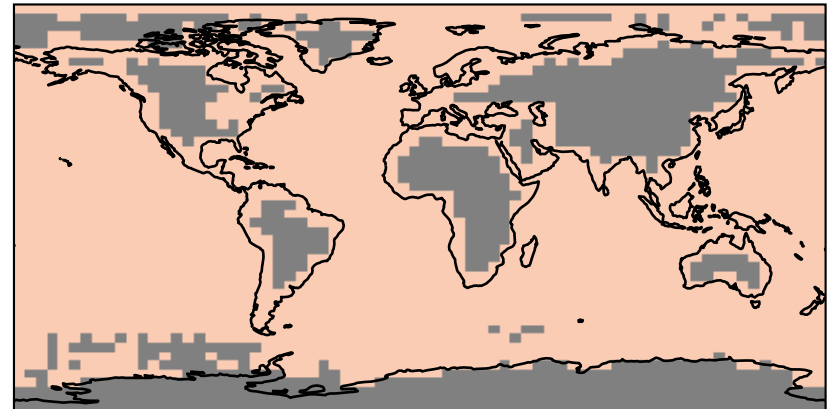
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

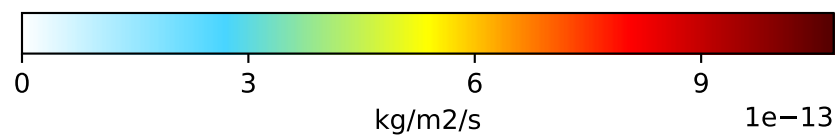
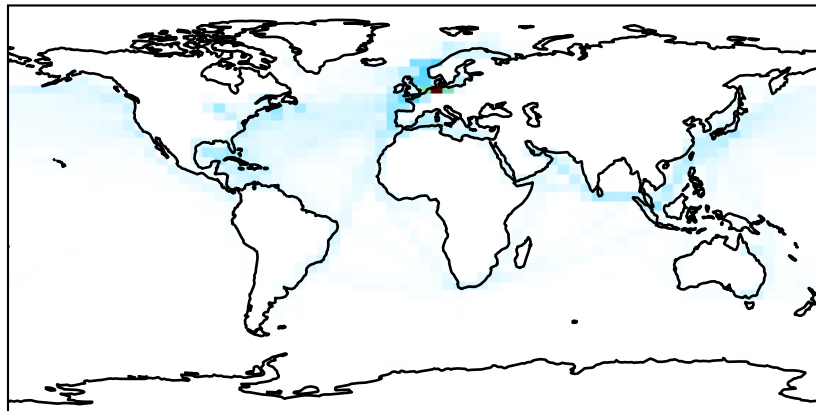


Ratio
Dev/Ref, Fixed Range

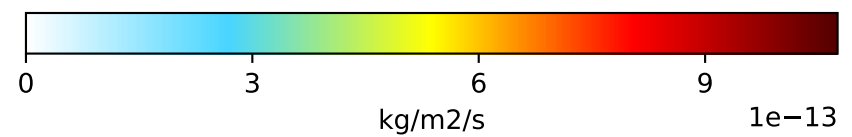
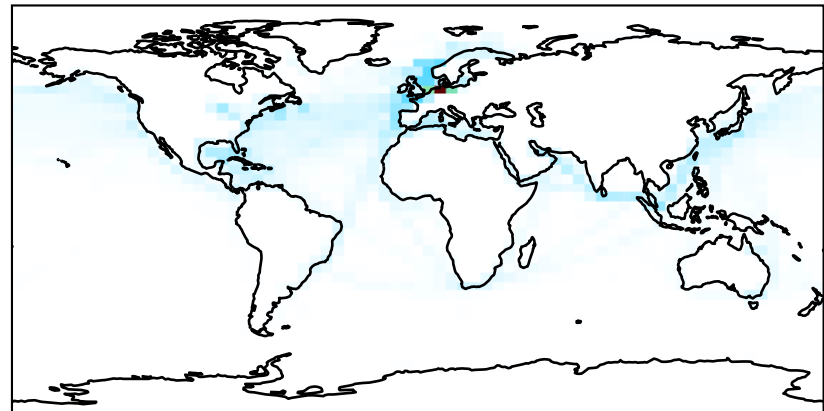


EmisPRPE_Ship

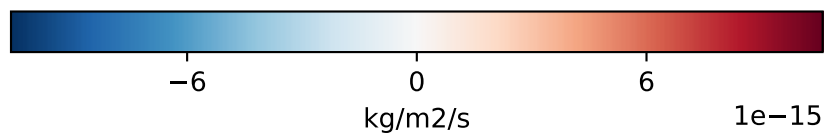
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



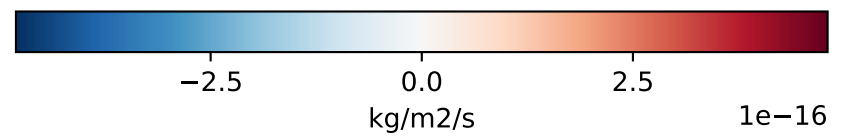
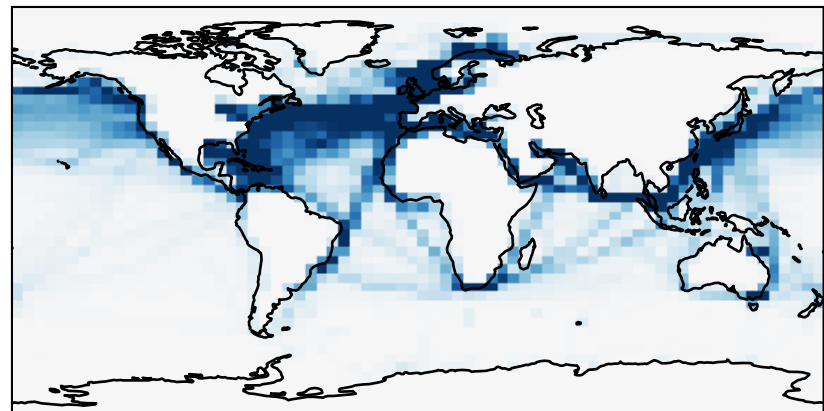
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



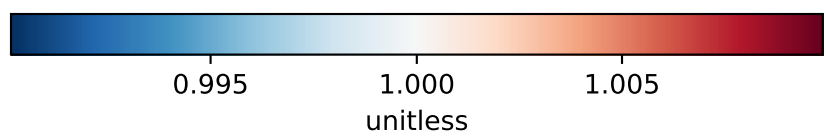
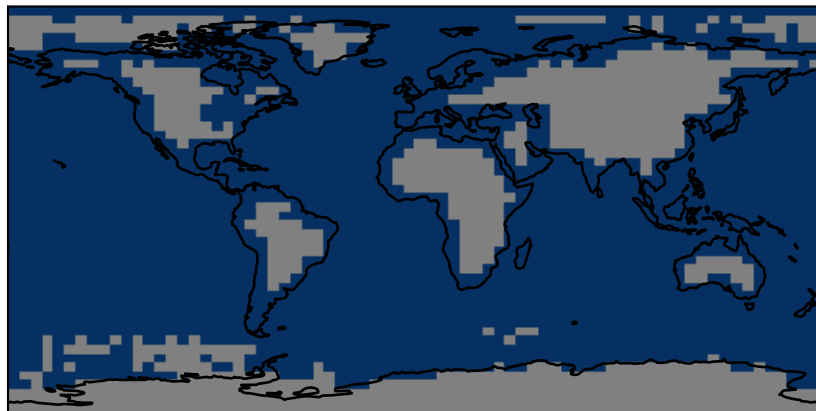
Difference
Dev - Ref, Dynamic Range



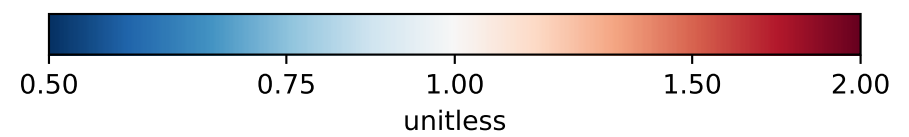
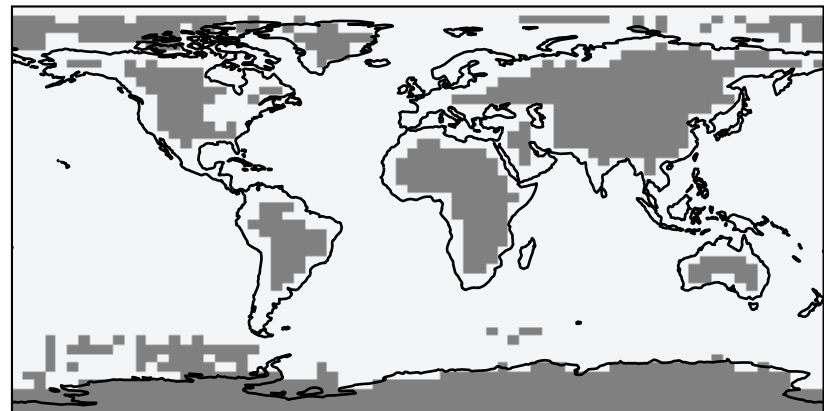
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

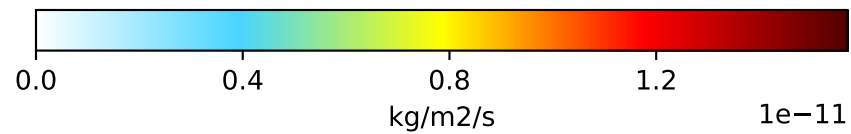
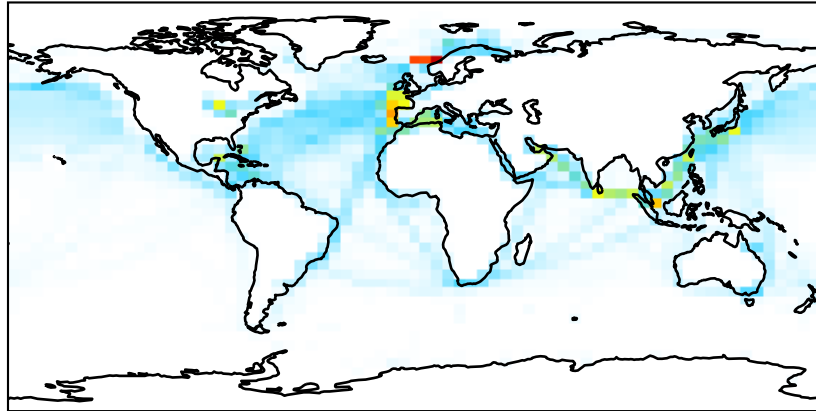


Ratio
Dev/Ref, Fixed Range

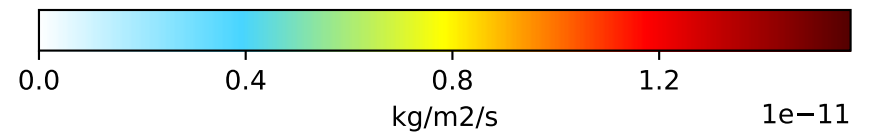
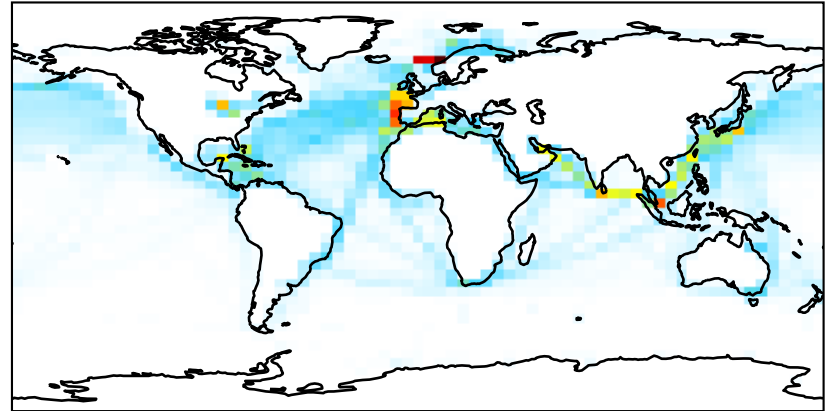


EmisSO2_Ship

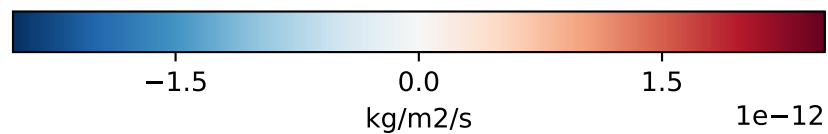
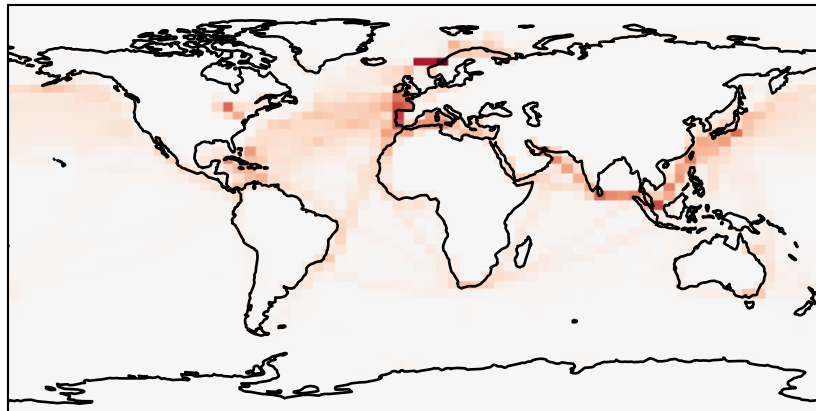
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



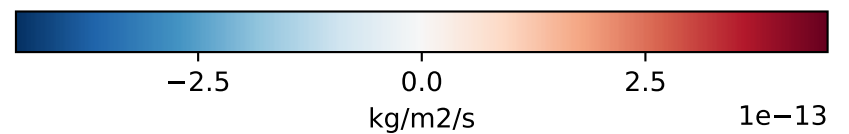
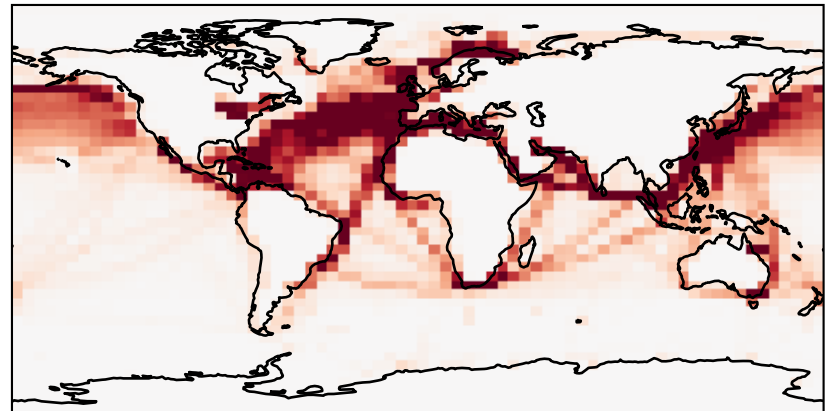
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



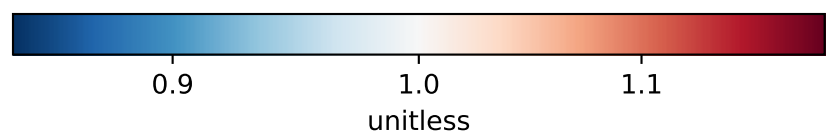
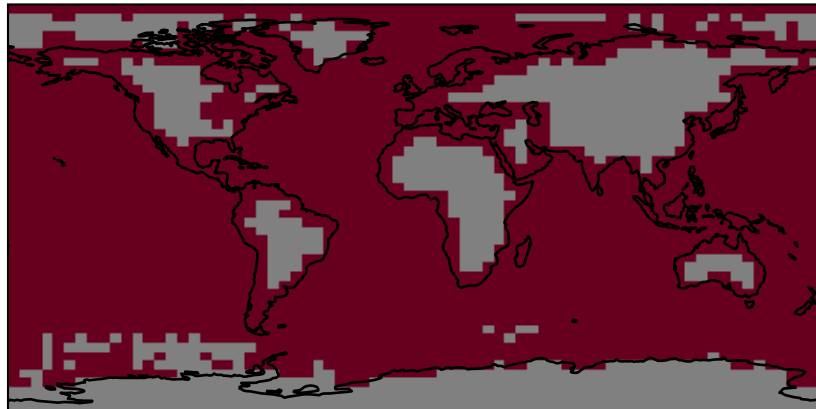
Difference
Dev - Ref, Dynamic Range



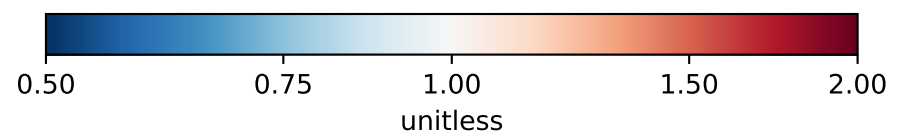
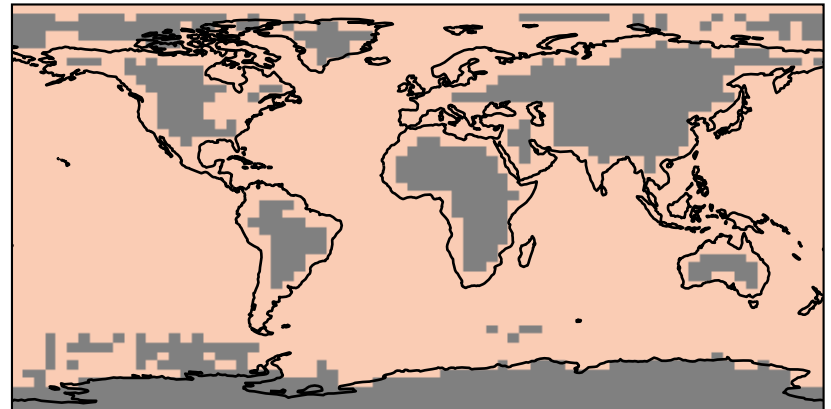
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

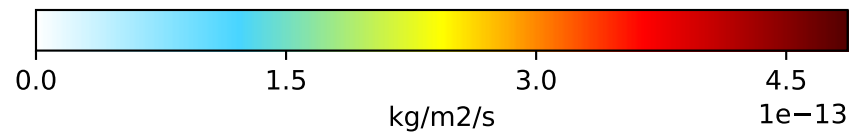
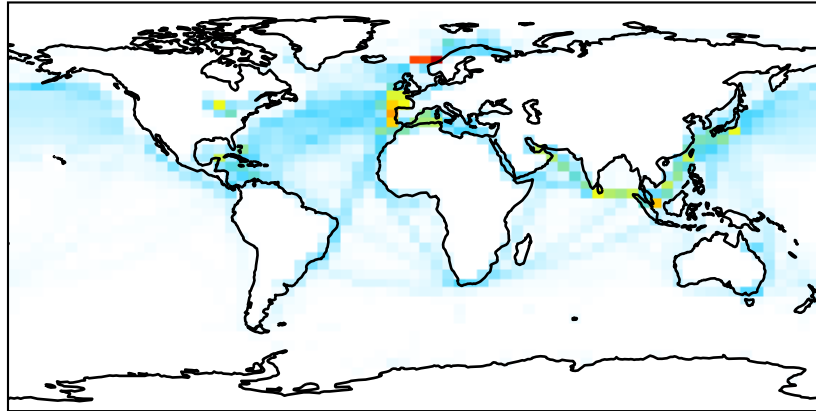


Ratio
Dev/Ref, Fixed Range

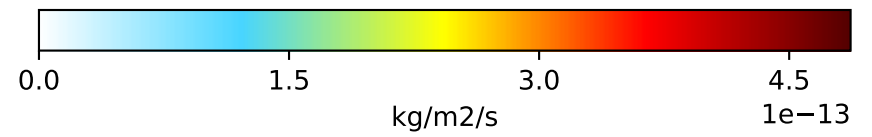
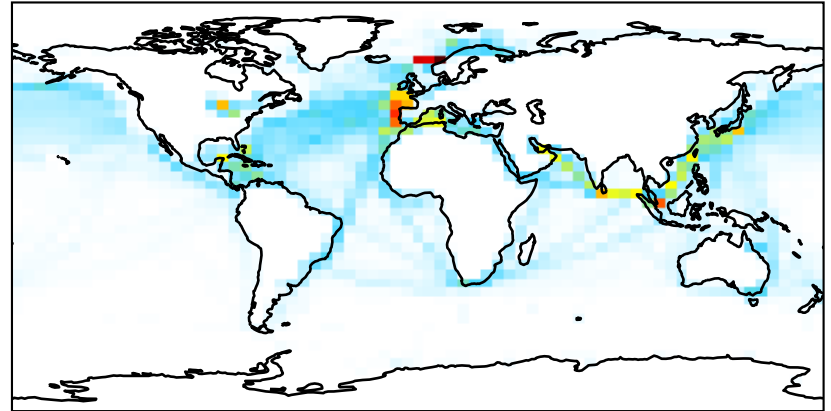


EmisSO4_Ship

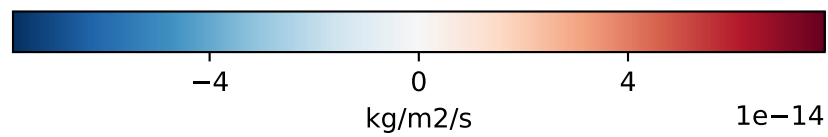
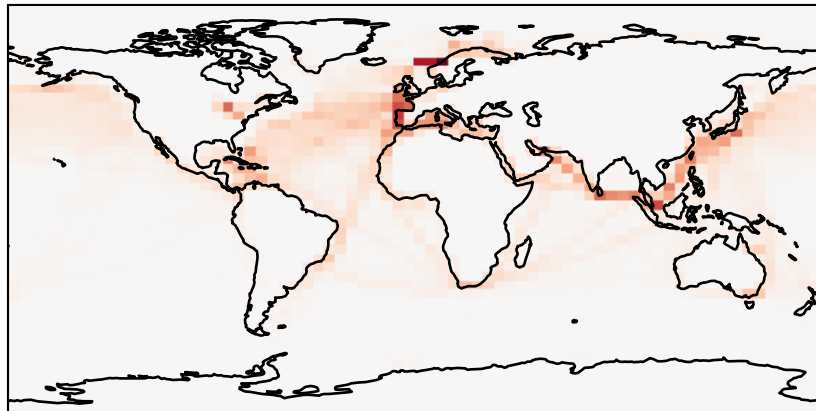
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



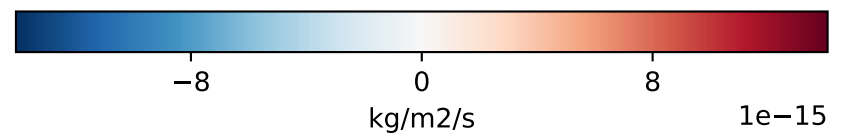
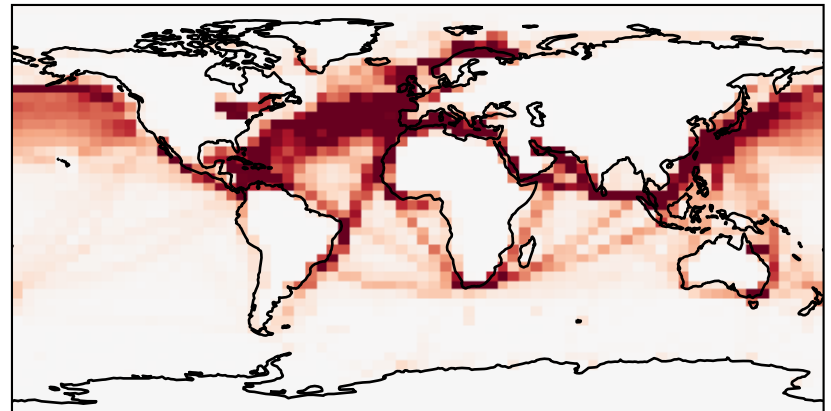
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



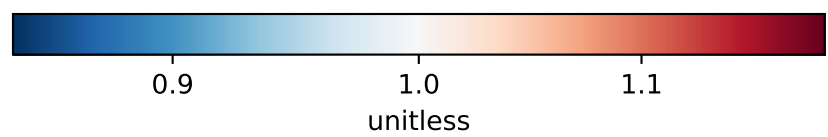
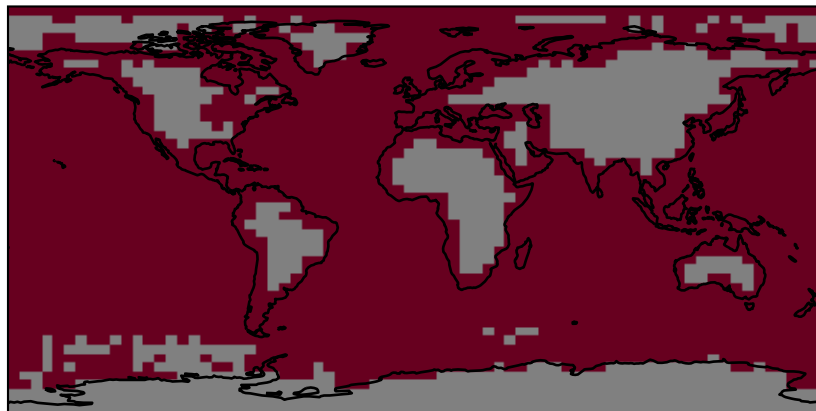
Difference
Dev - Ref, Dynamic Range



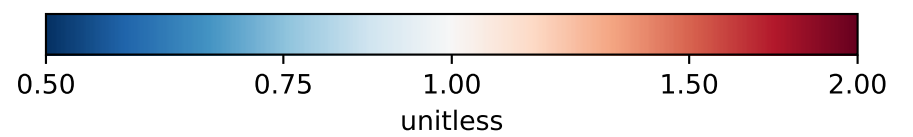
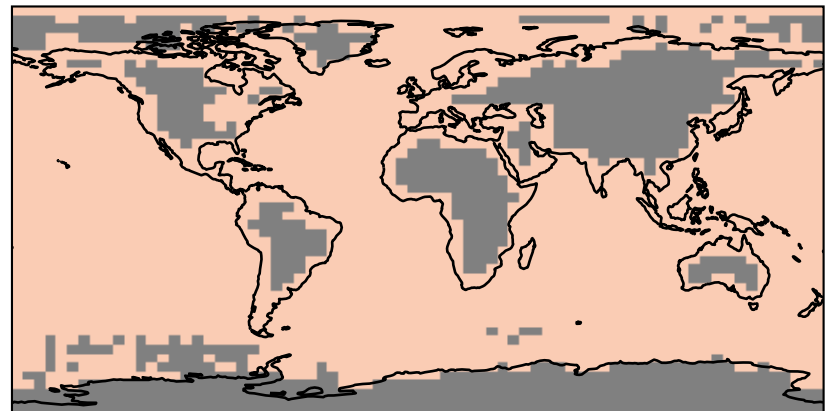
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

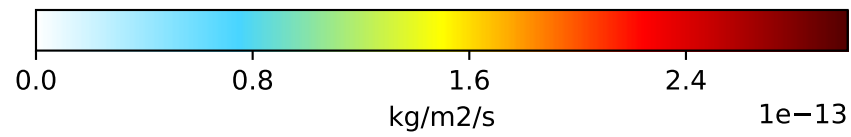
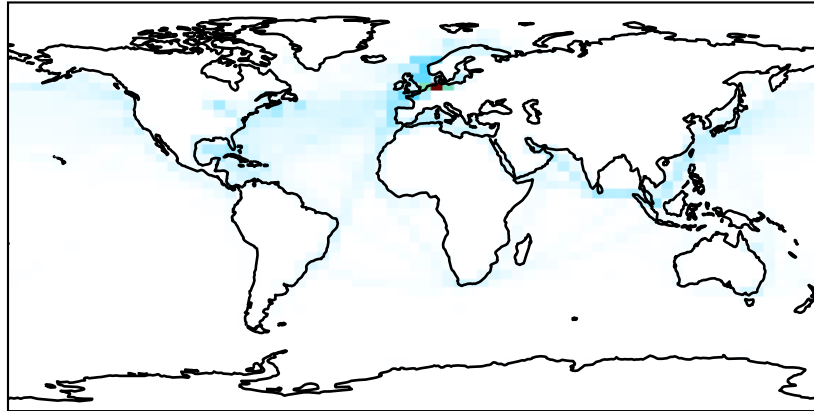


Ratio
Dev/Ref, Fixed Range

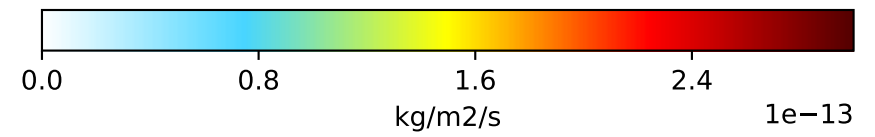
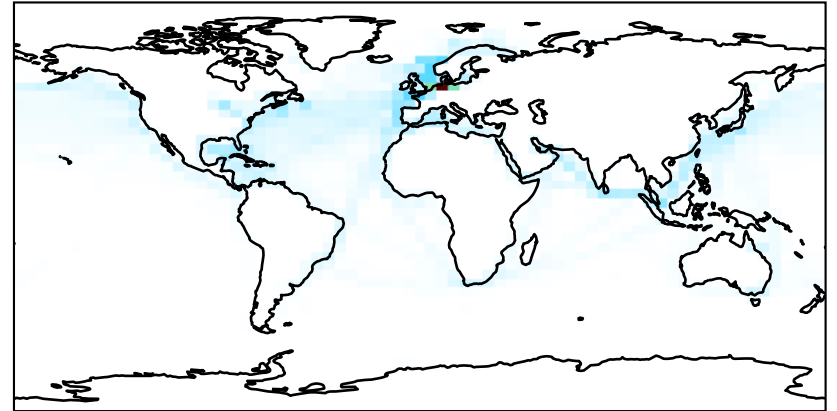


EmisSOAP_Ship

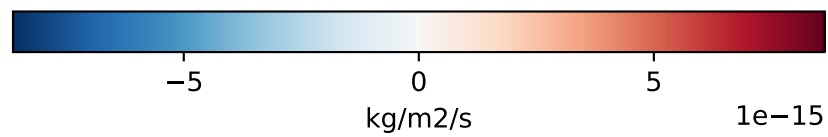
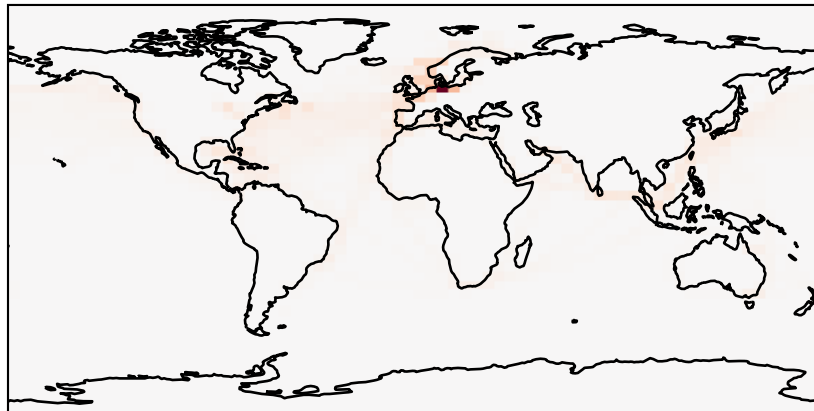
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



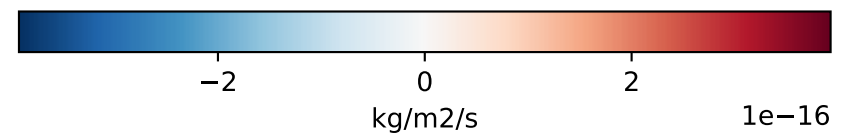
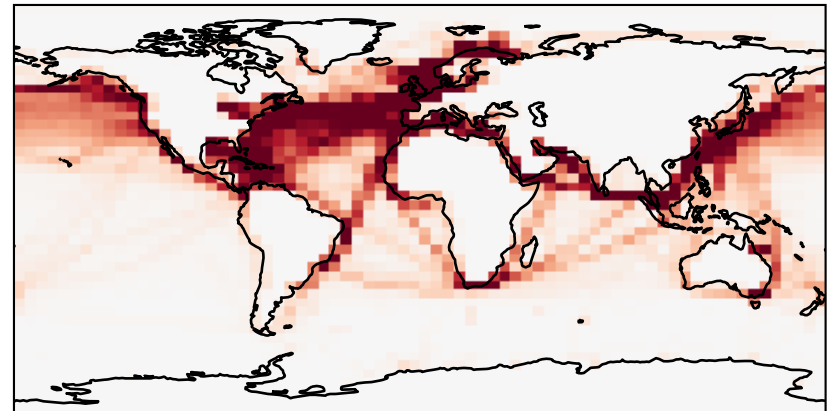
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



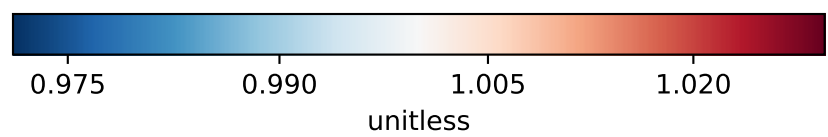
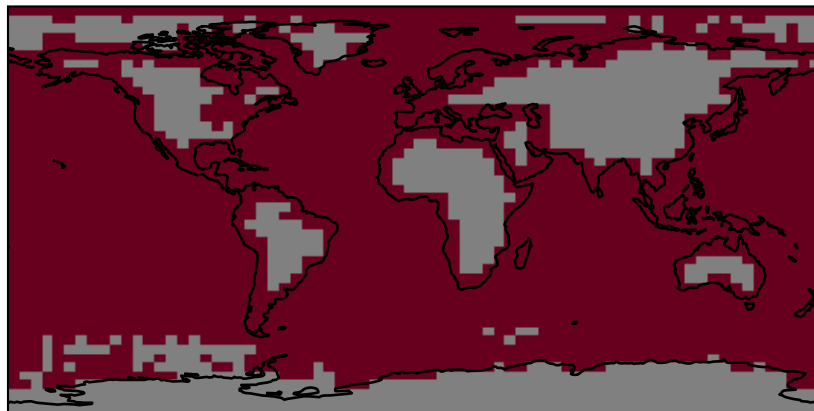
Difference
Dev - Ref, Dynamic Range



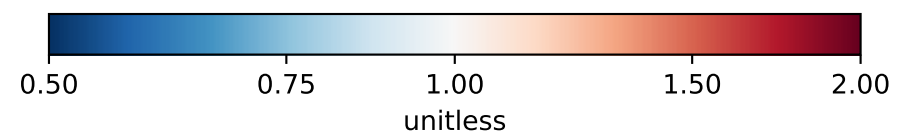
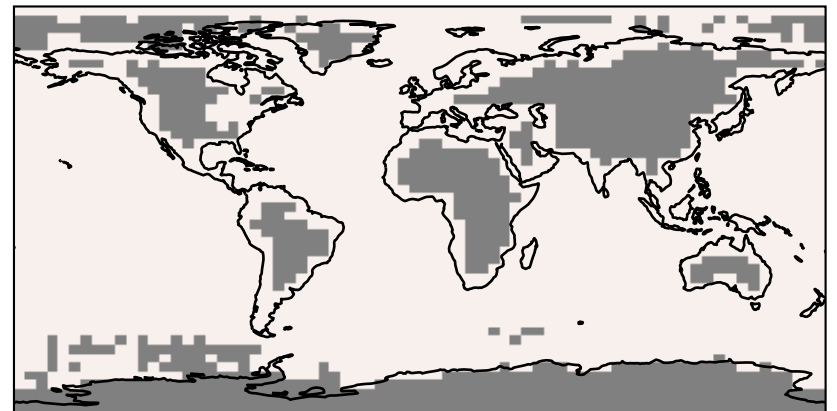
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

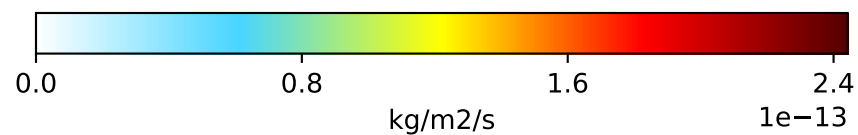
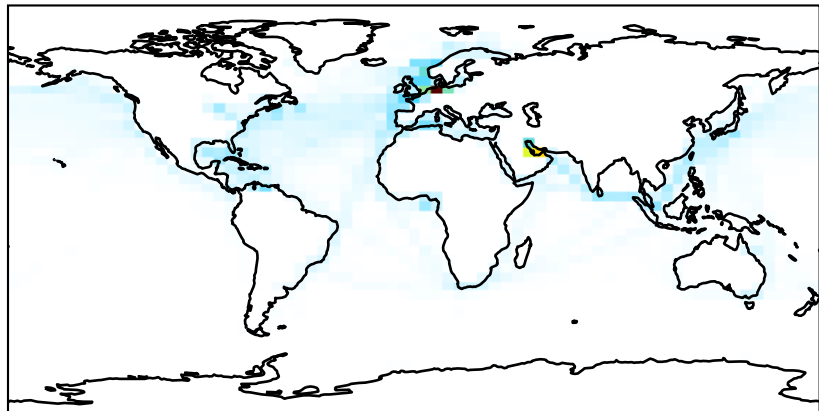


Ratio
Dev/Ref, Fixed Range

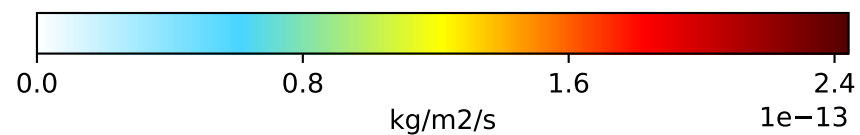
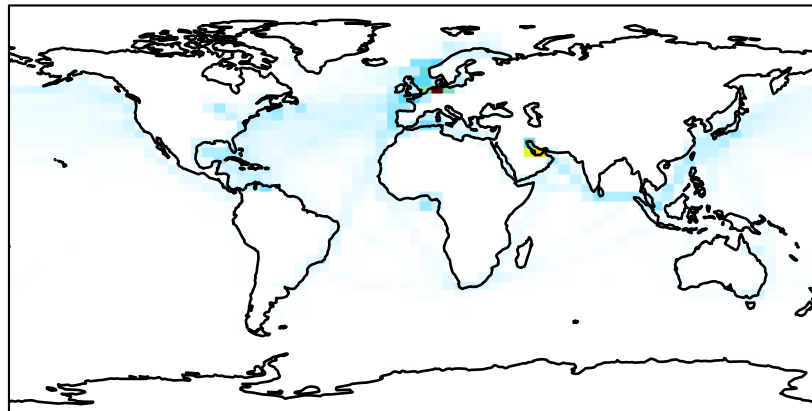


EmisTOLU_Ship

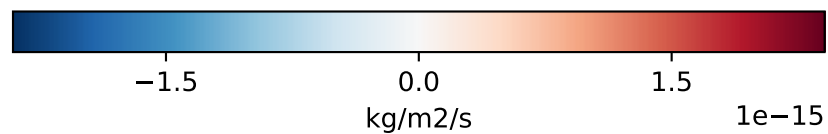
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



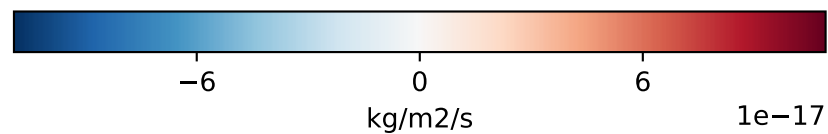
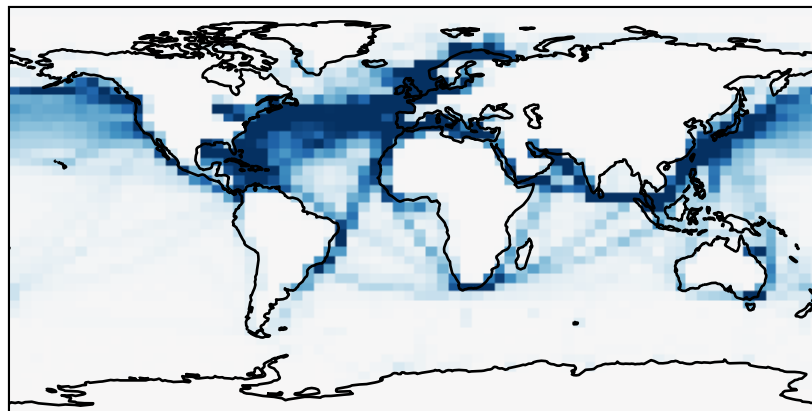
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



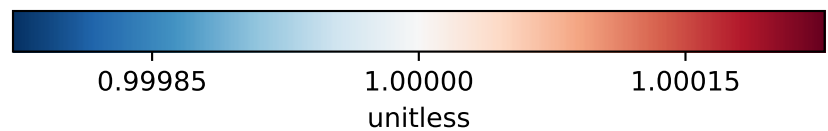
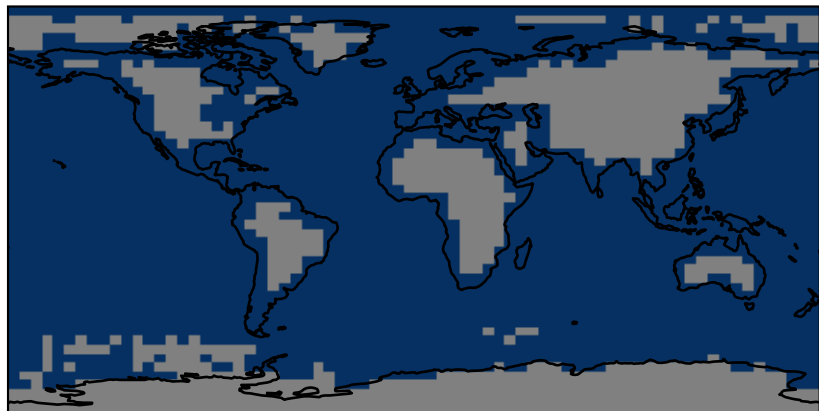
Difference
Dev - Ref, Dynamic Range



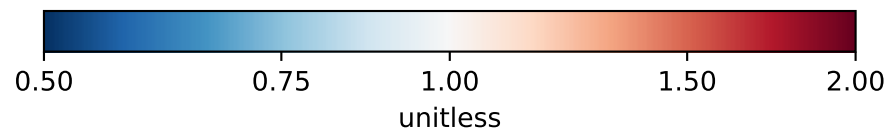
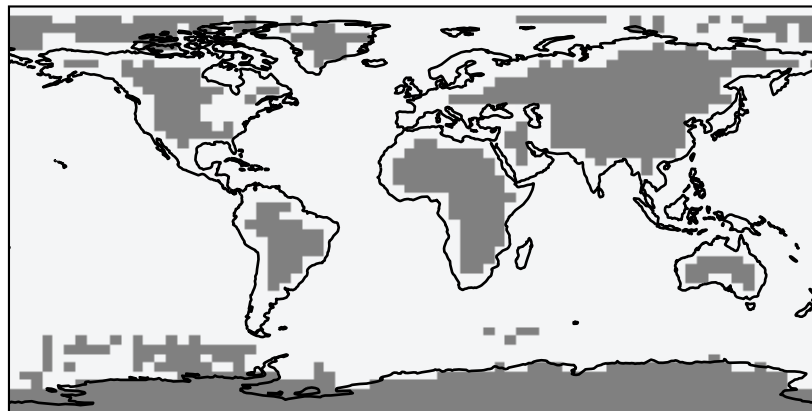
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

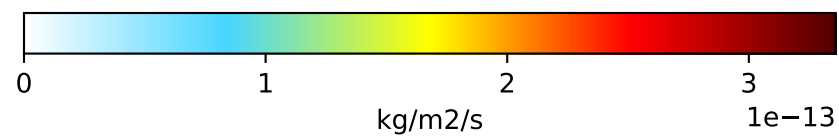
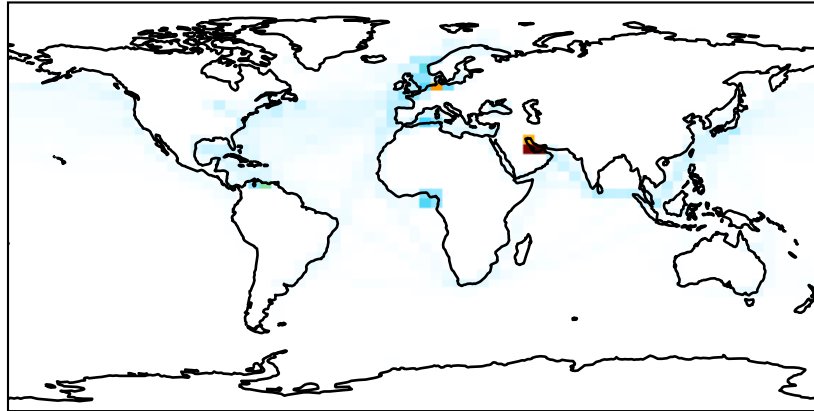


Ratio
Dev/Ref, Fixed Range

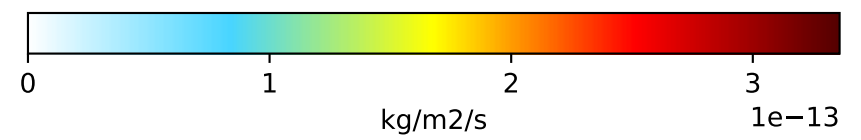
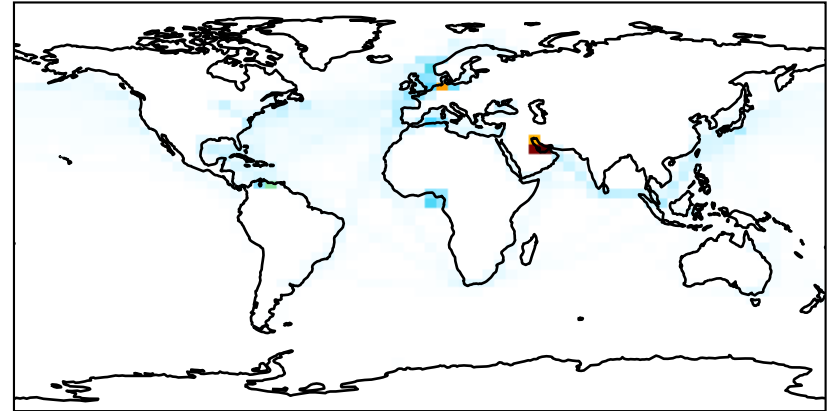


EmisXYLE_Ship

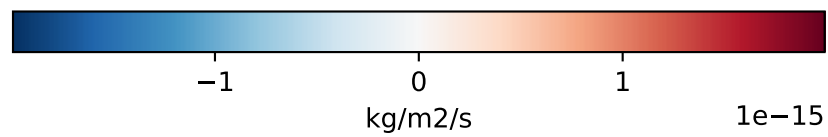
GCC_13.1.0-beta.1 (Ref)
4.0x5.0



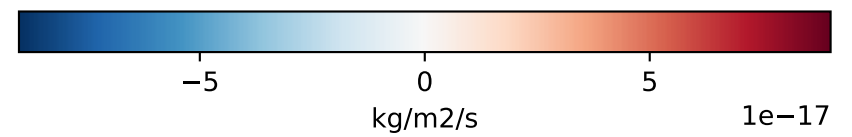
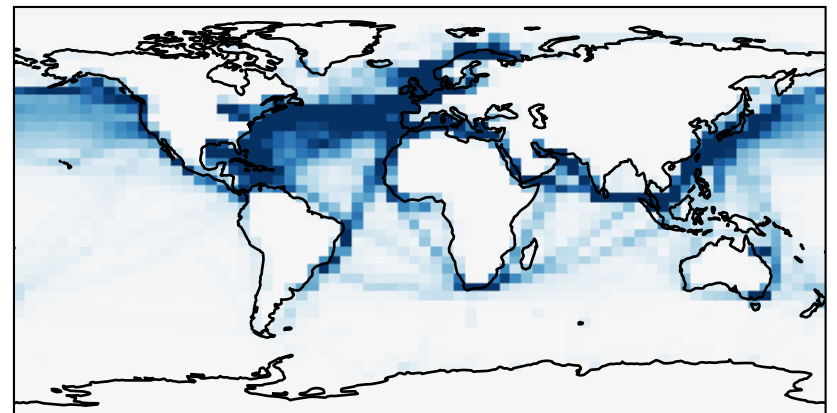
GCC_13.2.0-beta.0 (Dev)
4.0x5.0



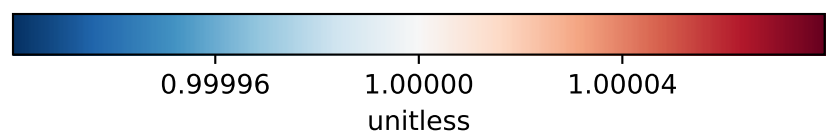
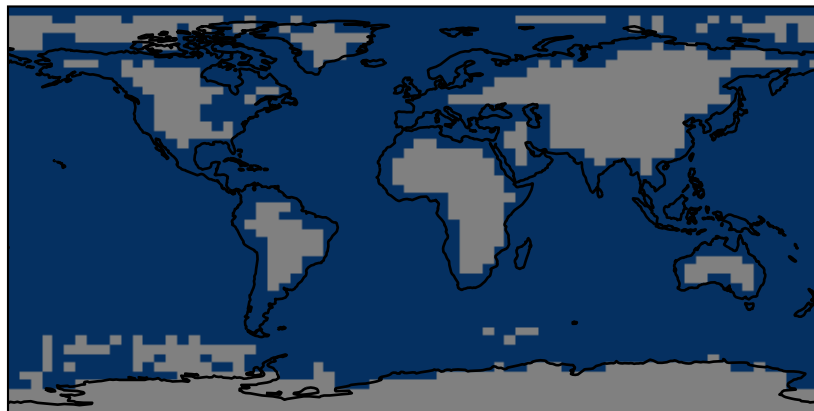
Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range



Ratio
Dev/Ref, Fixed Range

