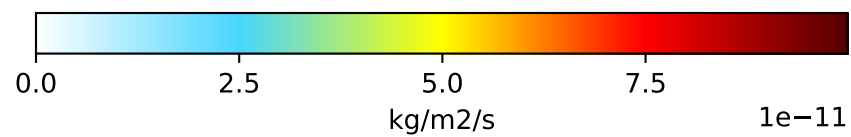
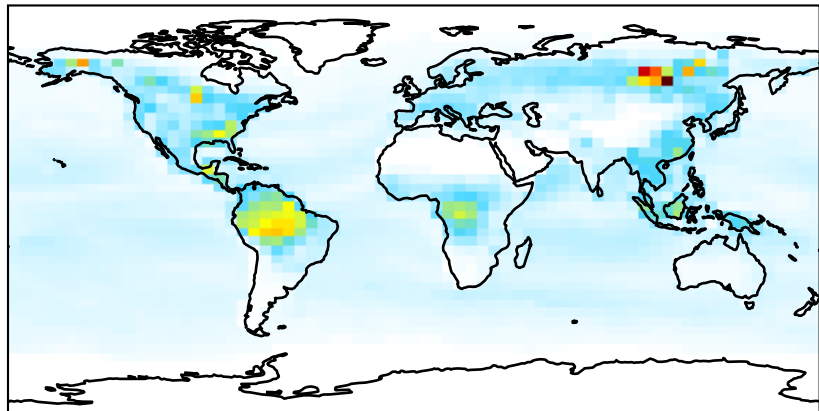
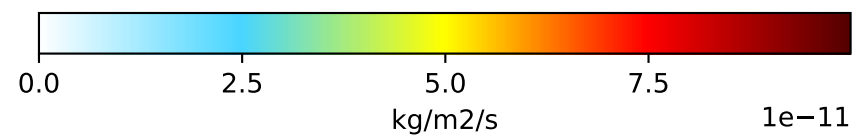
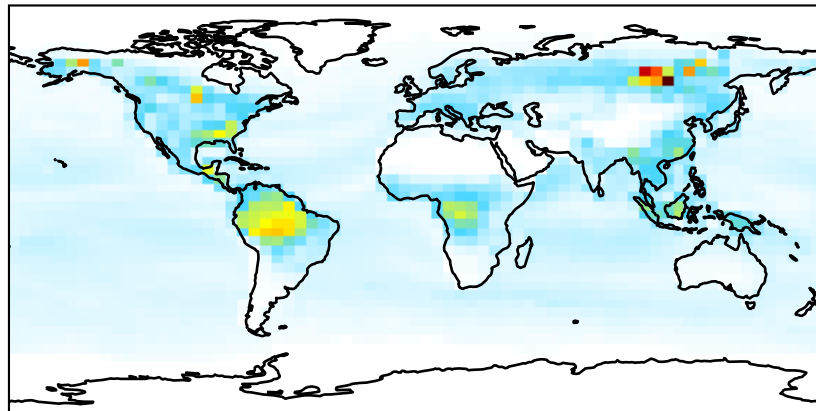


# EmisACET\_Total

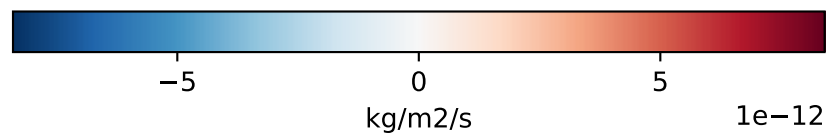
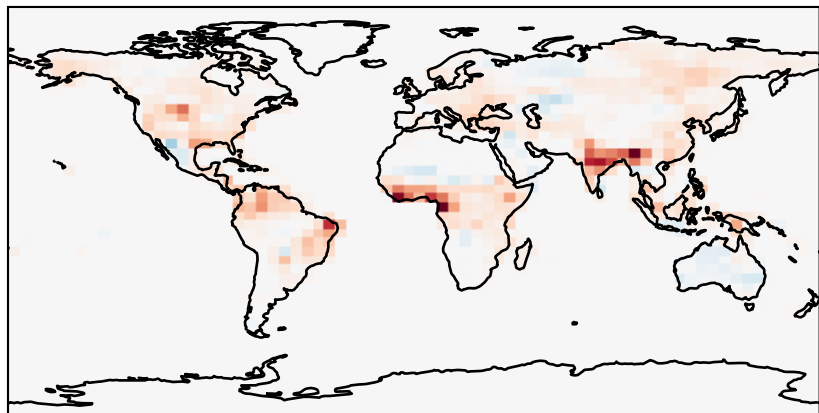
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



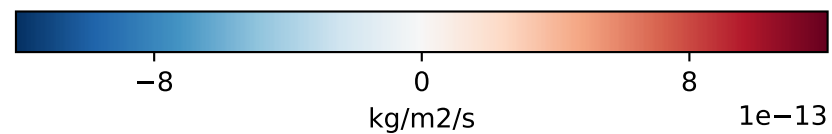
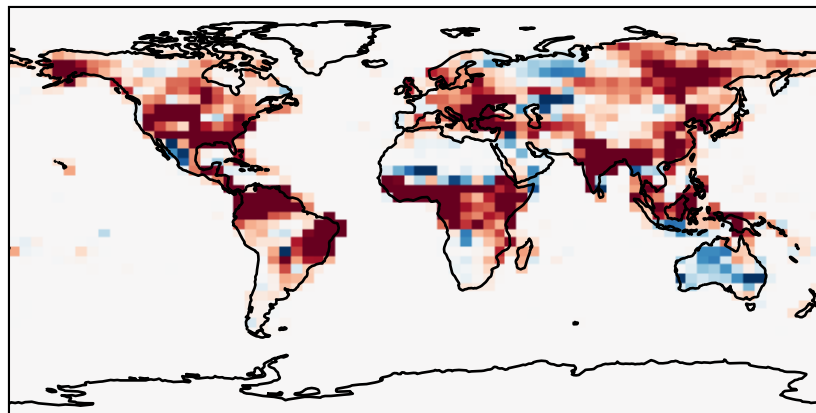
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



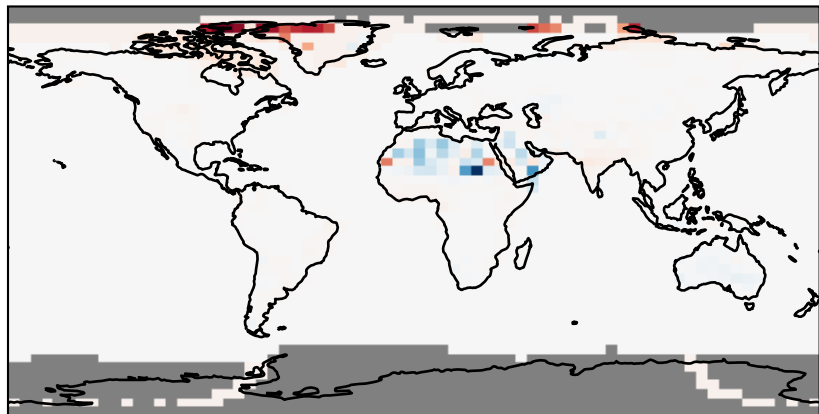
Difference  
Dev - Ref, Dynamic Range



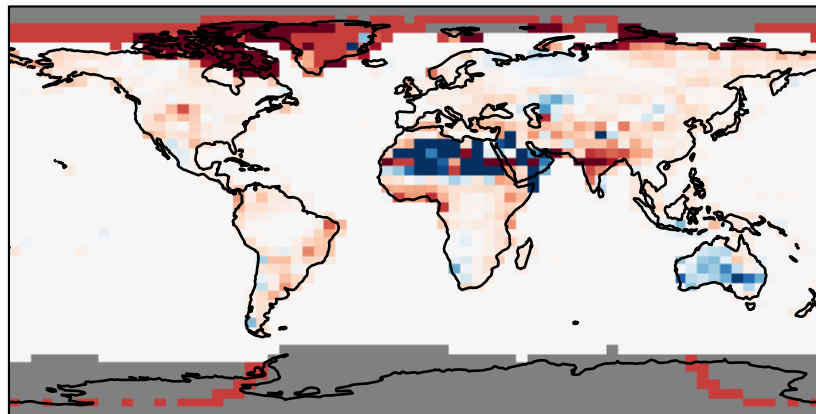
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

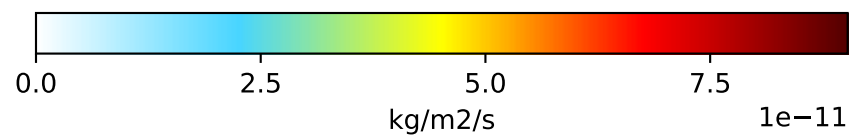
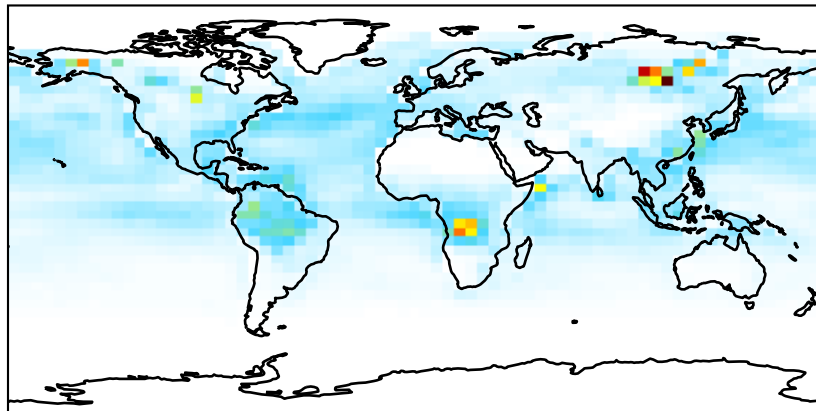


Ratio  
Dev/Ref, Fixed Range

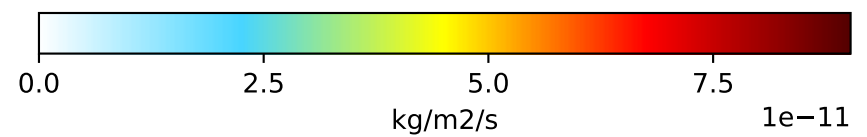
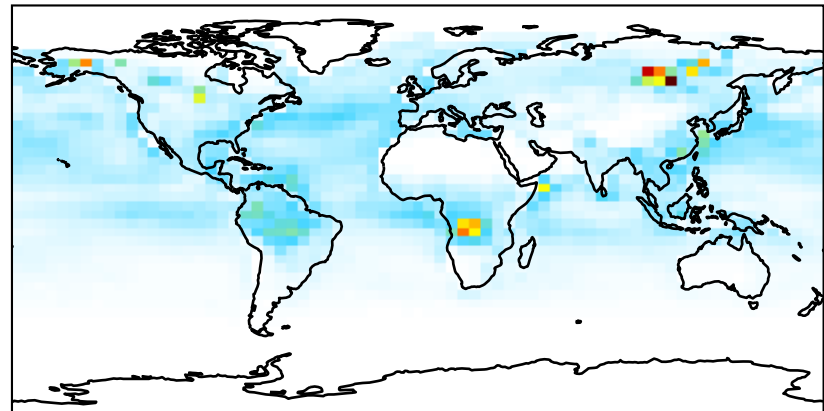


# EmisALD2\_Total

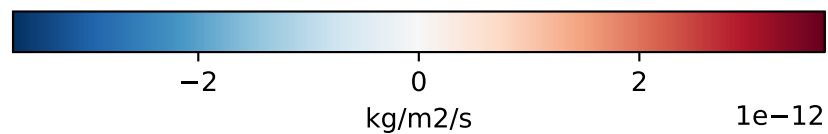
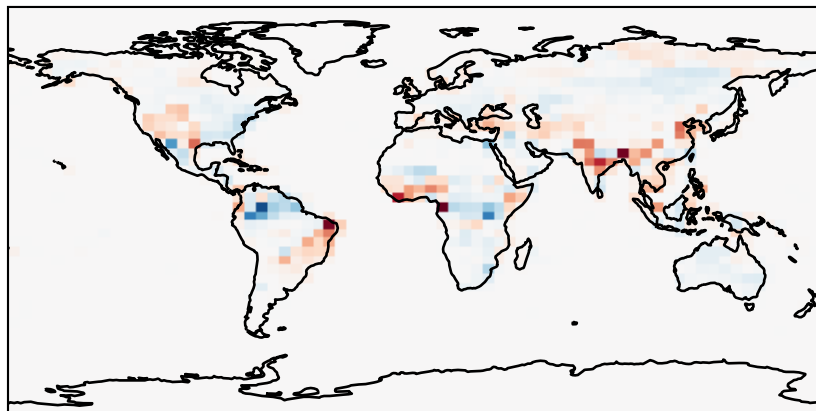
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



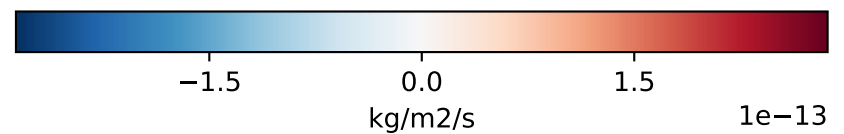
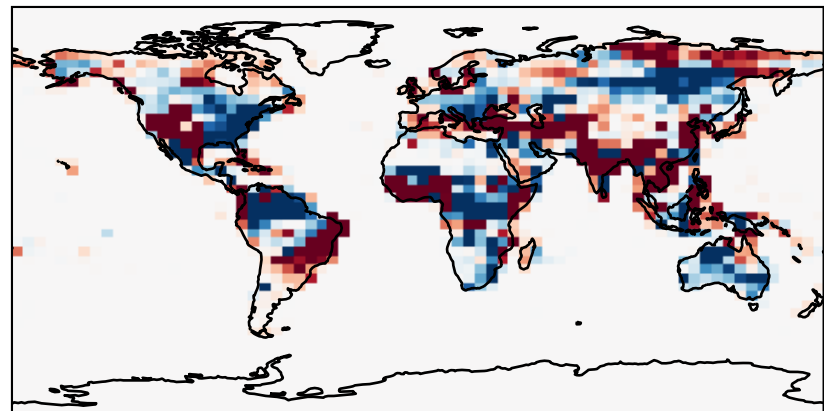
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



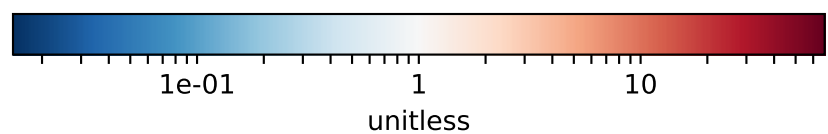
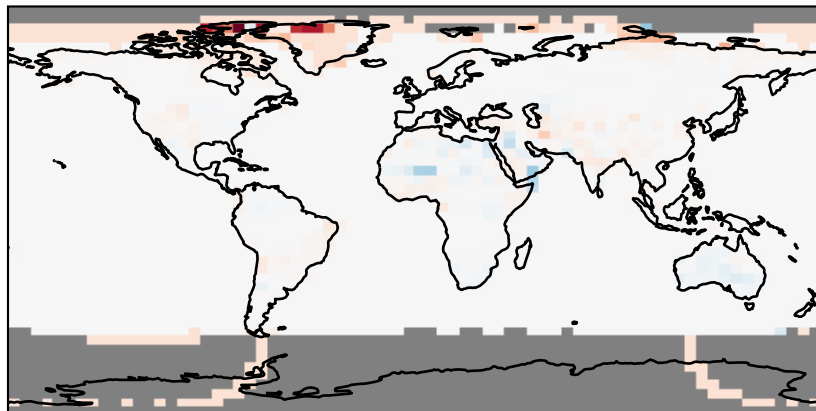
Difference  
Dev - Ref, Dynamic Range



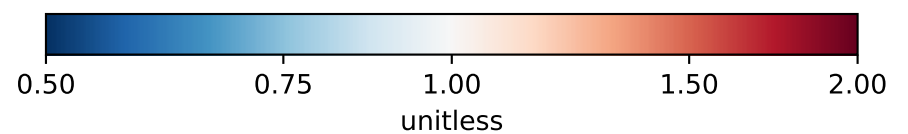
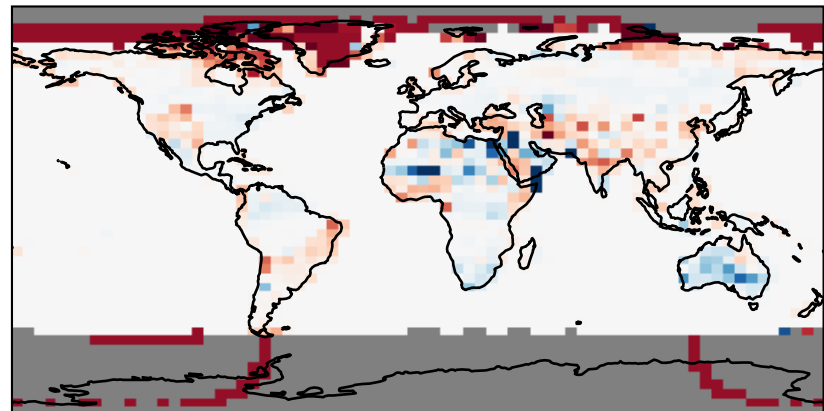
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

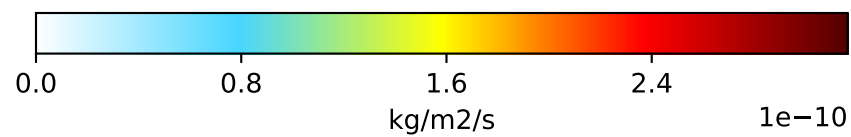
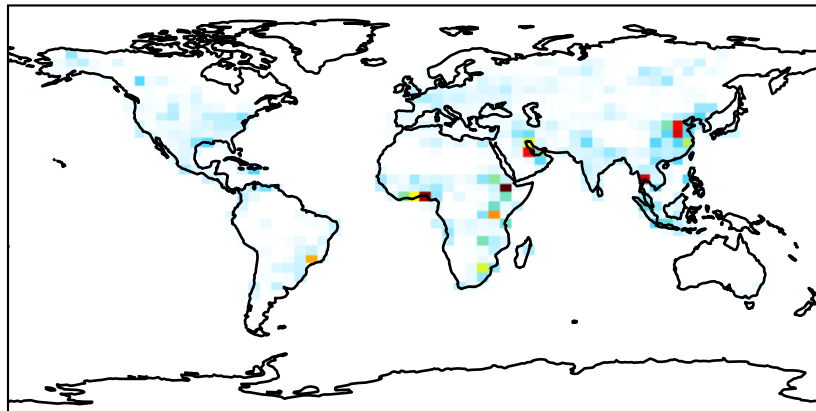


Ratio  
Dev/Ref, Fixed Range

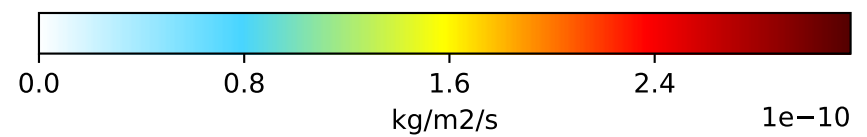
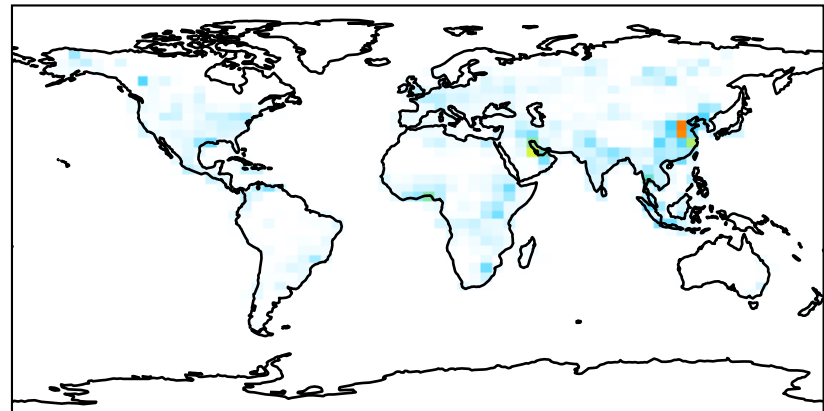


# EmisALK4\_Total

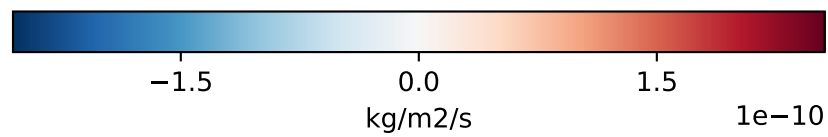
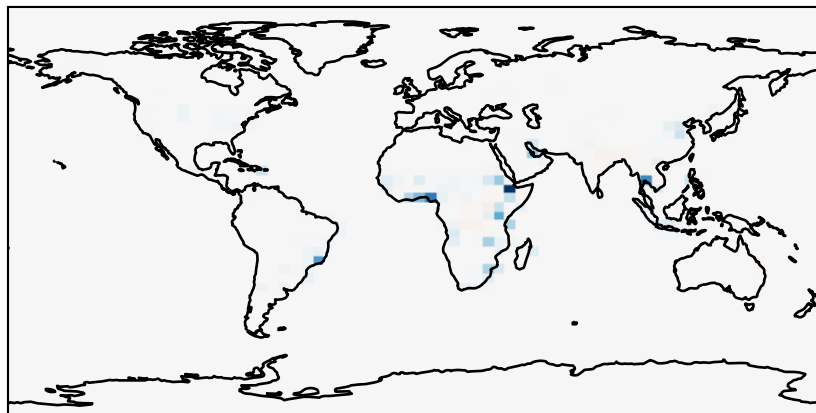
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



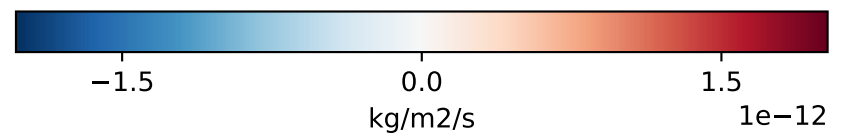
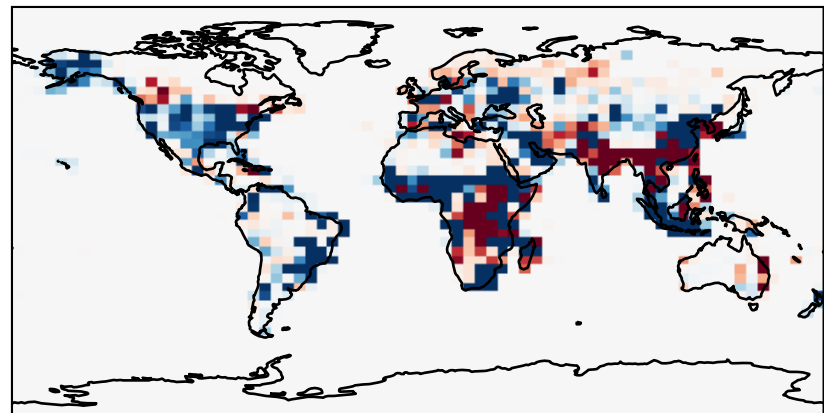
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



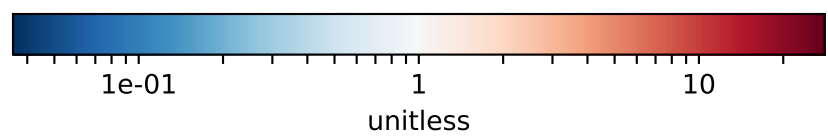
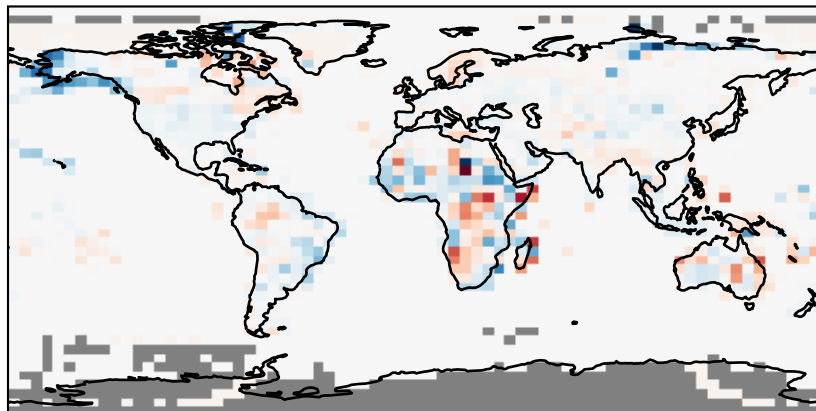
Difference  
Dev - Ref, Dynamic Range



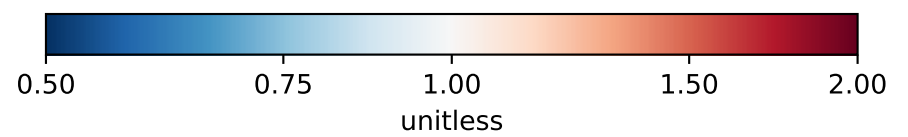
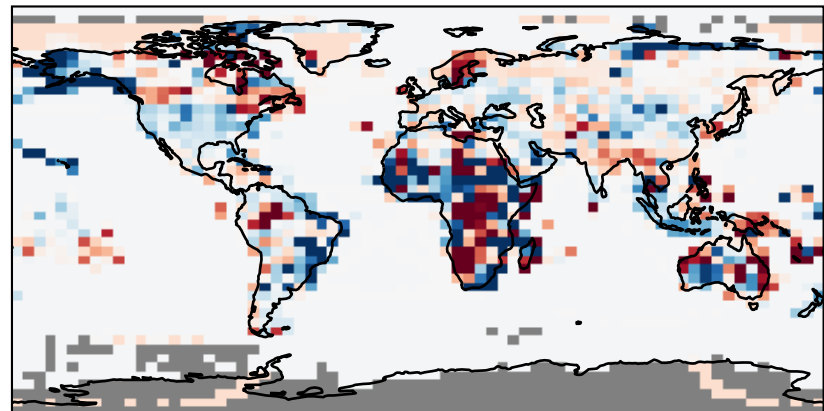
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

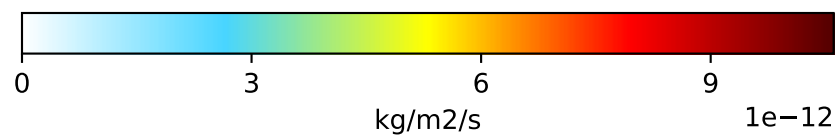
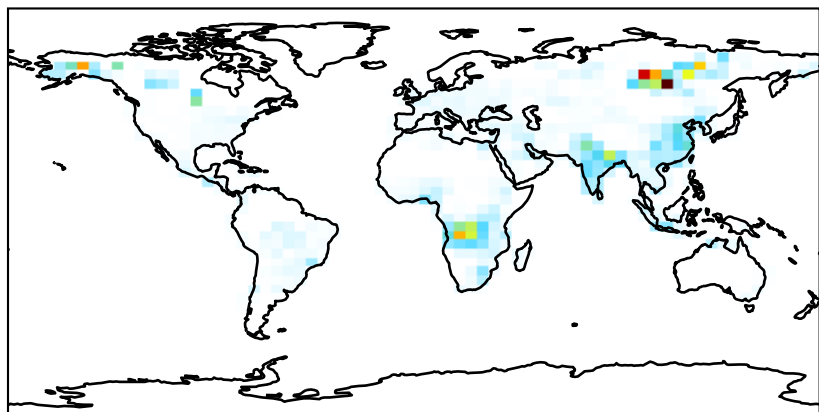


Ratio  
Dev/Ref, Fixed Range

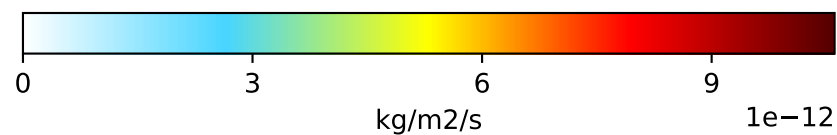
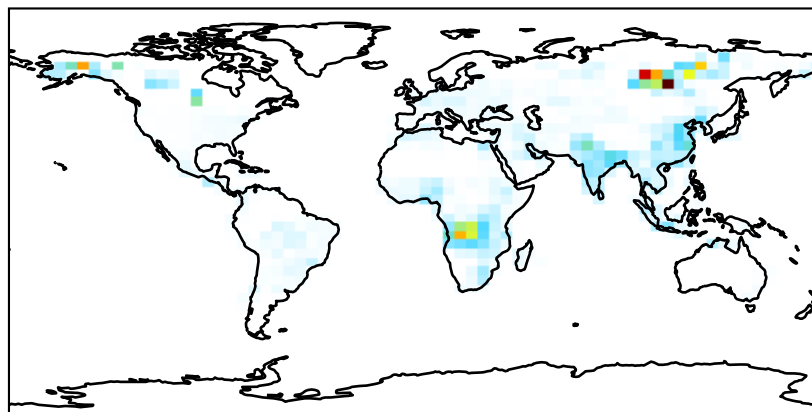


# EmisBCPI\_Total

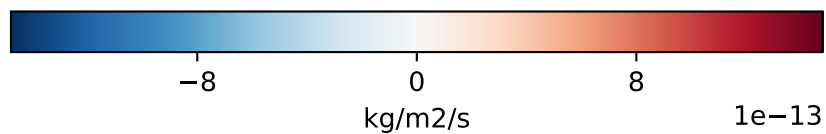
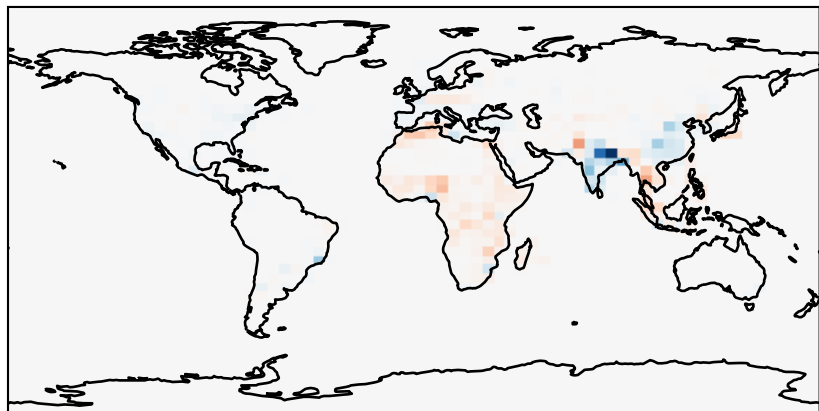
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



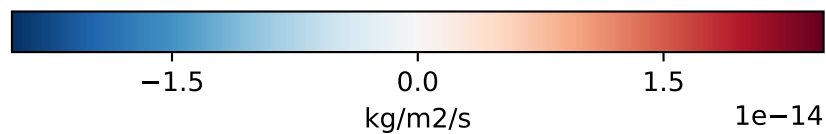
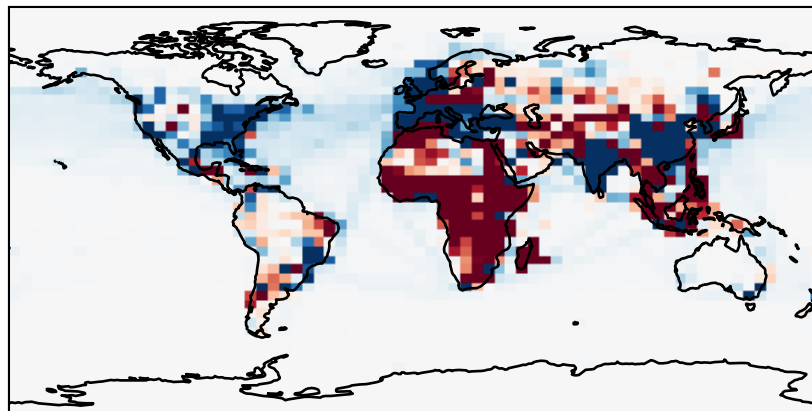
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



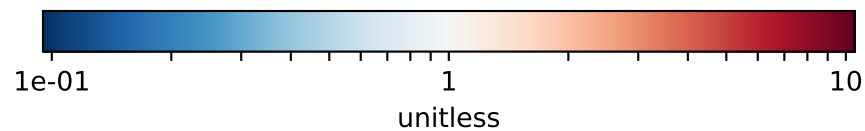
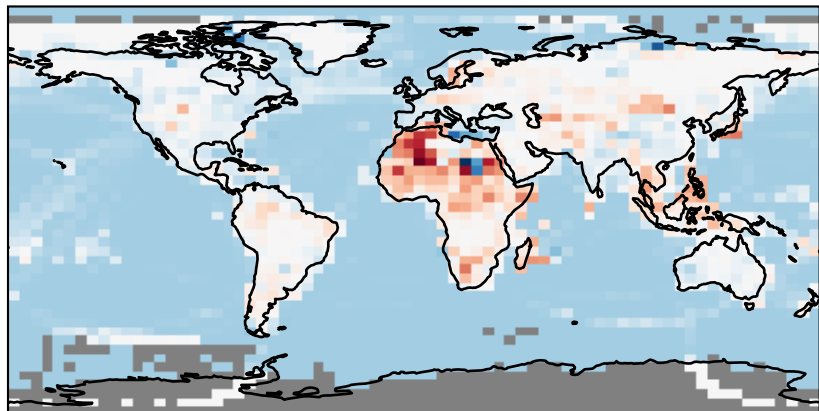
Difference  
Dev - Ref, Dynamic Range



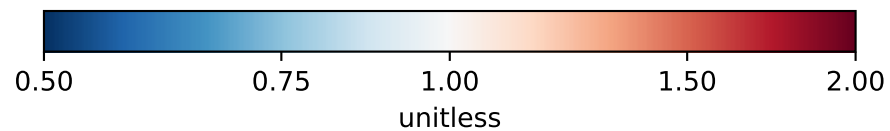
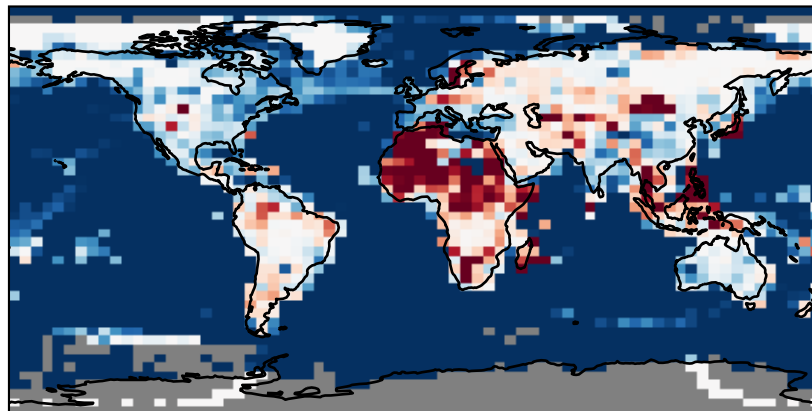
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



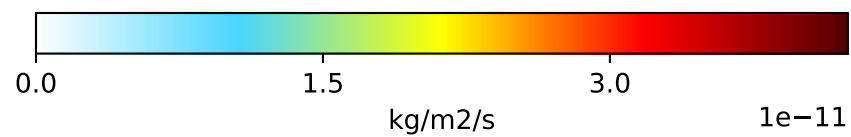
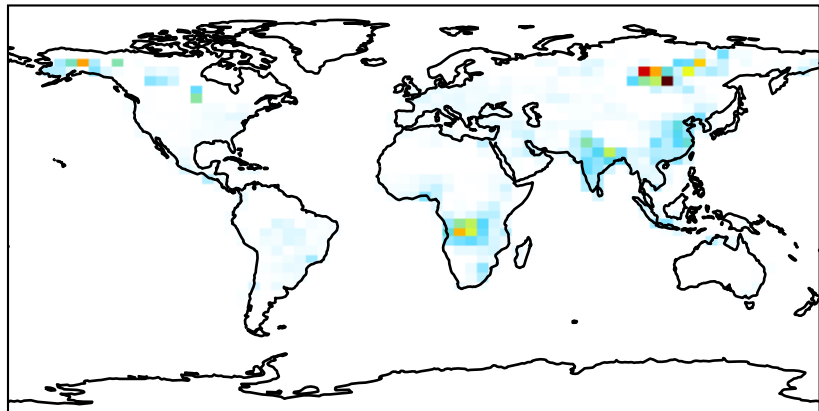
Ratio  
Dev/Ref, Fixed Range



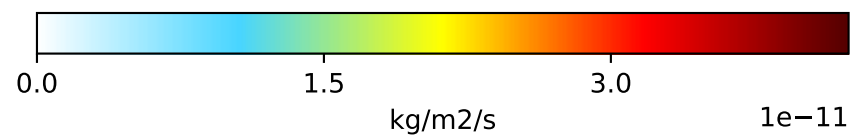
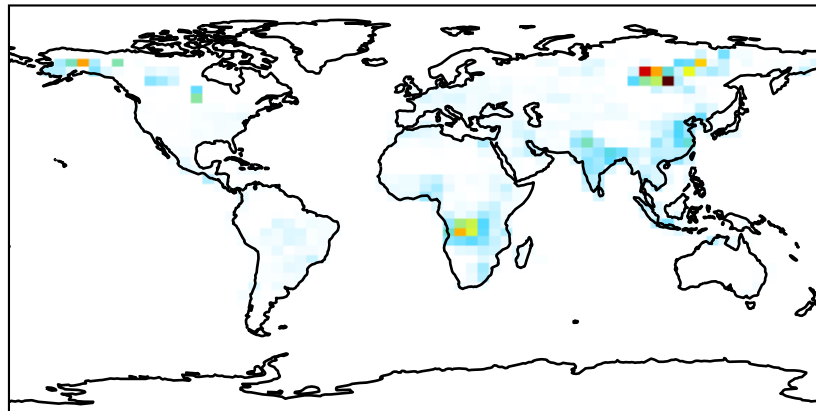


# EmisBCPO\_Total

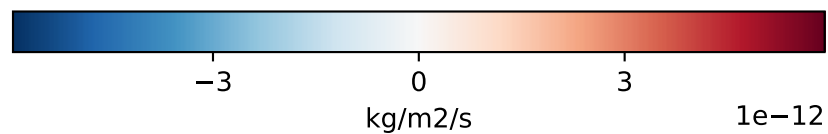
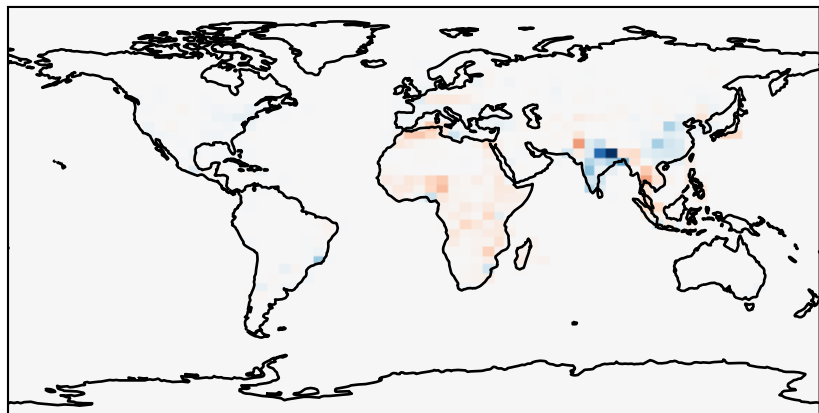
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



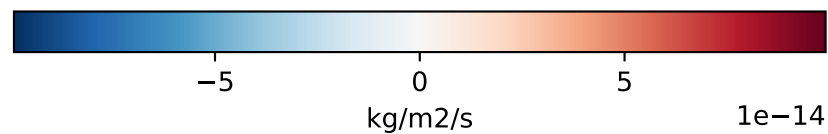
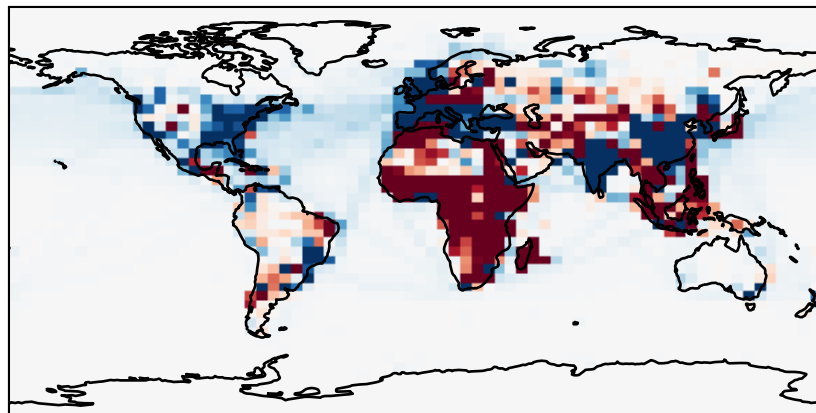
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



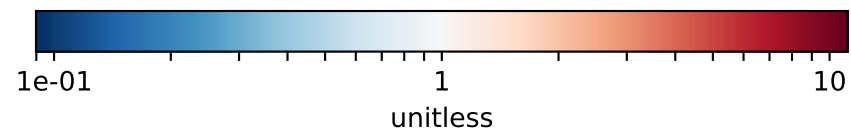
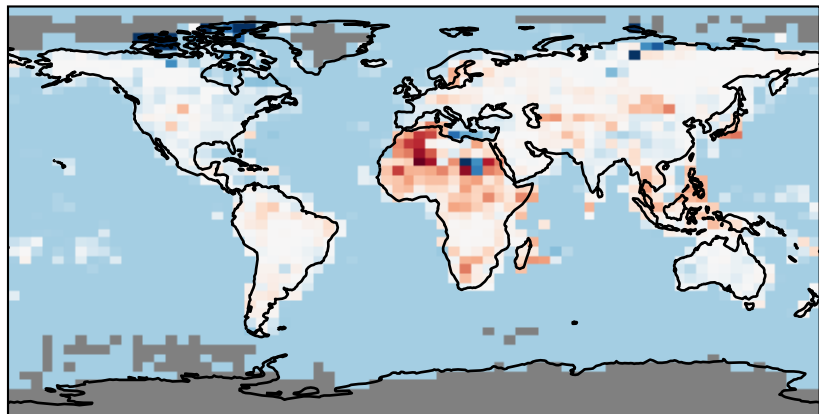
Difference  
Dev - Ref, Dynamic Range



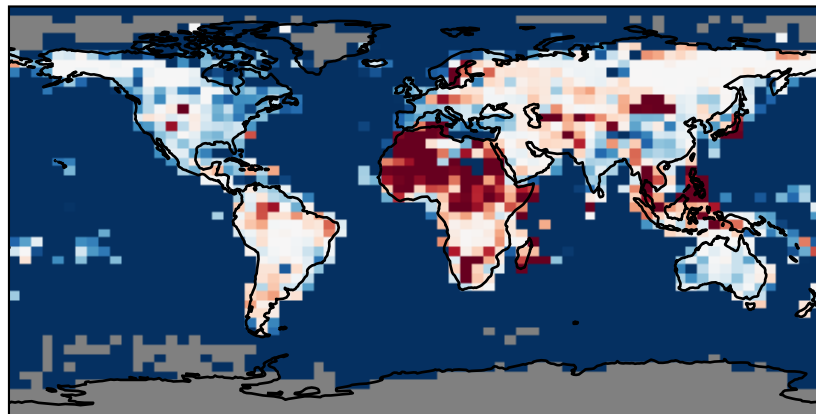
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

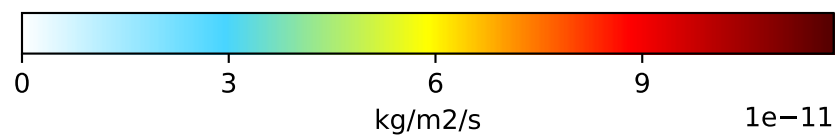
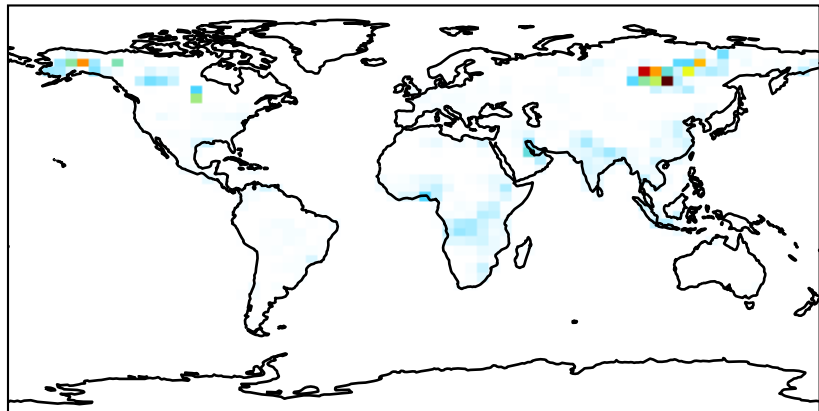


Ratio  
Dev/Ref, Fixed Range

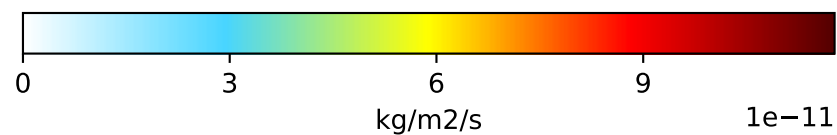
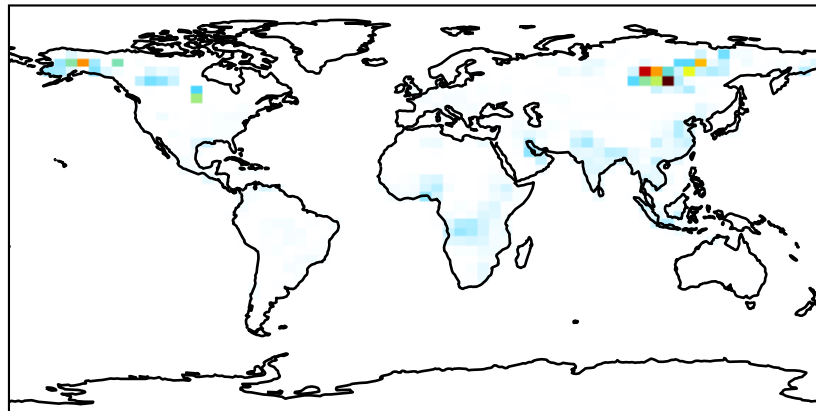


# EmisBENZ\_Total

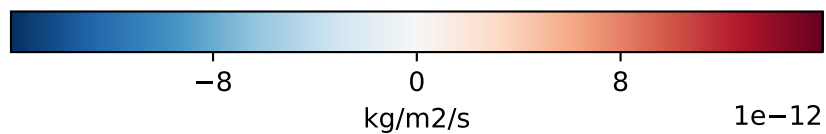
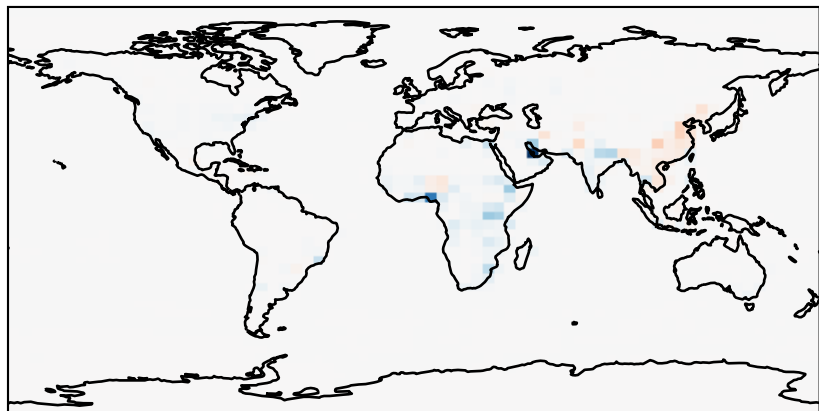
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



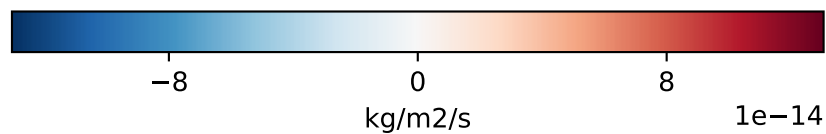
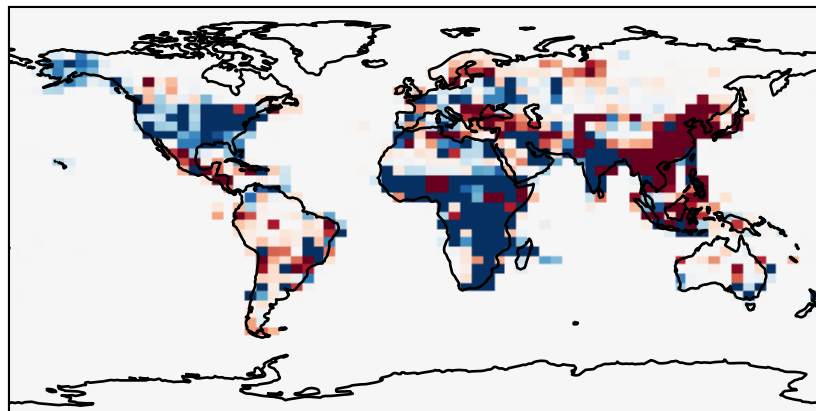
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



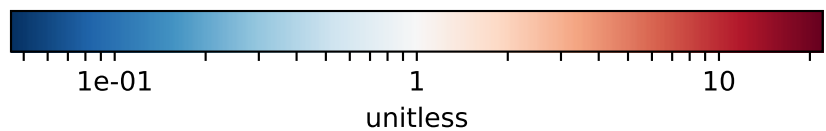
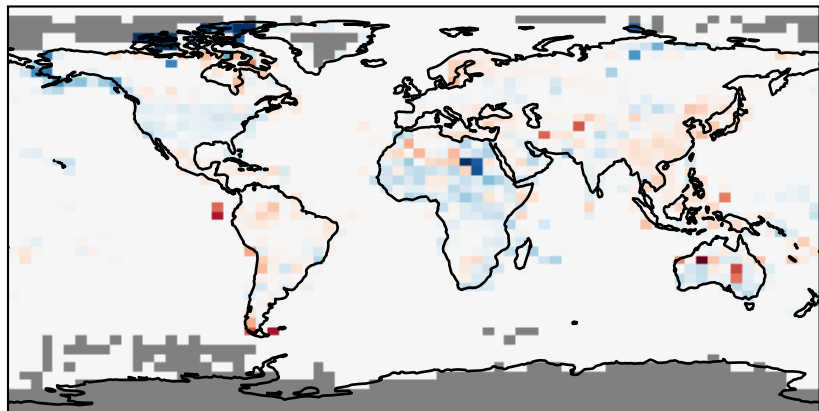
Difference  
Dev - Ref, Dynamic Range



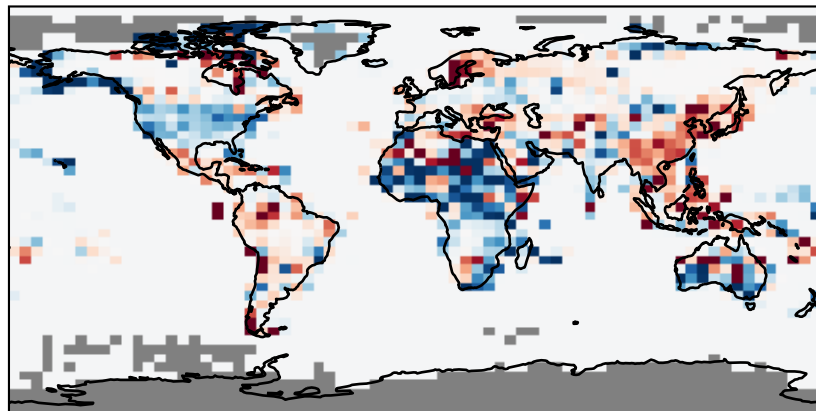
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

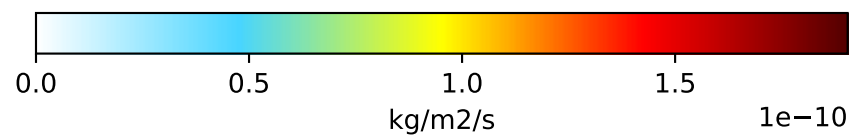
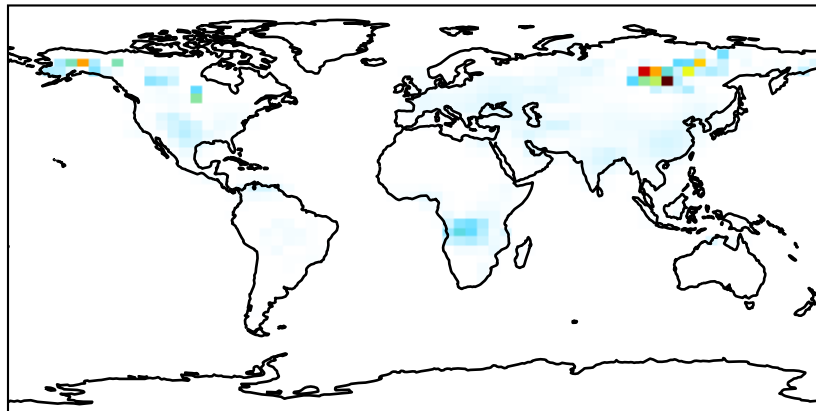


Ratio  
Dev/Ref, Fixed Range

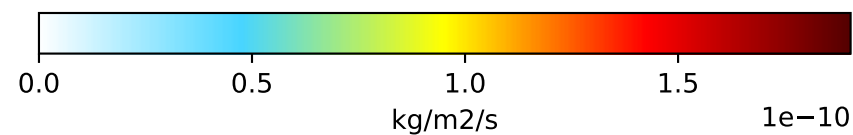
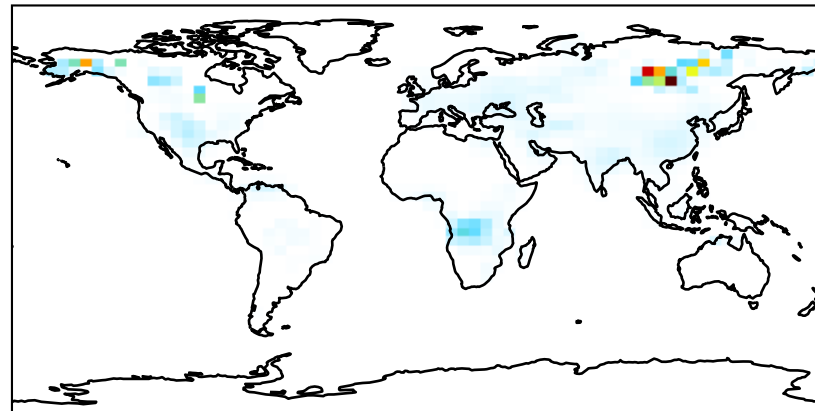


# EmisC2H6\_Total

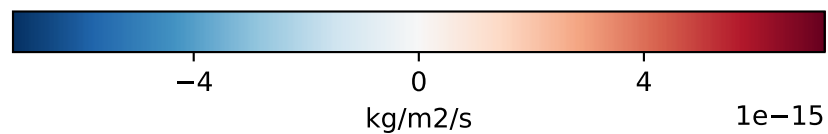
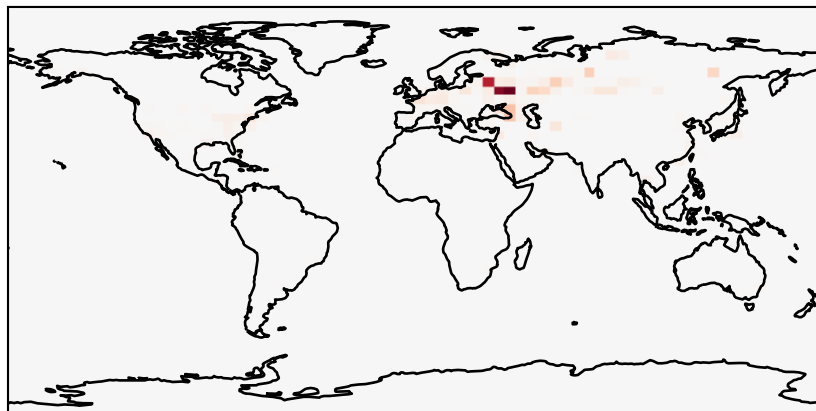
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



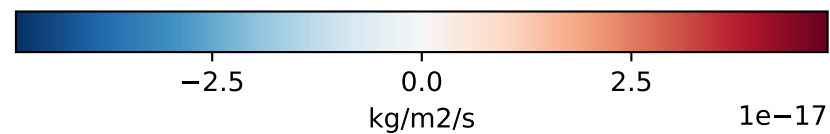
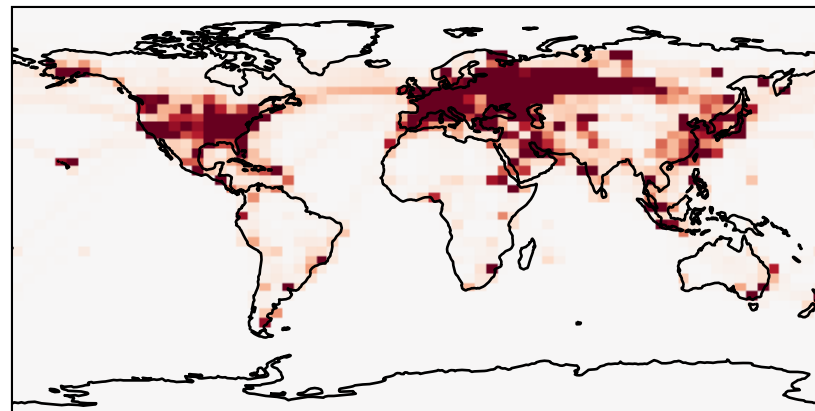
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



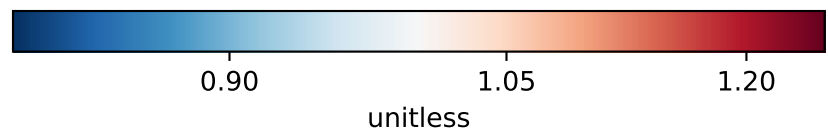
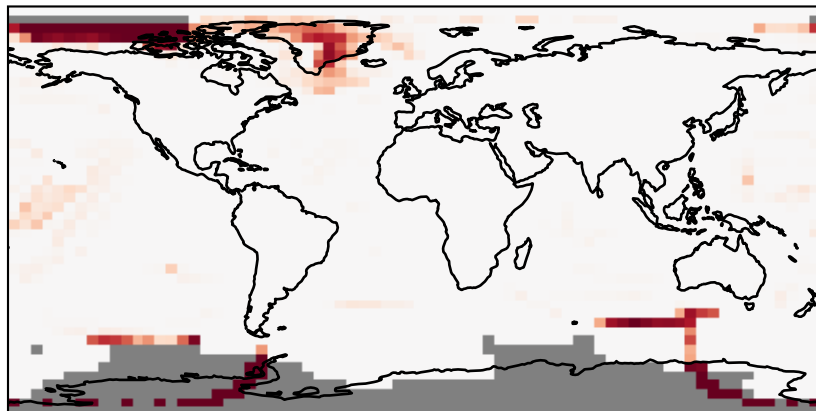
Difference  
Dev - Ref, Dynamic Range



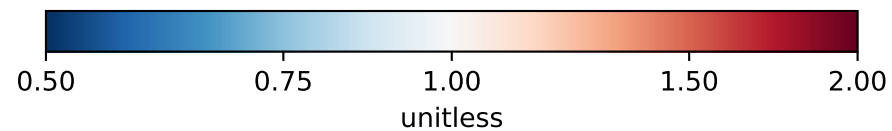
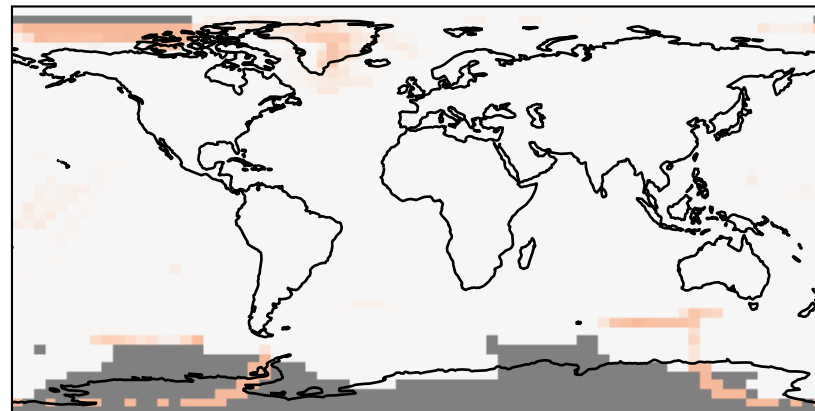
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

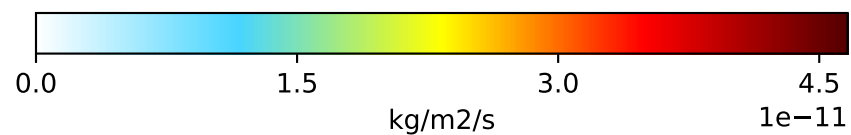
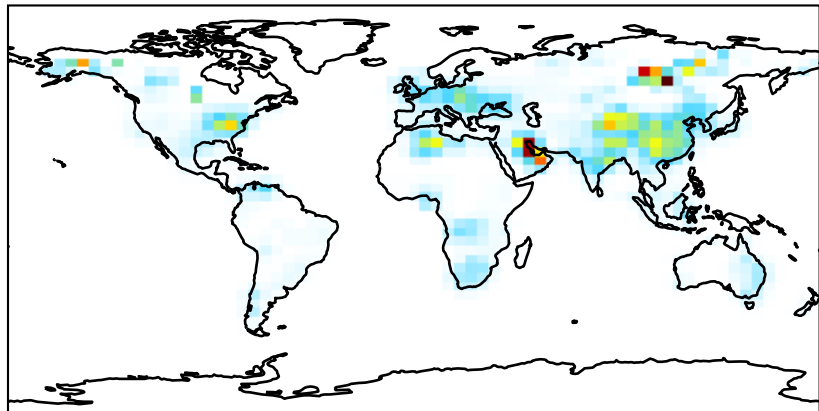


Ratio  
Dev/Ref, Fixed Range

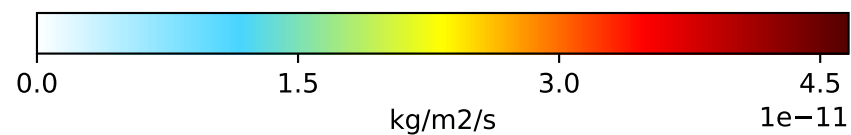
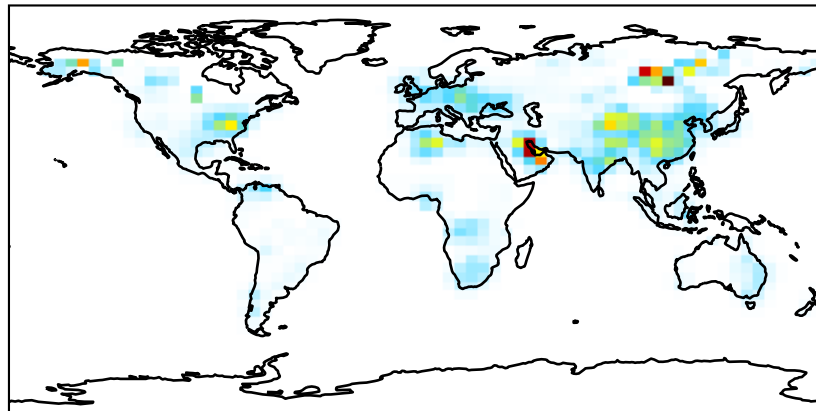


# EmisC3H8\_Total

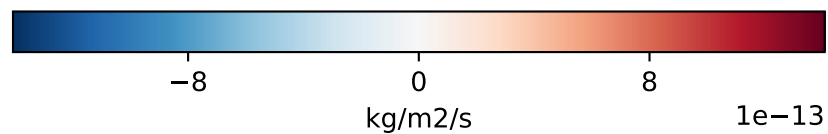
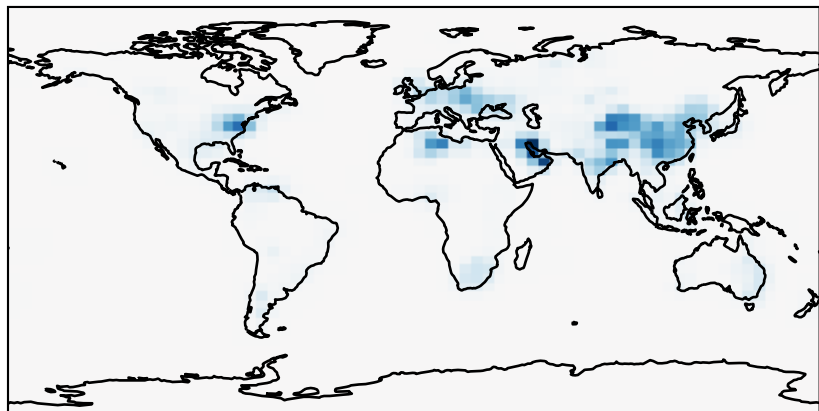
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



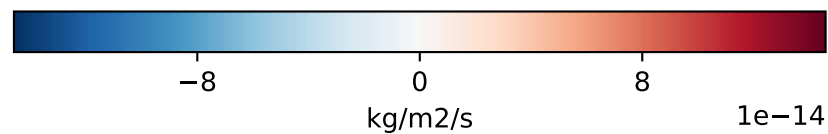
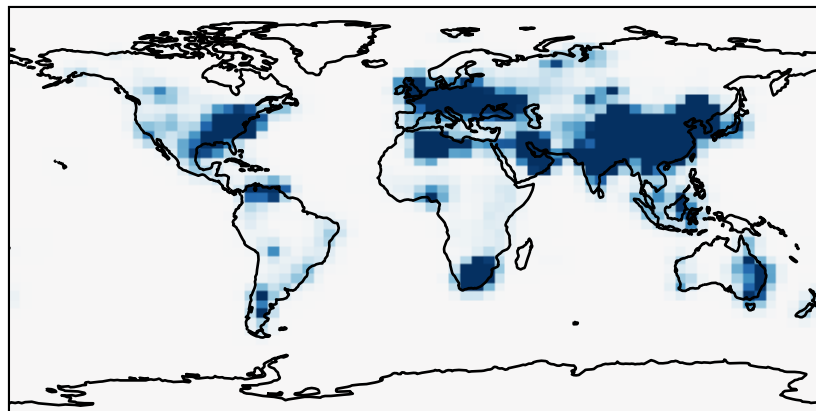
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



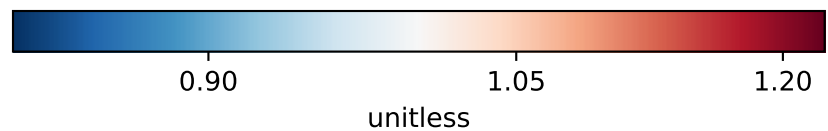
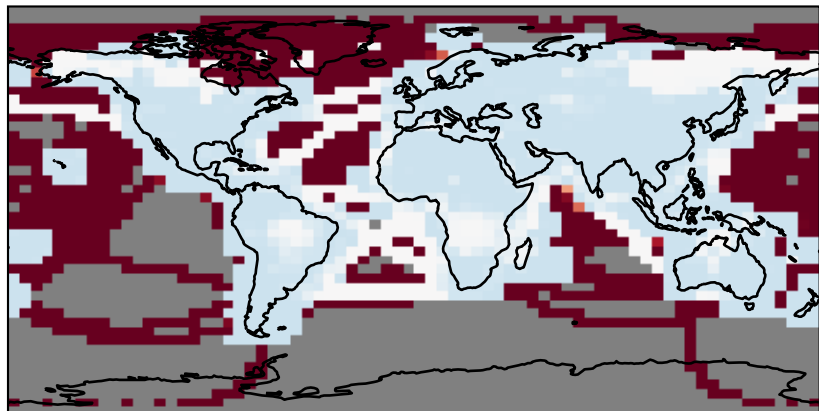
Difference  
Dev - Ref, Dynamic Range



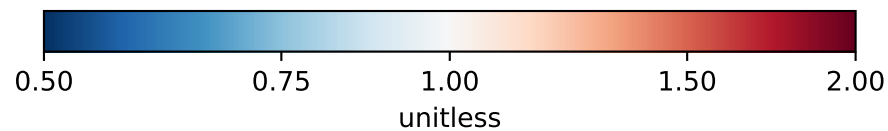
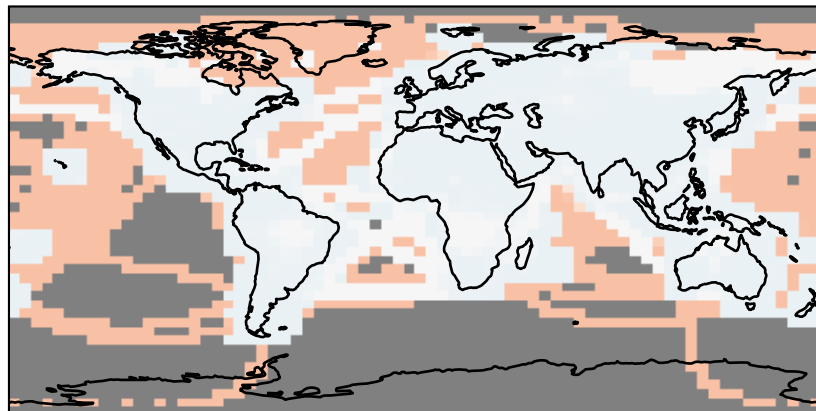
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

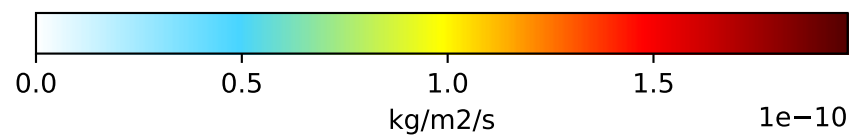
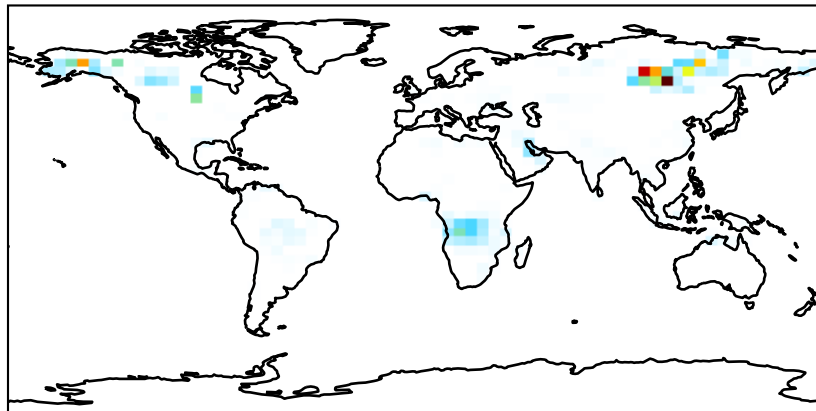


Ratio  
Dev/Ref, Fixed Range

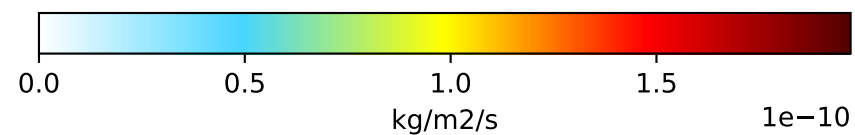
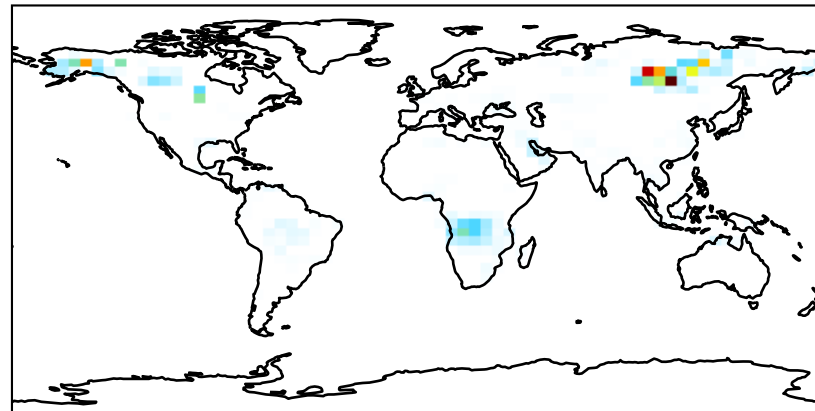


# EmisCH2O\_Total

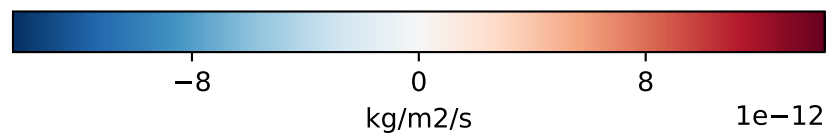
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



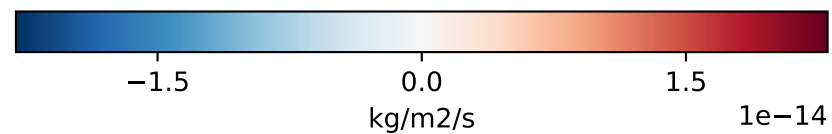
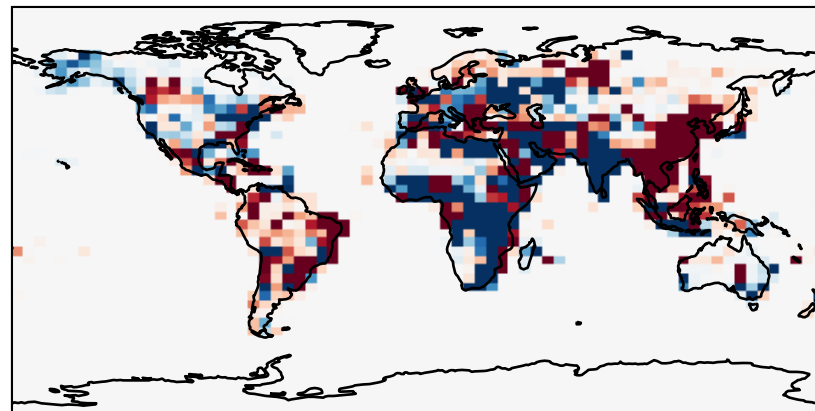
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



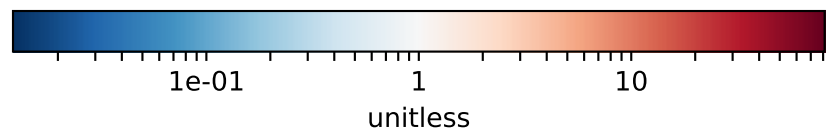
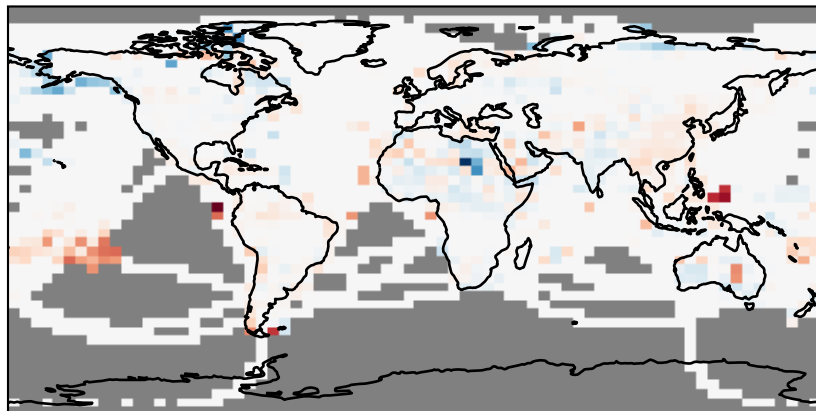
Difference  
Dev - Ref, Dynamic Range



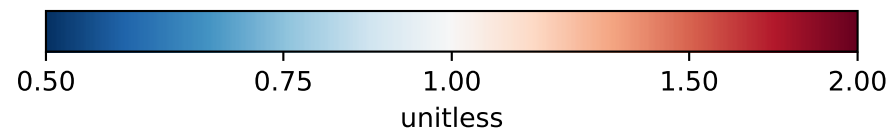
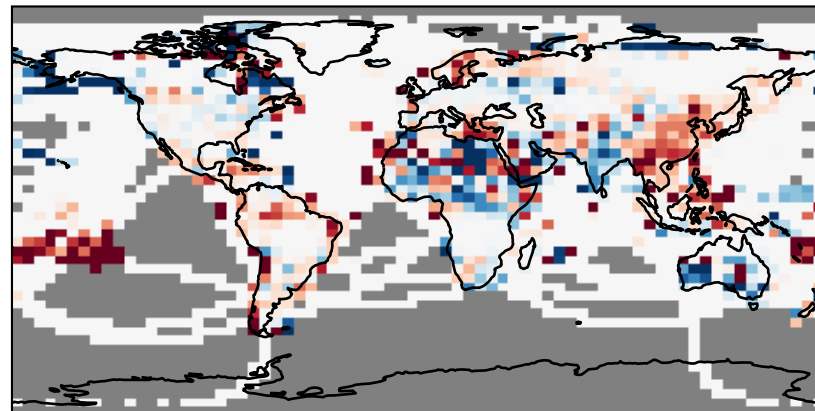
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



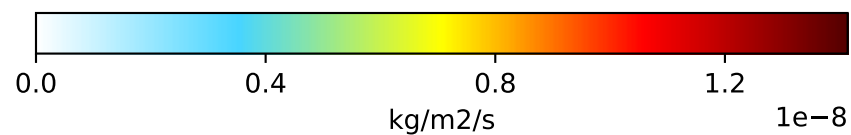
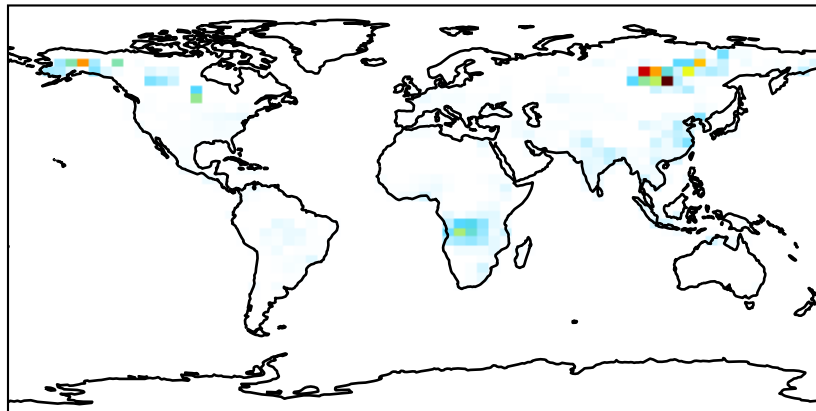
Ratio  
Dev/Ref, Fixed Range



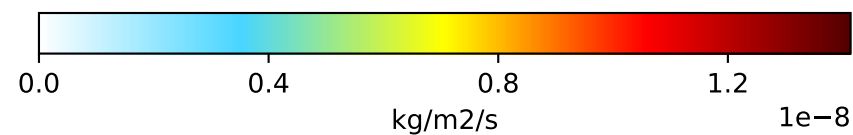
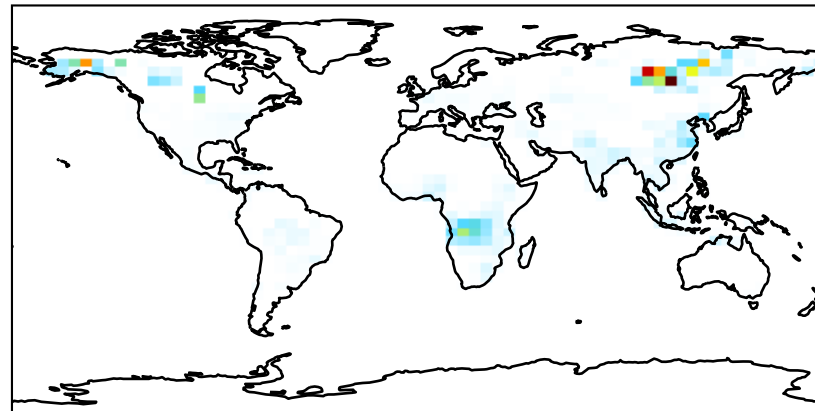


# EmisCO\_Total

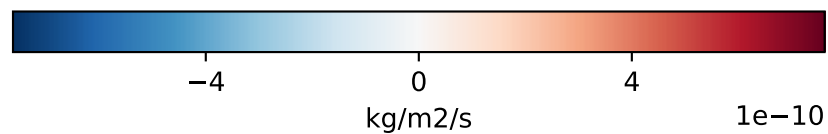
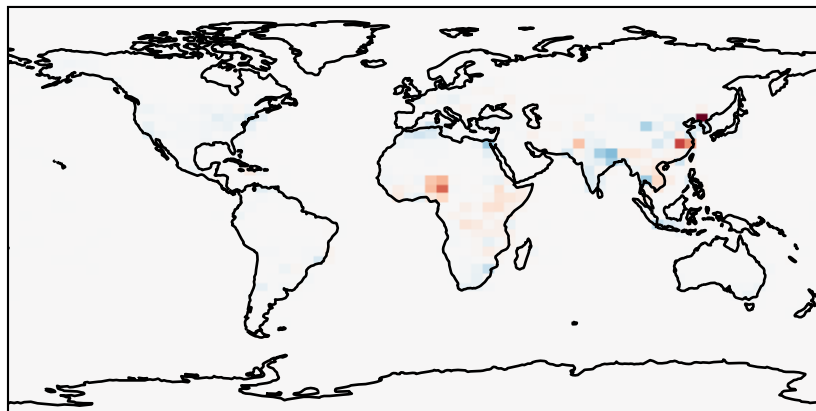
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



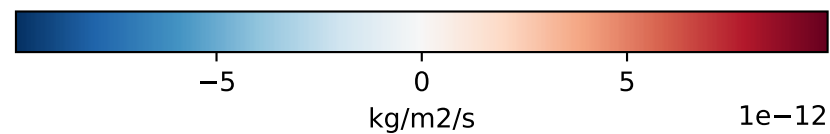
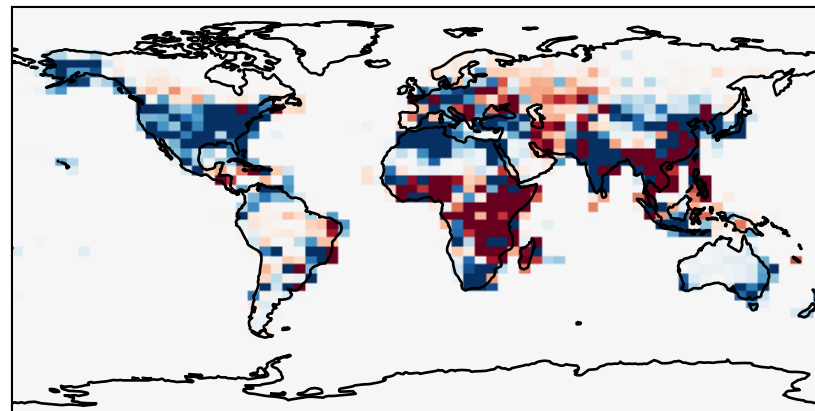
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



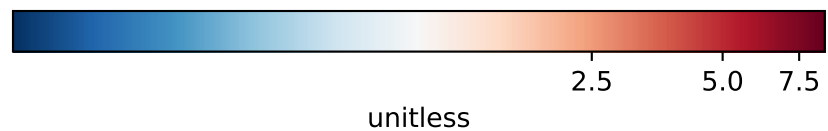
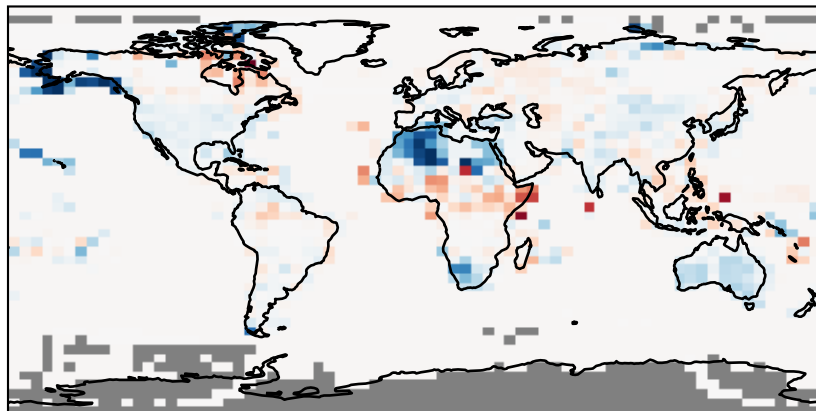
Difference  
Dev - Ref, Dynamic Range



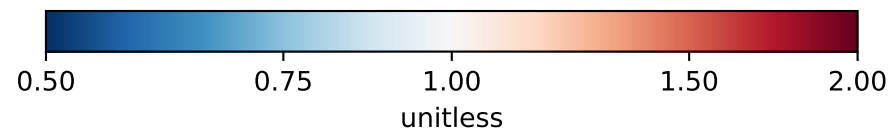
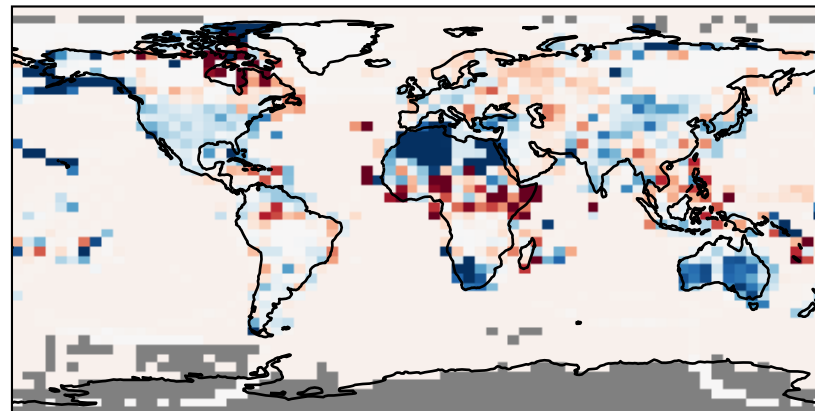
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

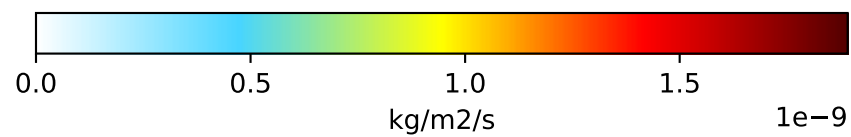
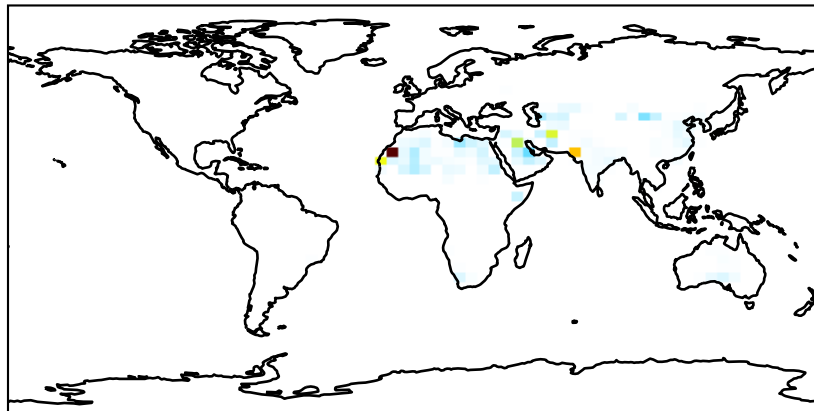


Ratio  
Dev/Ref, Fixed Range

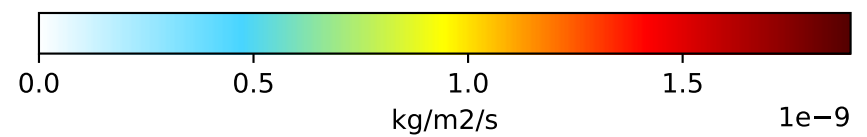
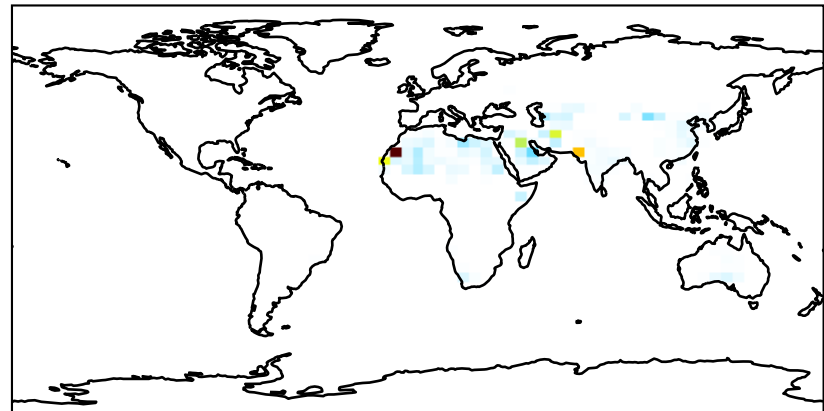


# EmisDST1\_Total

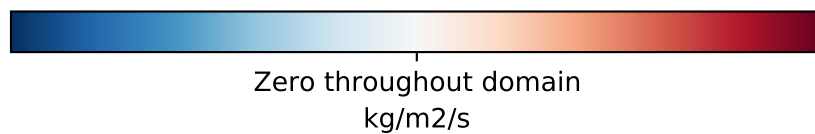
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



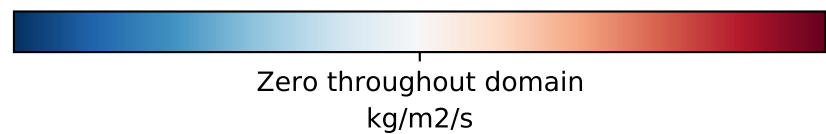
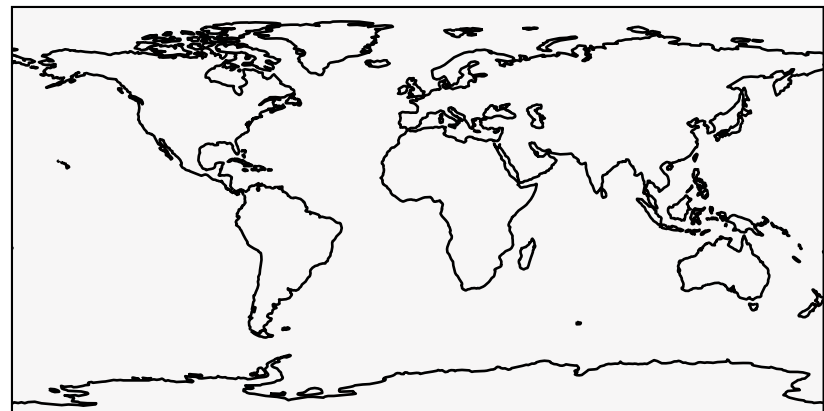
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



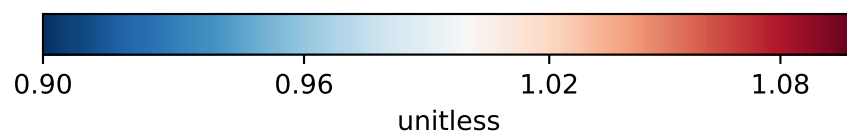
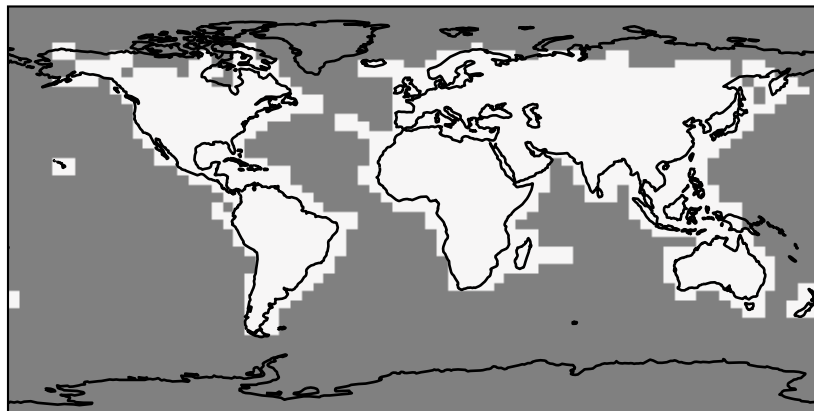
Difference  
Dev - Ref, Dynamic Range



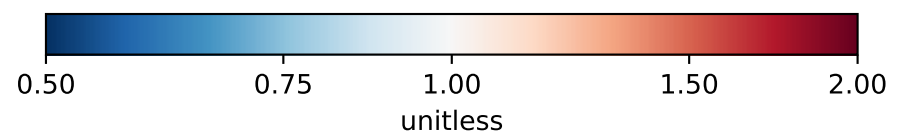
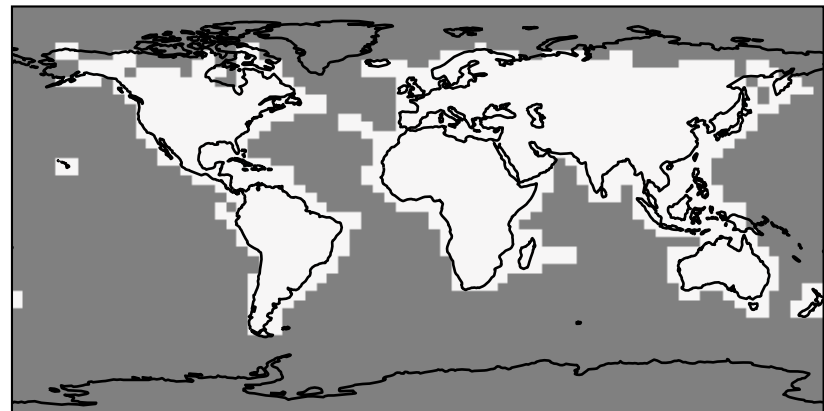
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

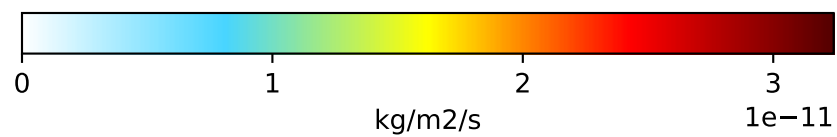
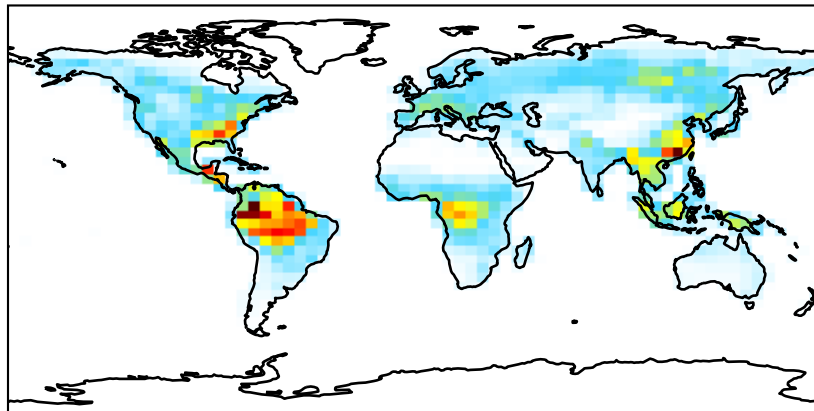


Ratio  
Dev/Ref, Fixed Range

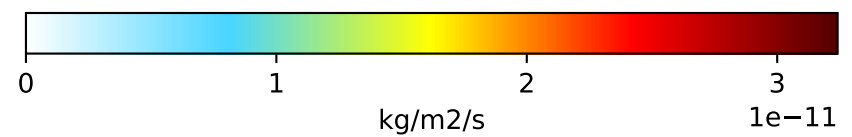
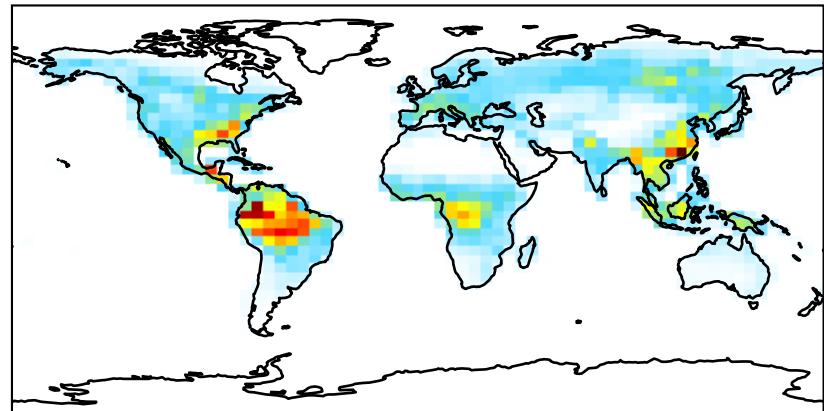


# EmisEOH\_Total

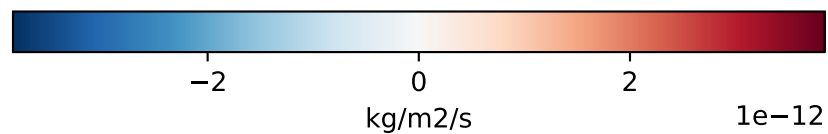
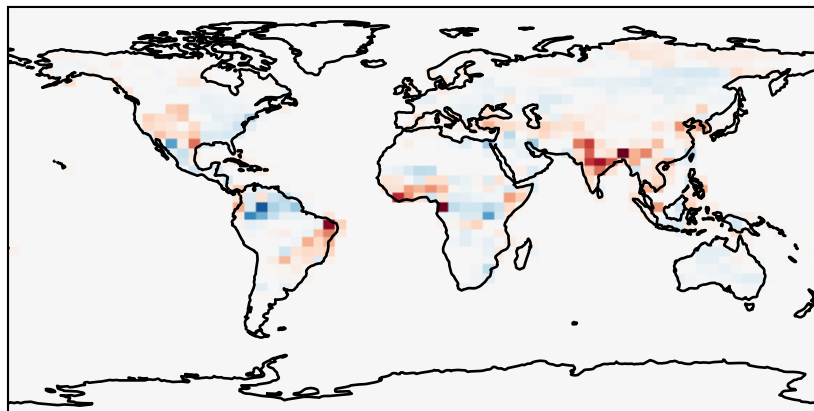
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



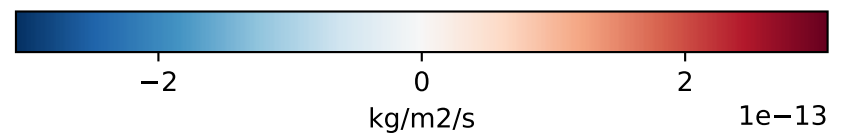
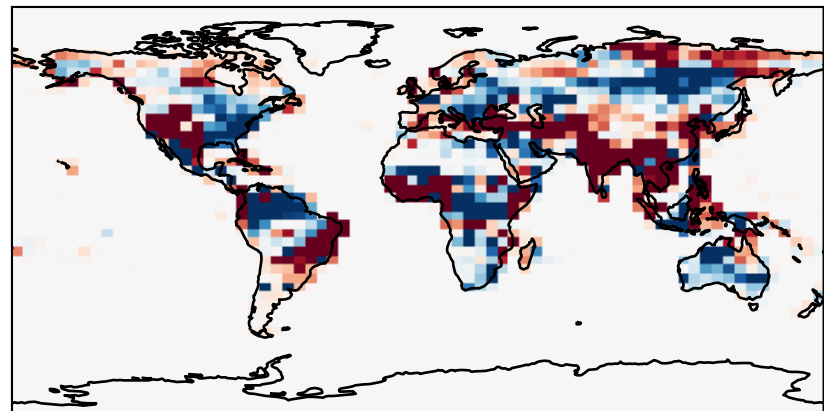
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



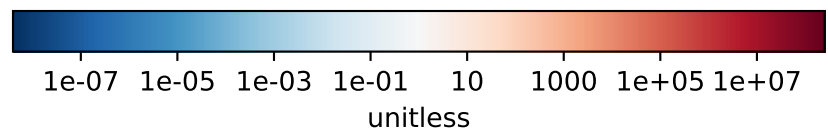
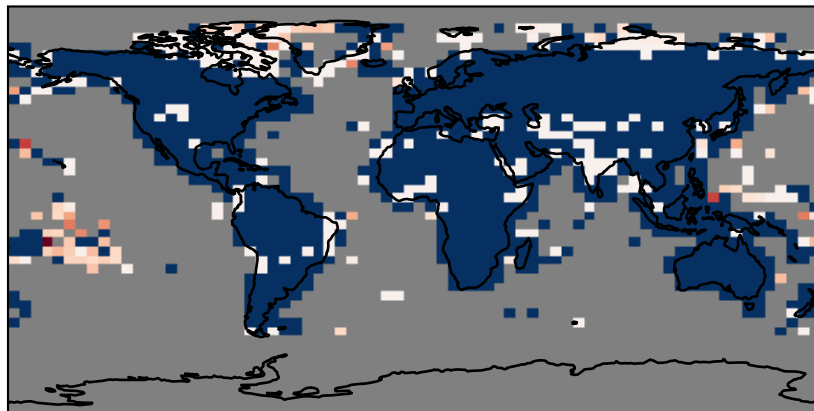
Difference  
Dev - Ref, Dynamic Range



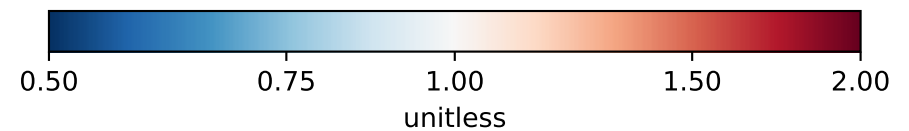
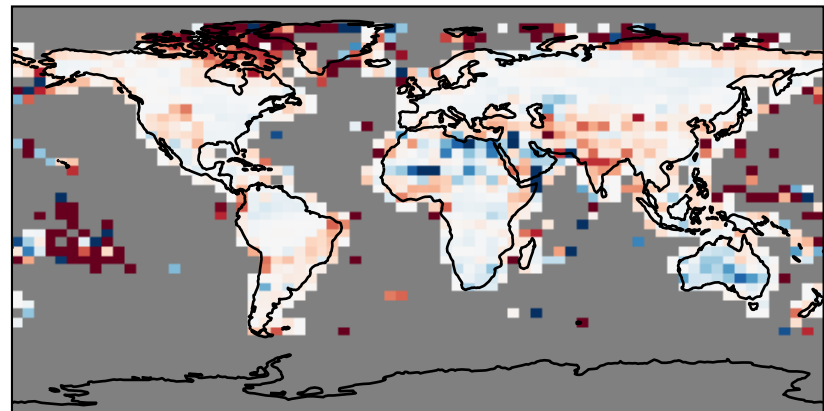
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

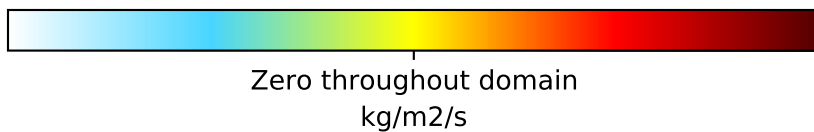


Ratio  
Dev/Ref, Fixed Range

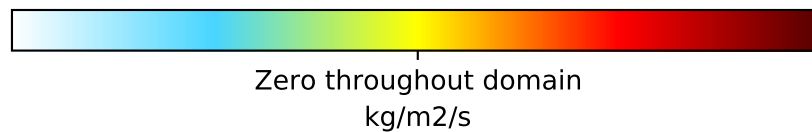
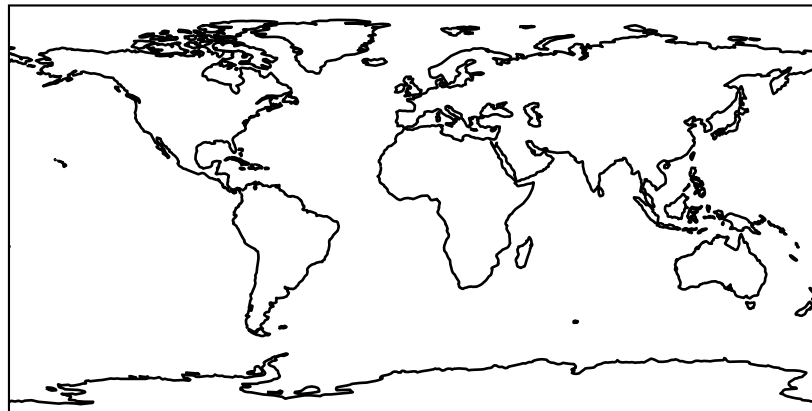


# EmisGLYC\_Total

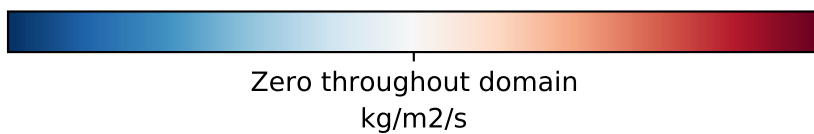
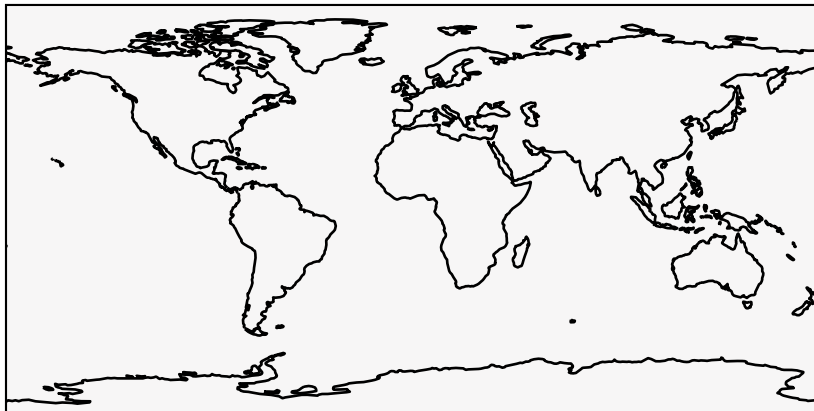
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



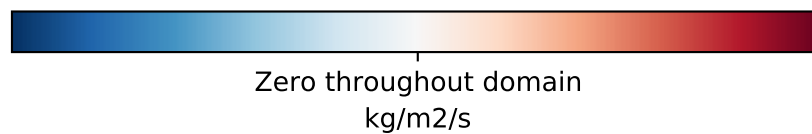
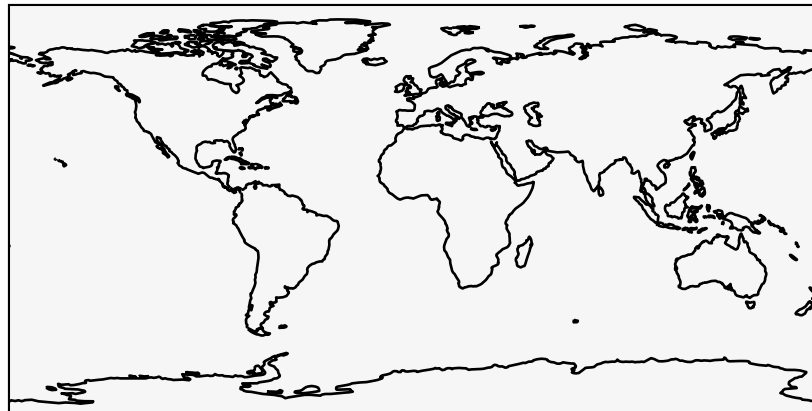
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



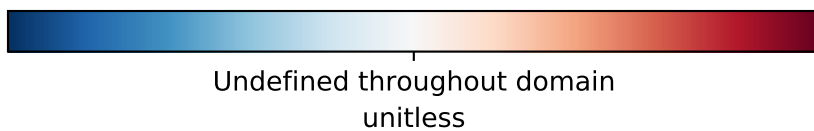
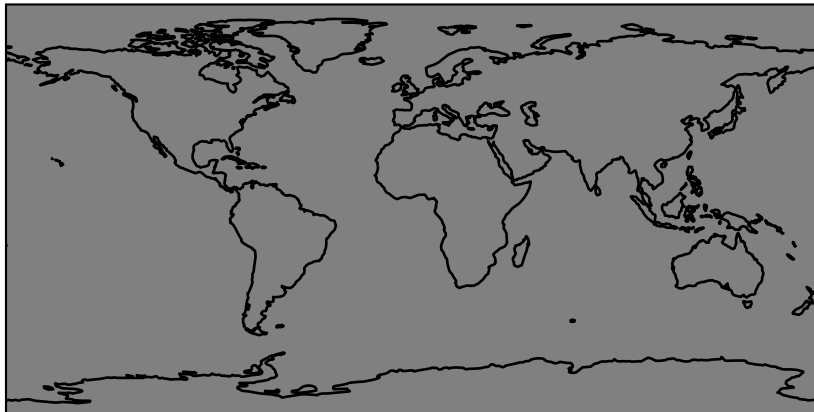
Difference  
Dev - Ref, Dynamic Range



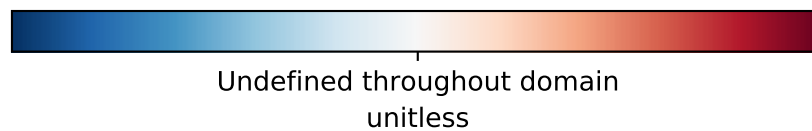
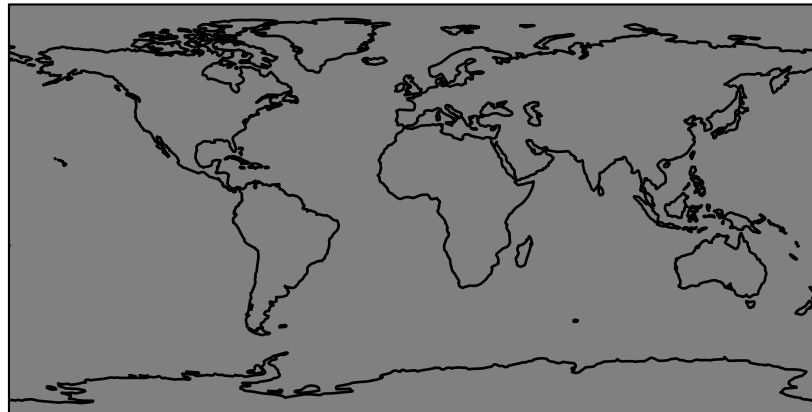
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

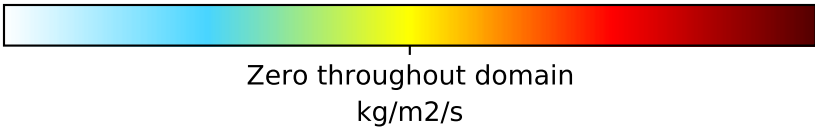


Ratio  
Dev/Ref, Fixed Range

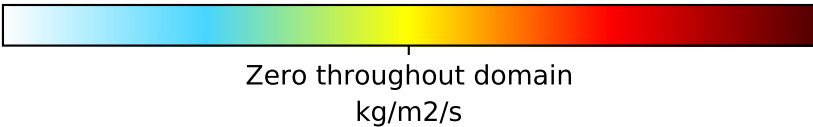


# EmisHAC\_Total

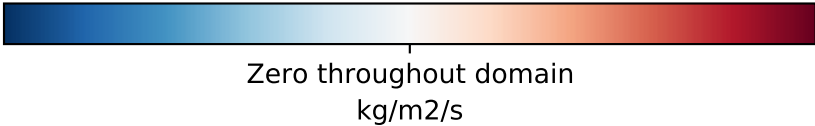
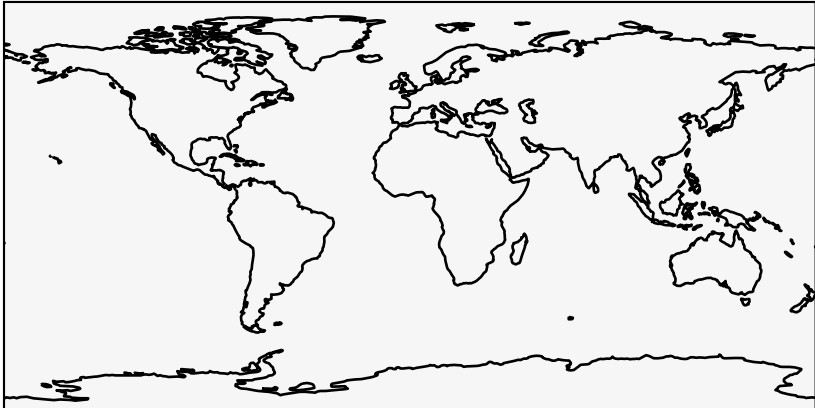
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



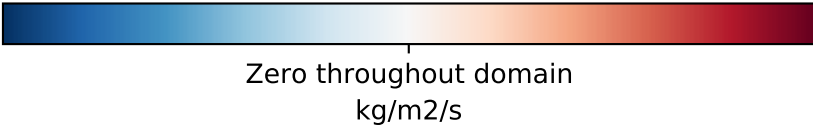
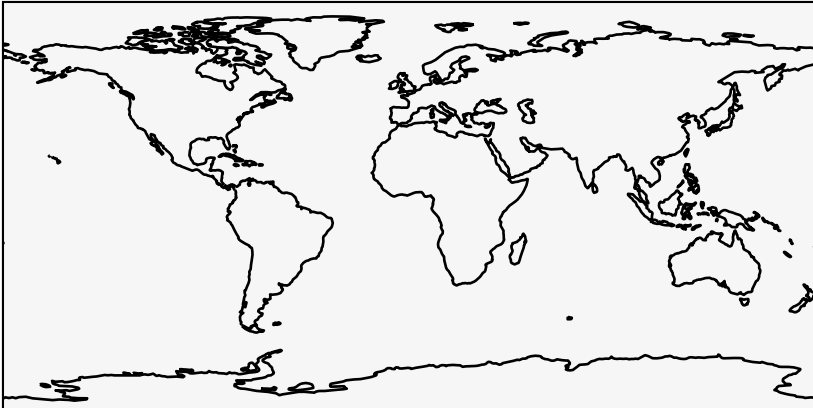
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



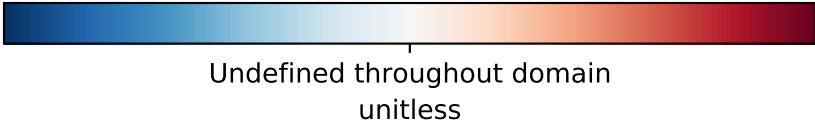
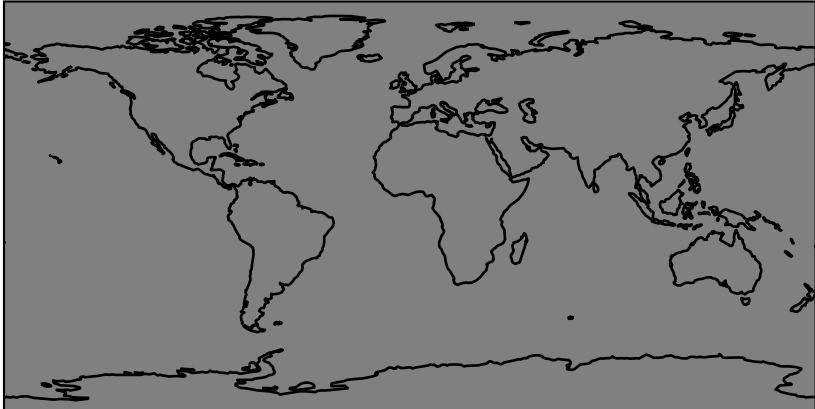
Difference  
Dev - Ref, Dynamic Range



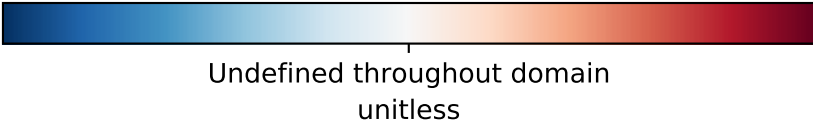
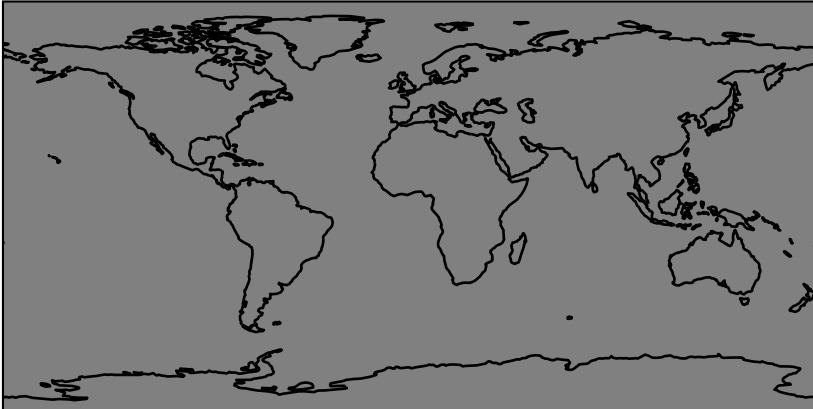
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



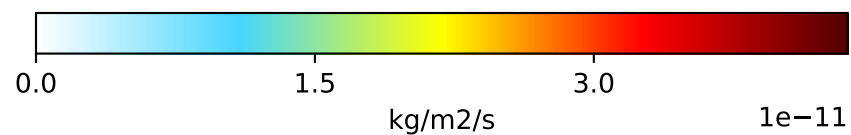
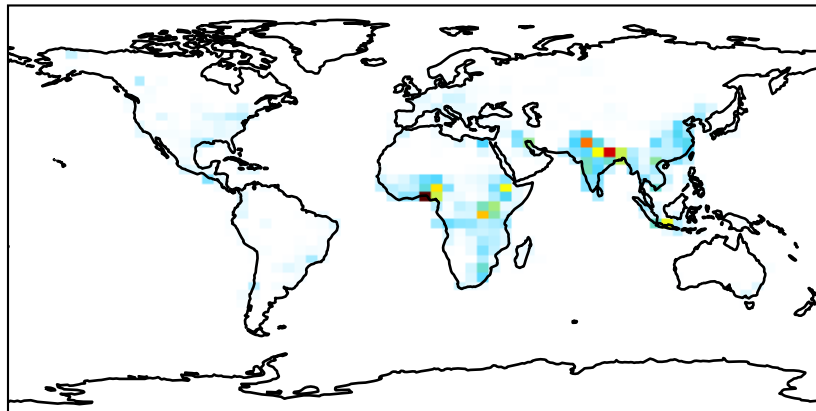
Ratio  
Dev/Ref, Fixed Range



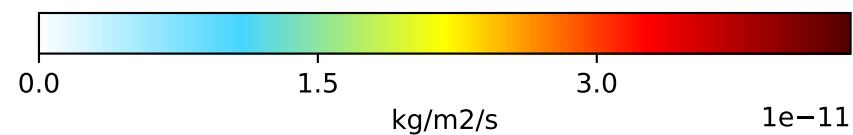
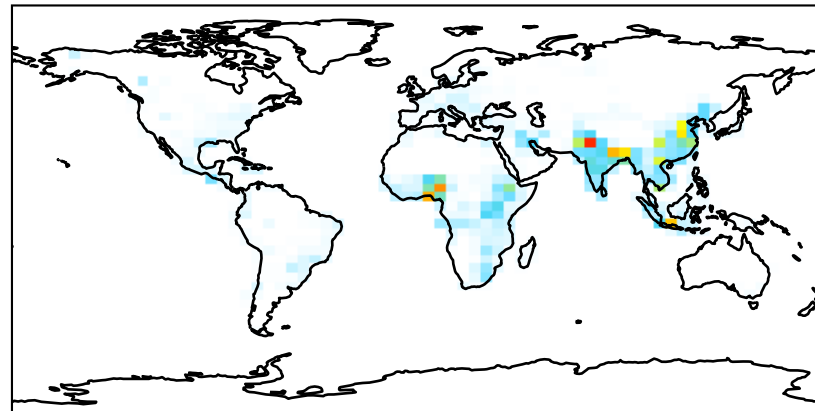


# EmisHCOOH\_Total

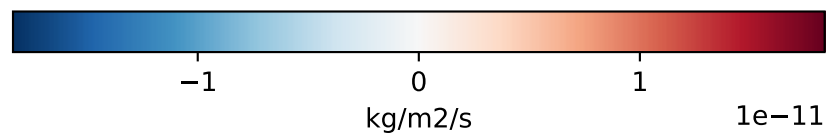
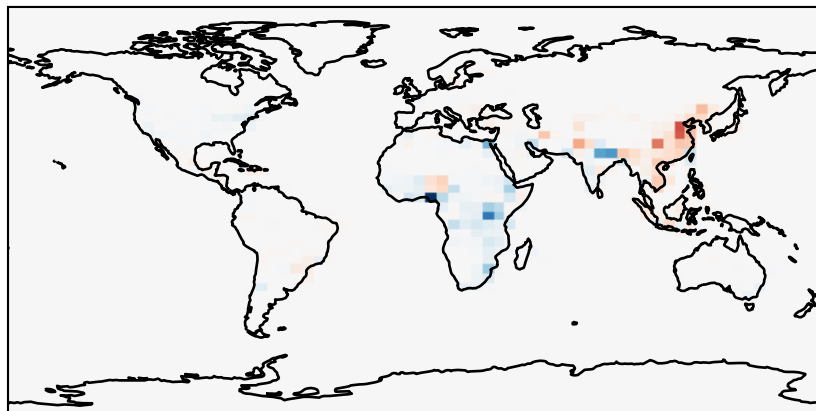
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



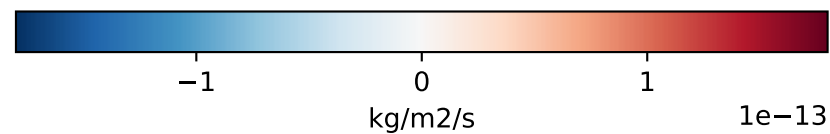
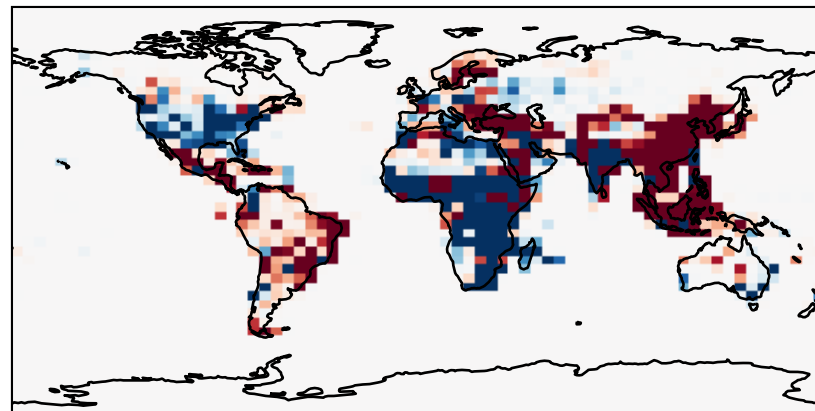
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



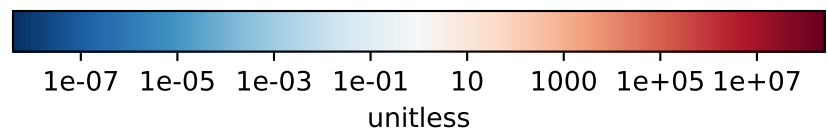
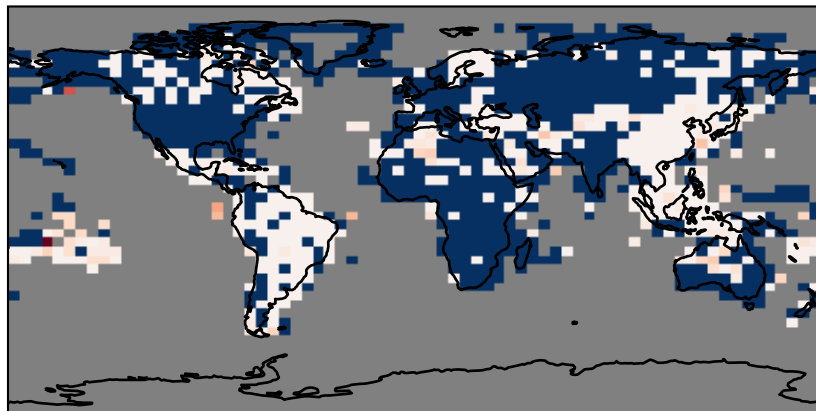
Difference  
Dev - Ref, Dynamic Range



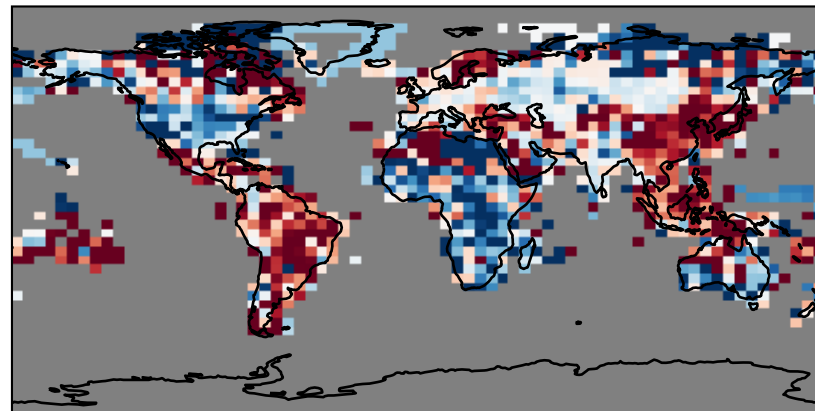
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

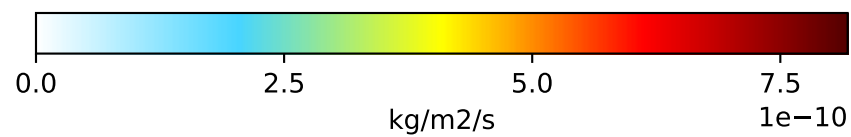
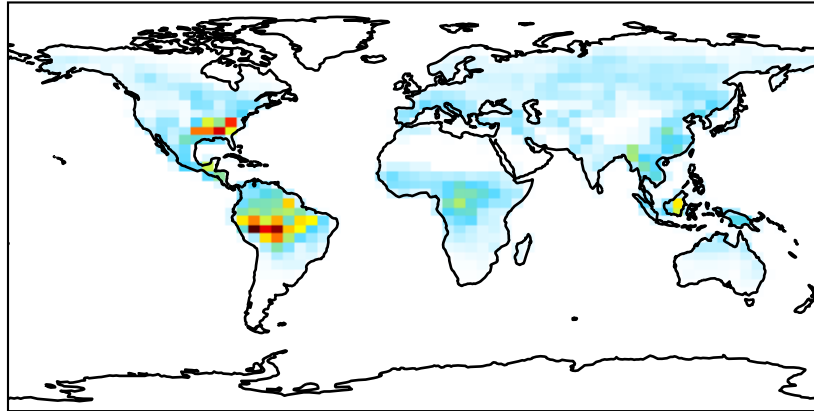


Ratio  
Dev/Ref, Fixed Range

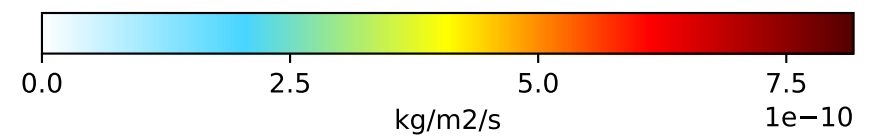
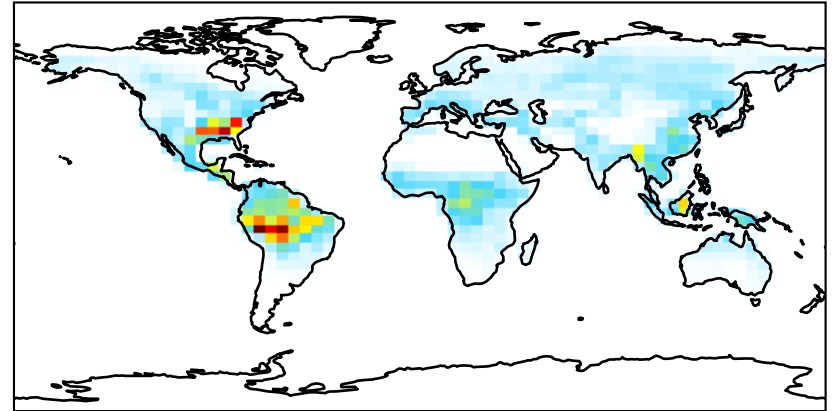


# EmisISOP\_Total

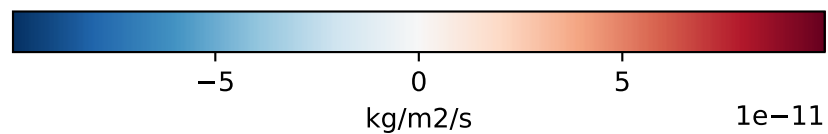
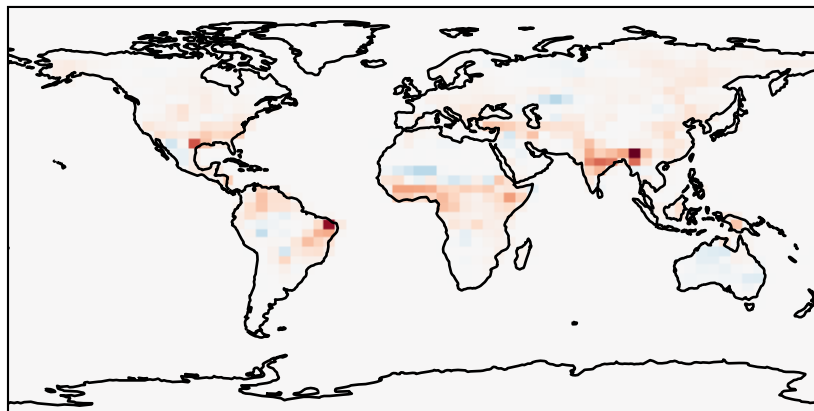
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



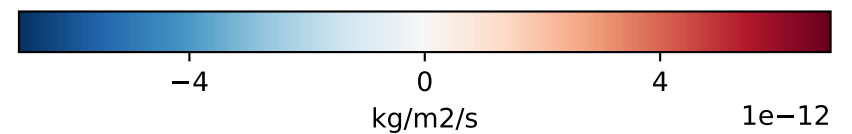
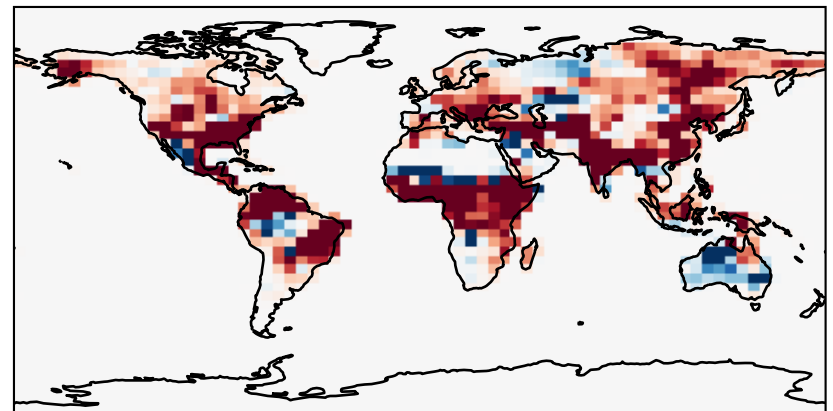
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



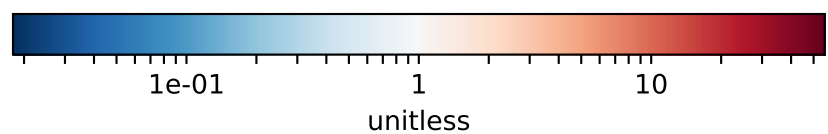
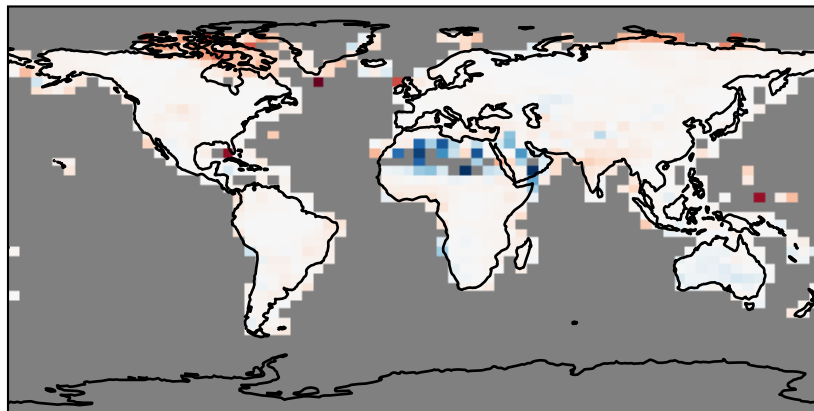
Difference  
Dev - Ref, Dynamic Range



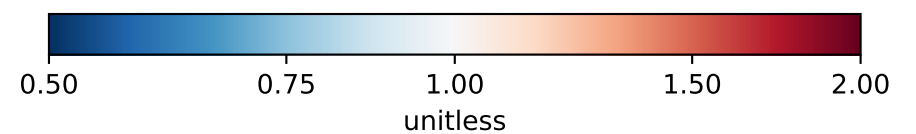
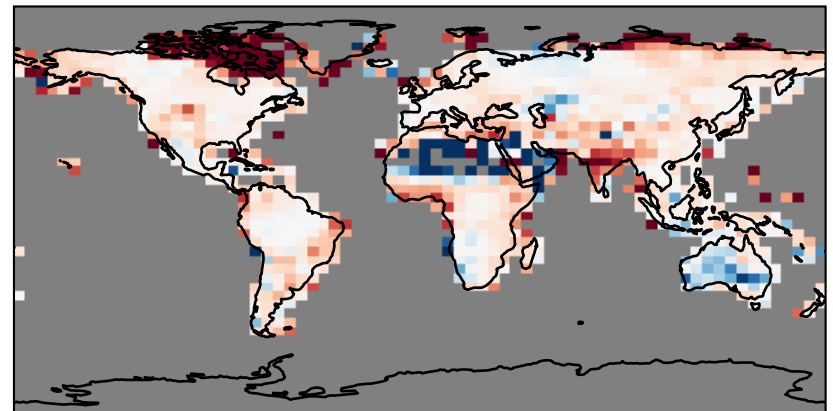
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

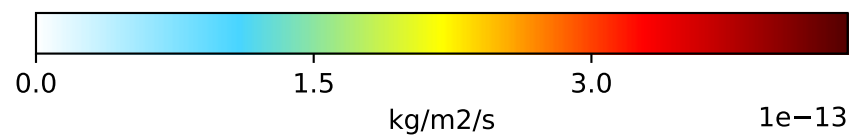
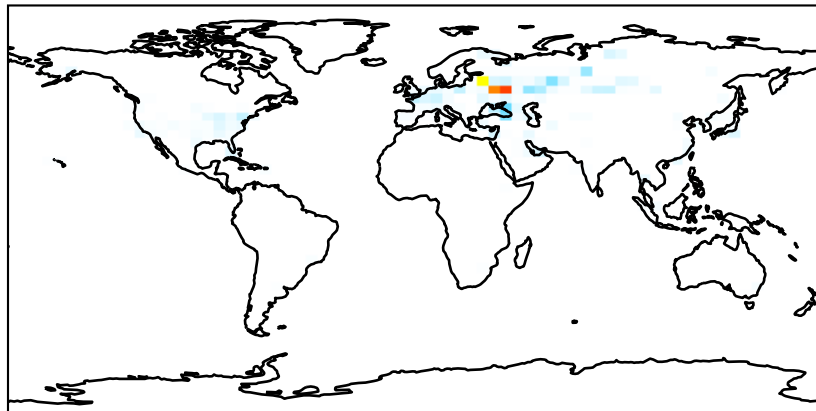


Ratio  
Dev/Ref, Fixed Range

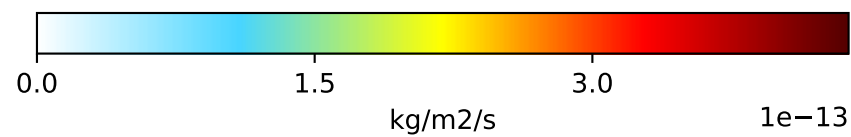
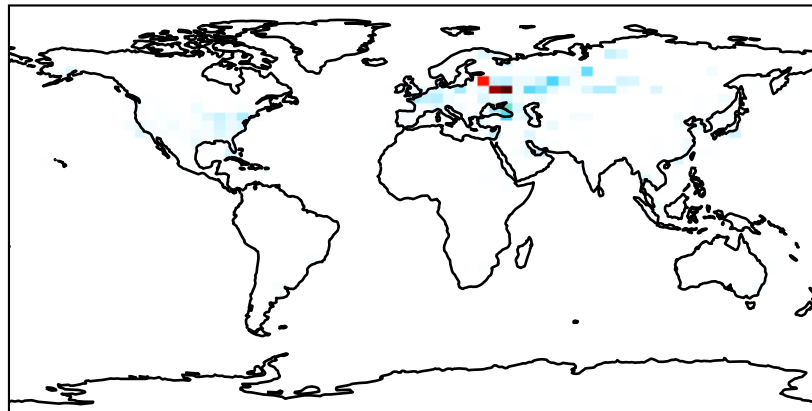


# EmisMACR\_Total

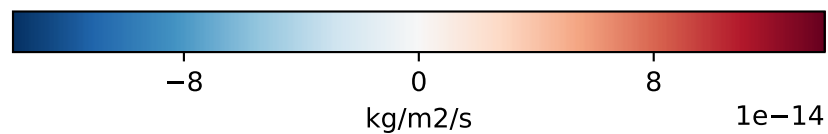
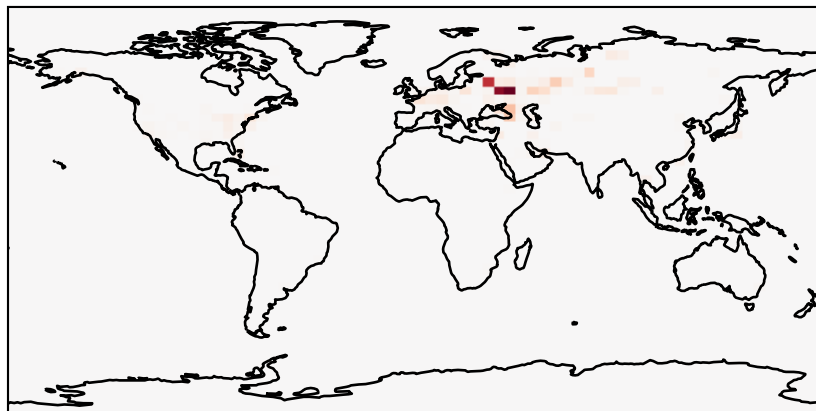
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



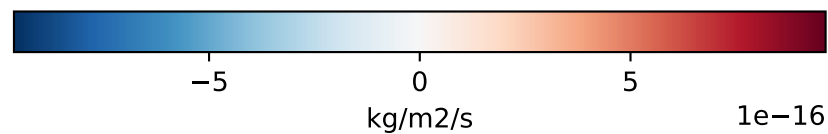
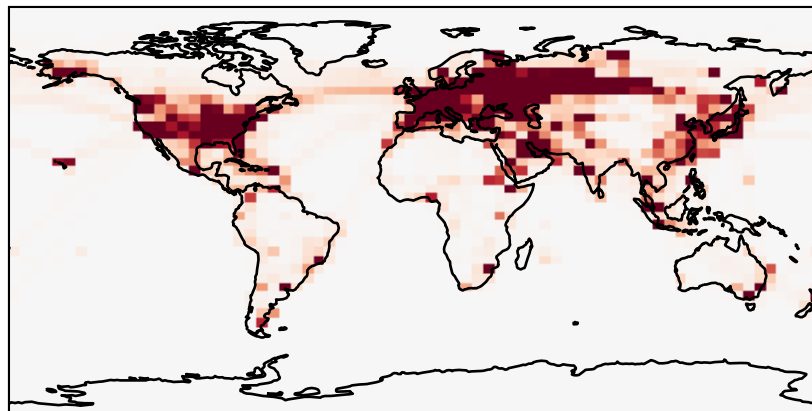
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



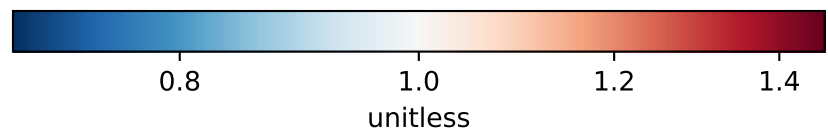
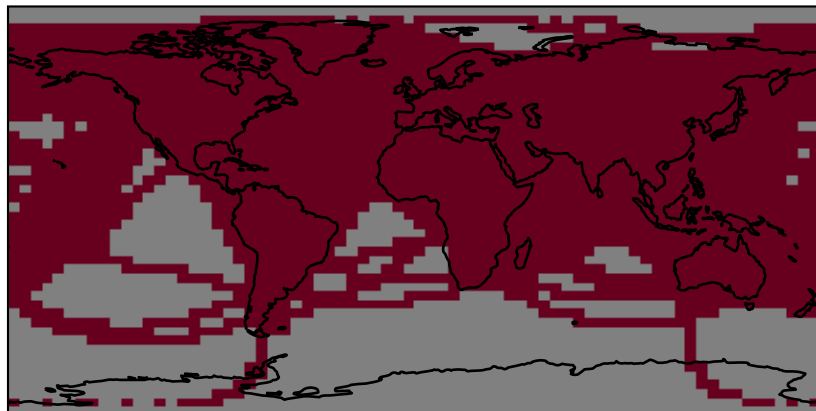
Difference  
Dev - Ref, Dynamic Range



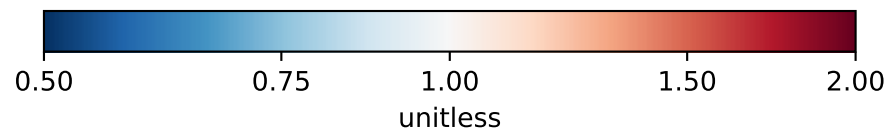
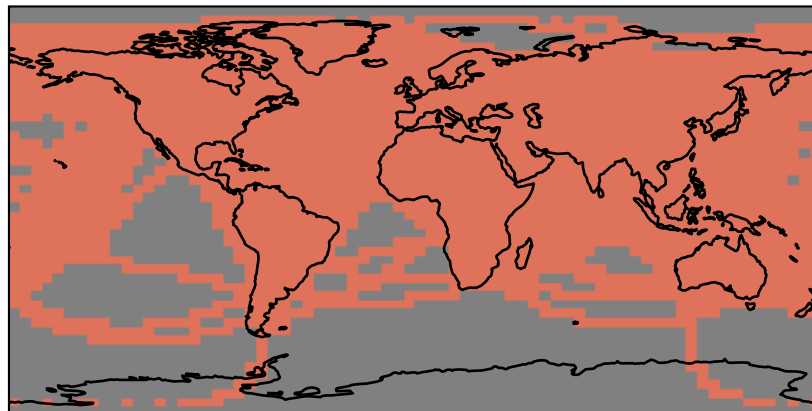
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

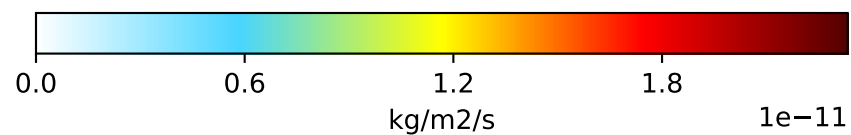
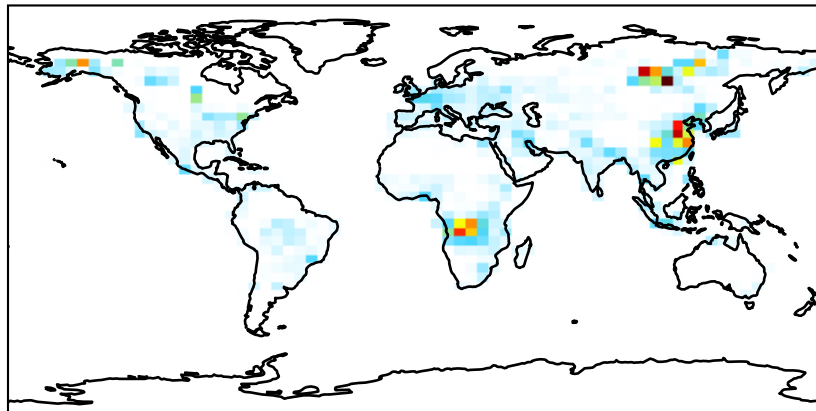


Ratio  
Dev/Ref, Fixed Range

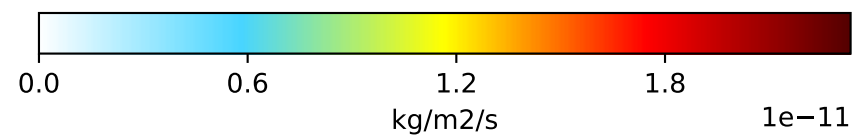
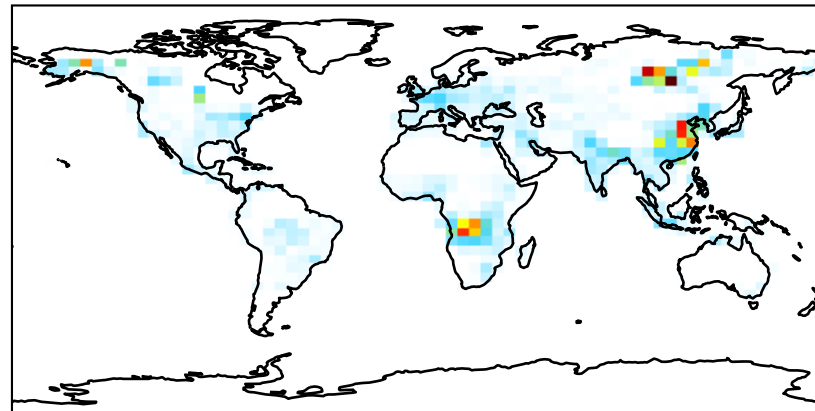


# EmisMEK\_Total

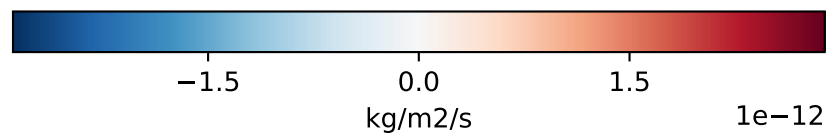
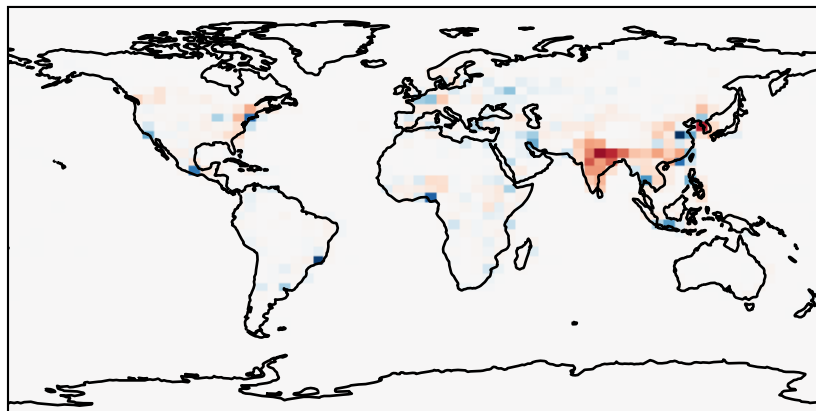
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



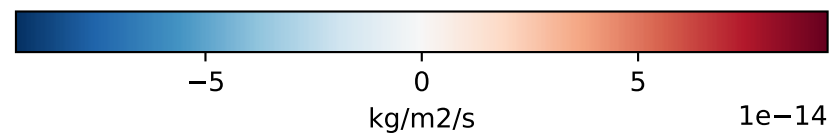
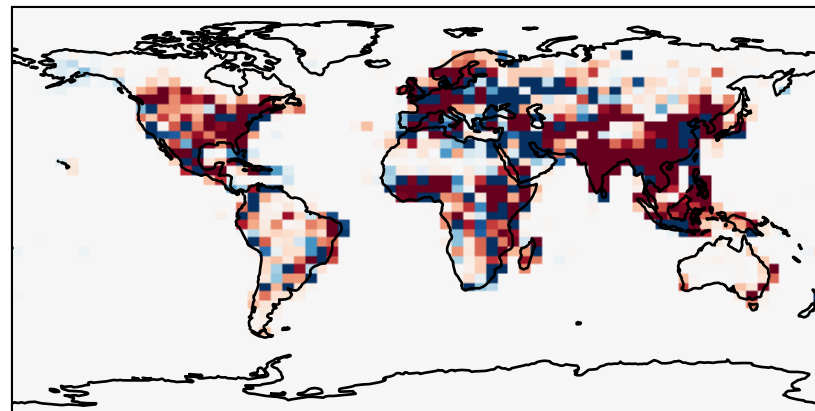
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



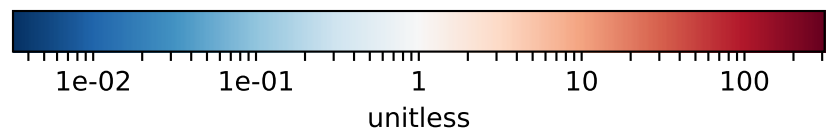
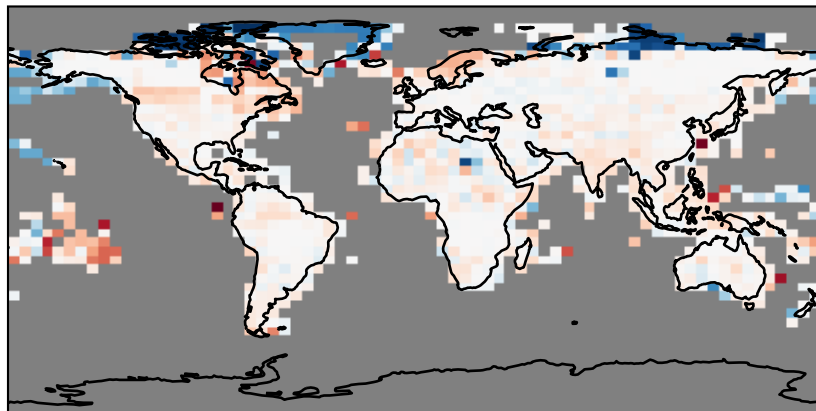
Difference  
Dev - Ref, Dynamic Range



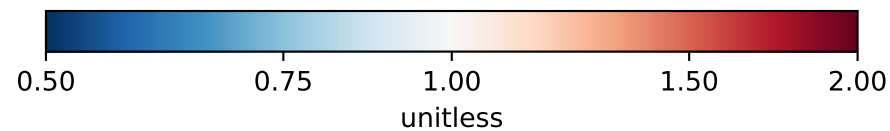
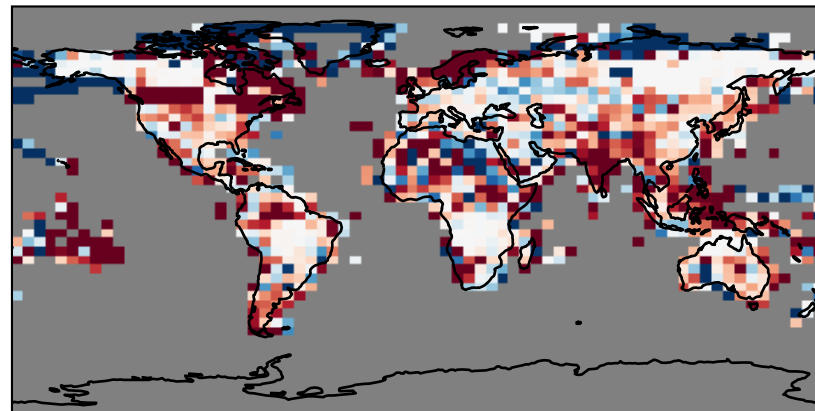
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

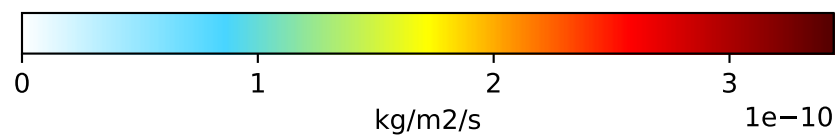
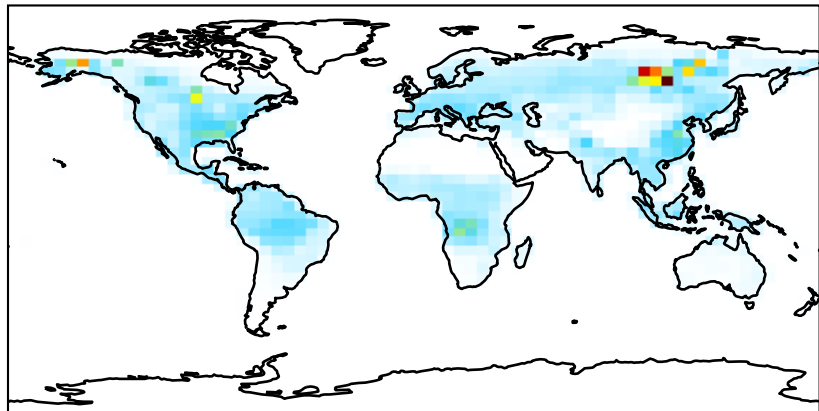


Ratio  
Dev/Ref, Fixed Range

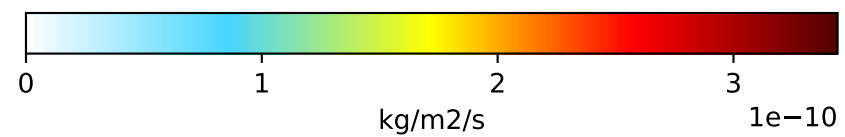
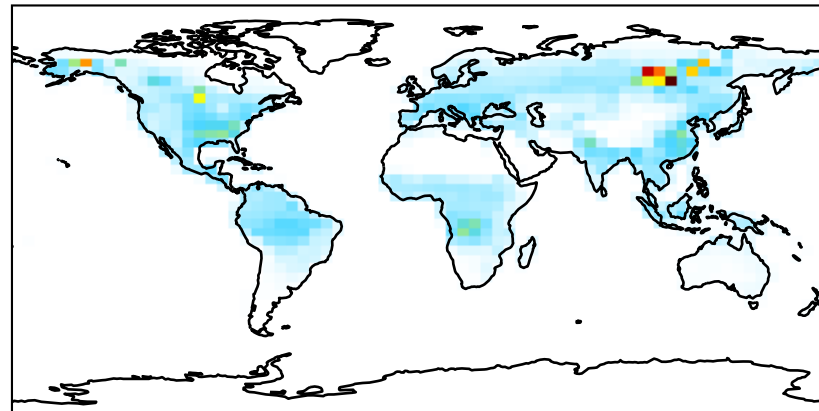


# EmisMOH\_Total

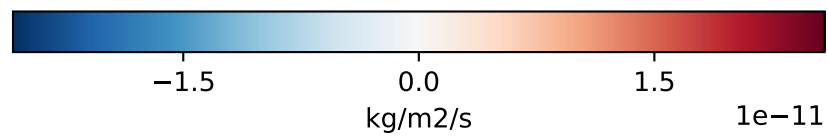
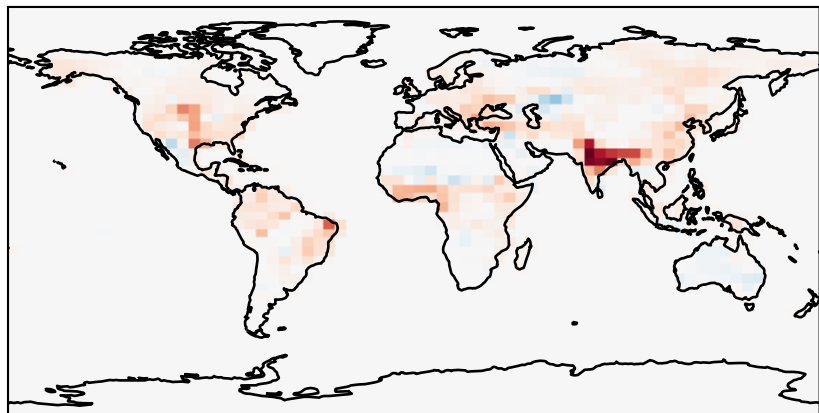
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



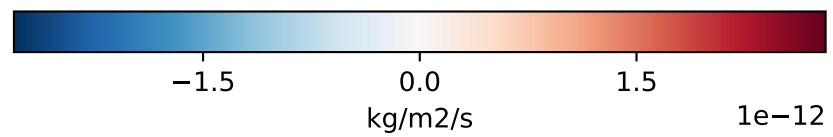
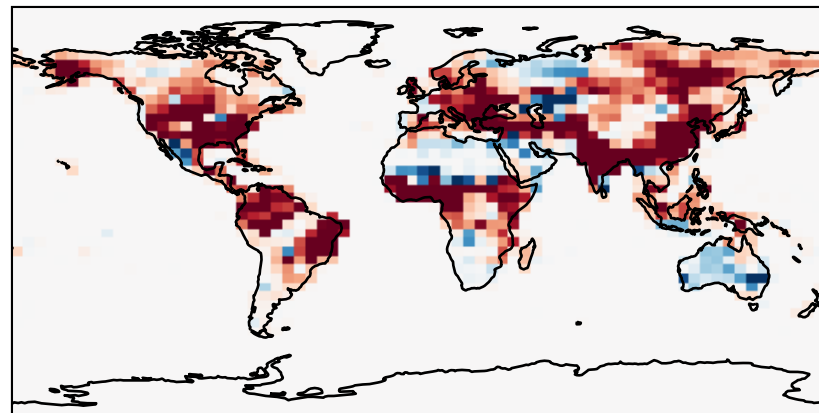
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



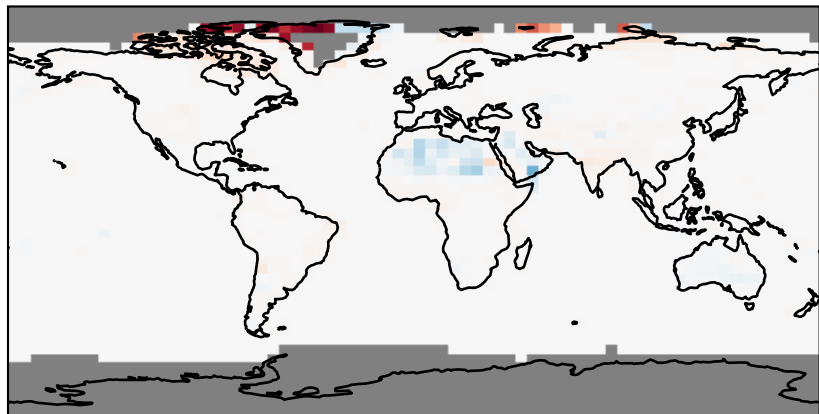
Difference  
Dev - Ref, Dynamic Range



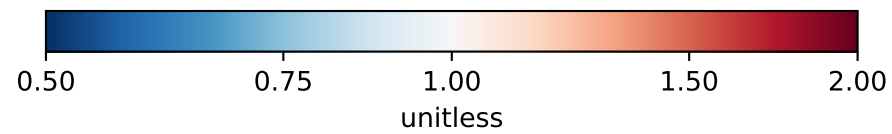
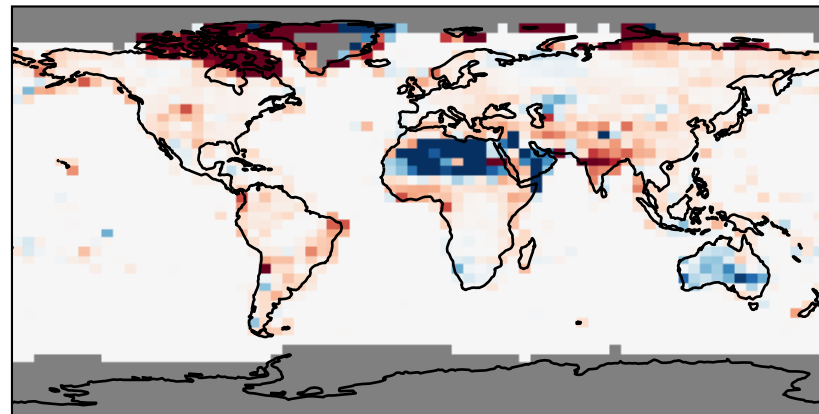
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



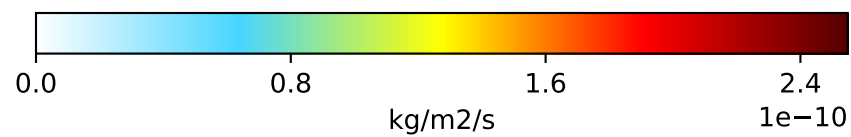
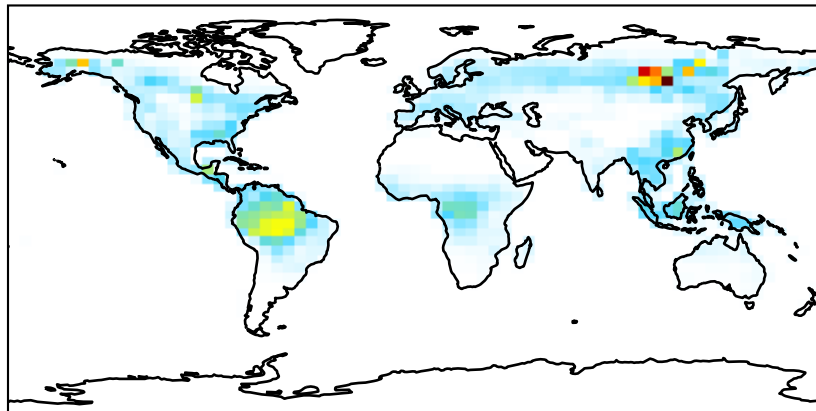
Ratio  
Dev/Ref, Fixed Range



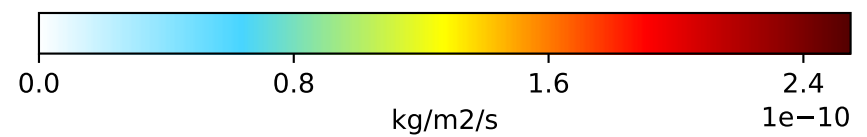
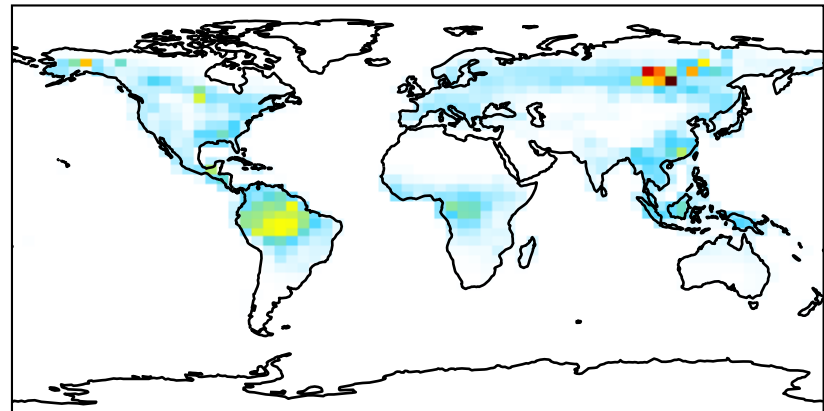


# EmisMTPA\_Total

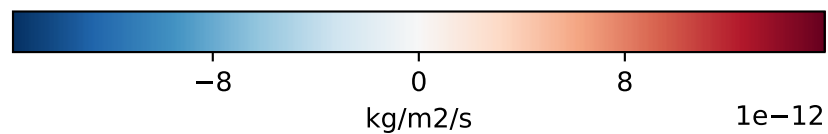
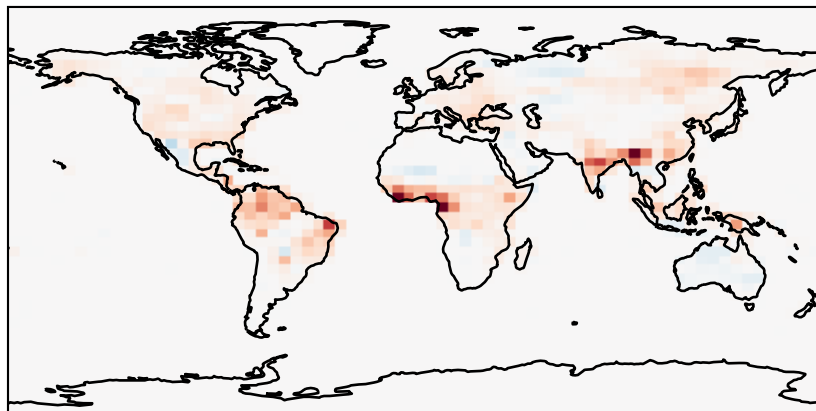
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



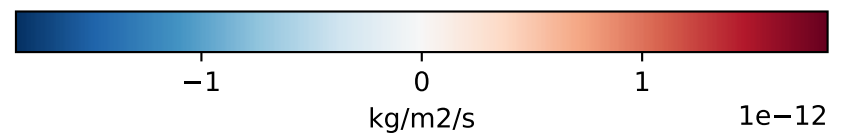
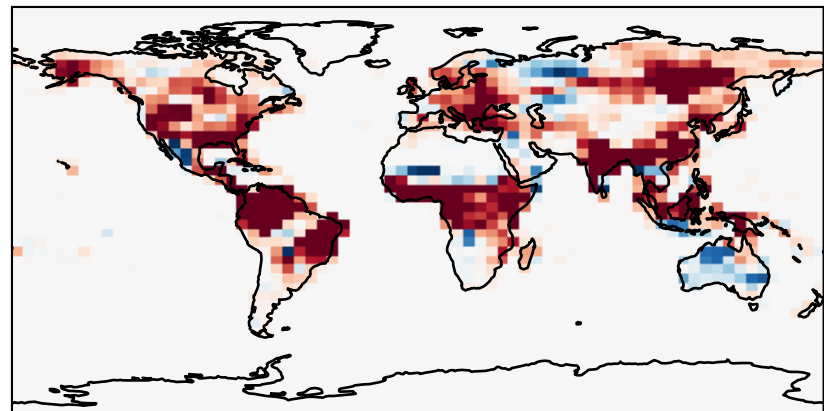
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



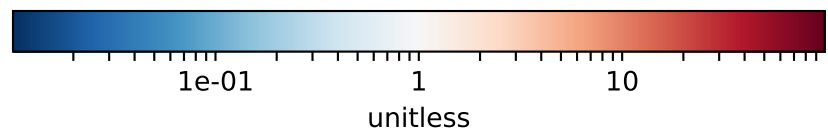
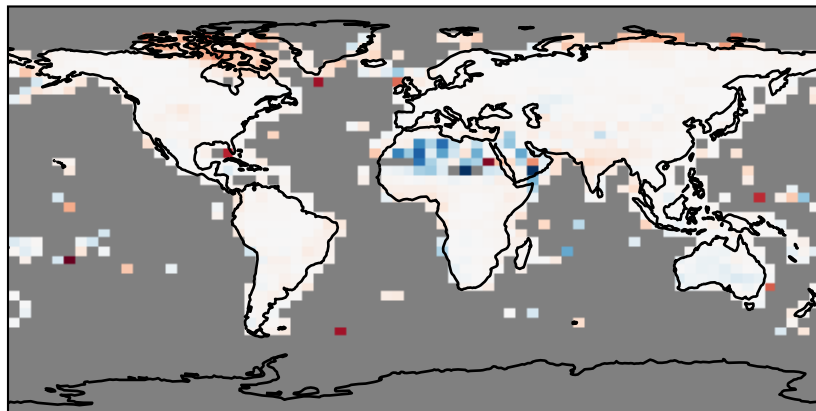
Difference  
Dev - Ref, Dynamic Range



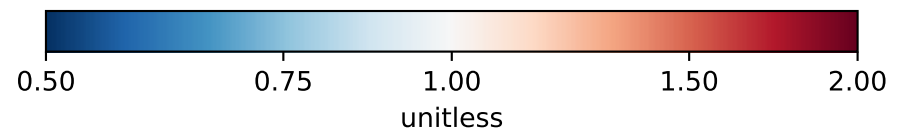
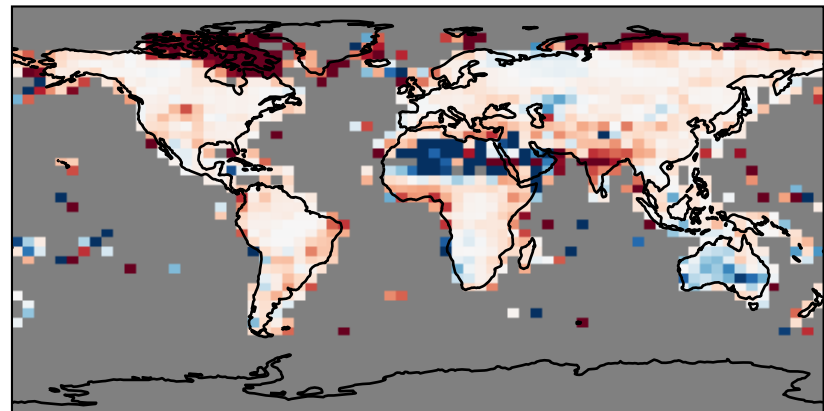
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

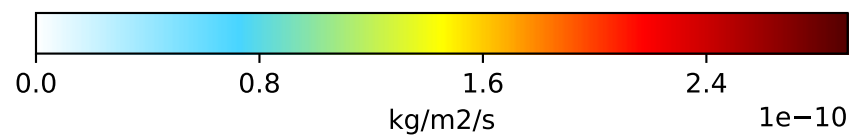
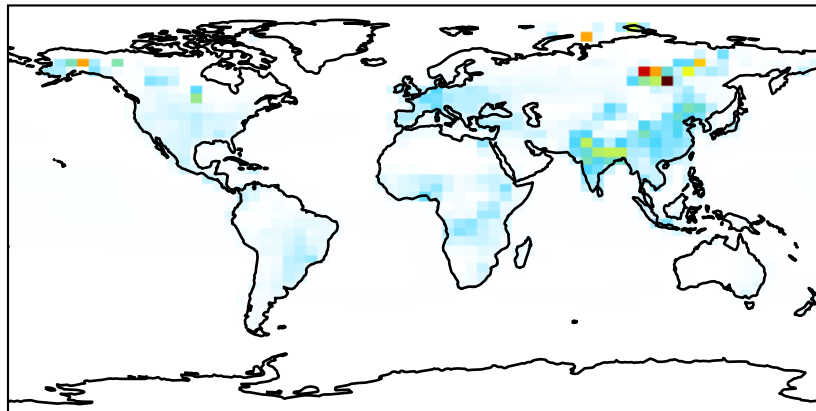


Ratio  
Dev/Ref, Fixed Range

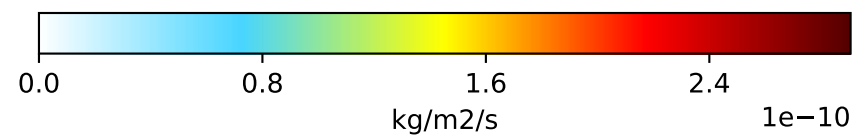
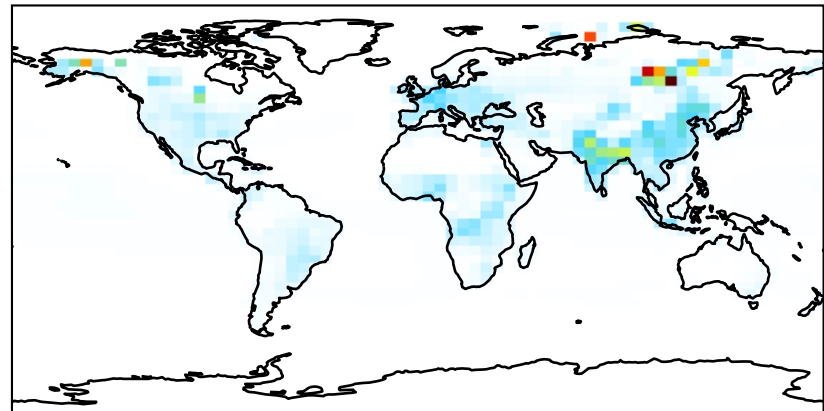


# EmisNH3\_Total

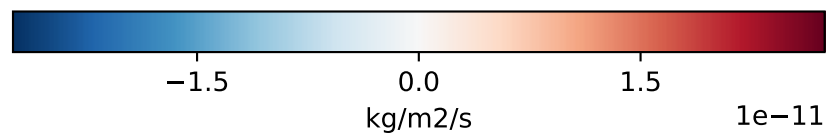
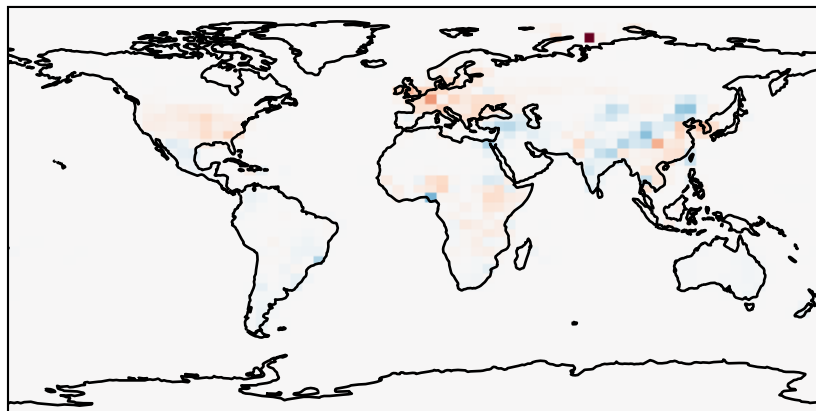
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



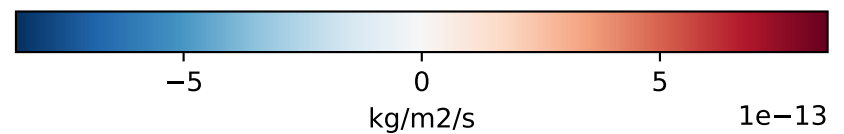
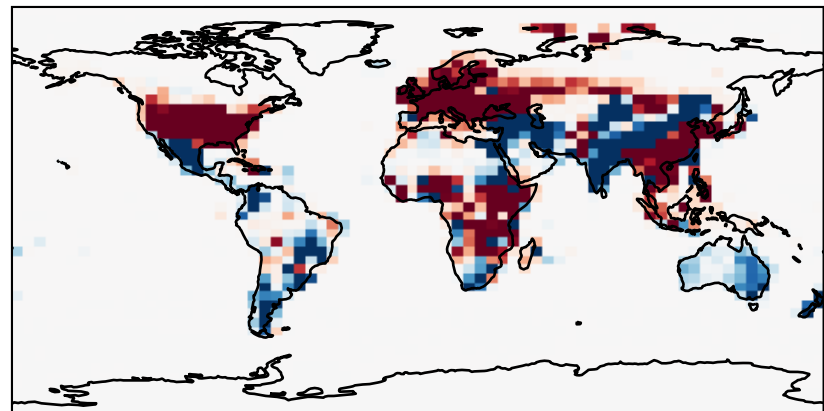
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



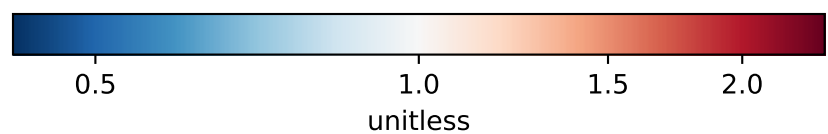
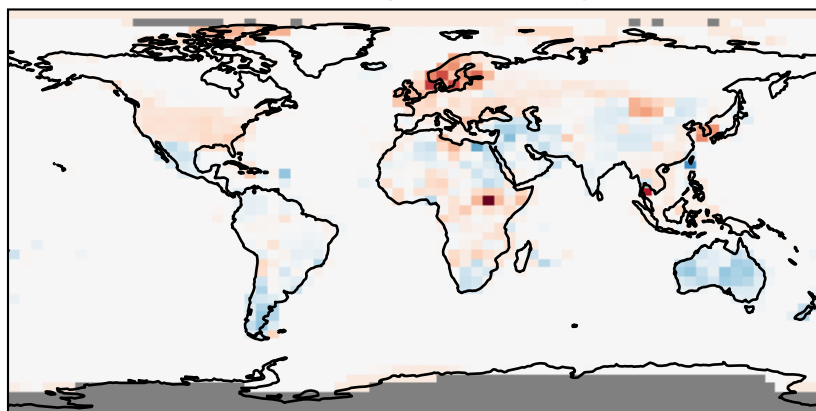
Difference  
Dev - Ref, Dynamic Range



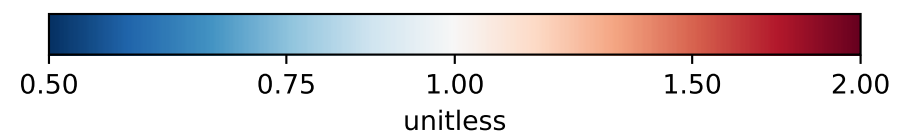
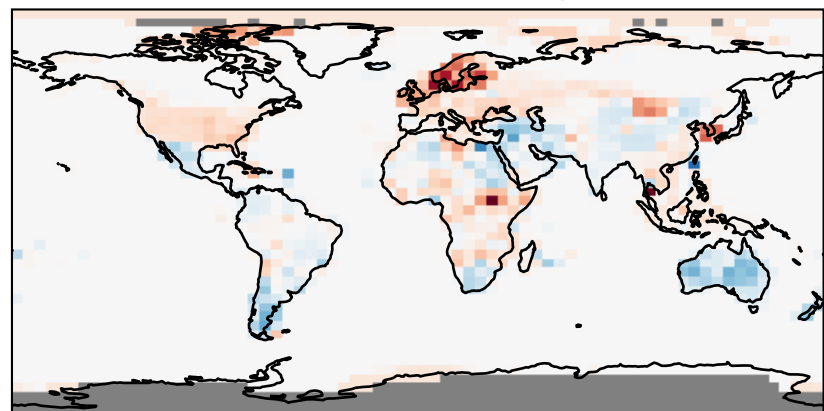
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

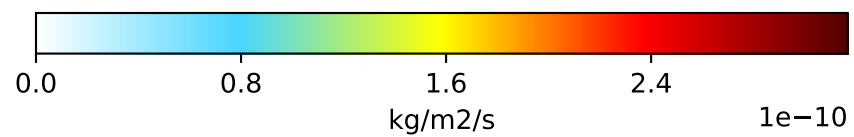
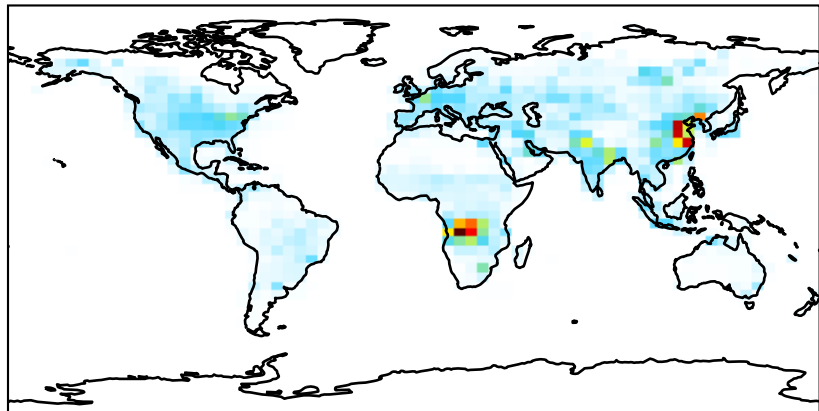


Ratio  
Dev/Ref, Fixed Range

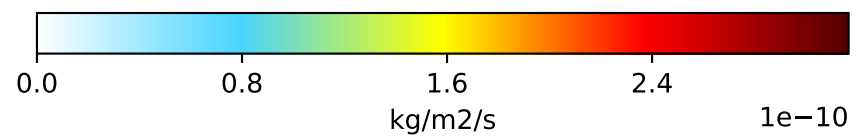
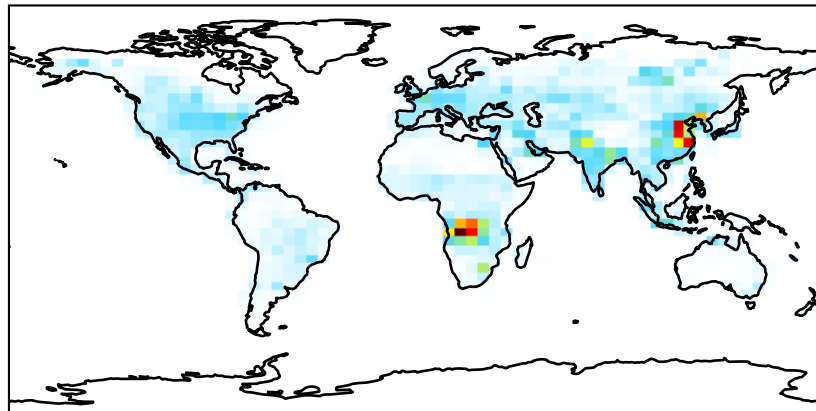


# EmisNO\_Total

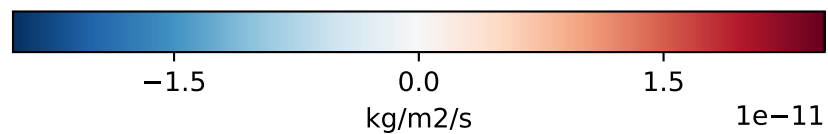
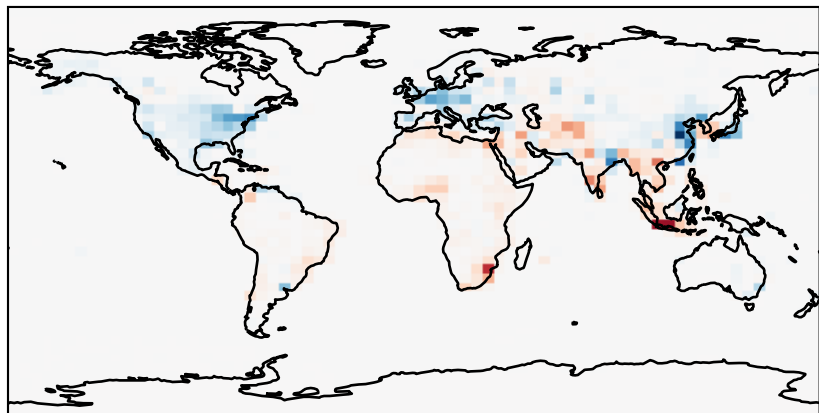
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



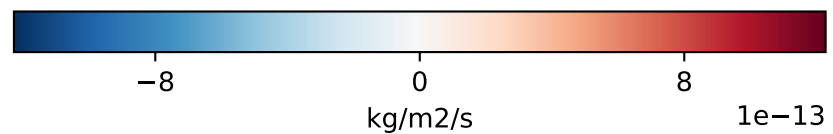
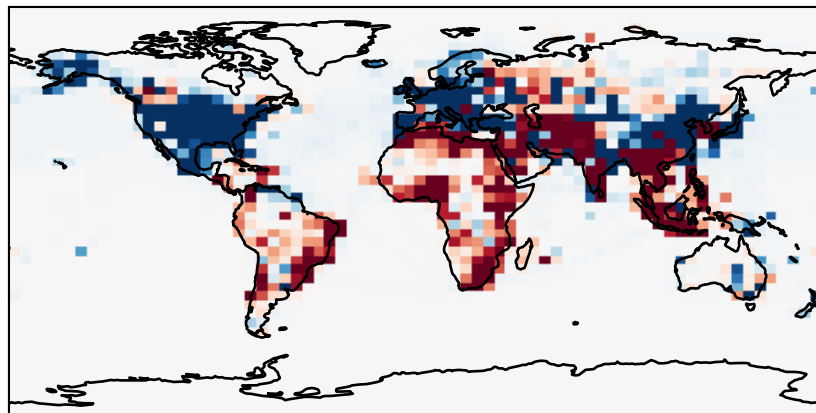
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



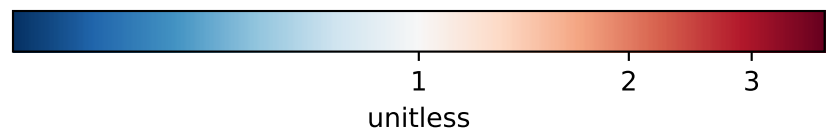
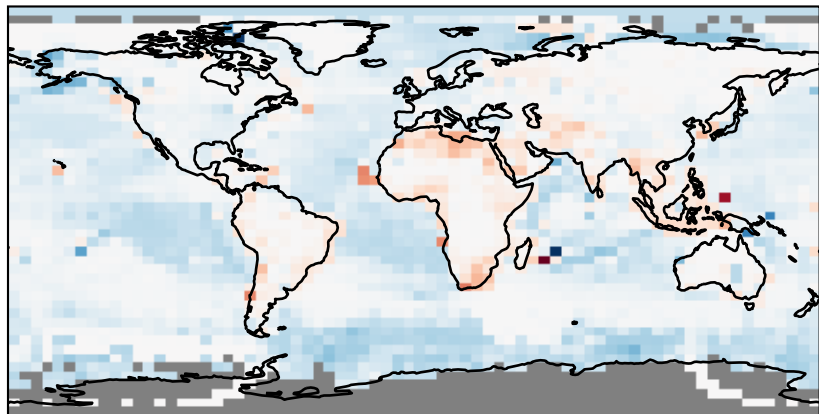
Difference  
Dev - Ref, Dynamic Range



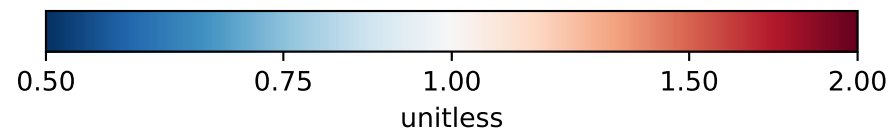
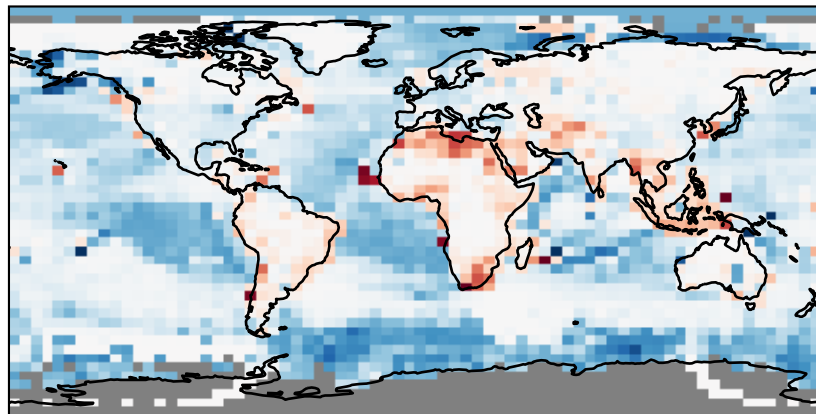
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

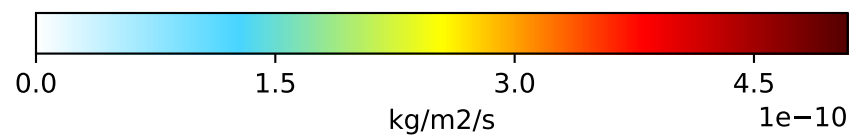
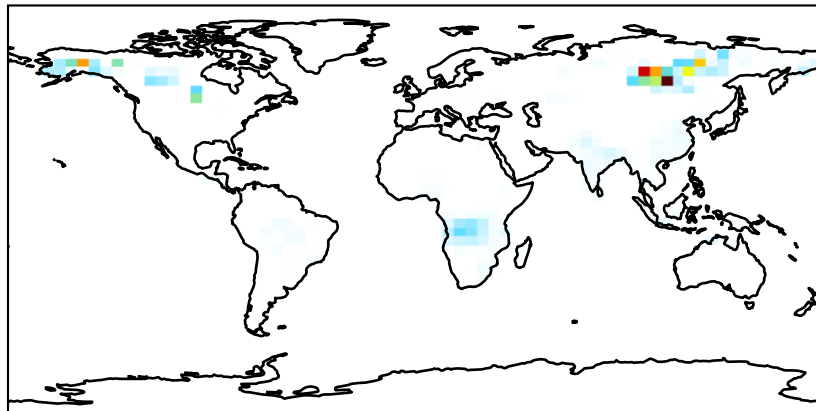


Ratio  
Dev/Ref, Fixed Range

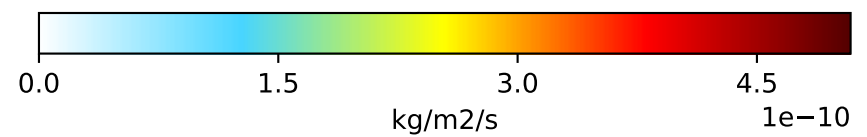
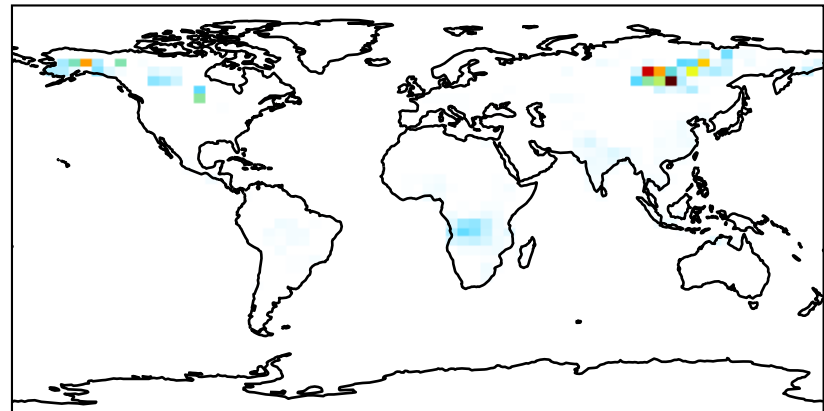


# EmisOCPI\_Total

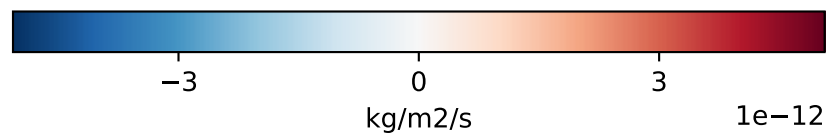
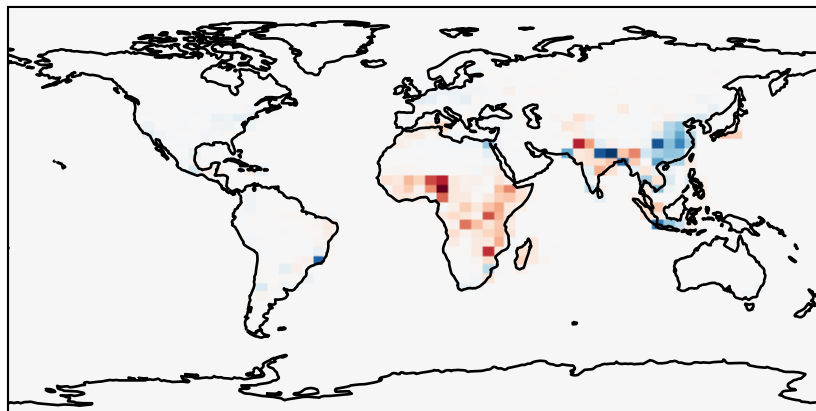
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



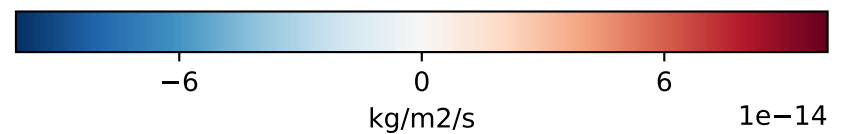
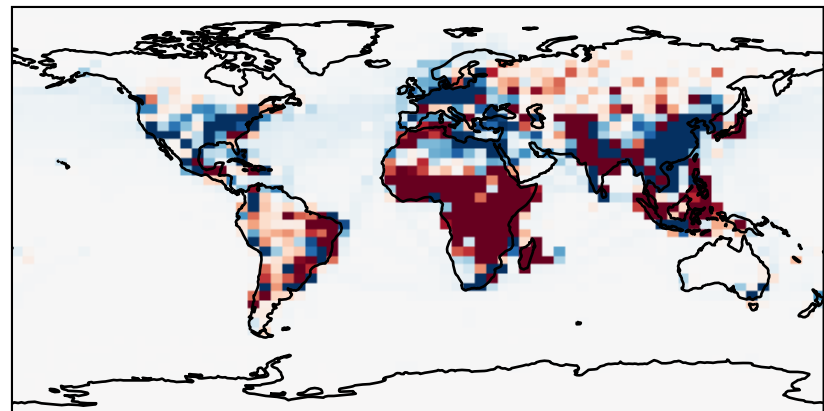
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



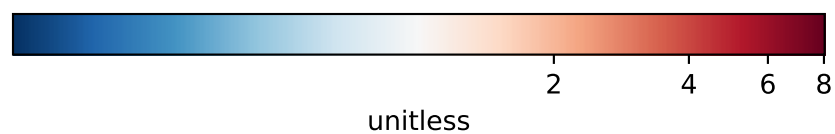
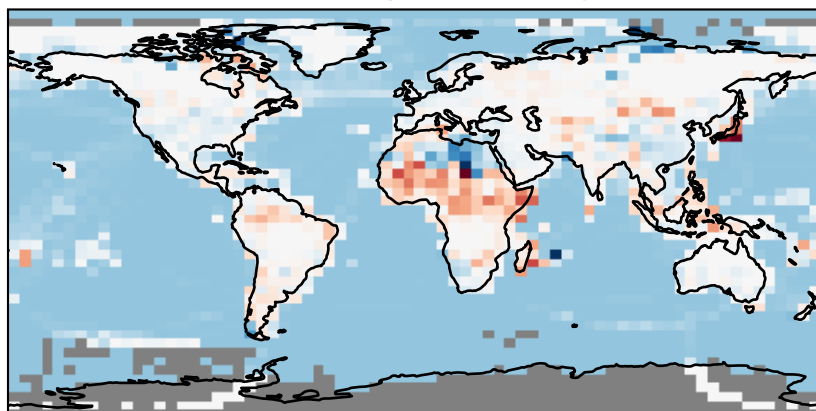
Difference  
Dev - Ref, Dynamic Range



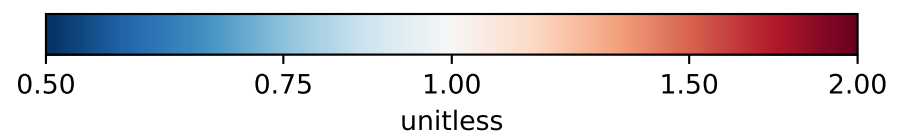
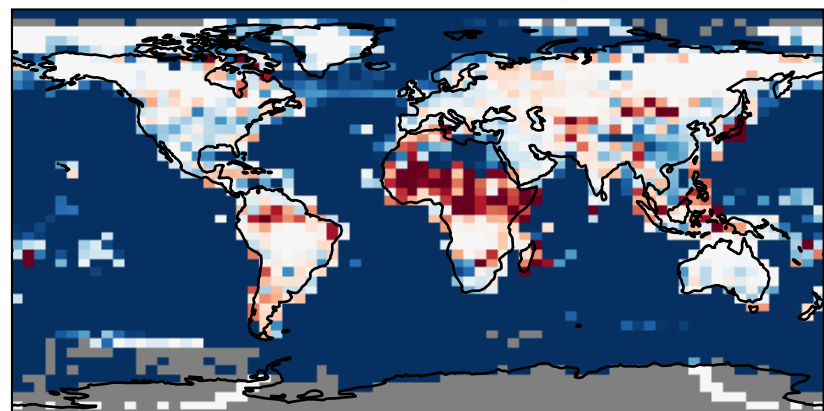
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

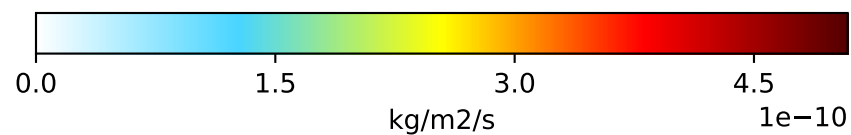
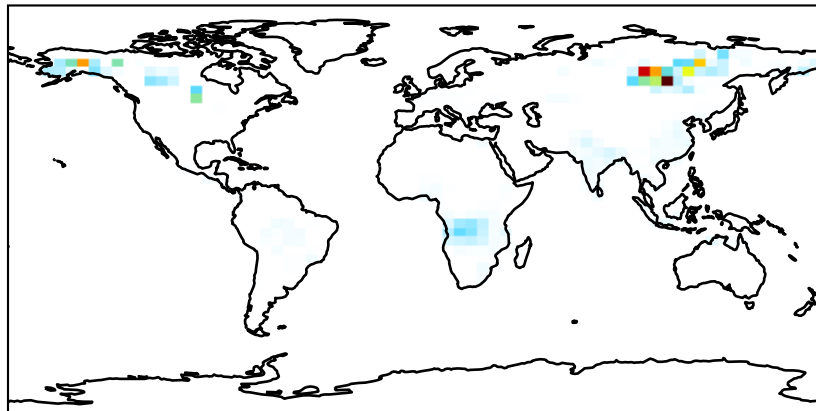


Ratio  
Dev/Ref, Fixed Range

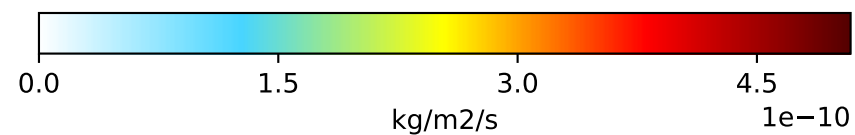
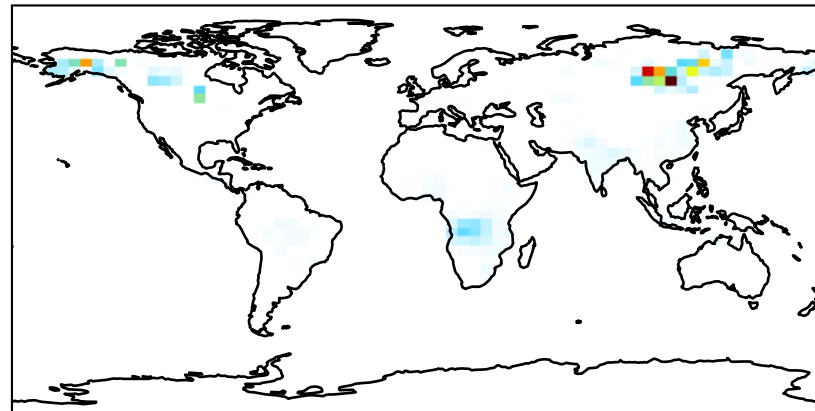


# EmisOCPO\_Total

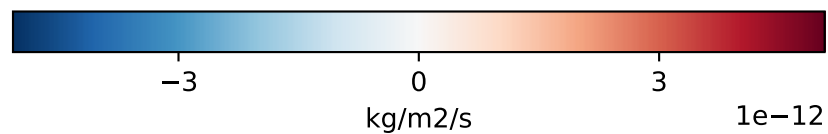
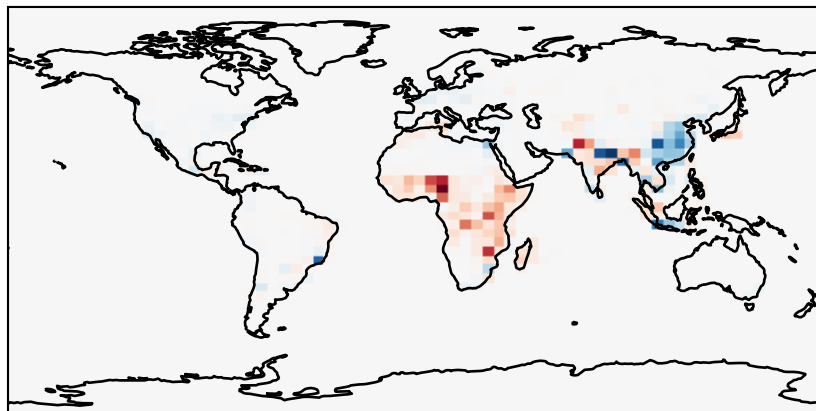
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



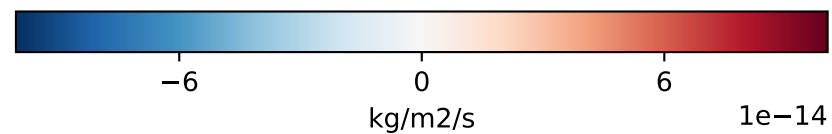
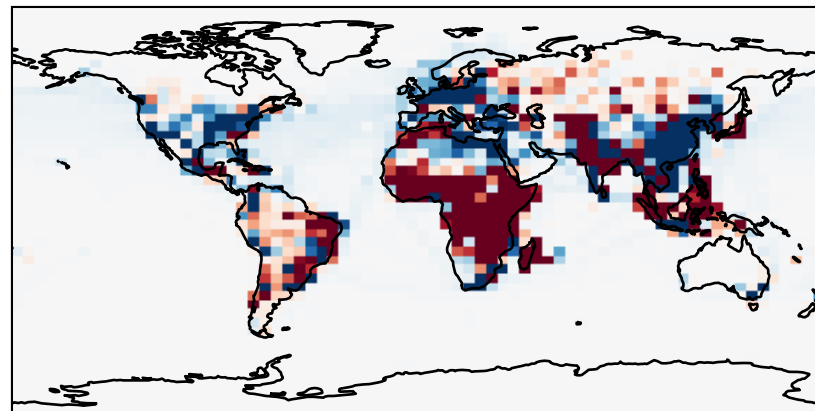
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



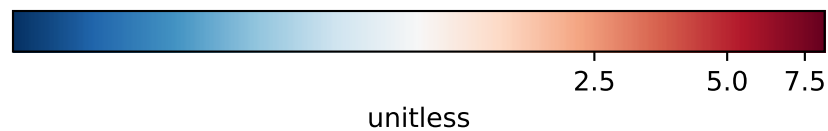
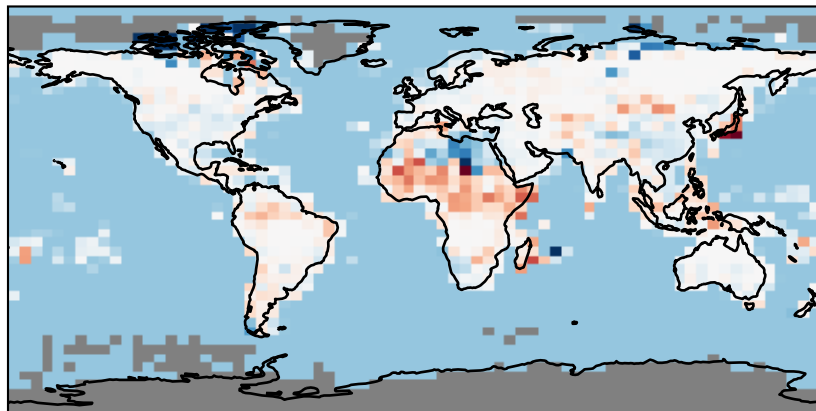
Difference  
Dev - Ref, Dynamic Range



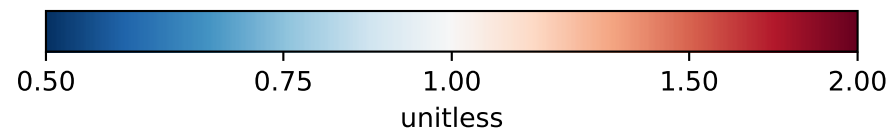
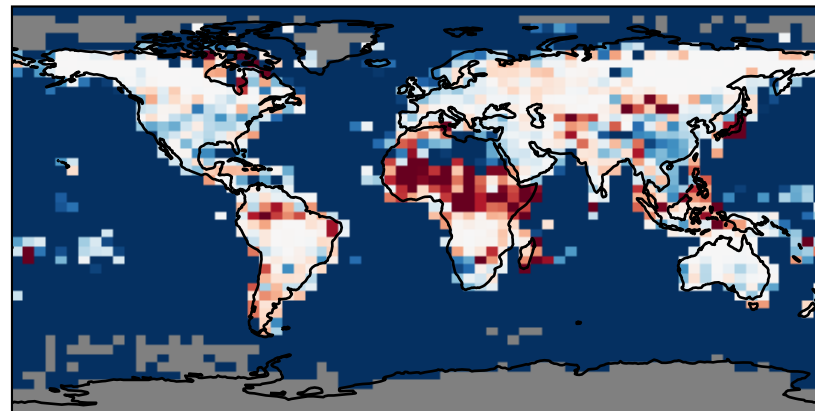
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



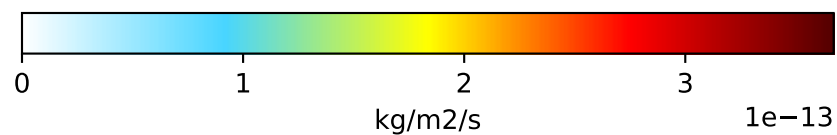
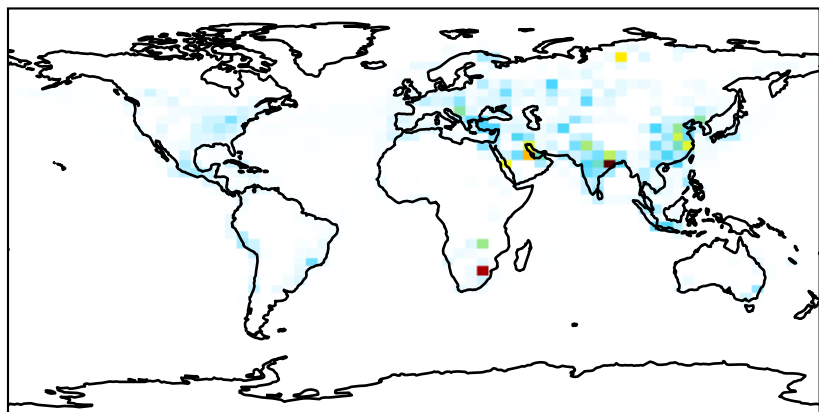
Ratio  
Dev/Ref, Fixed Range



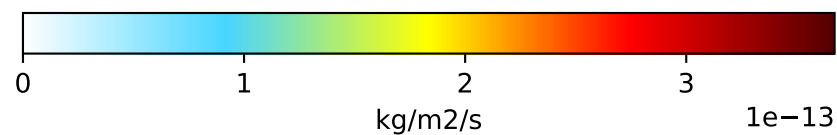
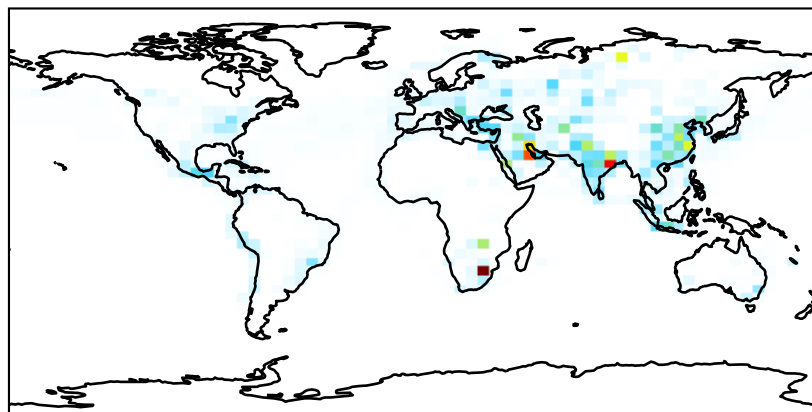


# EmispFe\_Total

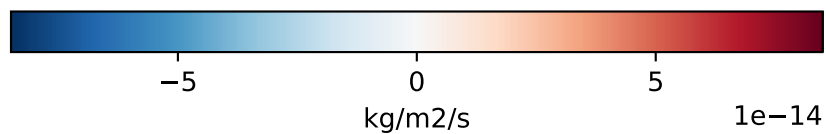
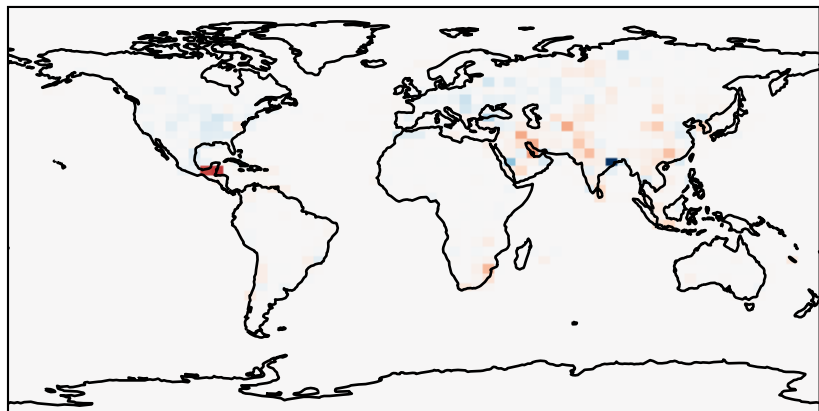
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



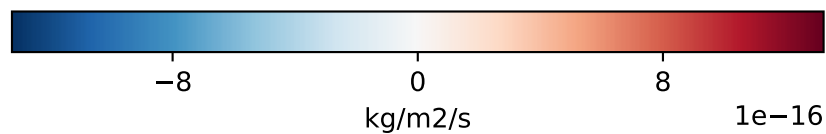
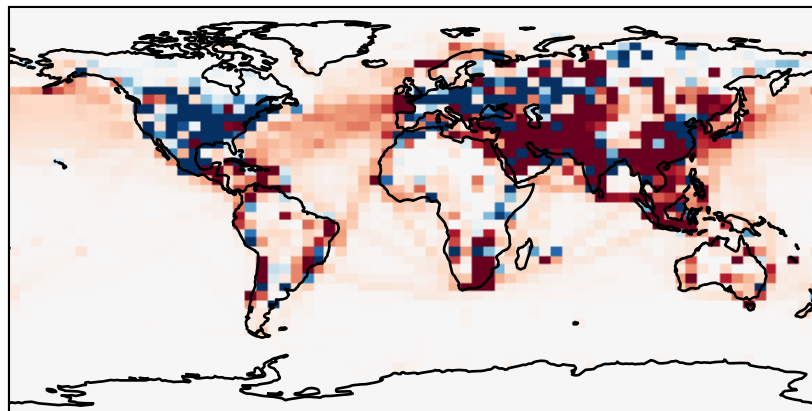
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



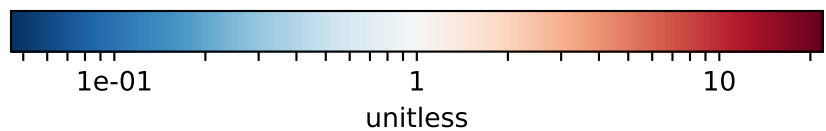
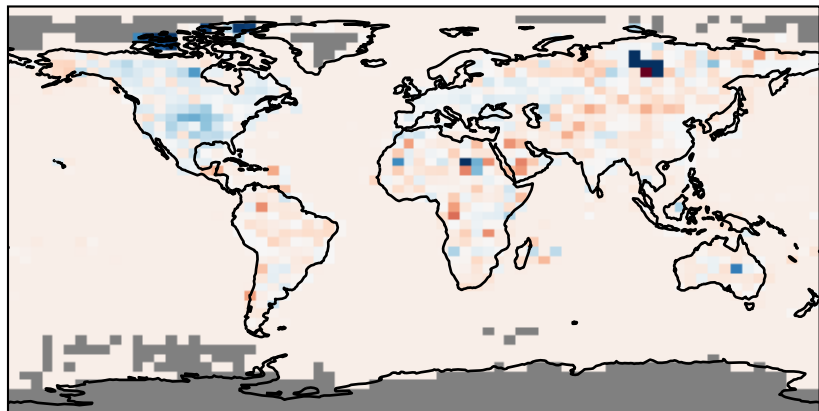
Difference  
Dev - Ref, Dynamic Range



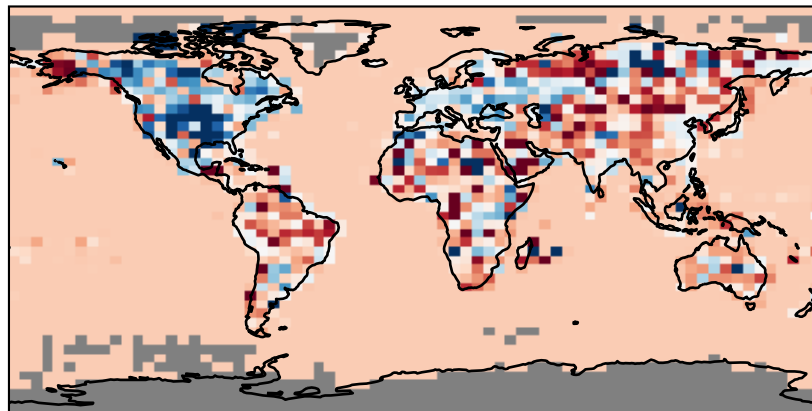
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

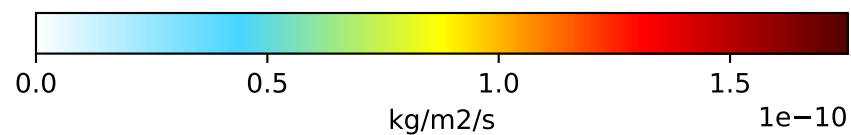
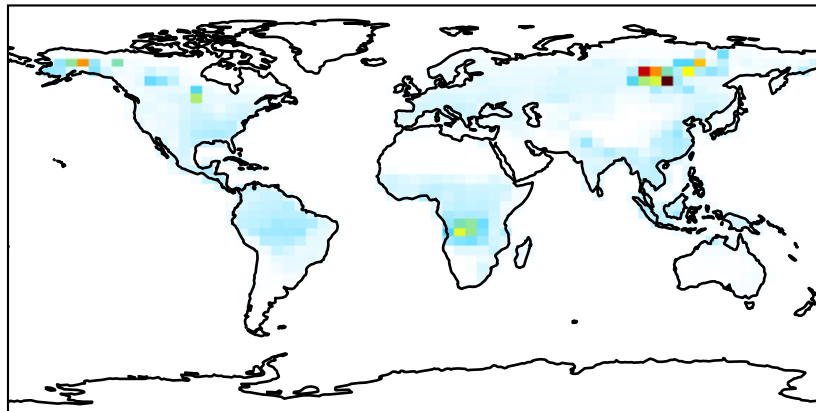


Ratio  
Dev/Ref, Fixed Range

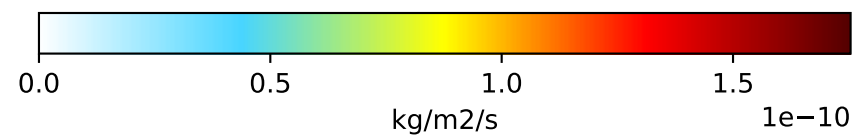
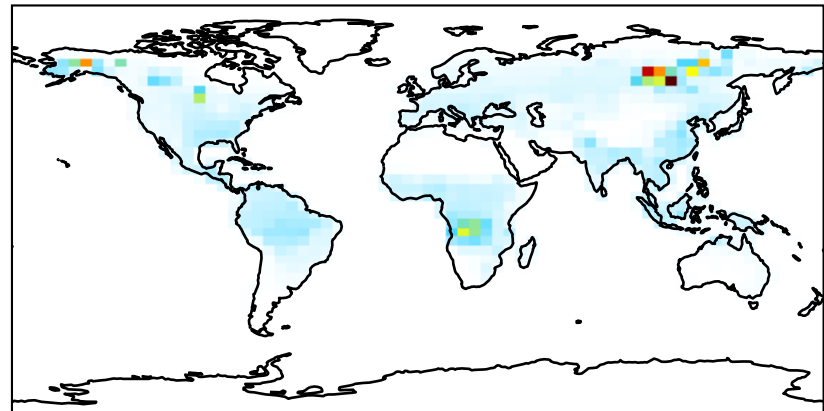


# EmisPRPE\_Total

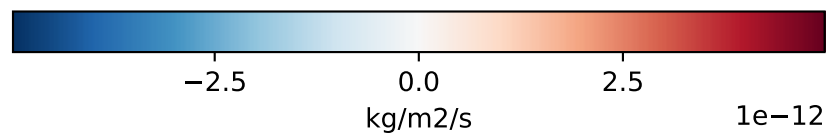
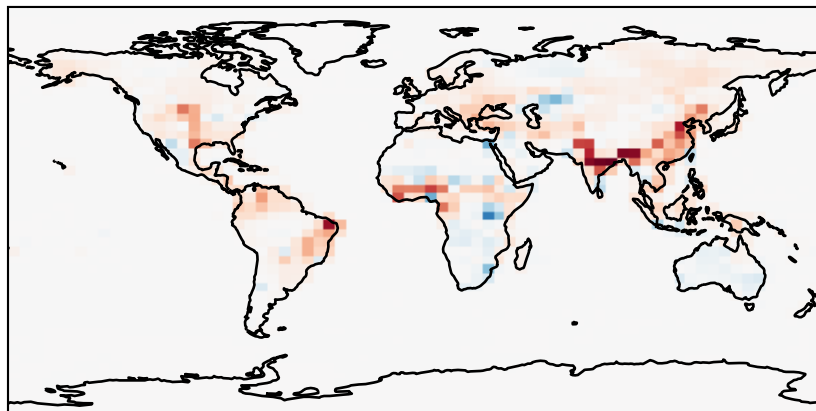
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



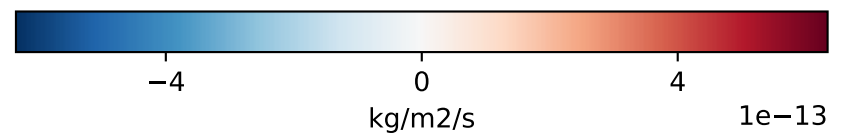
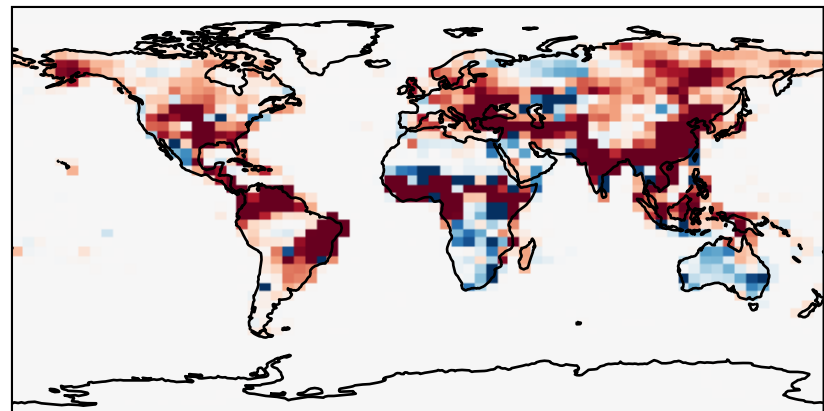
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



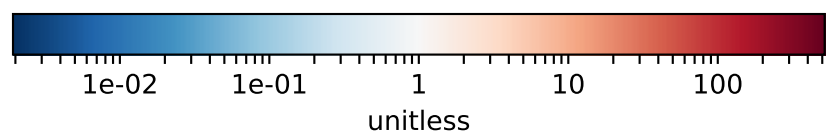
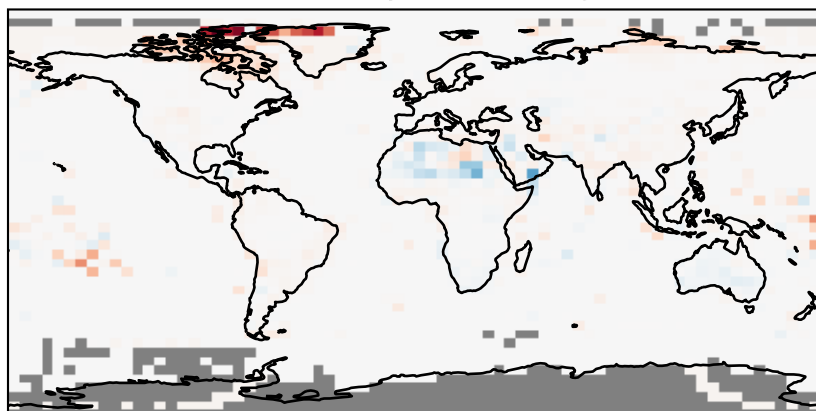
Difference  
Dev - Ref, Dynamic Range



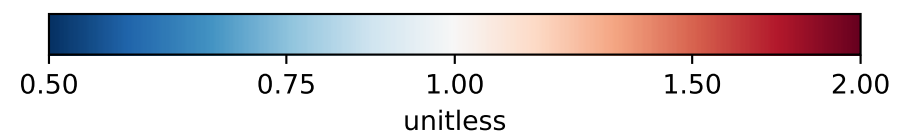
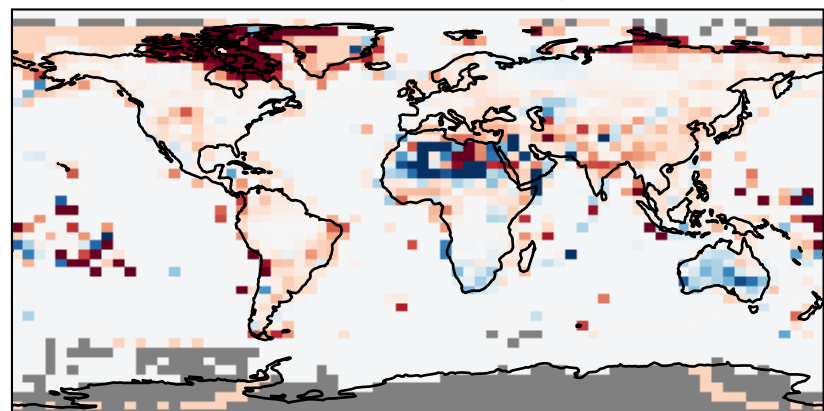
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

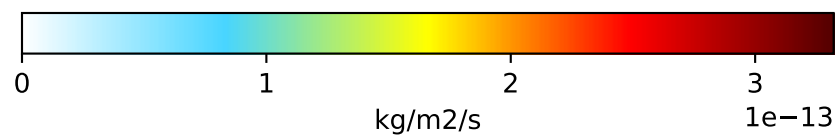
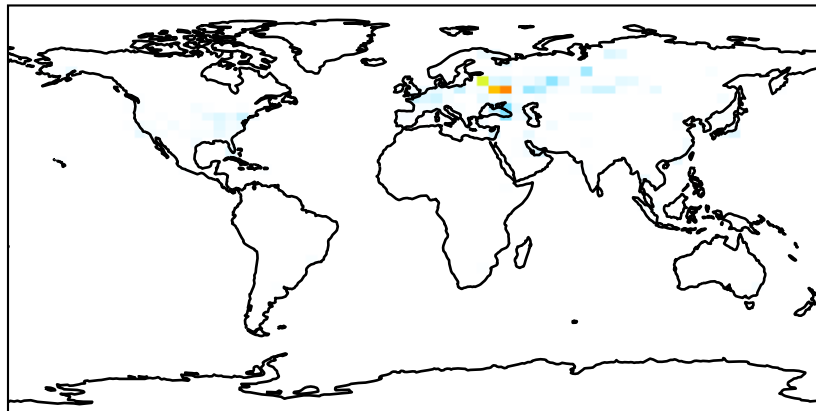


Ratio  
Dev/Ref, Fixed Range

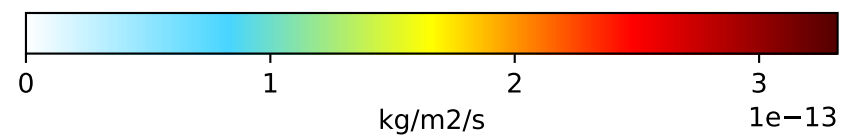
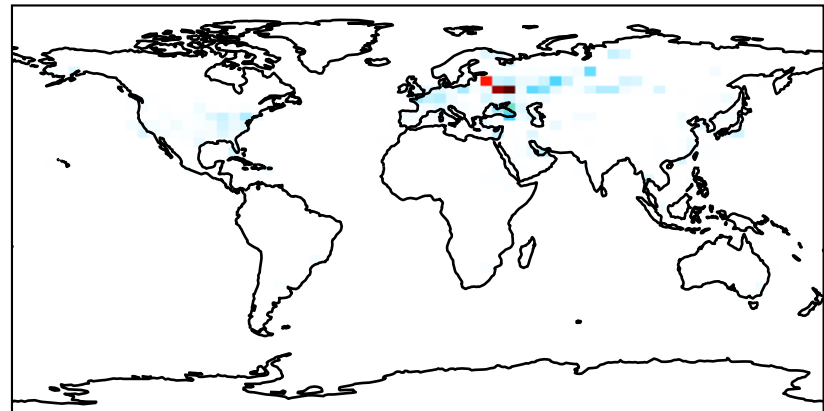


# EmisRCHO\_Total

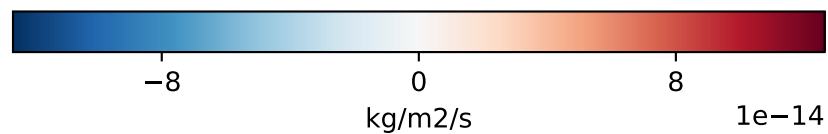
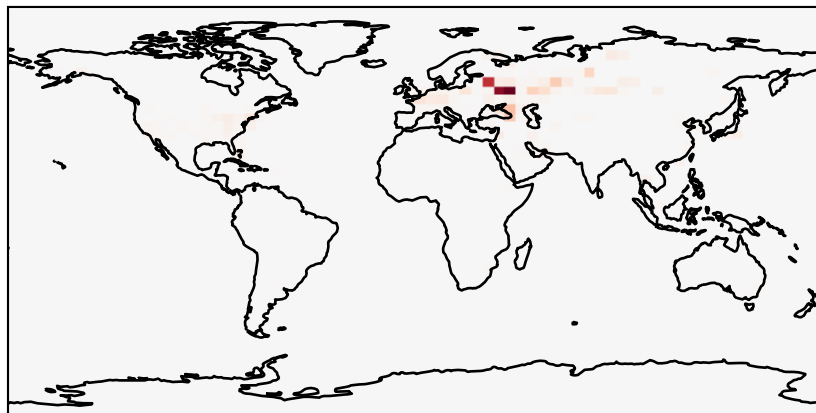
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



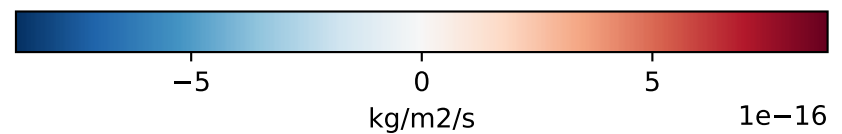
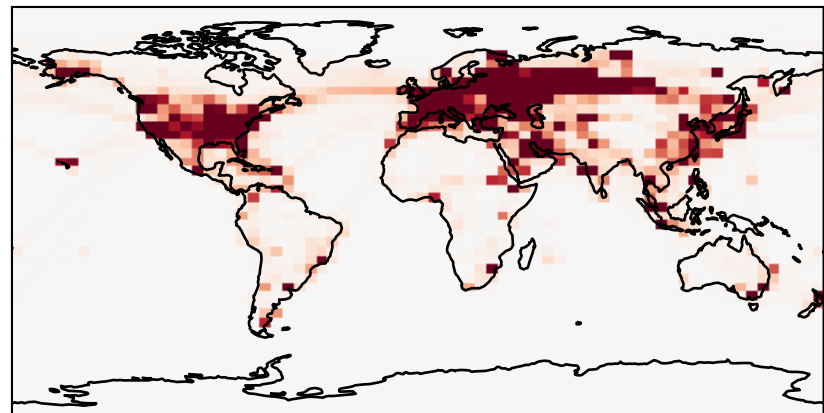
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



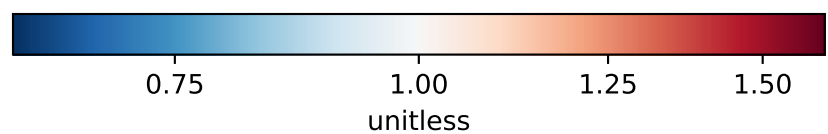
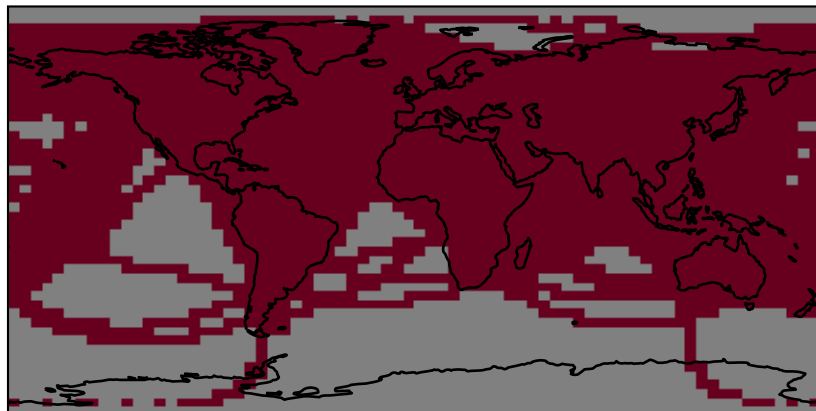
Difference  
Dev - Ref, Dynamic Range



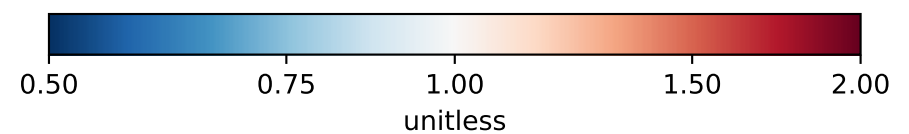
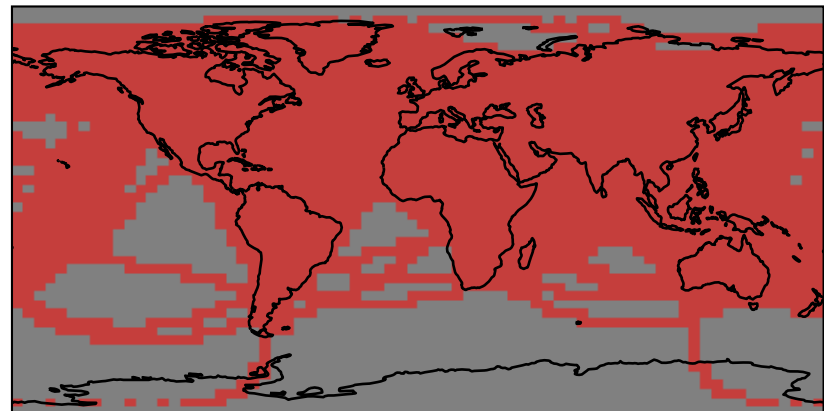
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

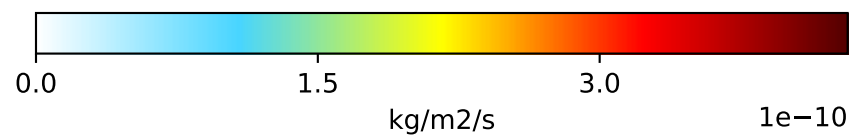
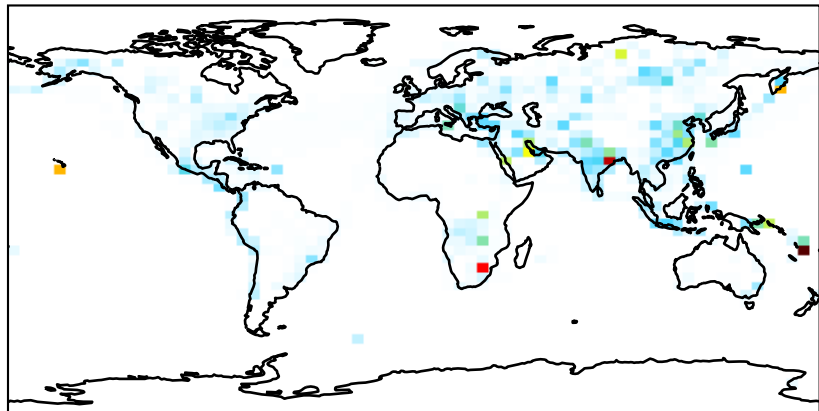


Ratio  
Dev/Ref, Fixed Range

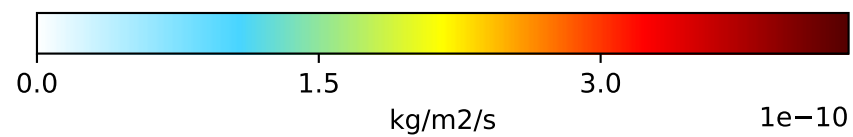
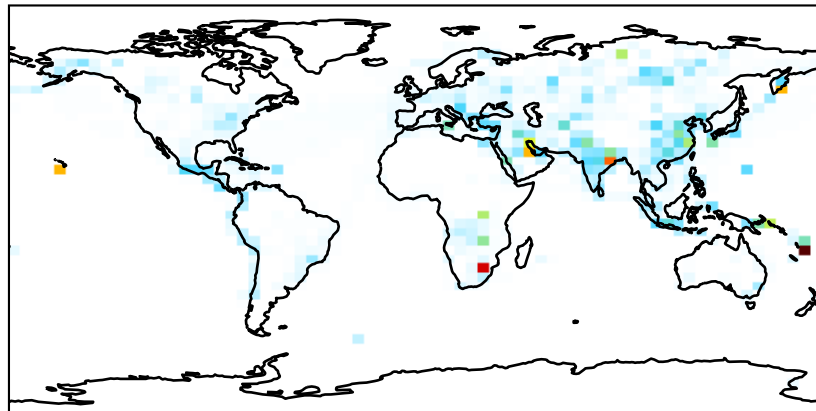


# EmisSO2\_Total

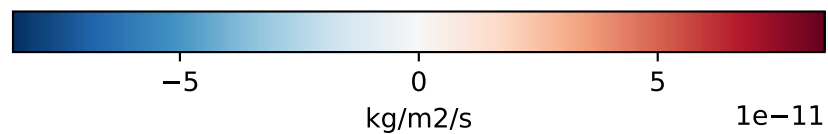
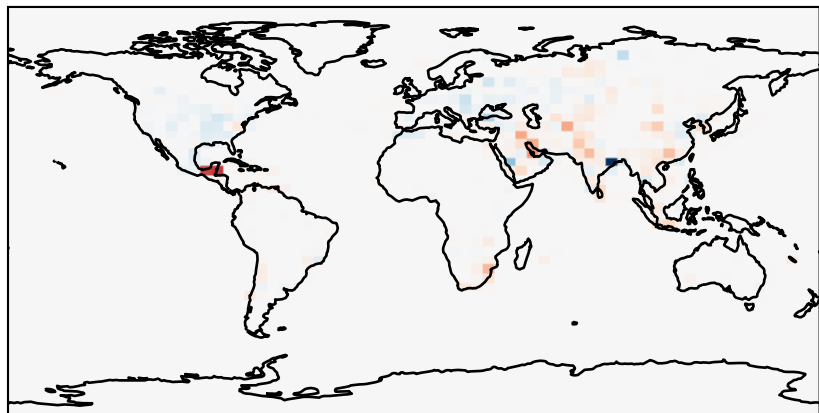
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



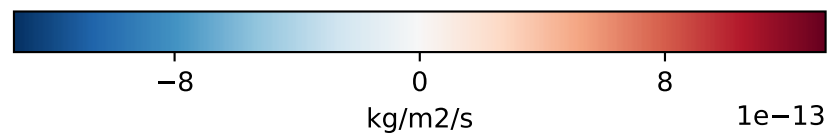
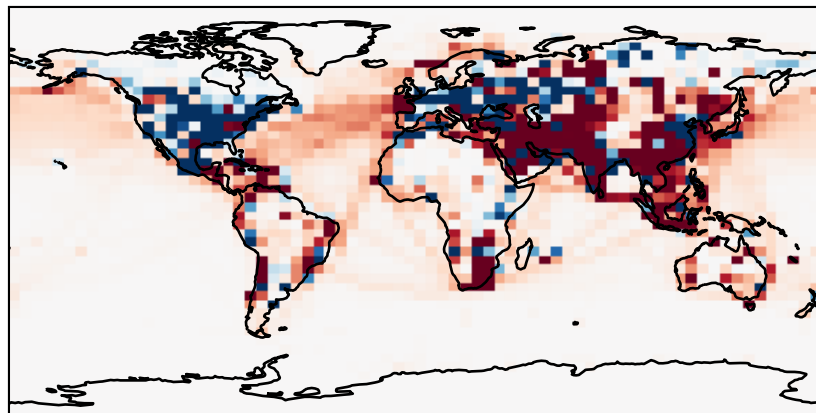
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



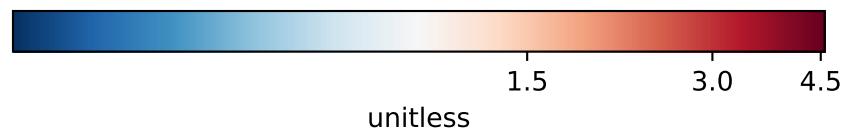
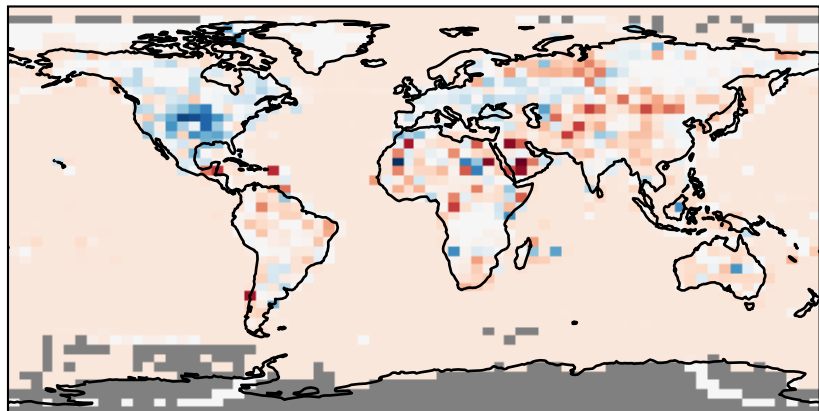
Difference  
Dev - Ref, Dynamic Range



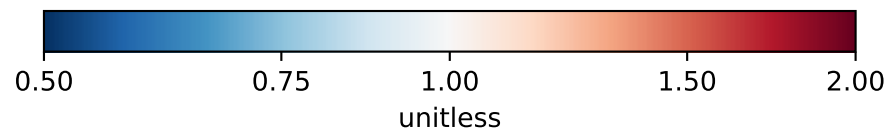
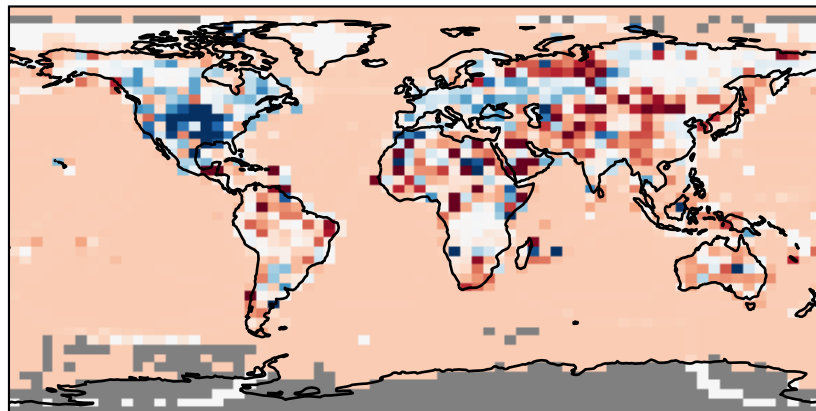
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

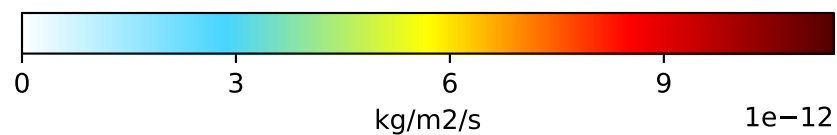
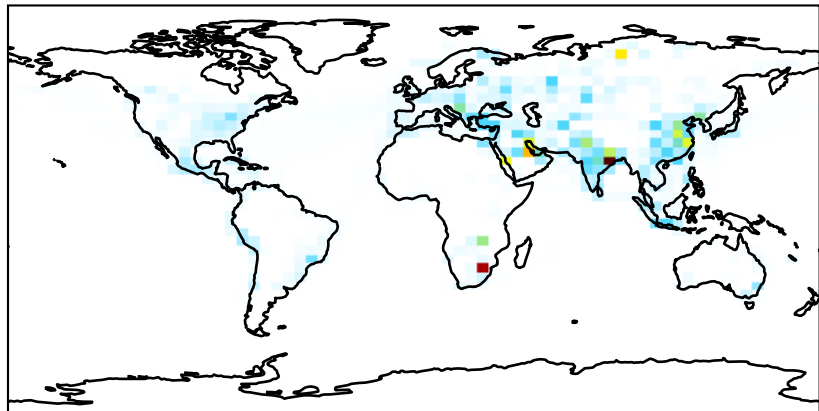


Ratio  
Dev/Ref, Fixed Range

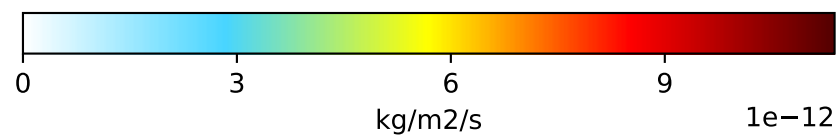
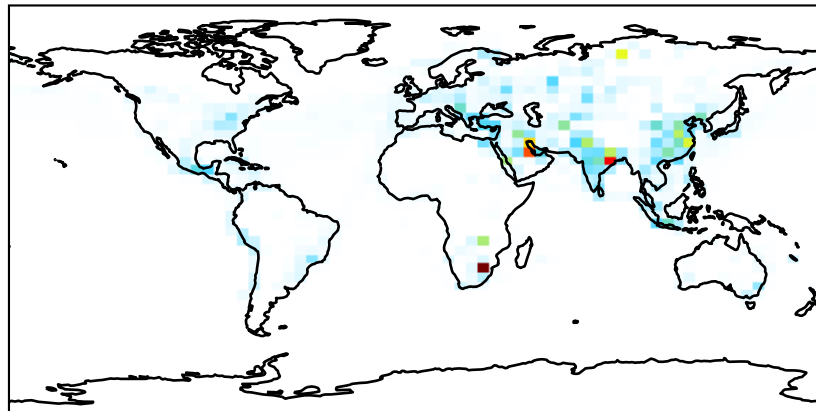


# EmisSO4\_Total

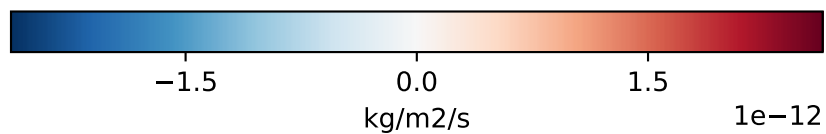
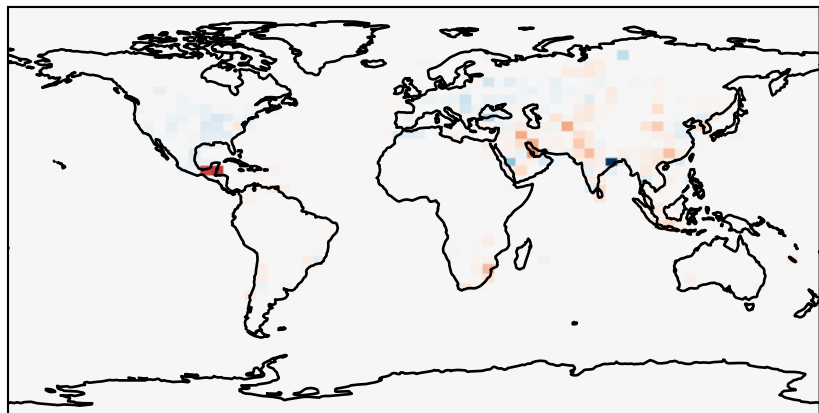
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



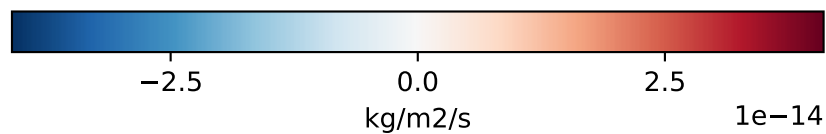
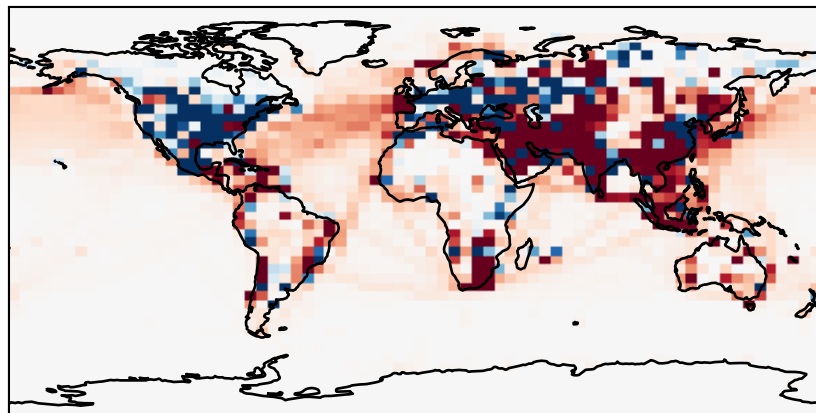
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



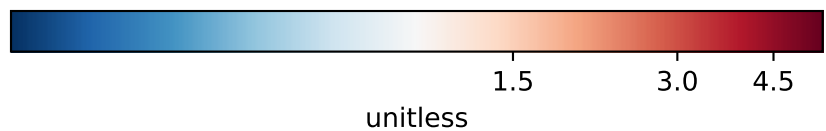
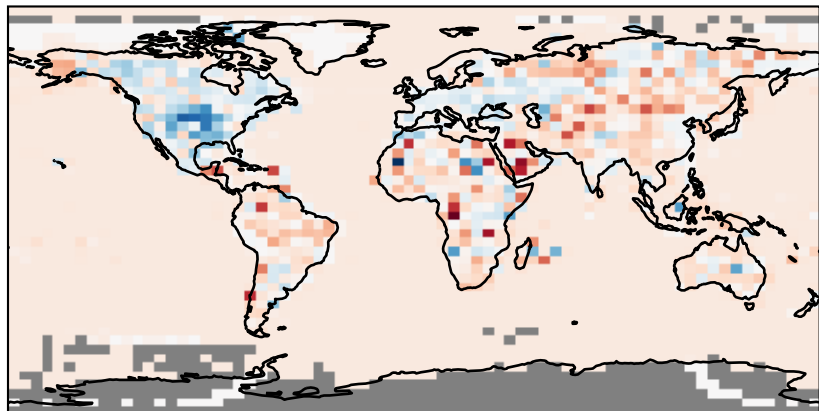
Difference  
Dev - Ref, Dynamic Range



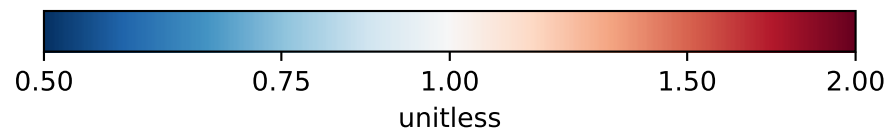
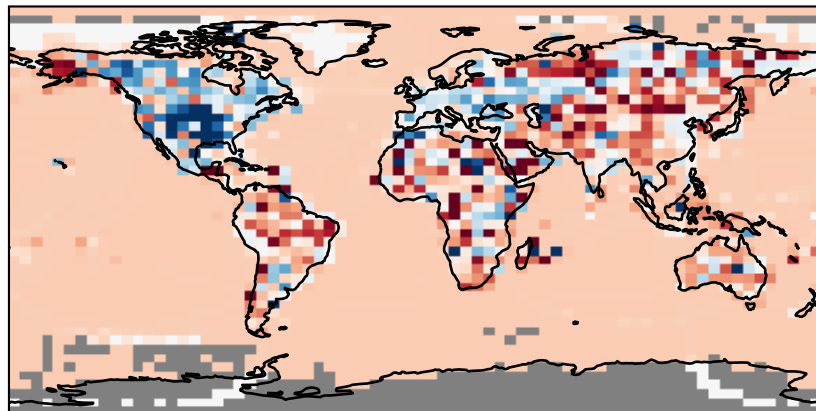
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



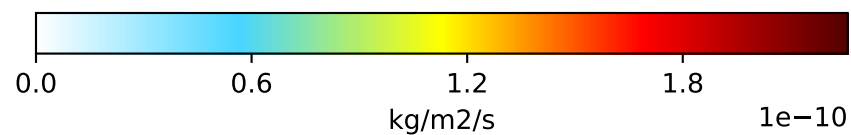
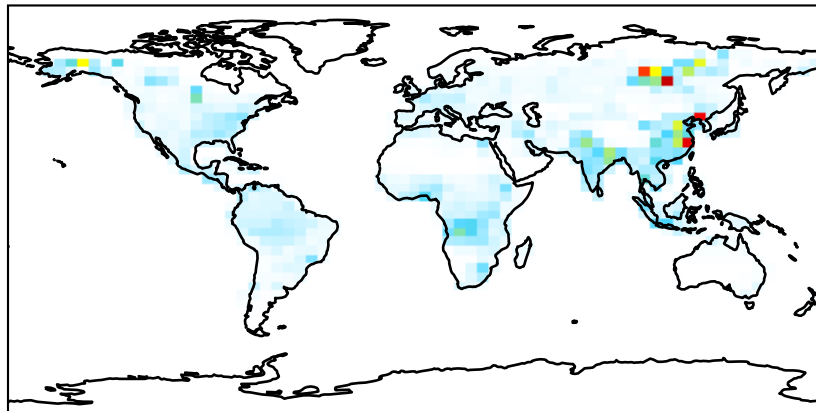
Ratio  
Dev/Ref, Fixed Range



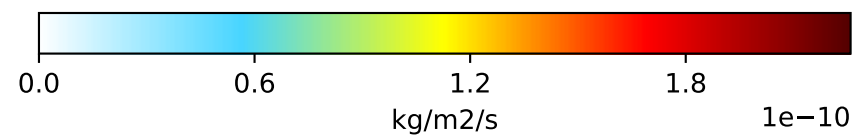
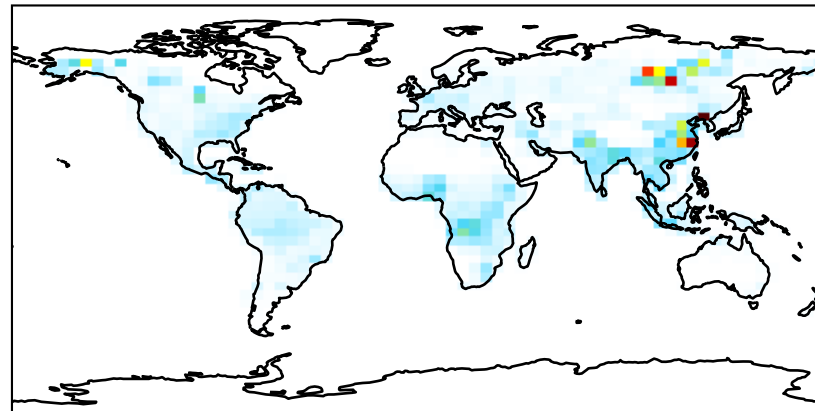


# EmisSOAP\_Total

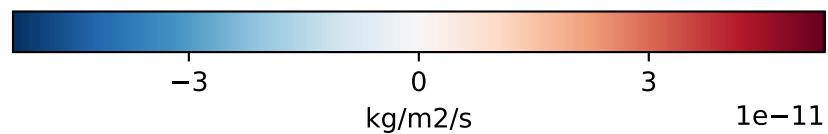
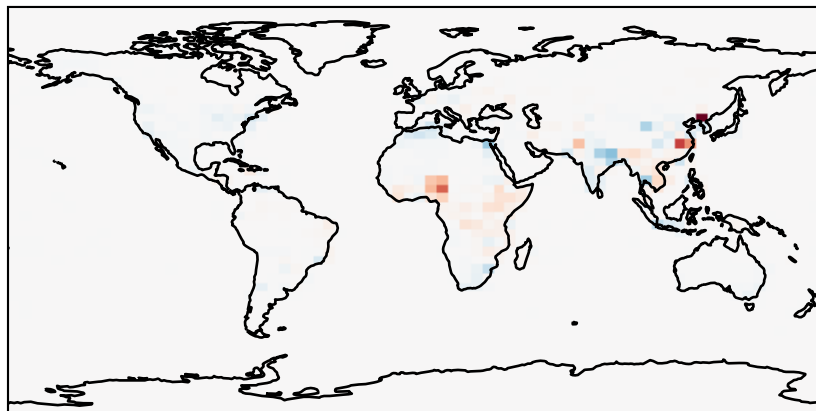
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



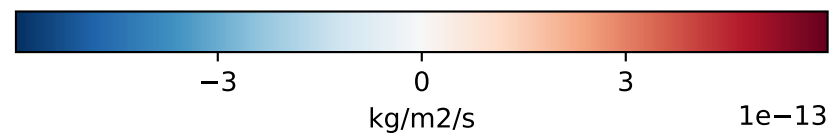
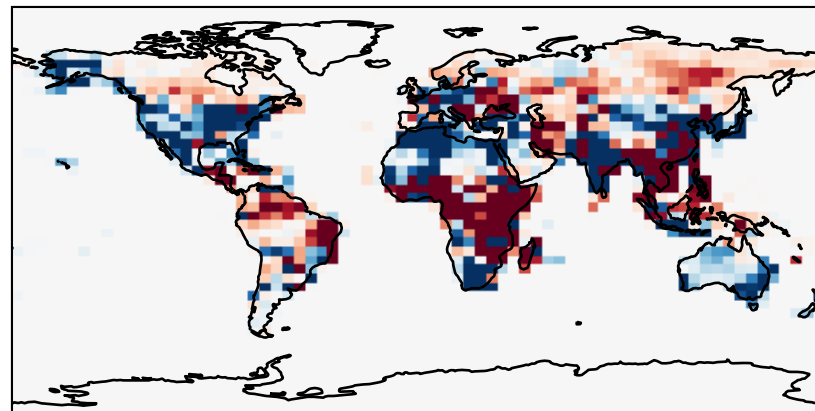
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



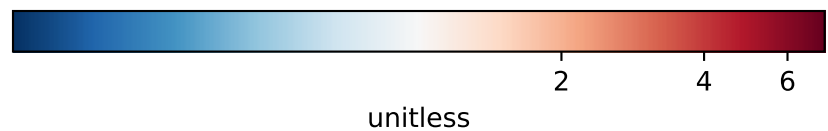
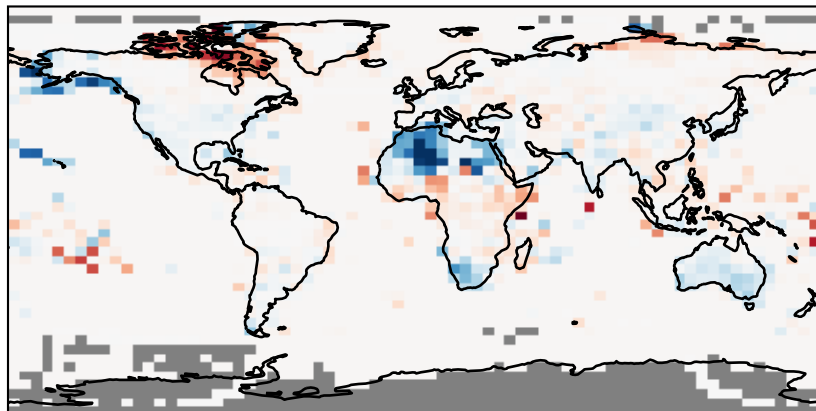
Difference  
Dev - Ref, Dynamic Range



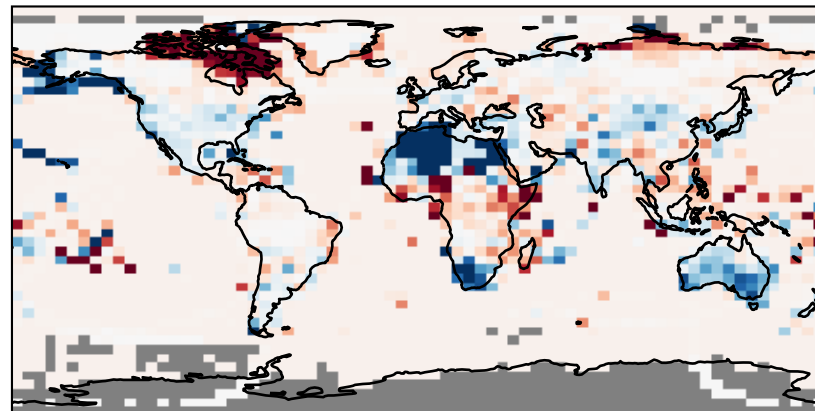
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

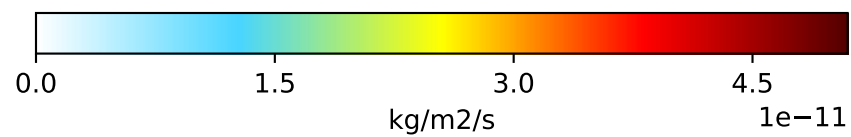
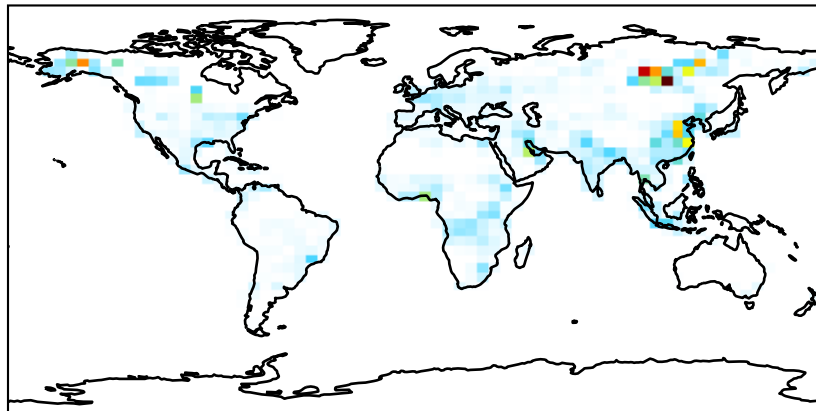


Ratio  
Dev/Ref, Fixed Range

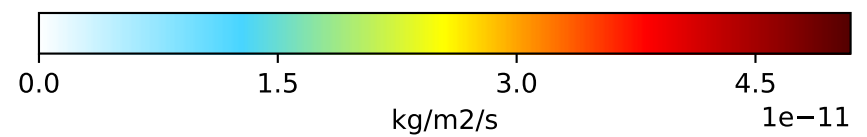
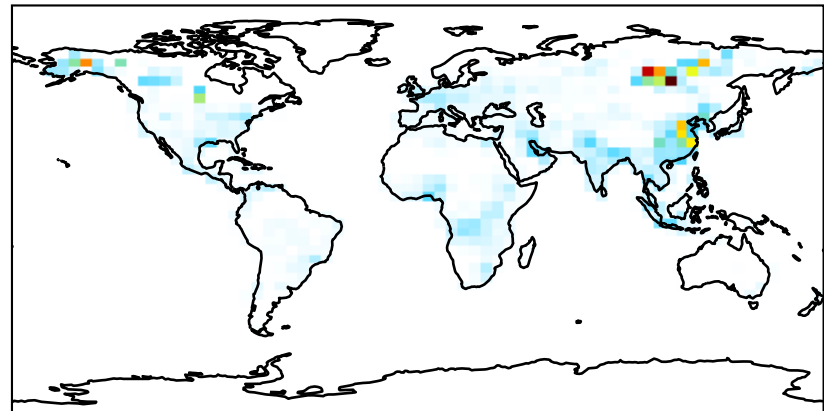


# EmisTOLU\_Total

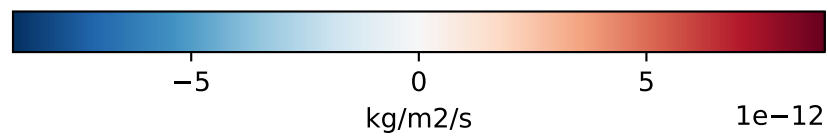
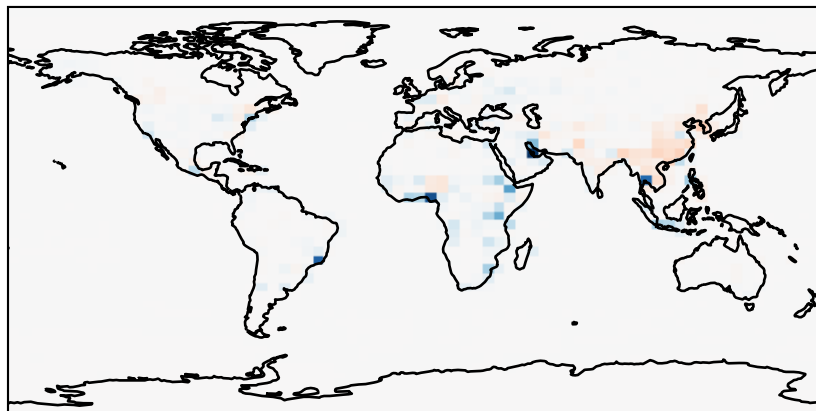
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



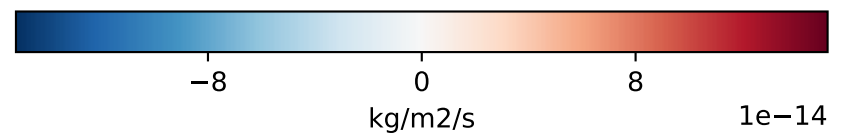
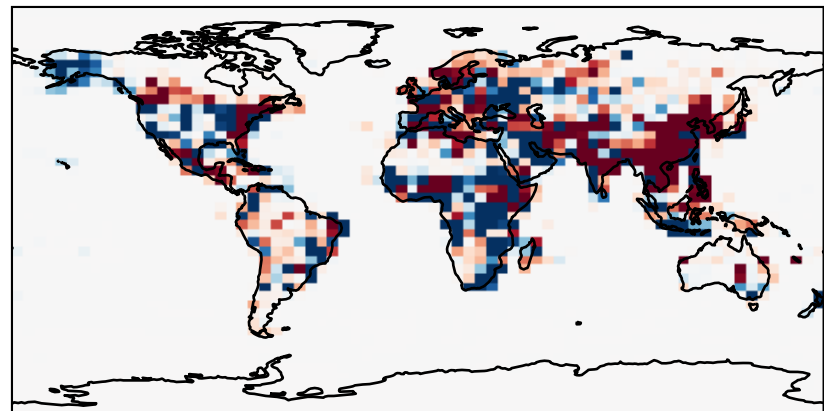
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



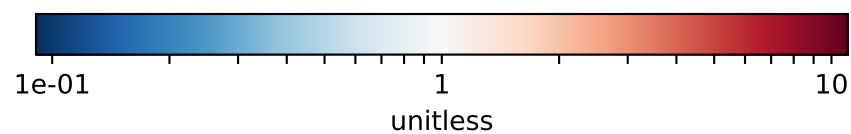
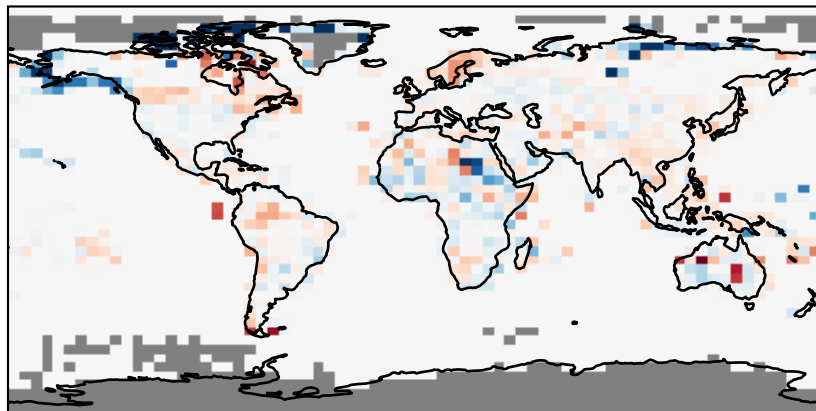
Difference  
Dev - Ref, Dynamic Range



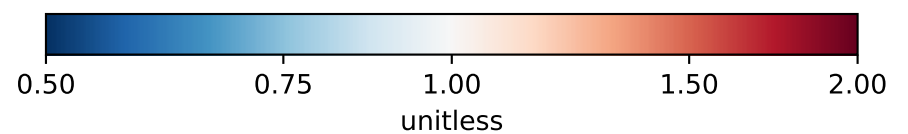
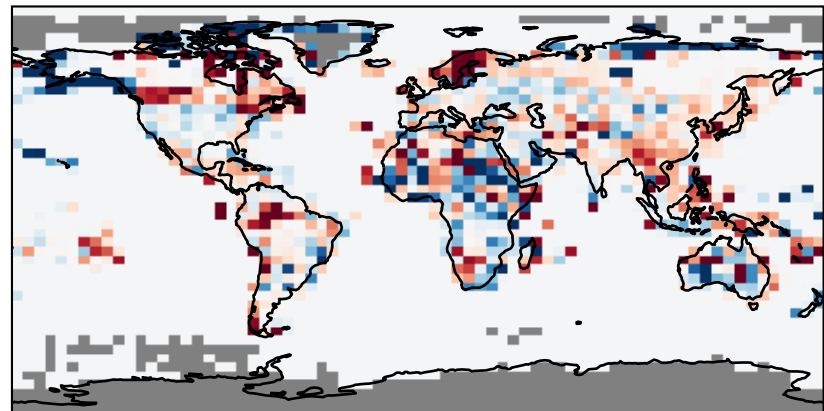
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

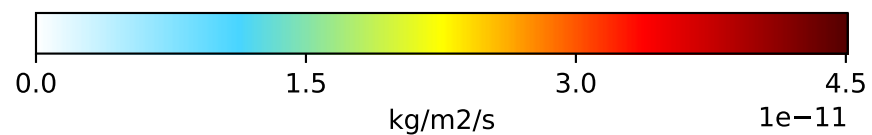
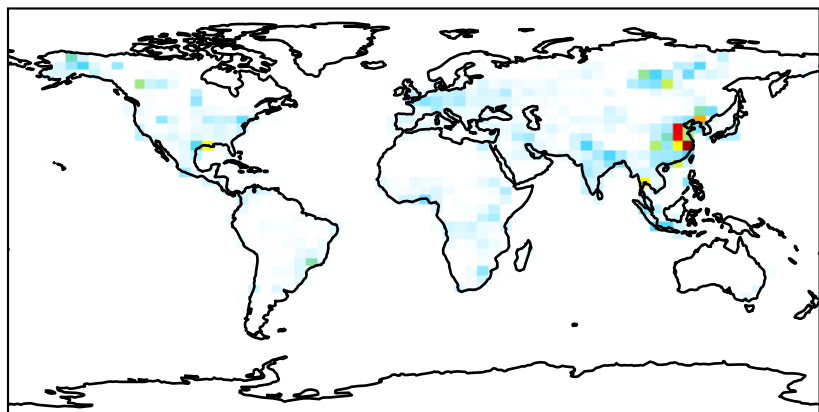


Ratio  
Dev/Ref, Fixed Range

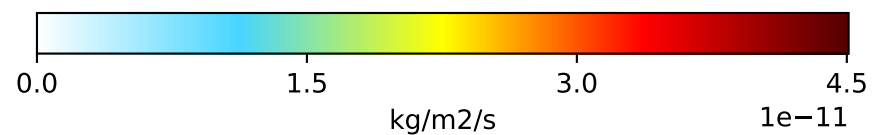
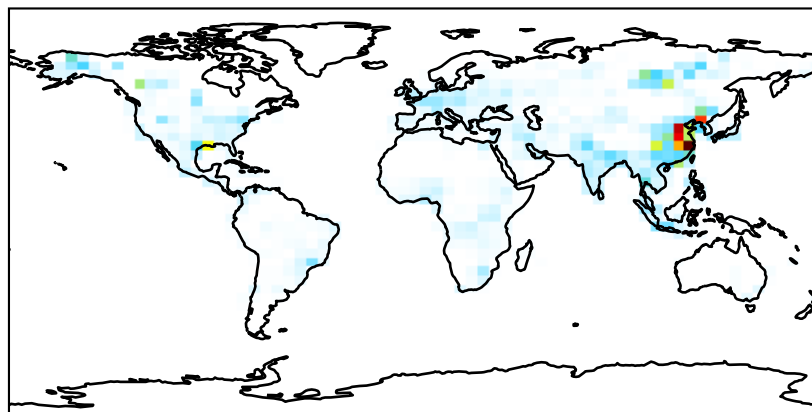


# EmisXYLE\_Total

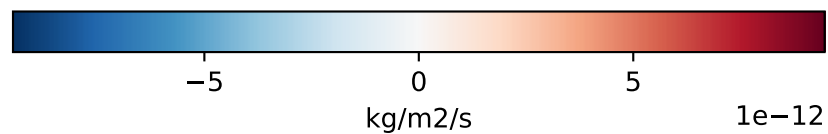
GCC\_13.1.0-beta.1 (Ref)  
4.0x5.0



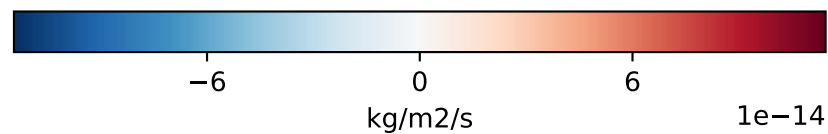
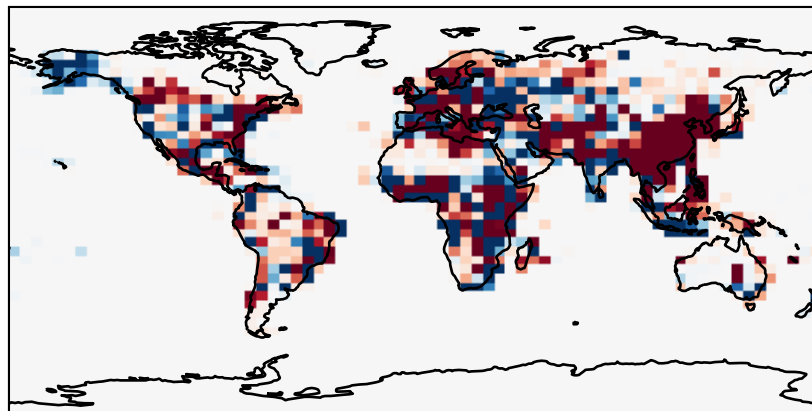
GCC\_13.2.0-beta.0 (Dev)  
4.0x5.0



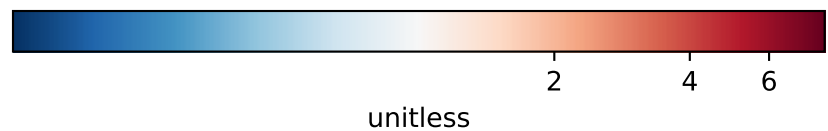
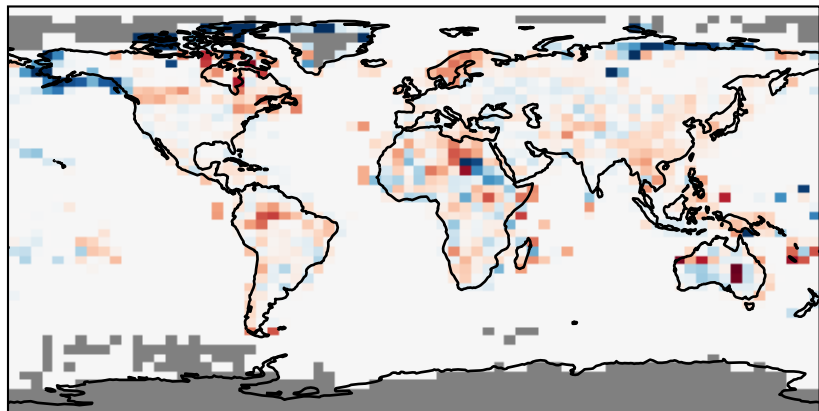
Difference  
Dev - Ref, Dynamic Range



Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

