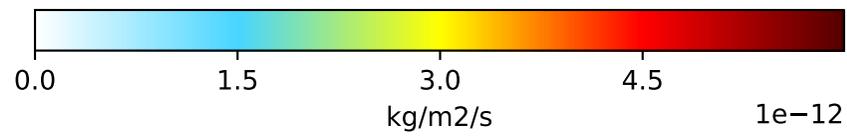
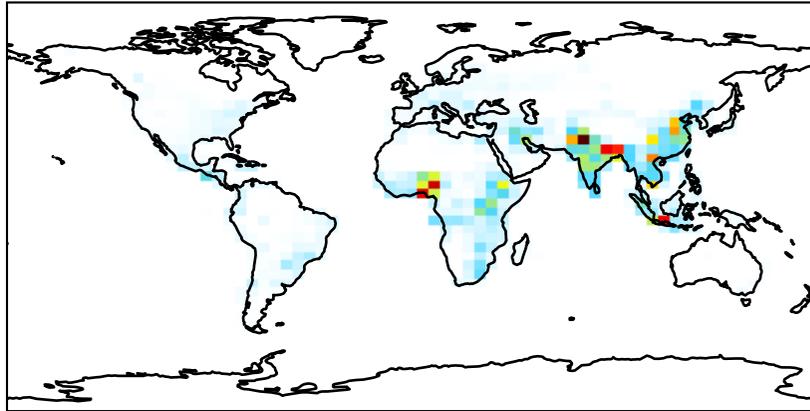
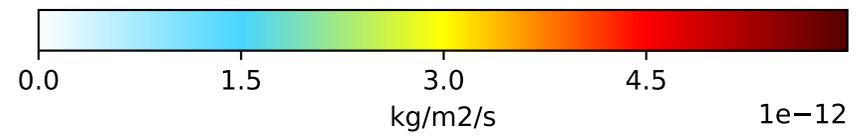
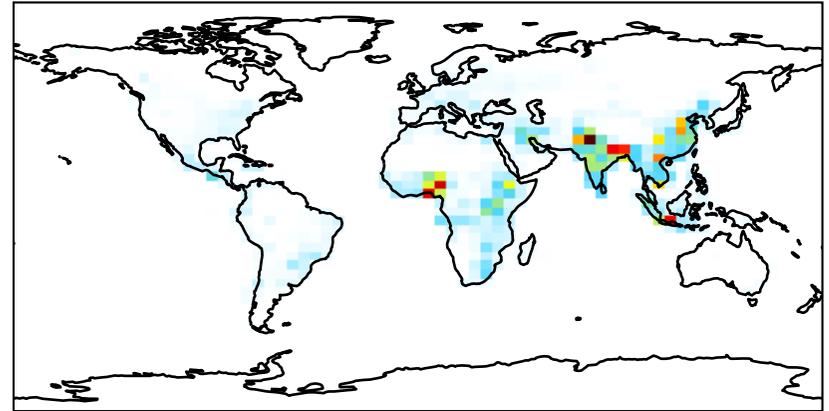


# EmisALD2\_Anthro

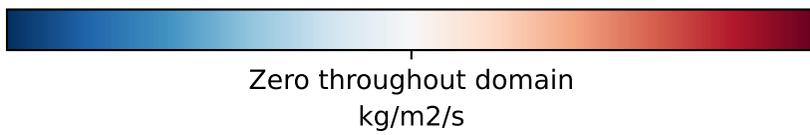
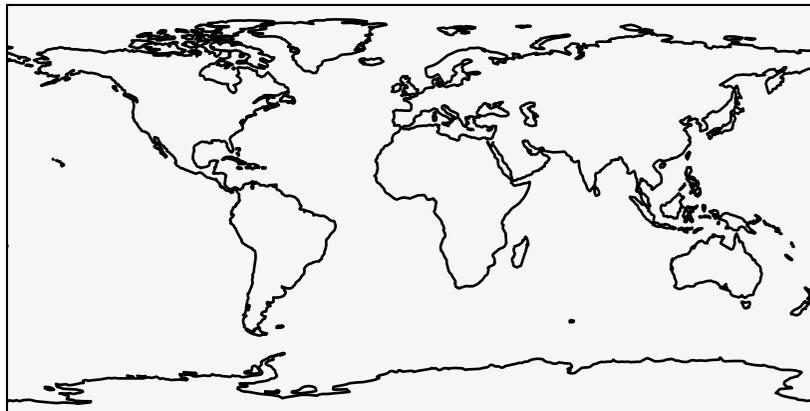
GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



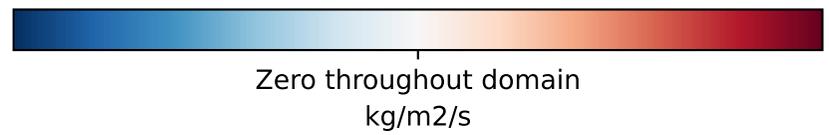
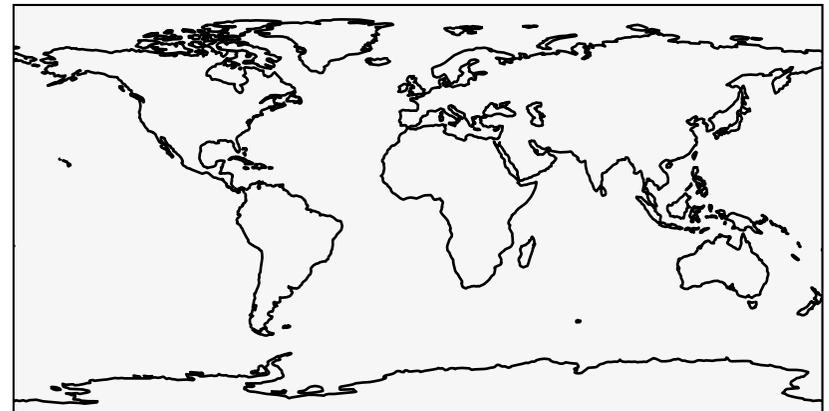
GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



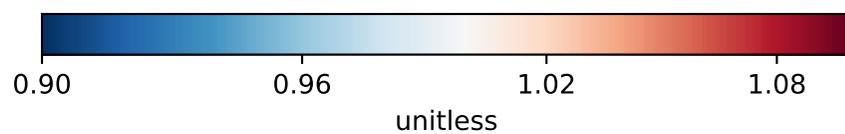
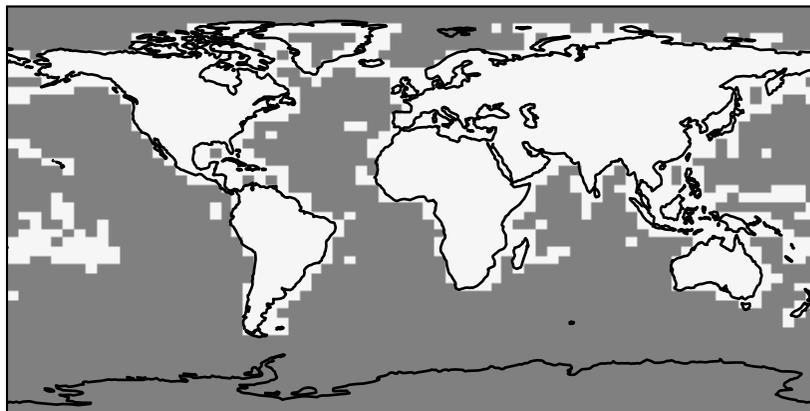
Difference  
Dev - Ref, Dynamic Range



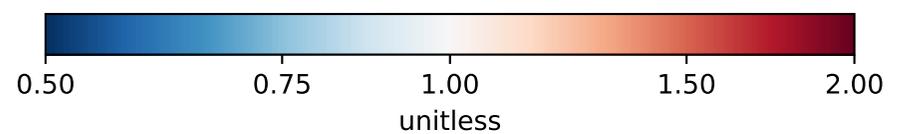
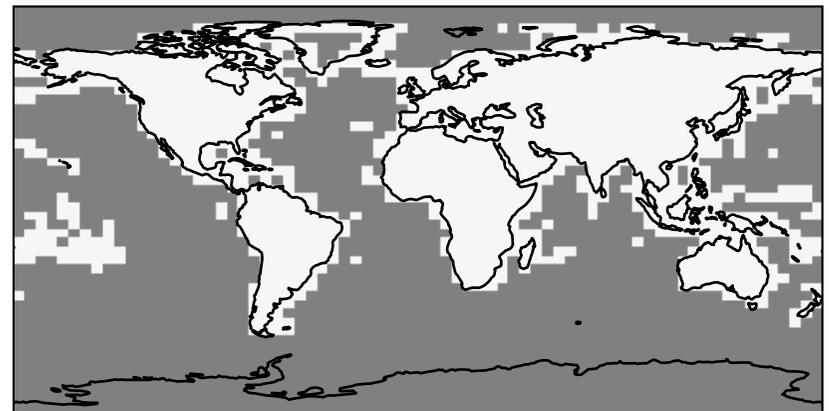
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

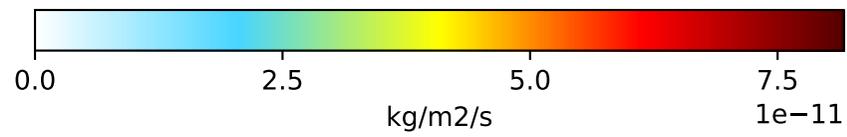
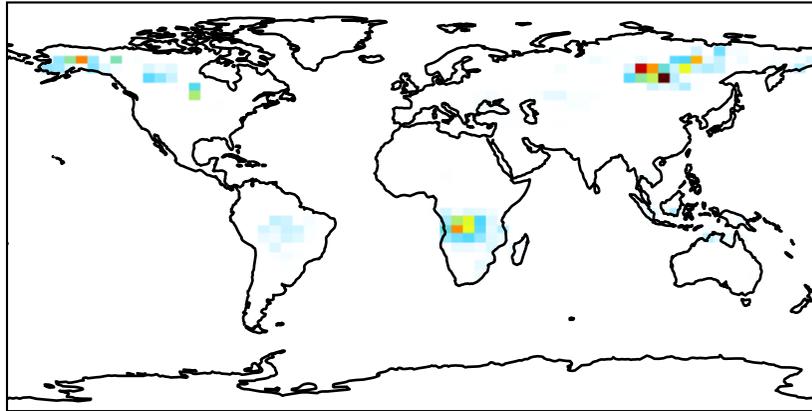


Ratio  
Dev/Ref, Fixed Range

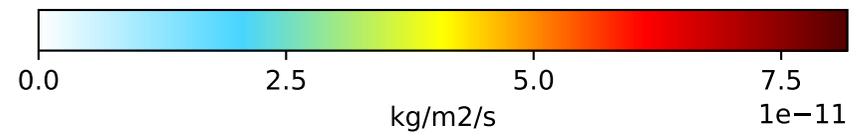
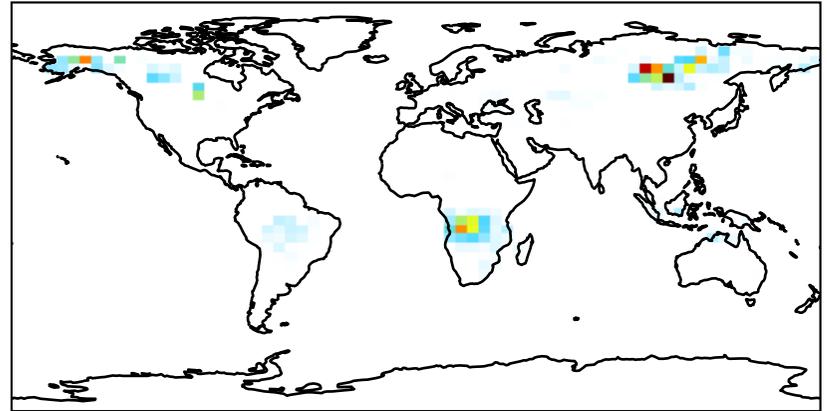


# EmisALD2\_BioBurn

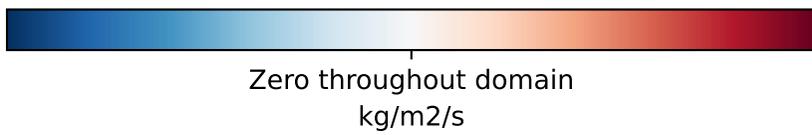
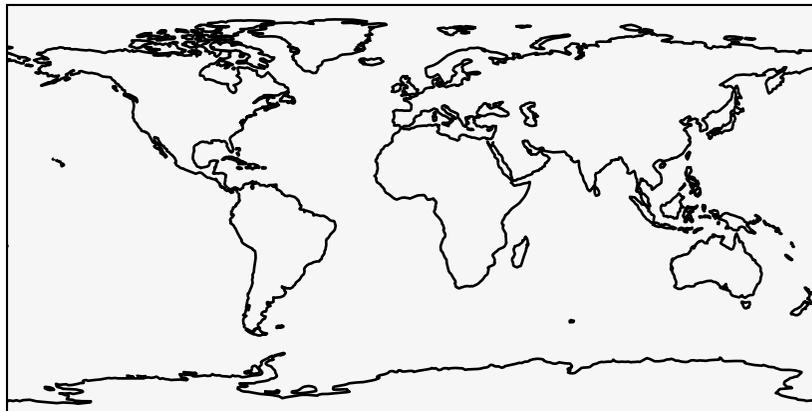
GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



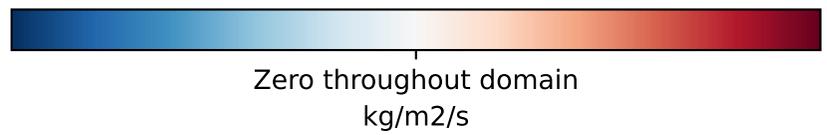
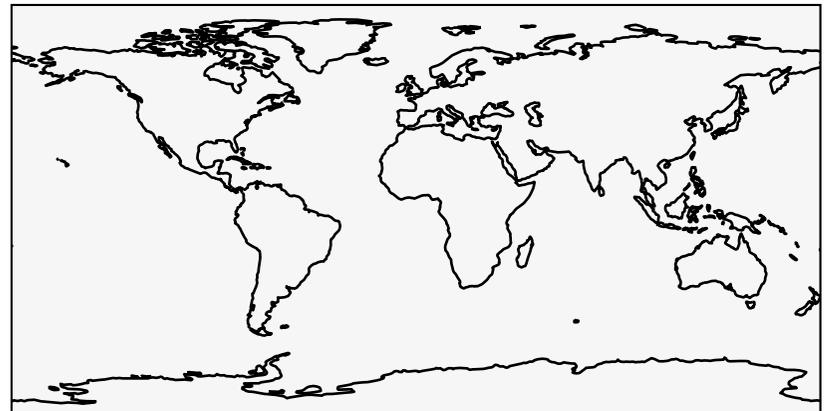
GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



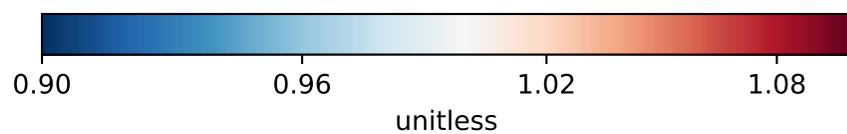
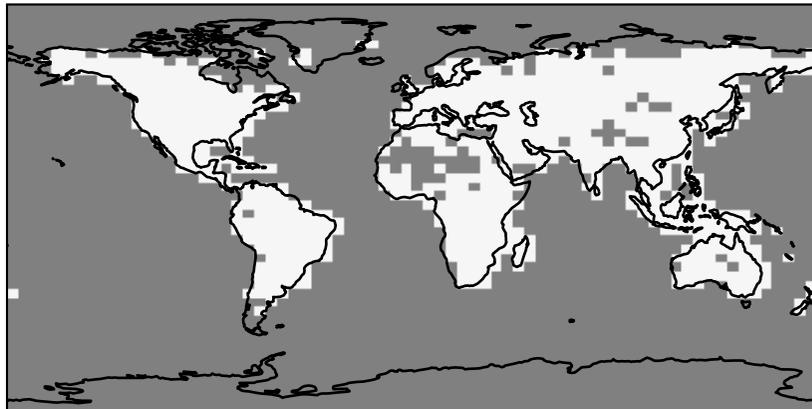
Difference  
Dev - Ref, Dynamic Range



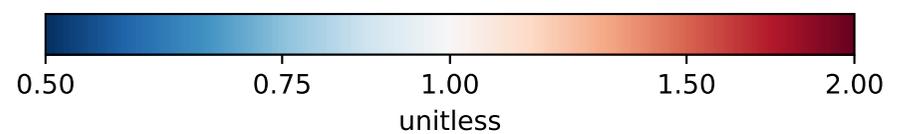
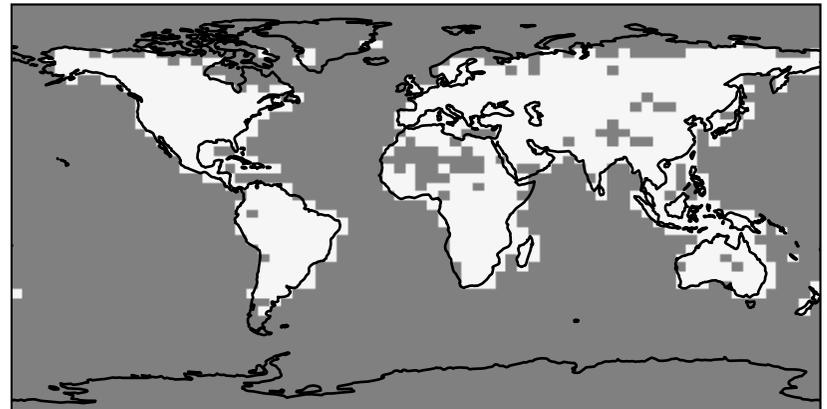
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

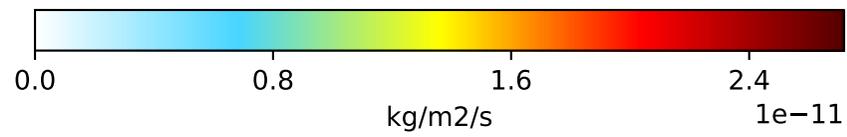
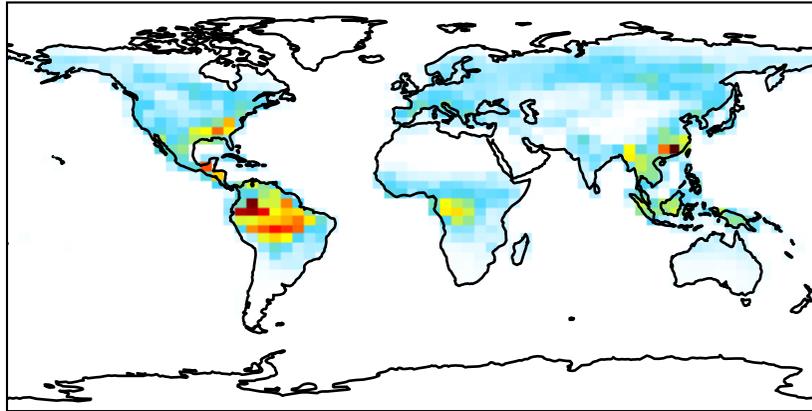


Ratio  
Dev/Ref, Fixed Range

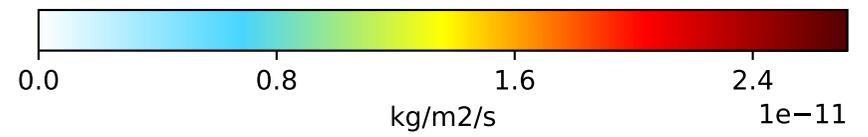
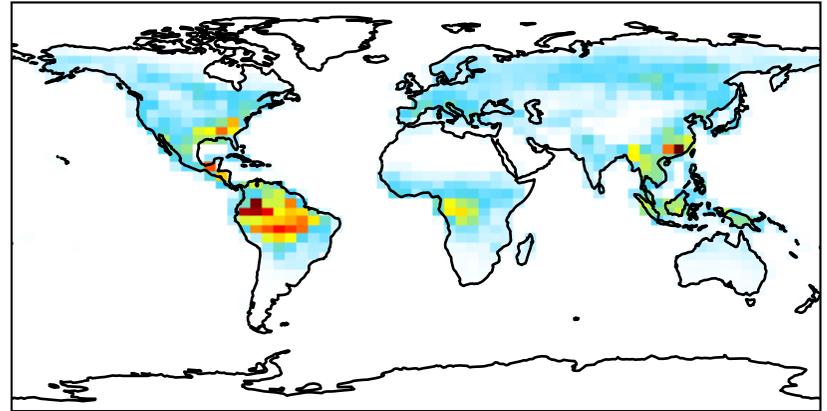


# EmisALD2\_Biogenic

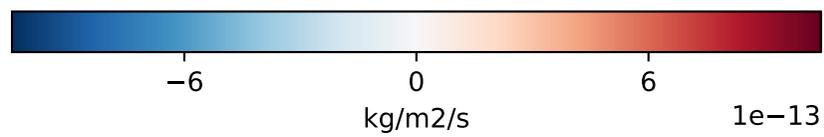
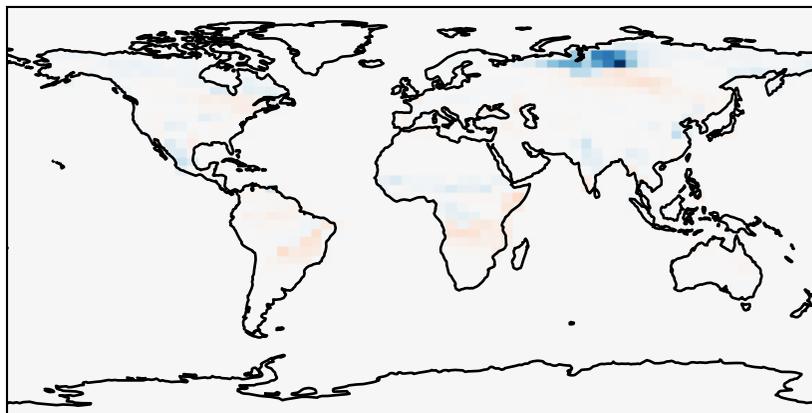
GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



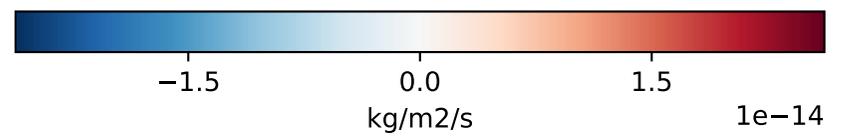
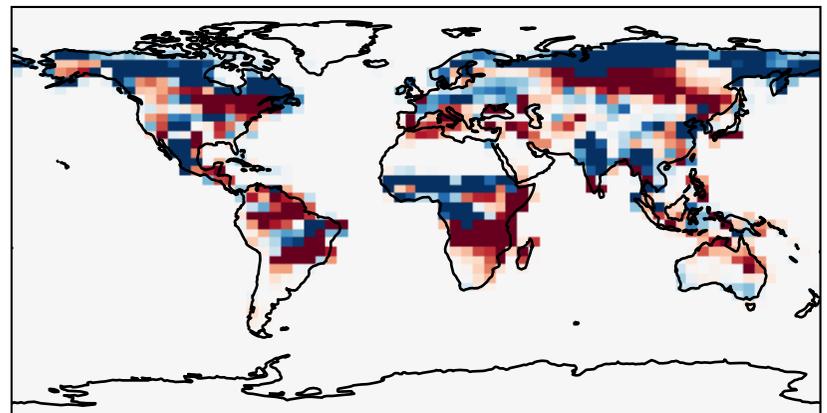
GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



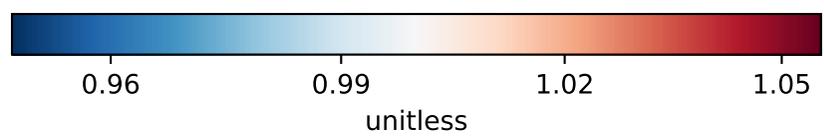
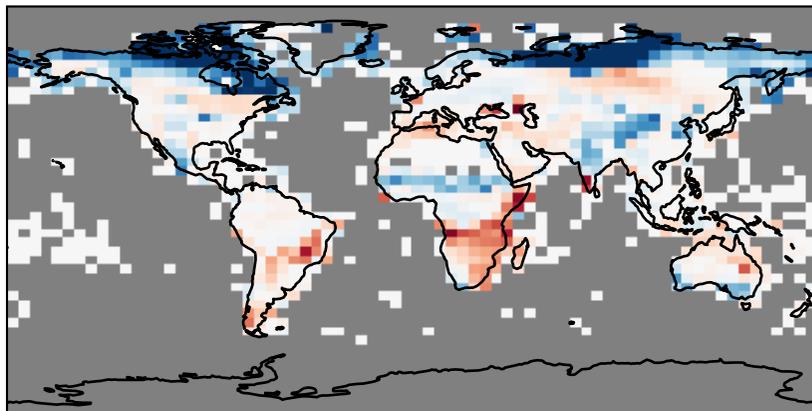
Difference  
Dev - Ref, Dynamic Range



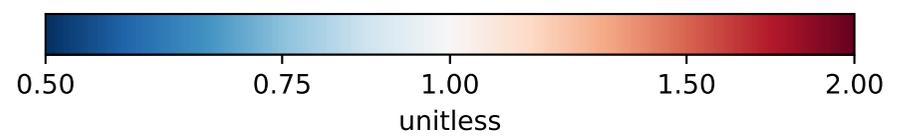
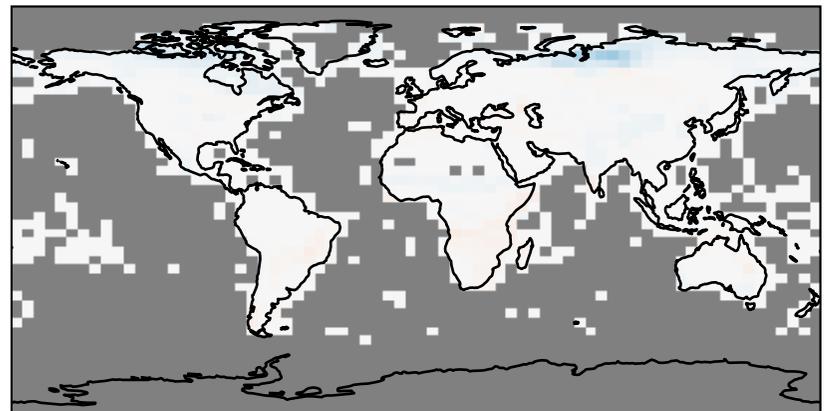
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

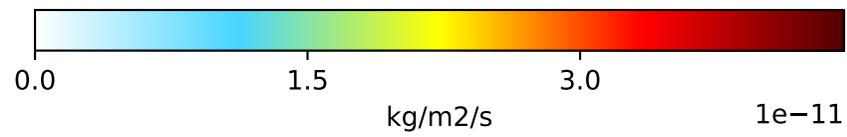
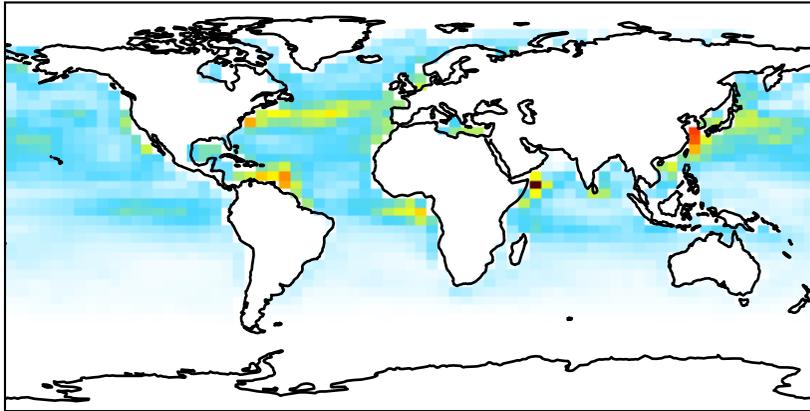


Ratio  
Dev/Ref, Fixed Range

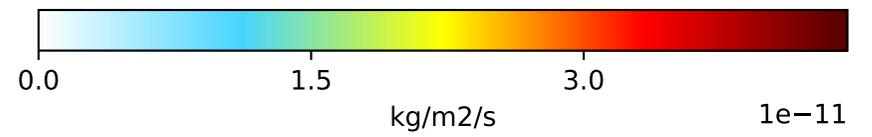
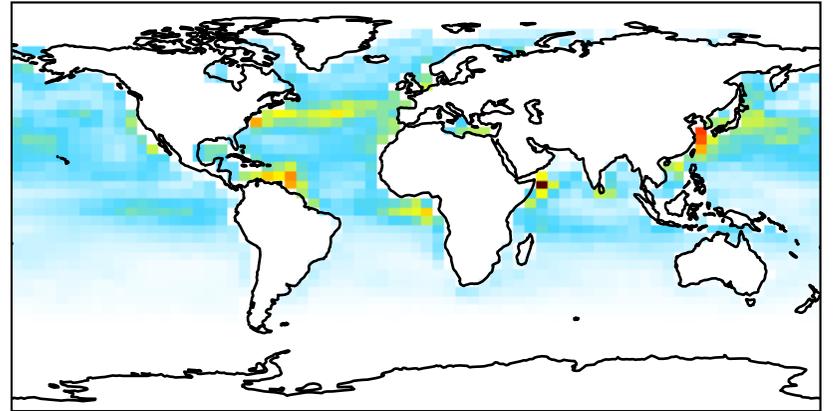


# EmisALD2\_Ocean

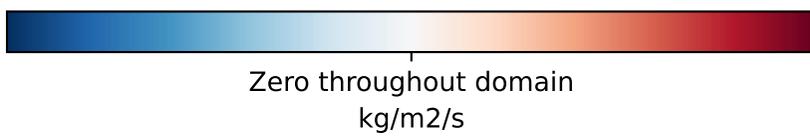
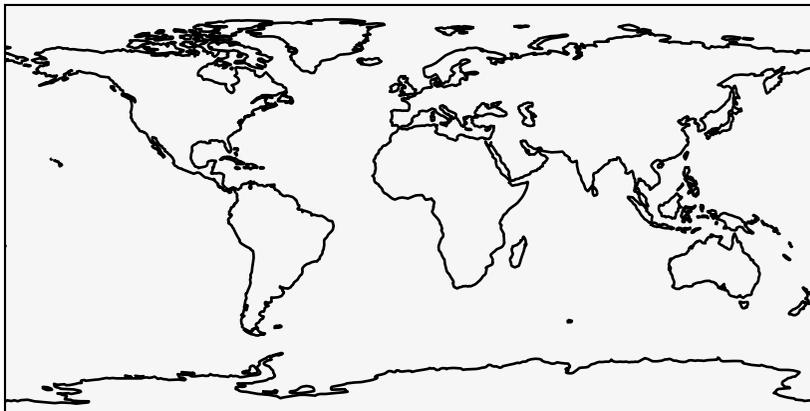
GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



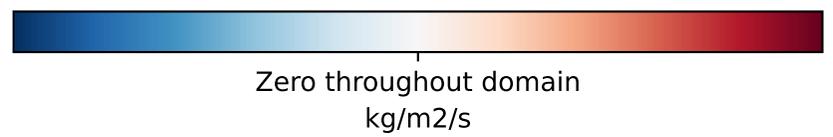
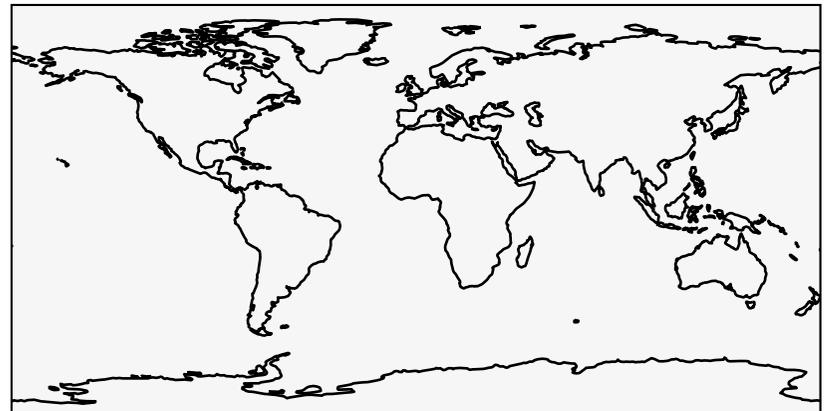
GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



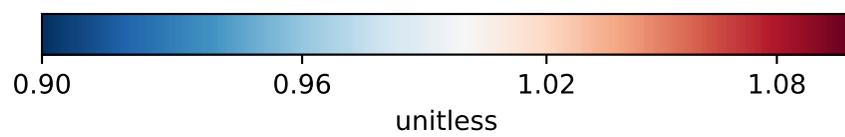
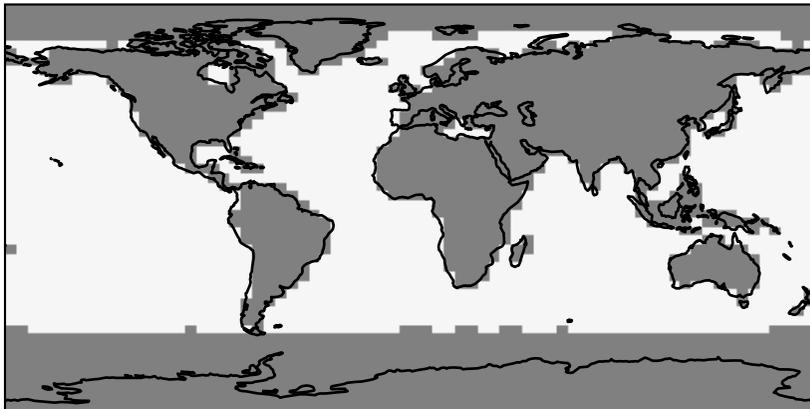
Difference  
Dev - Ref, Dynamic Range



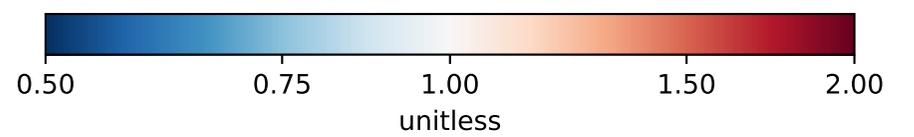
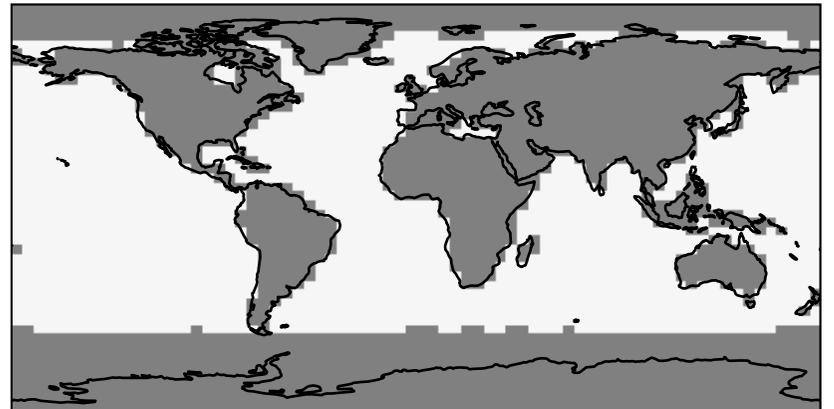
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

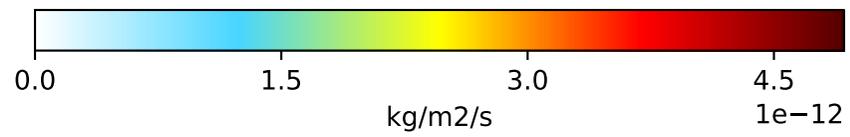
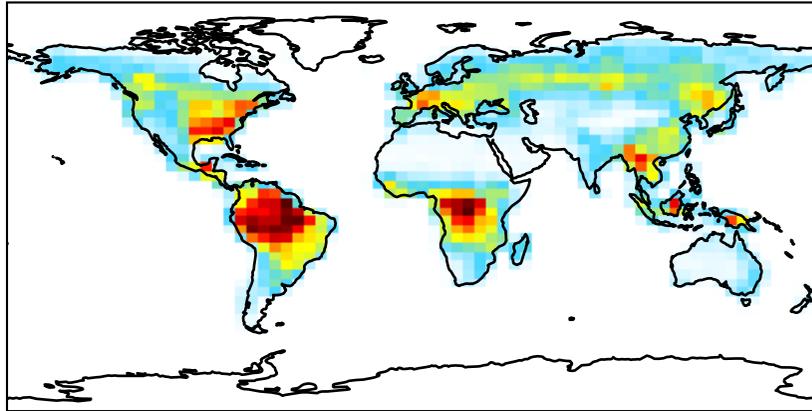


Ratio  
Dev/Ref, Fixed Range

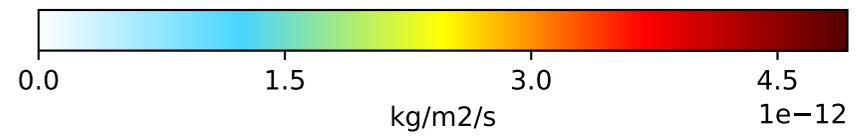
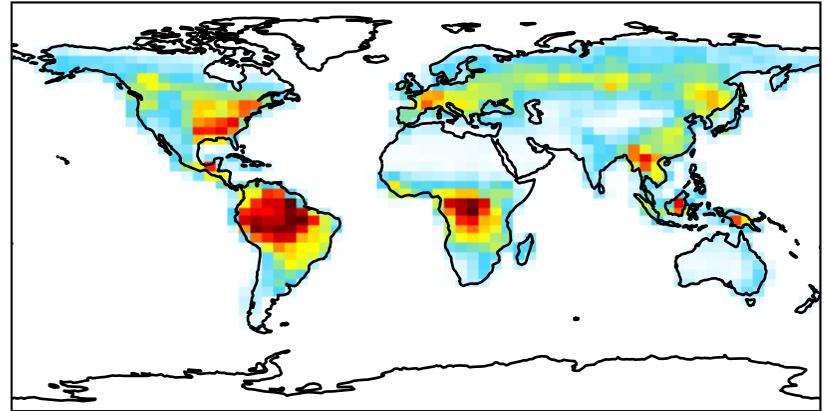


# EmisALD2\_PlantDecay

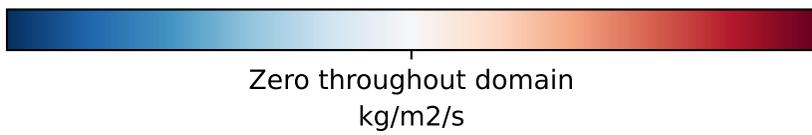
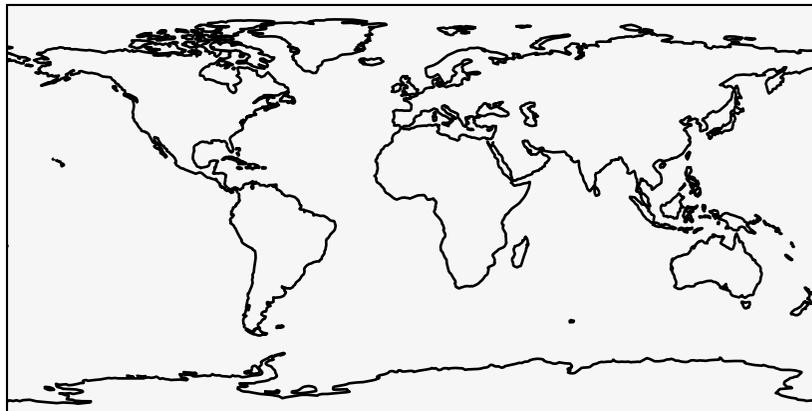
GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



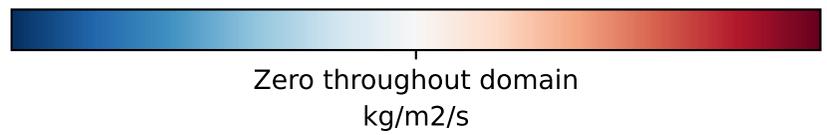
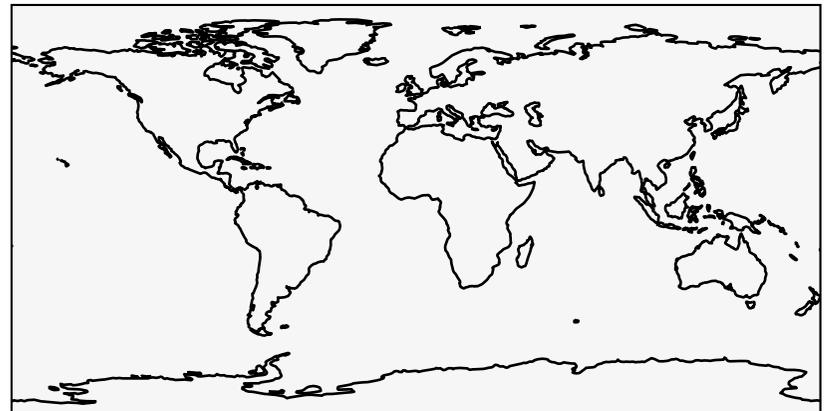
GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



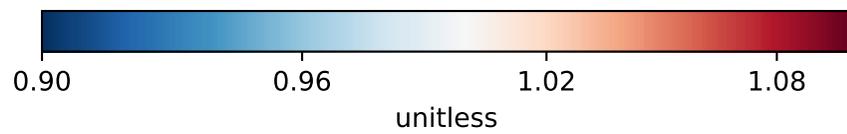
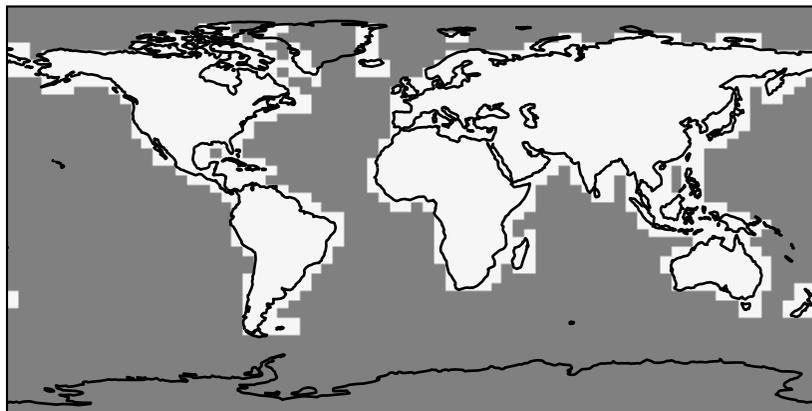
Difference  
Dev - Ref, Dynamic Range



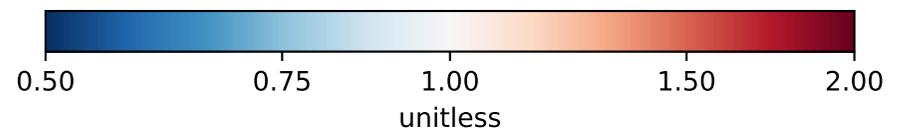
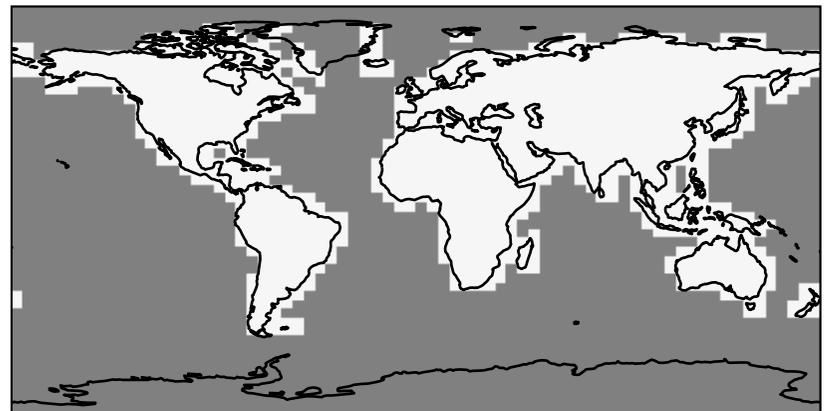
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

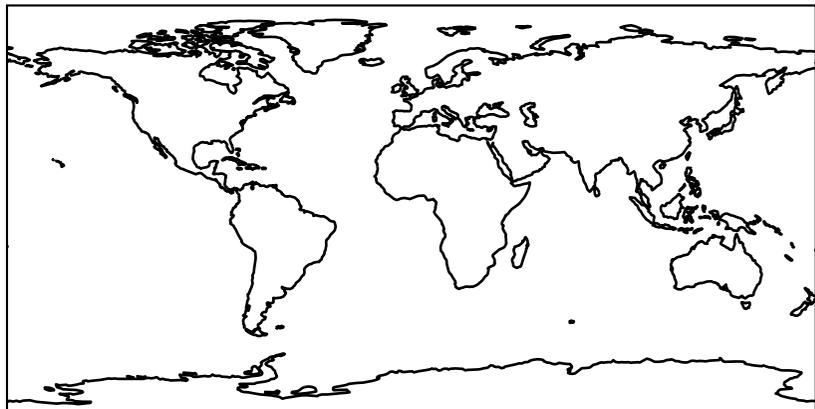


Ratio  
Dev/Ref, Fixed Range



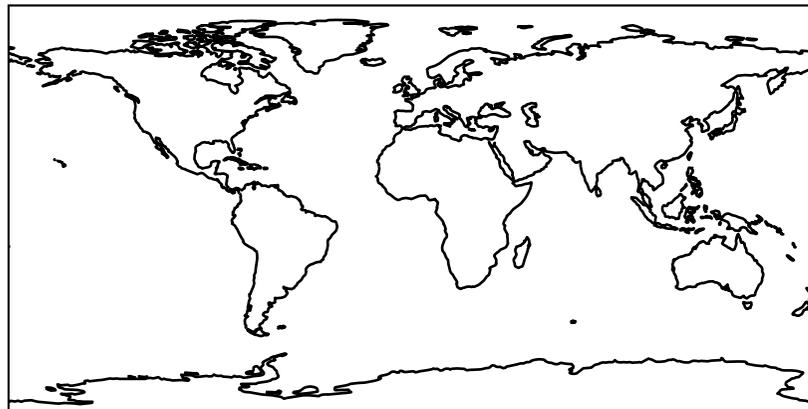
# EmisALD2\_Ship

GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



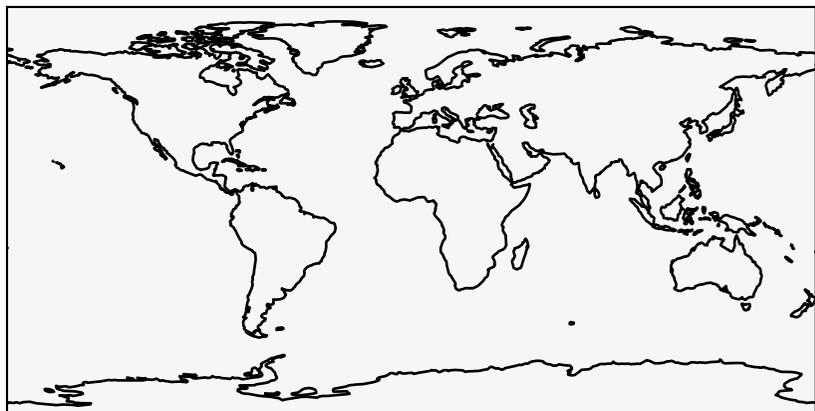
Zero throughout domain  
kg/m2/s

GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



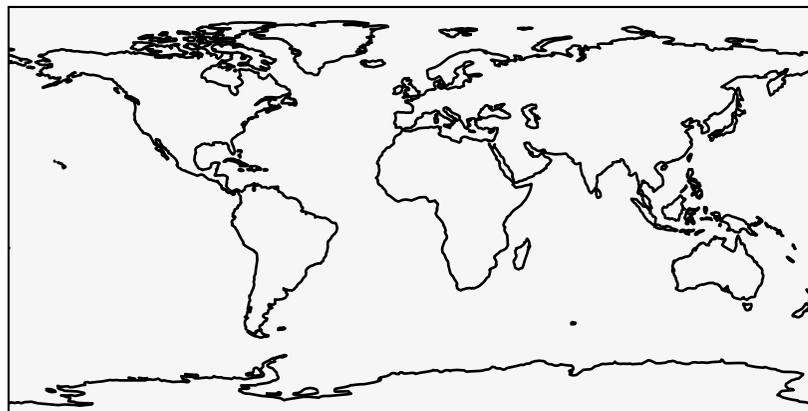
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Dynamic Range



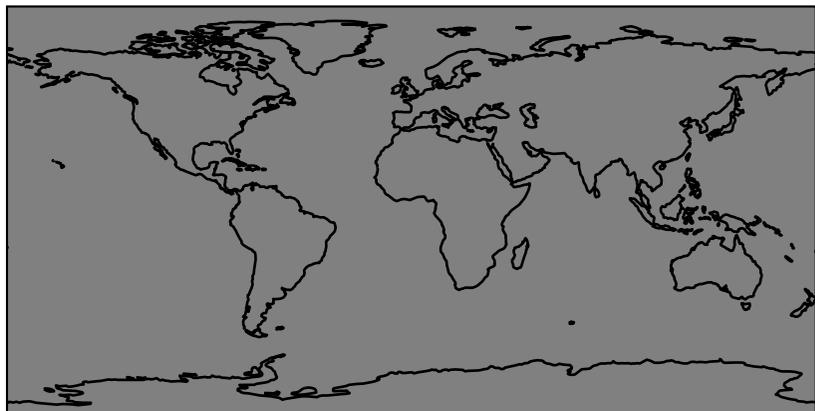
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



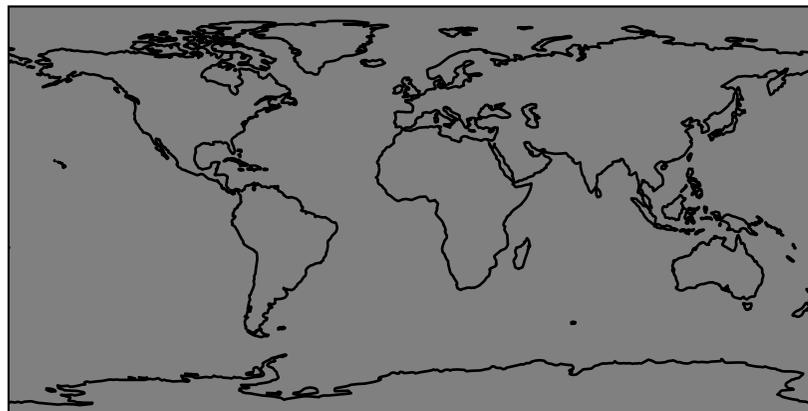
Zero throughout domain  
kg/m2/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

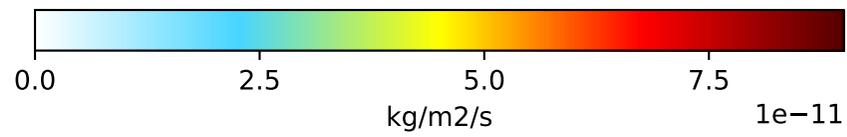
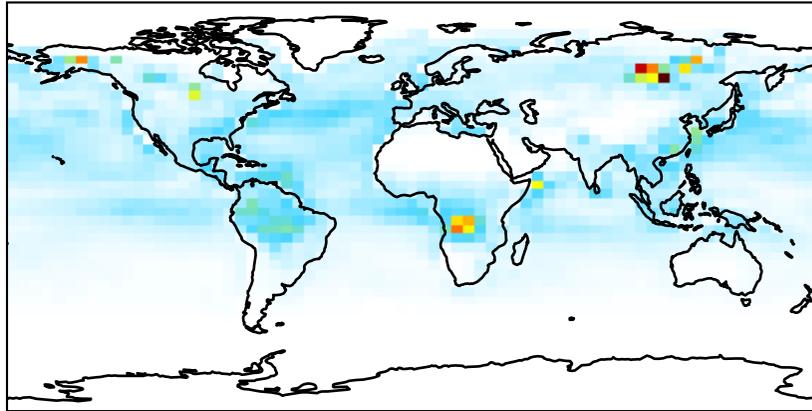
Ratio  
Dev/Ref, Fixed Range



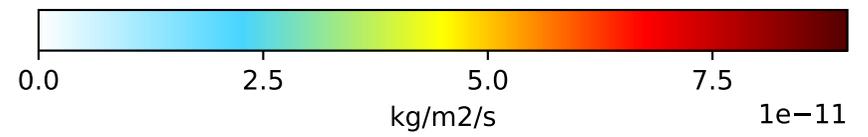
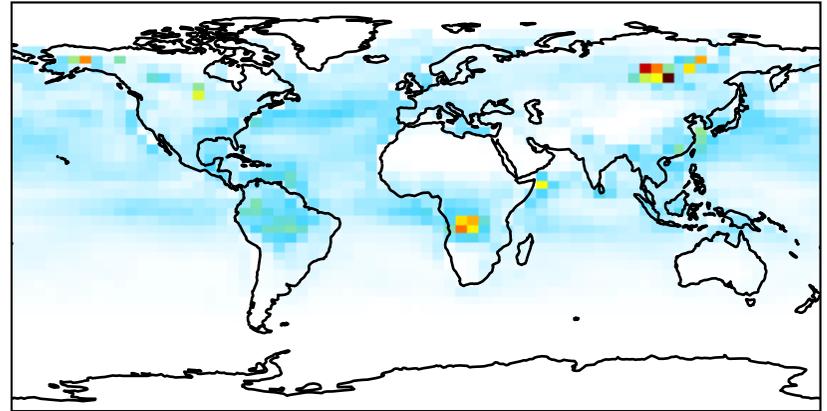
Undefined throughout domain  
unitless

# EmisALD2\_Total

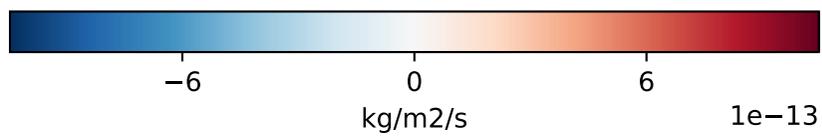
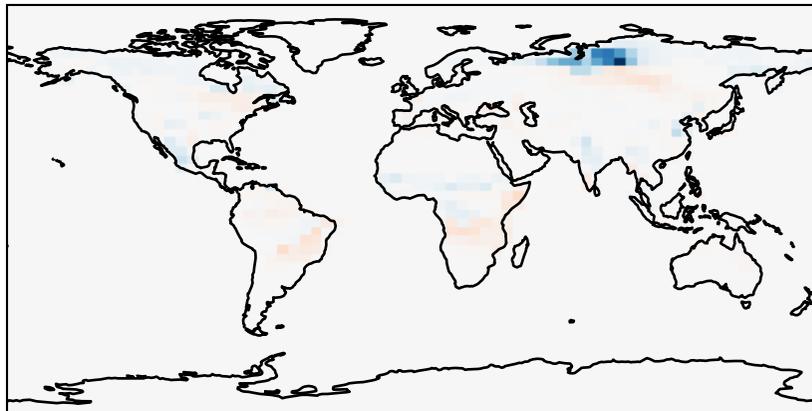
GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



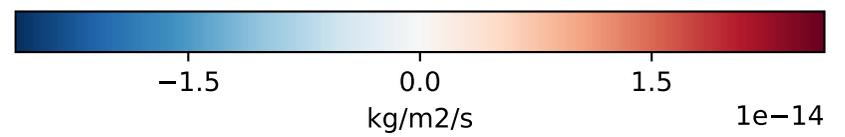
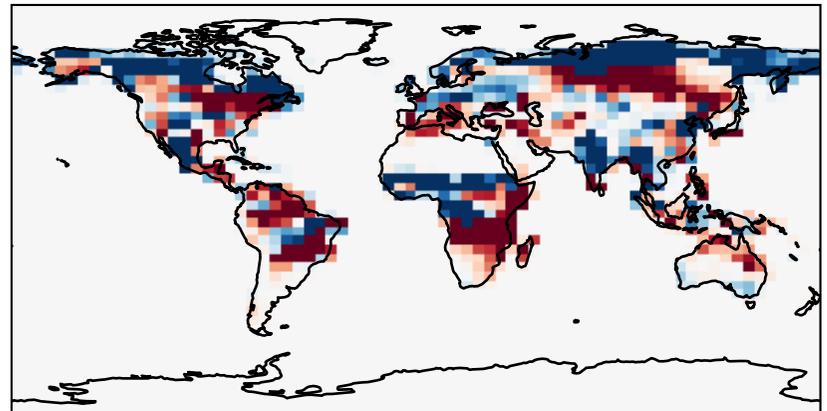
GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



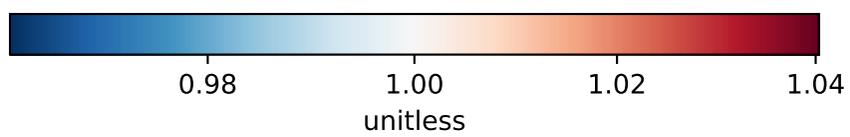
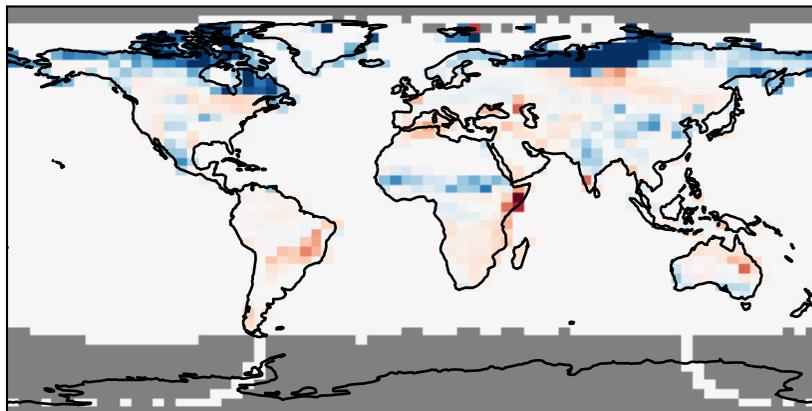
Difference  
Dev - Ref, Dynamic Range



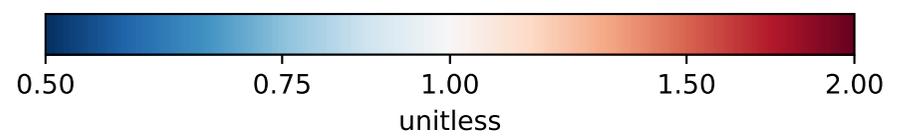
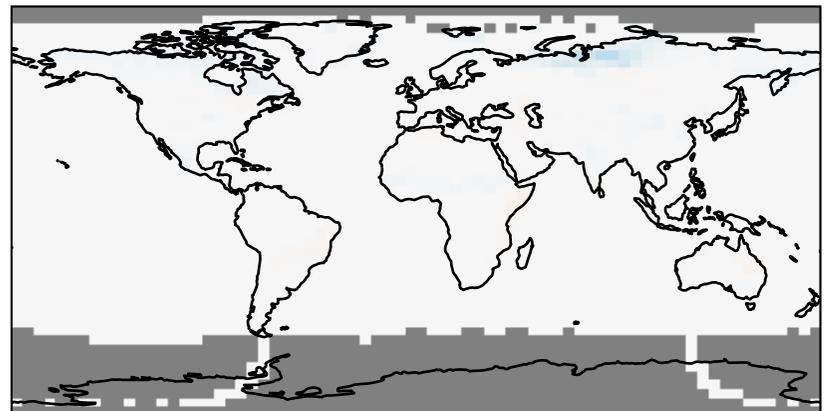
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

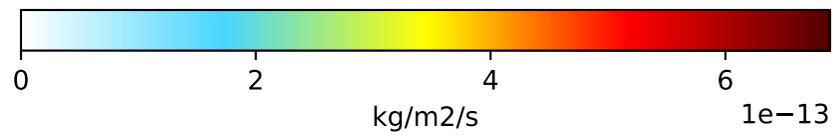
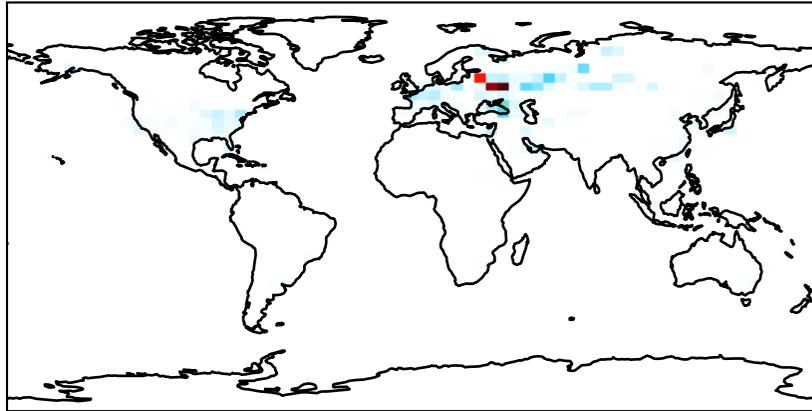


Ratio  
Dev/Ref, Fixed Range

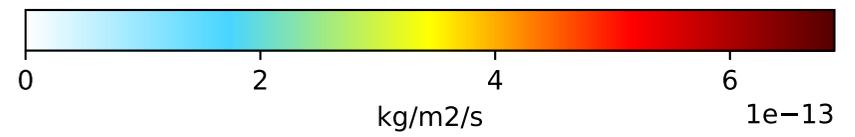
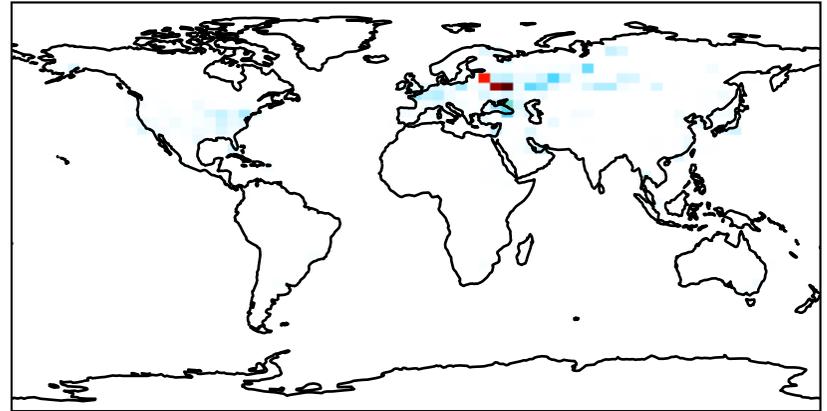


# EmisCH2O\_Aircraft

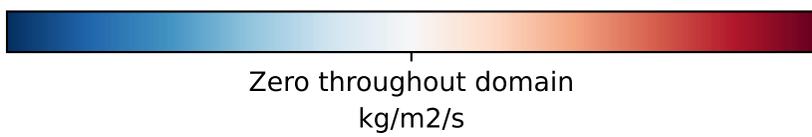
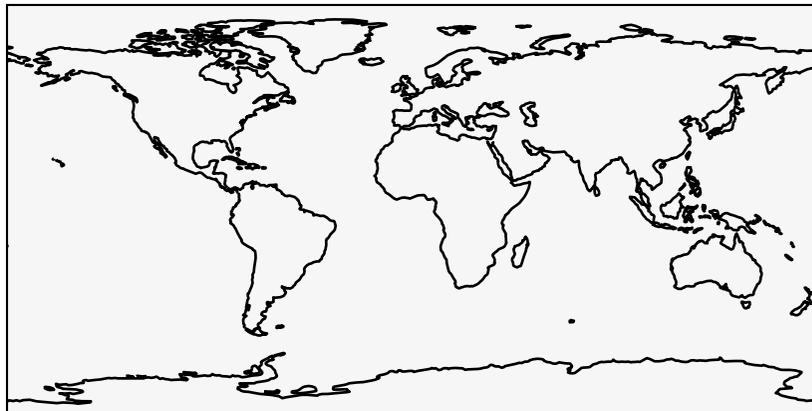
GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



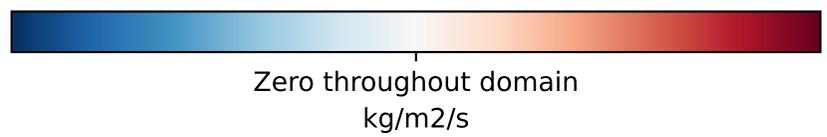
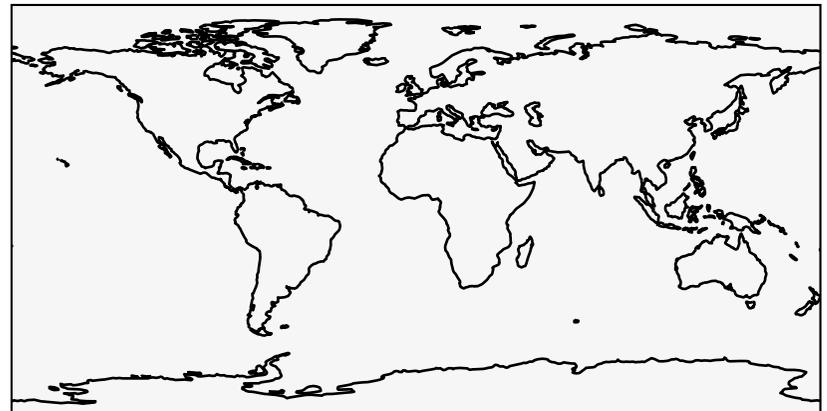
GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



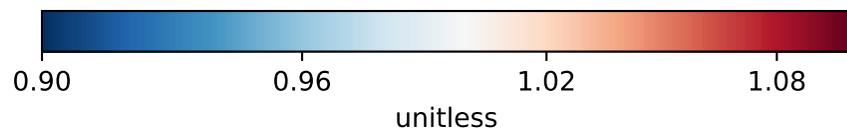
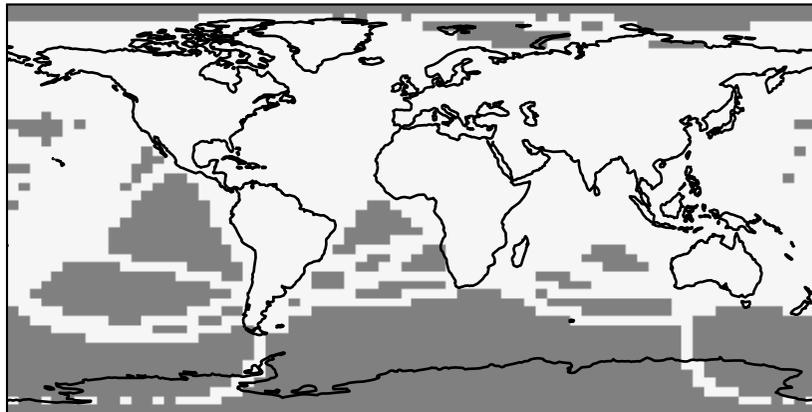
Difference  
Dev - Ref, Dynamic Range



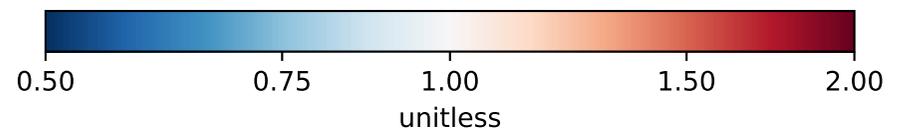
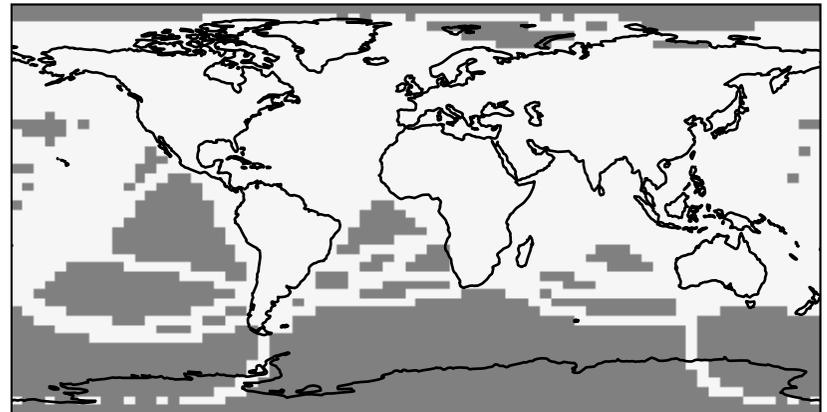
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

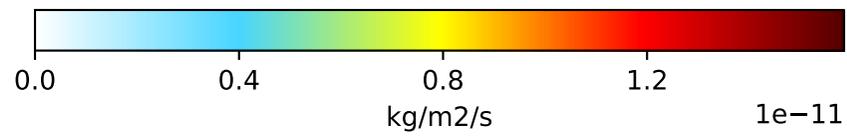
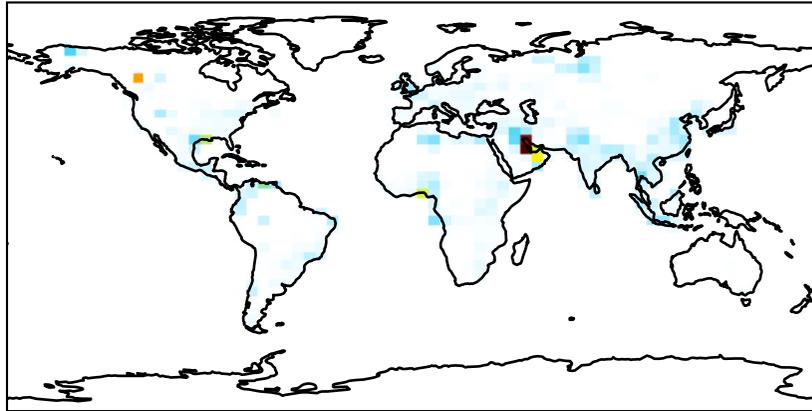


Ratio  
Dev/Ref, Fixed Range

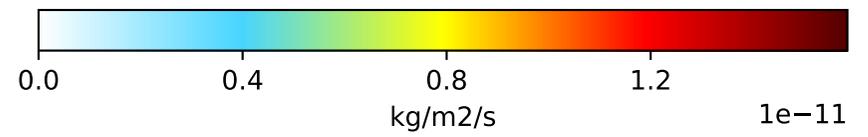
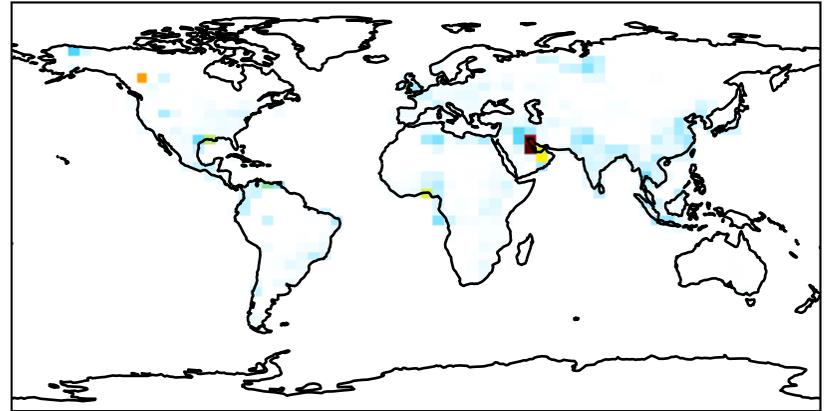


# EmisCH2O\_Anthro

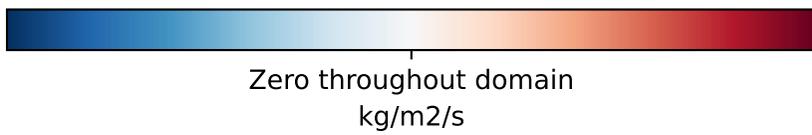
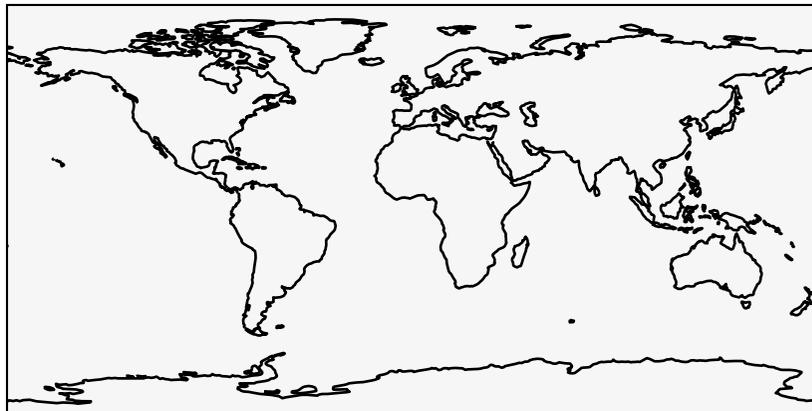
GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



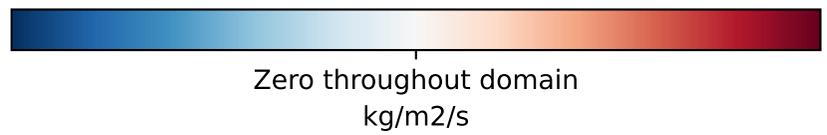
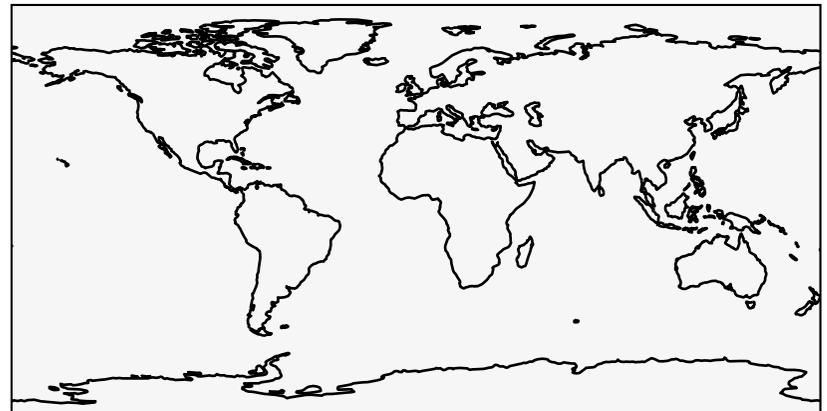
GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



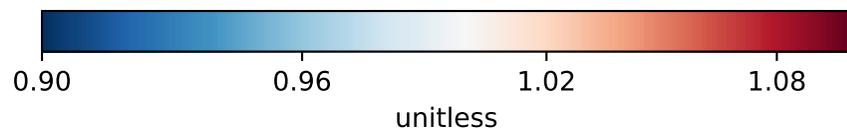
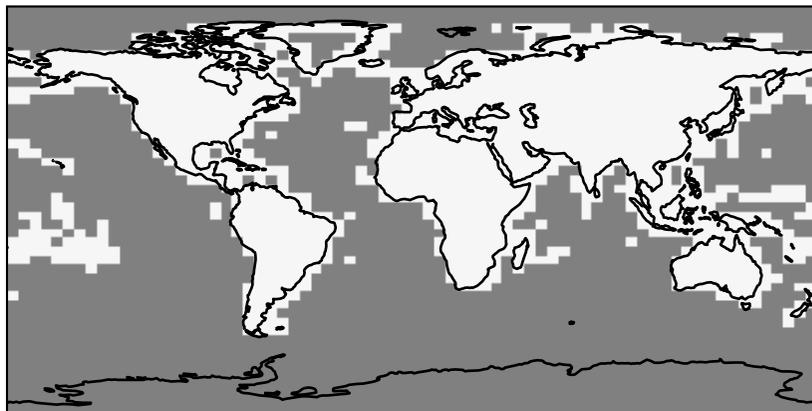
Difference  
Dev - Ref, Dynamic Range



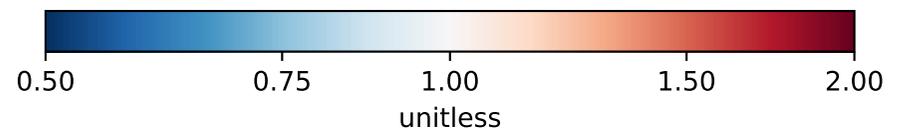
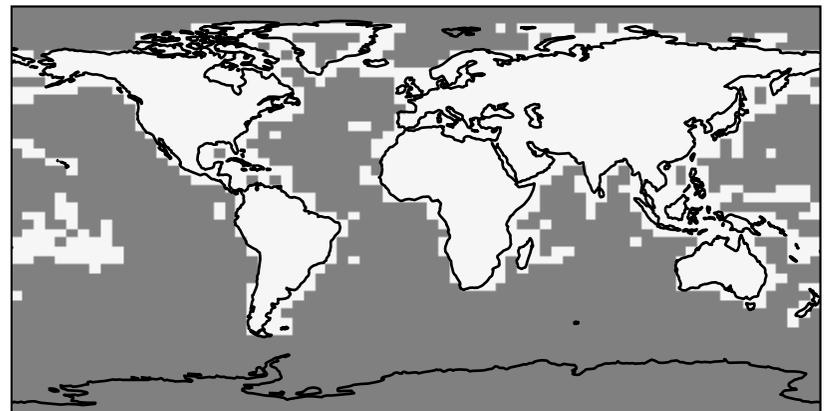
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

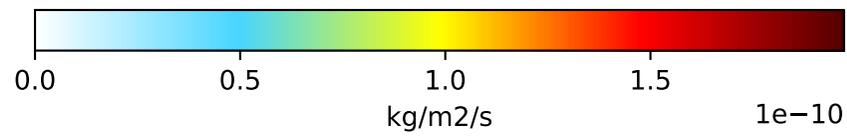
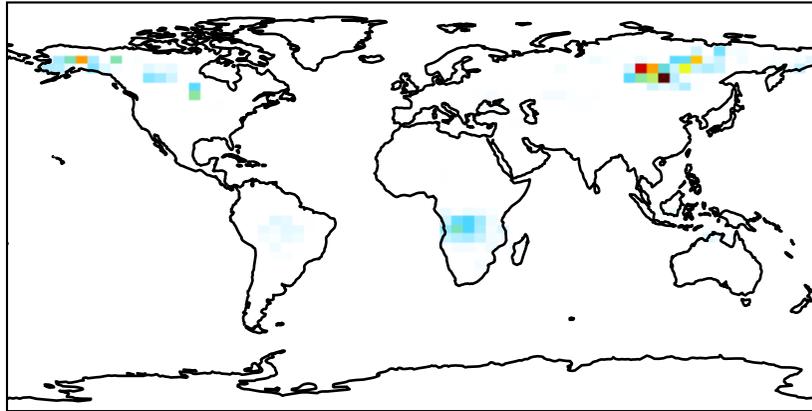


Ratio  
Dev/Ref, Fixed Range

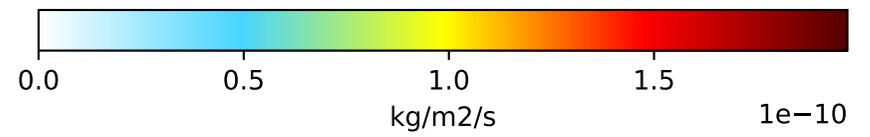
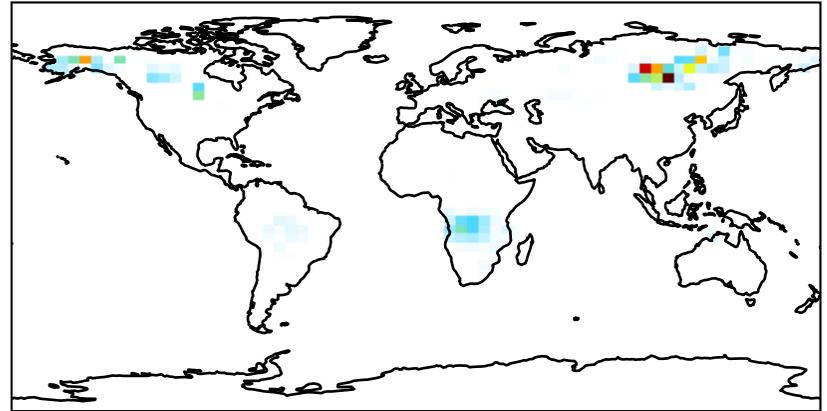


# EmisCH2O\_BioBurn

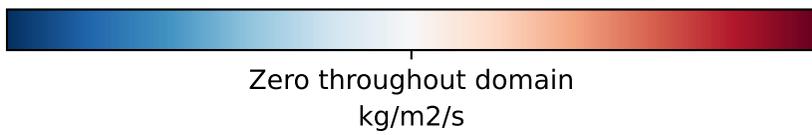
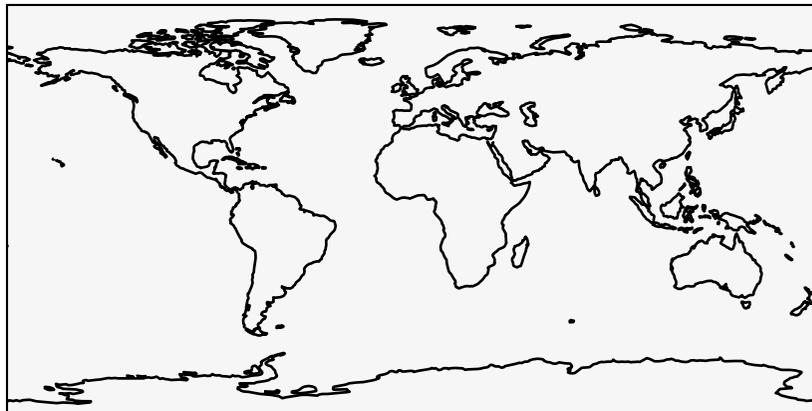
GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



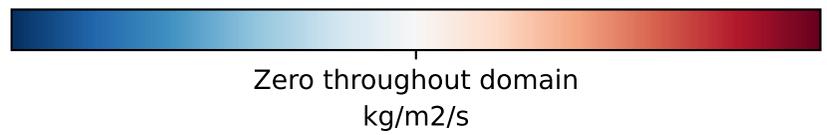
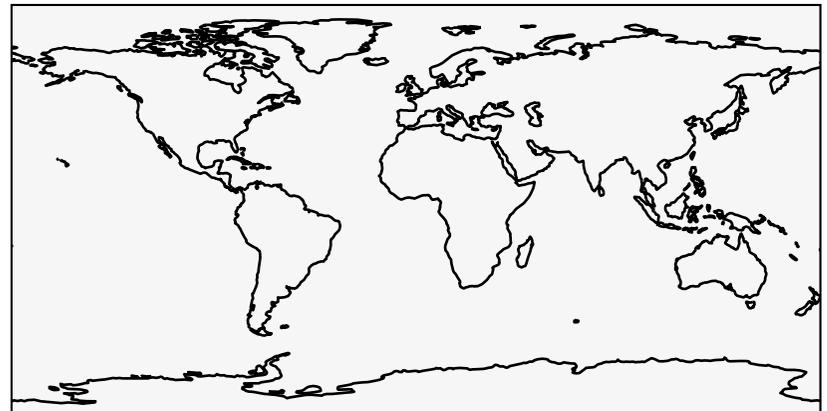
GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



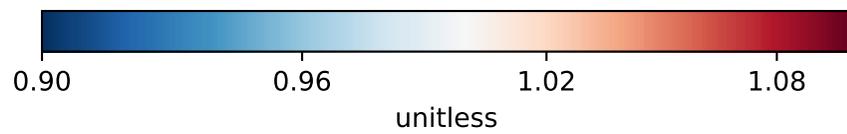
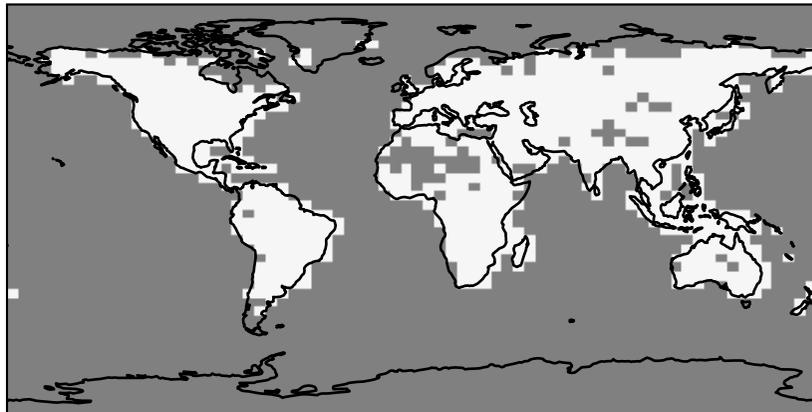
Difference  
Dev - Ref, Dynamic Range



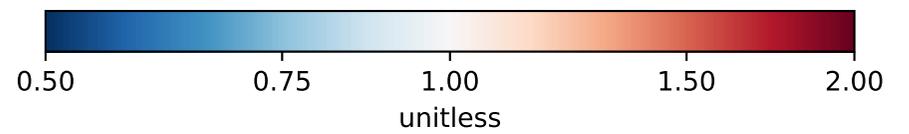
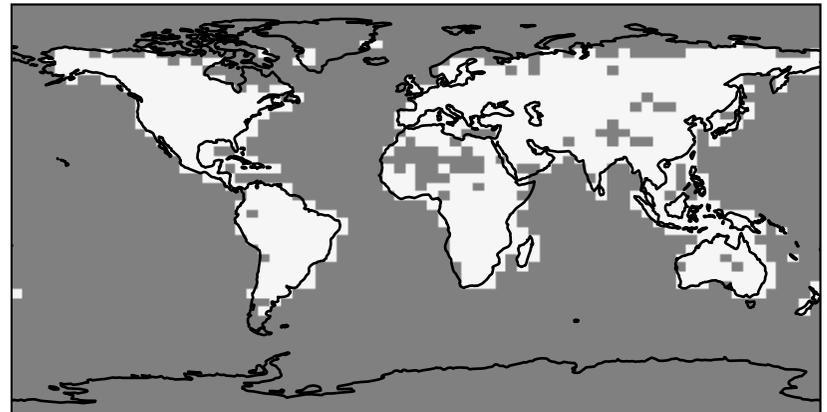
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

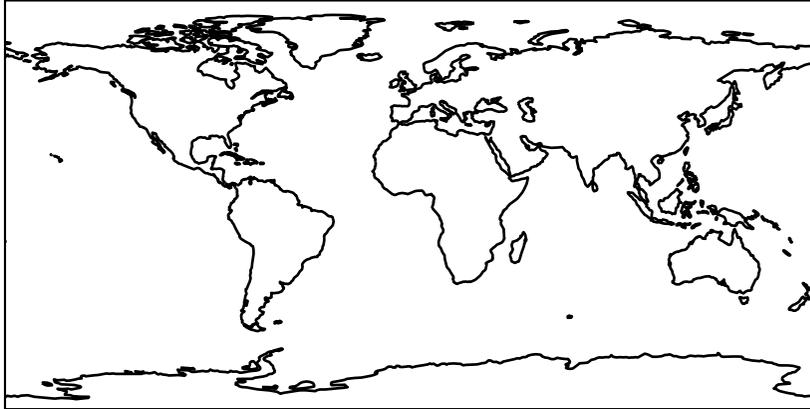


Ratio  
Dev/Ref, Fixed Range



# EmisCH2O\_Ship

GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



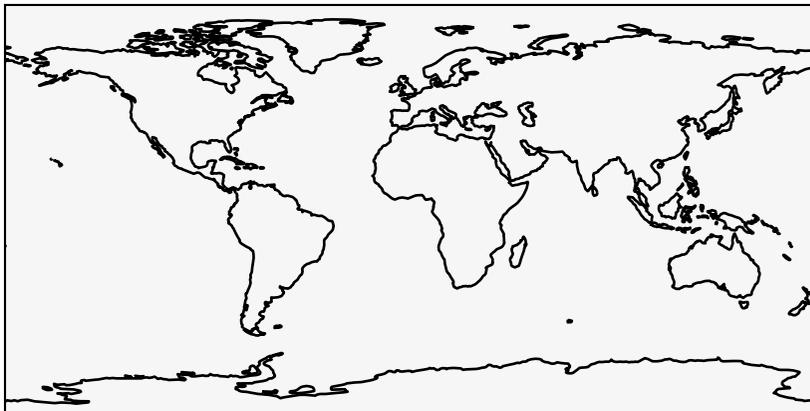
Zero throughout domain  
kg/m2/s

GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



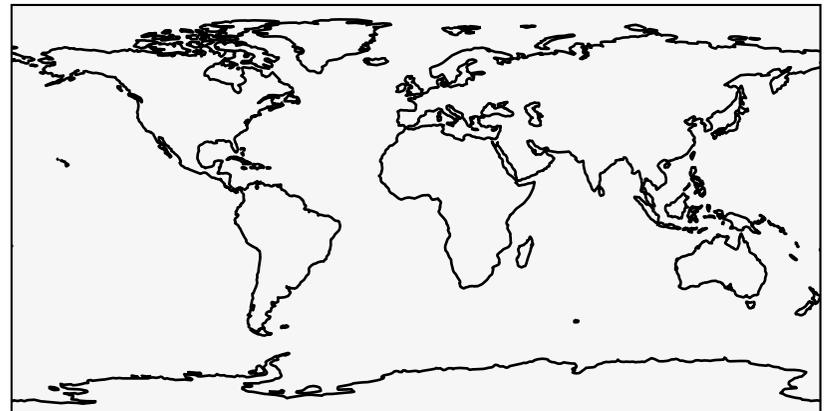
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Dynamic Range



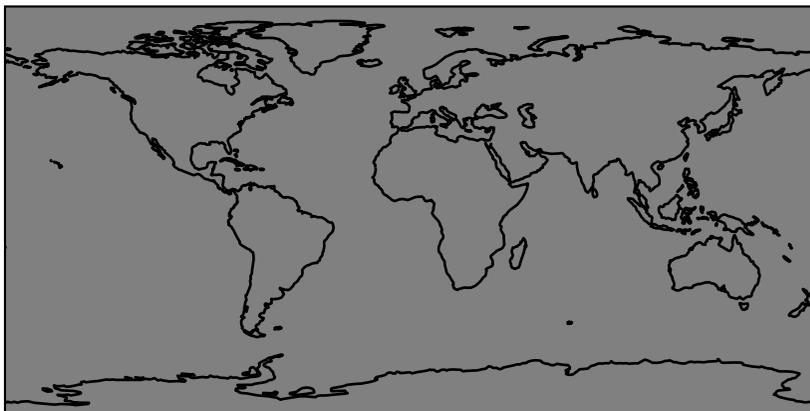
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



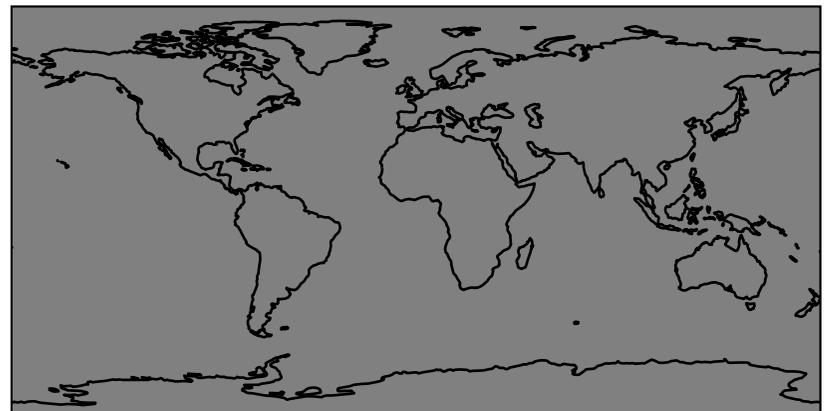
Zero throughout domain  
kg/m2/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

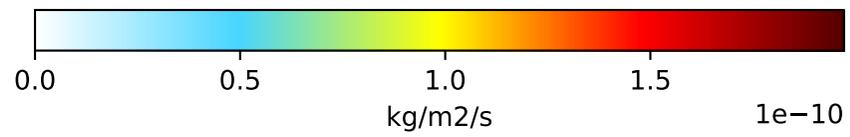
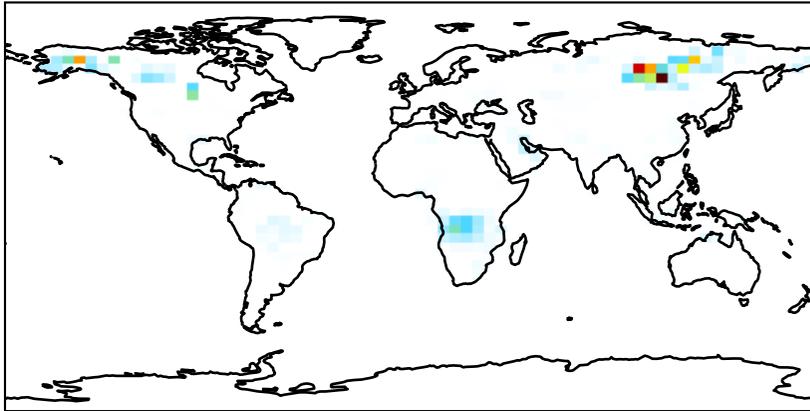
Ratio  
Dev/Ref, Fixed Range



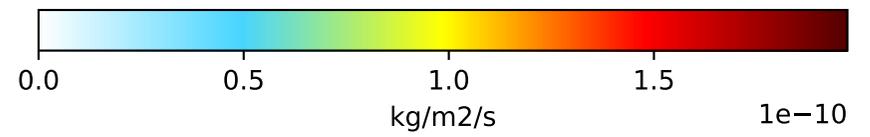
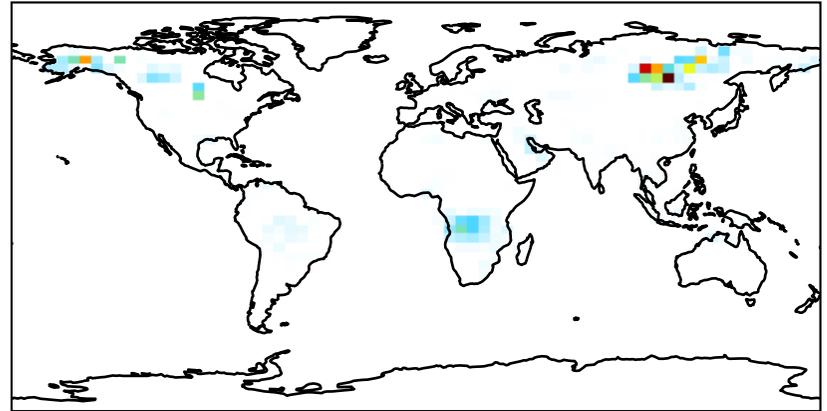
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unitless

# EmisCH2O\_Total

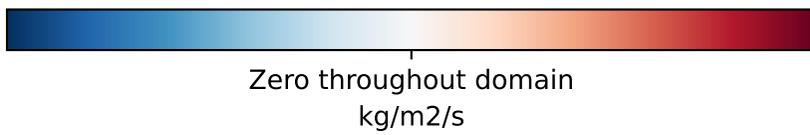
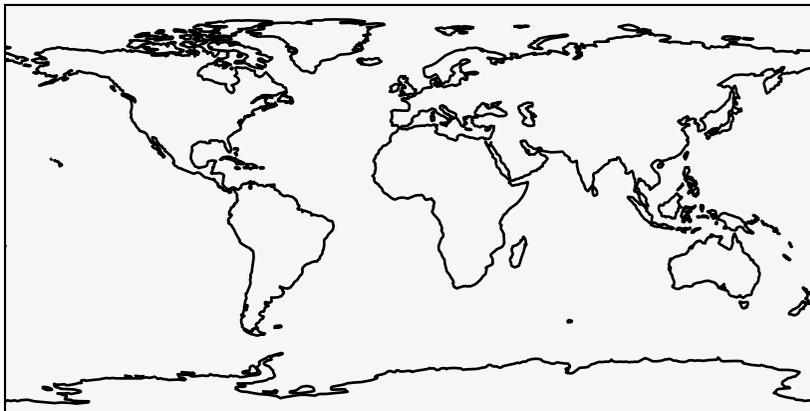
GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



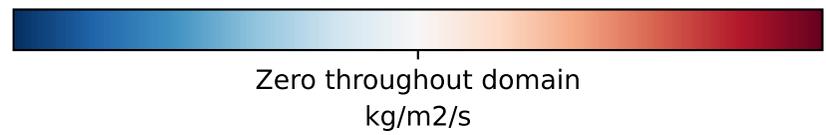
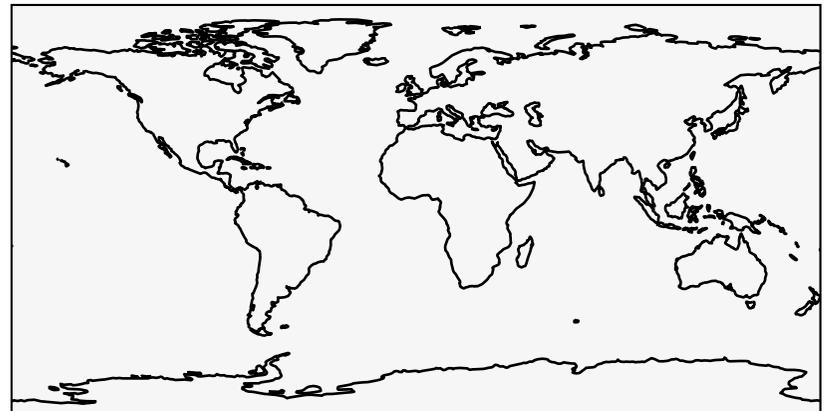
GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



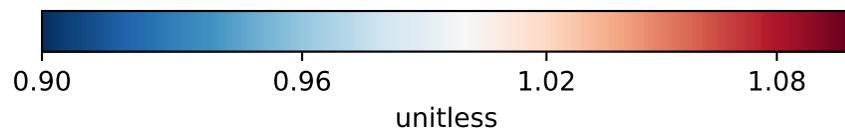
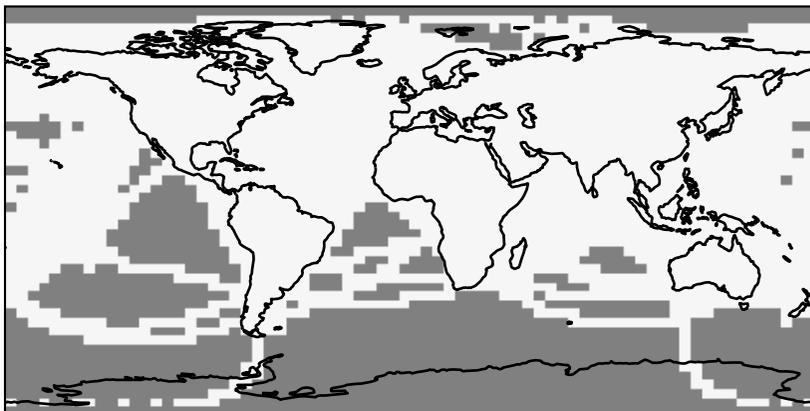
Difference  
Dev - Ref, Dynamic Range



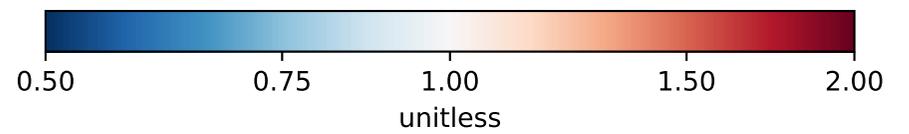
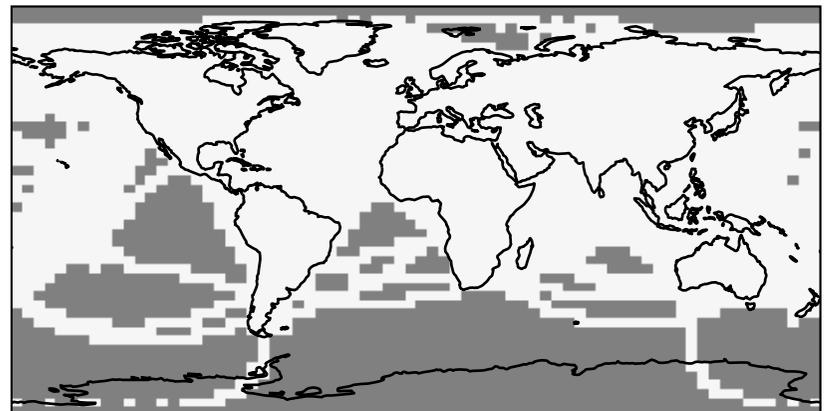
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

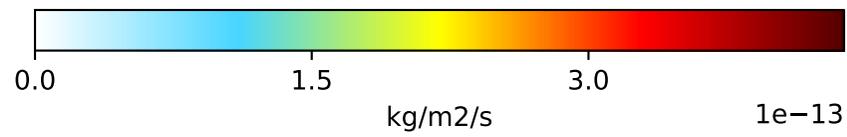
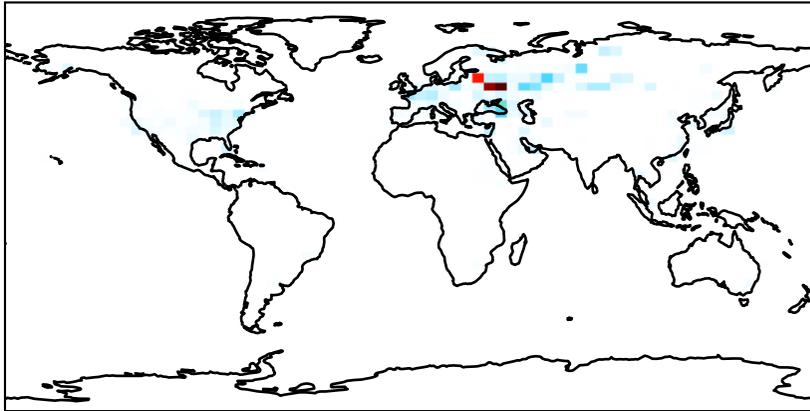


Ratio  
Dev/Ref, Fixed Range

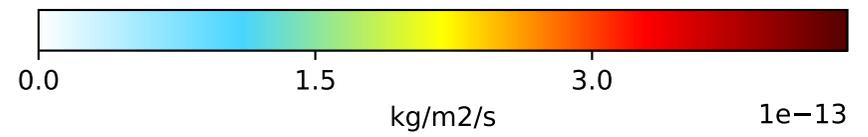
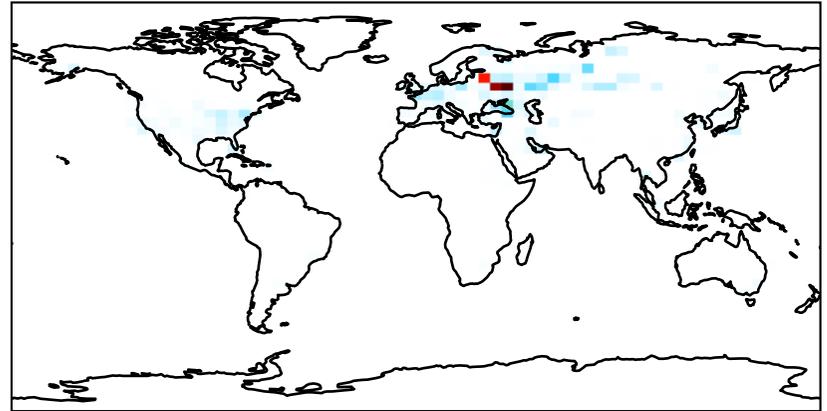


# EmisMACR\_Aircraft

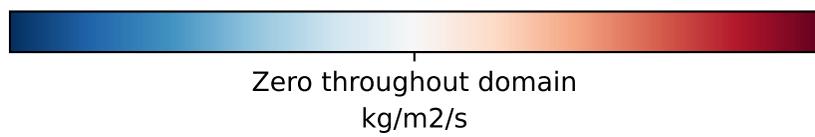
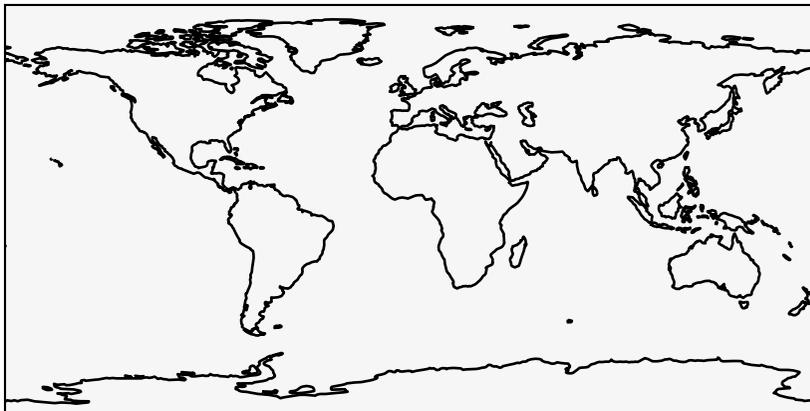
GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



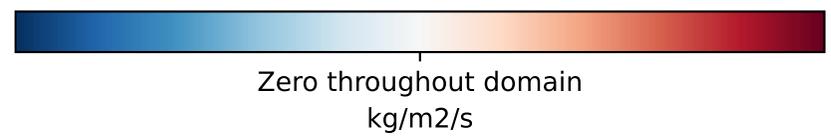
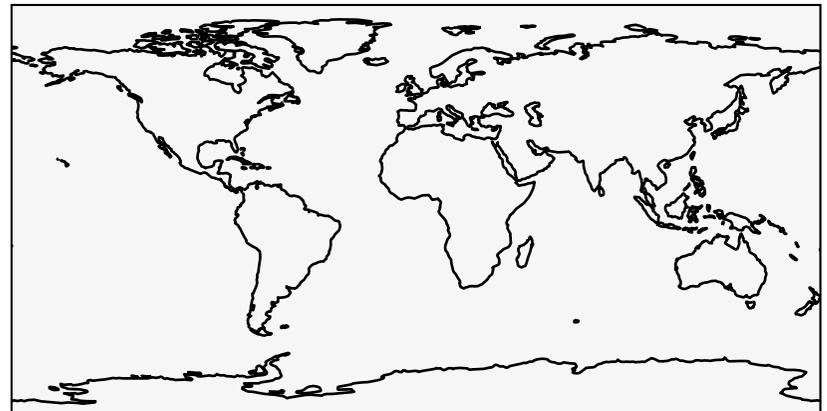
GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



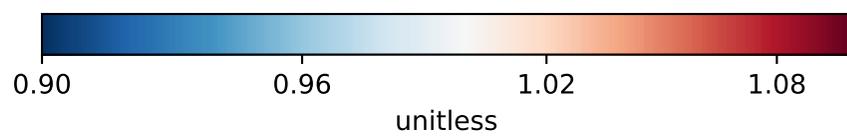
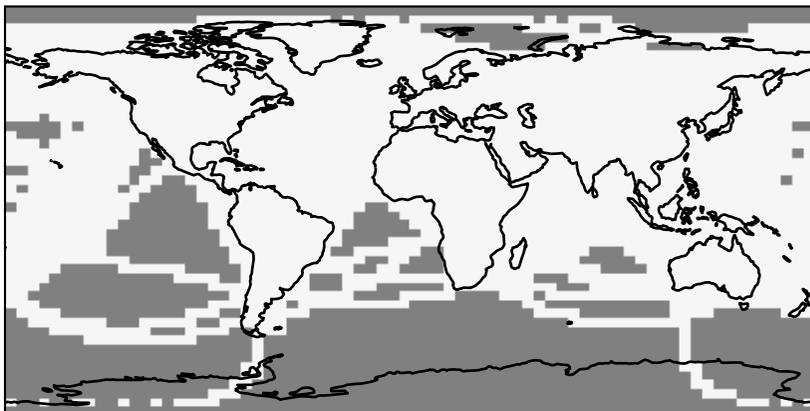
Difference  
Dev - Ref, Dynamic Range



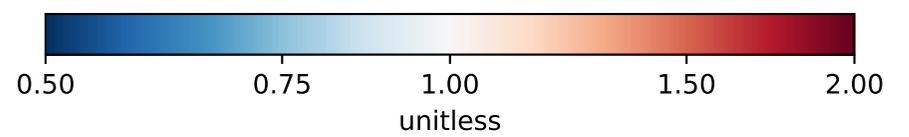
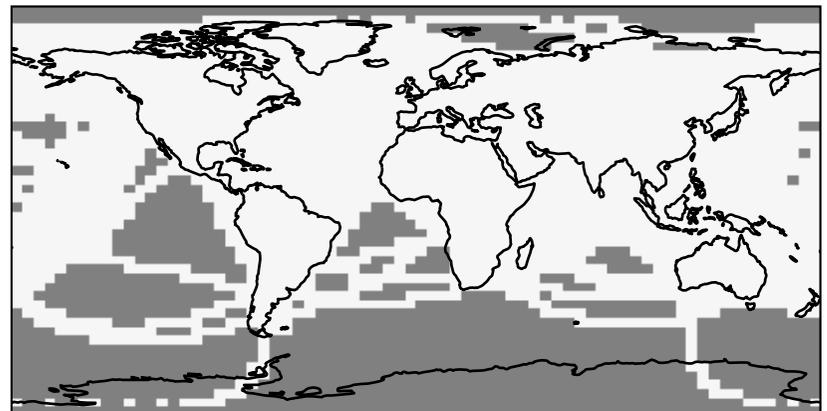
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

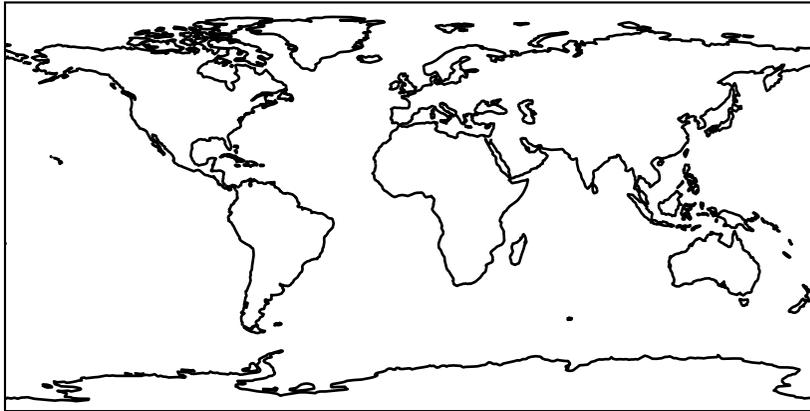


Ratio  
Dev/Ref, Fixed Range



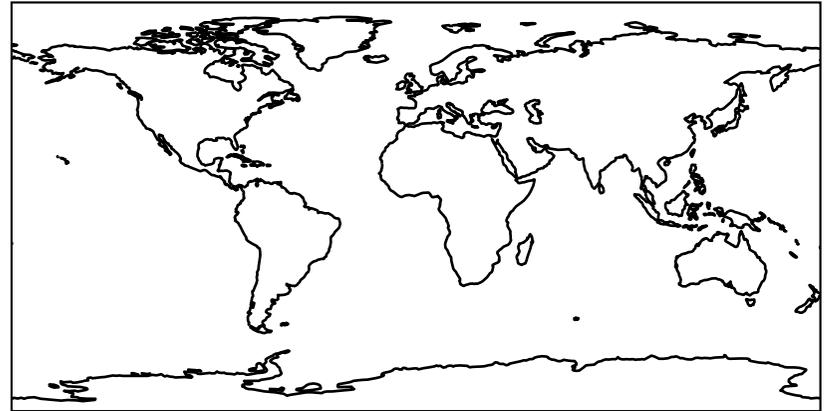
# EmisMACR\_Anthro

GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



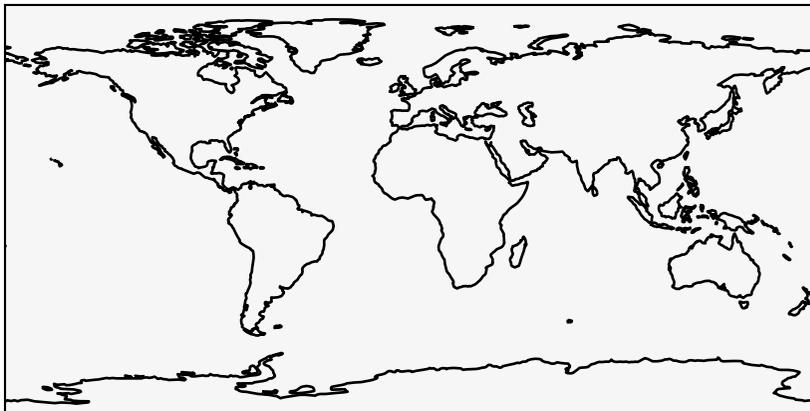
Zero throughout domain  
kg/m2/s

GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



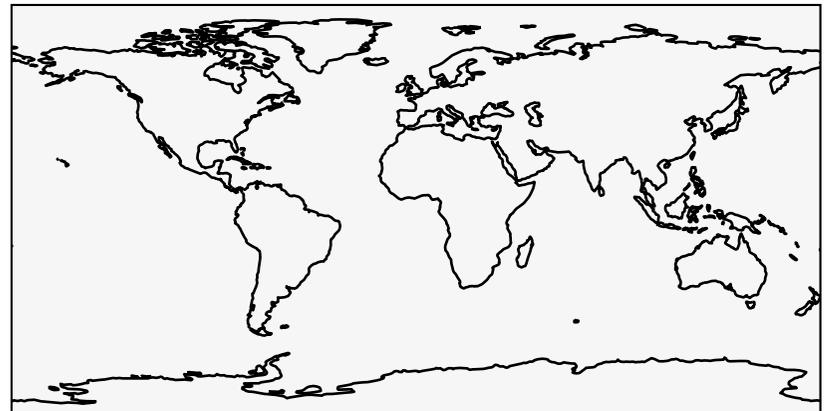
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Dynamic Range



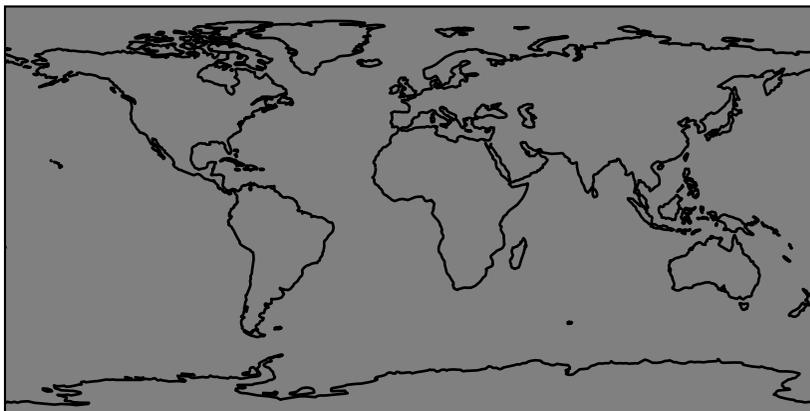
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



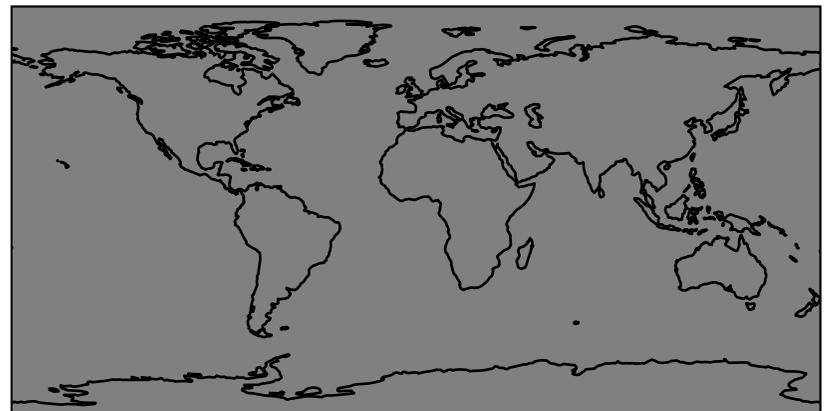
Zero throughout domain  
kg/m2/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

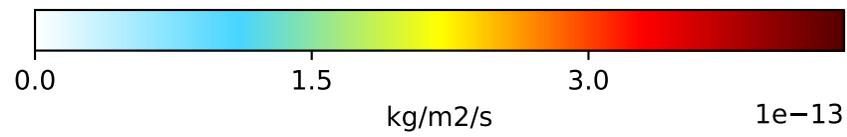
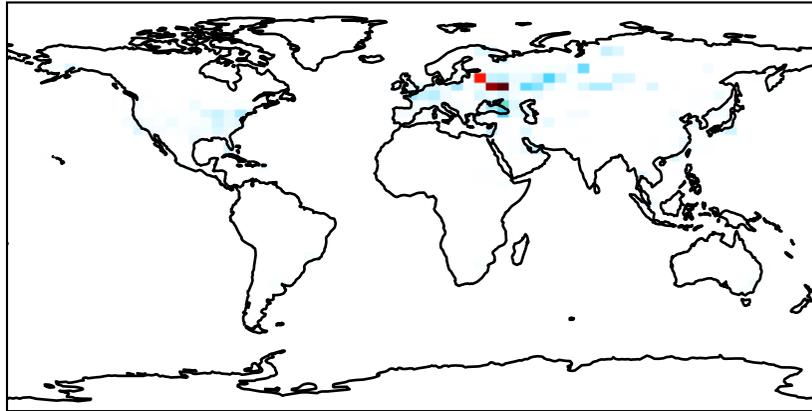
Ratio  
Dev/Ref, Fixed Range



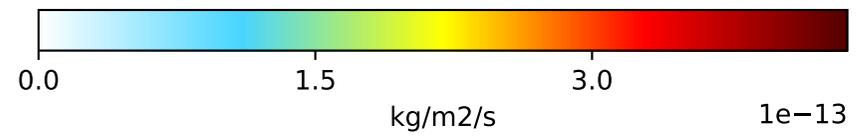
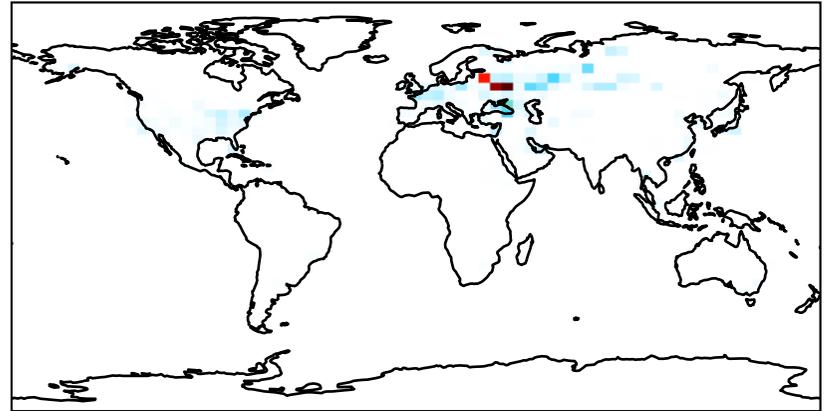
Undefined throughout domain  
unitless

# EmisMACR\_Total

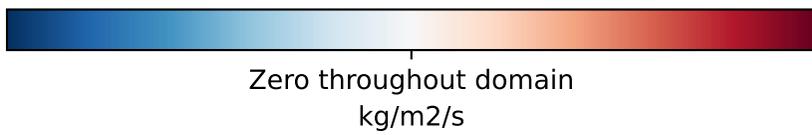
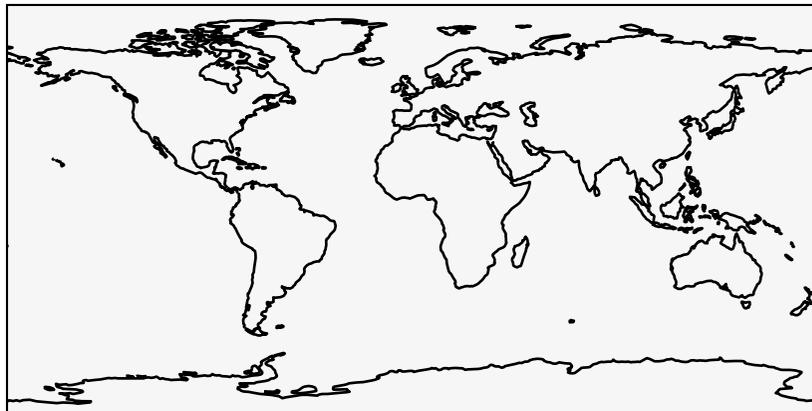
GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



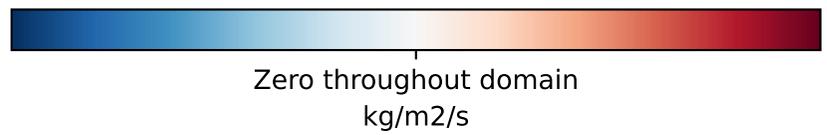
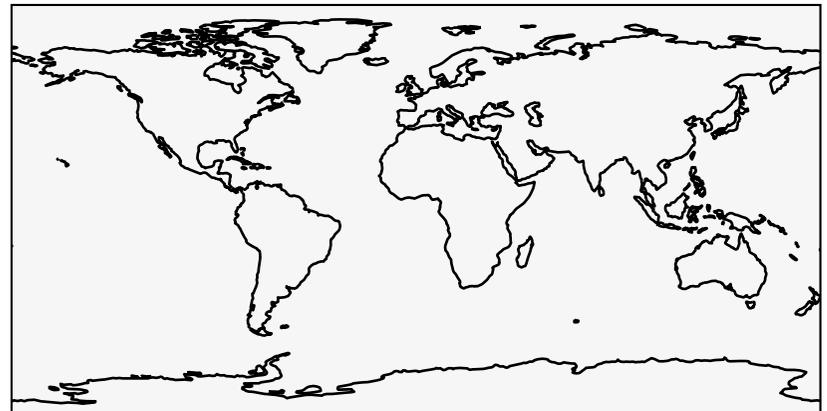
GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



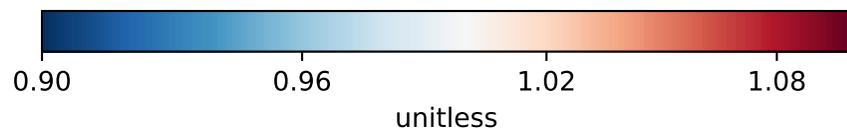
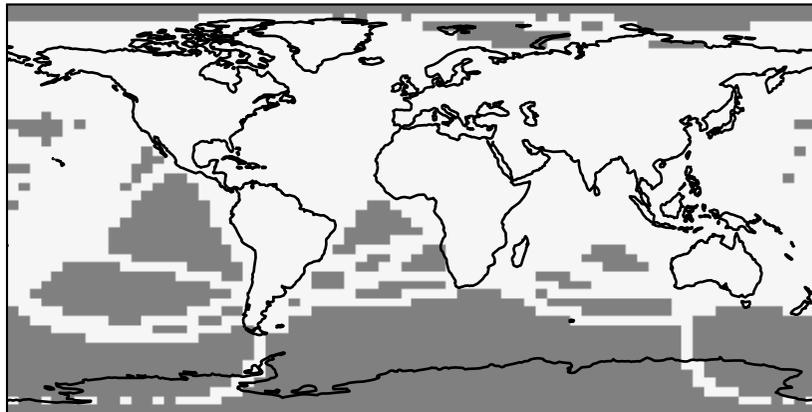
Difference  
Dev - Ref, Dynamic Range



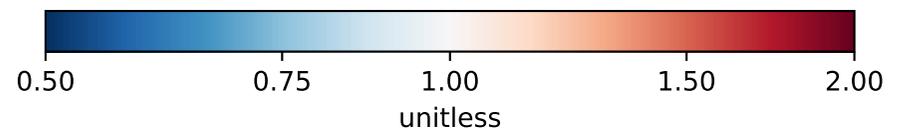
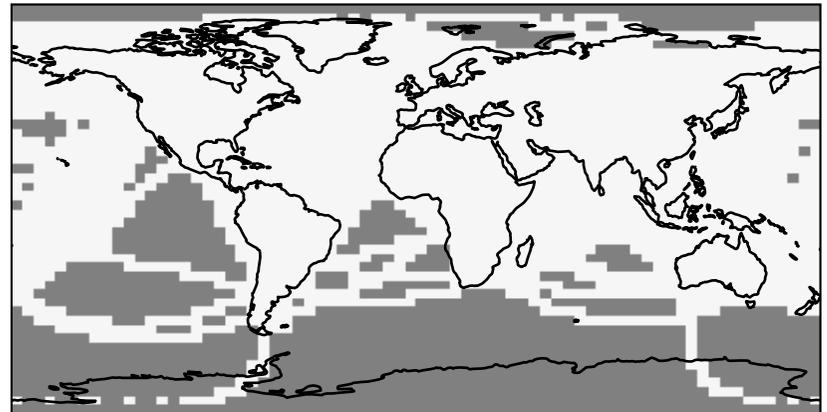
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

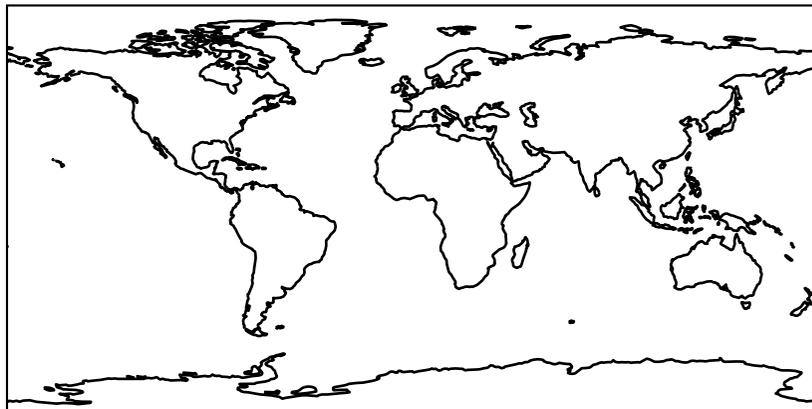


Ratio  
Dev/Ref, Fixed Range



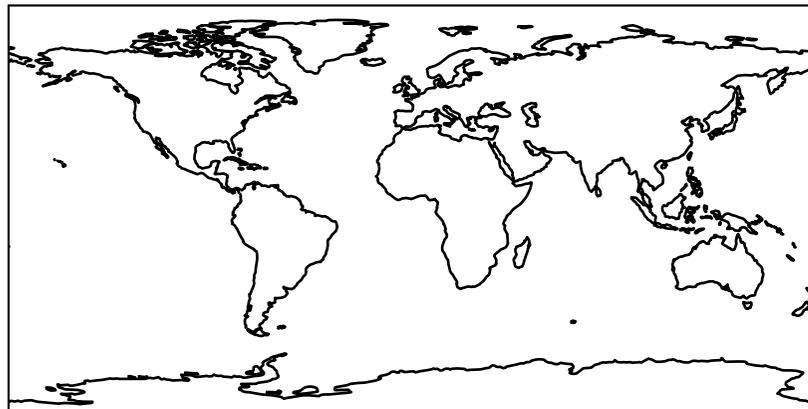
# EmisACET\_Anthro

GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



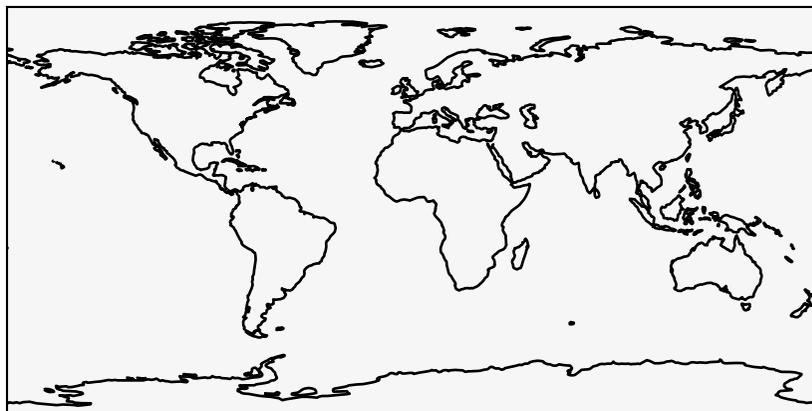
Zero throughout domain  
kg/m2/s

GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



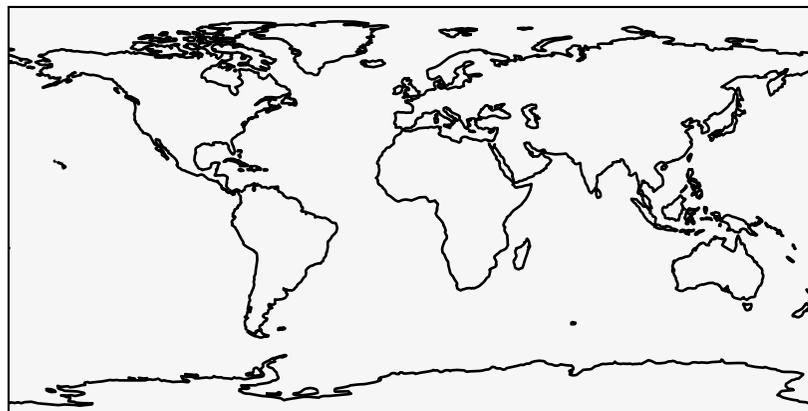
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Dynamic Range



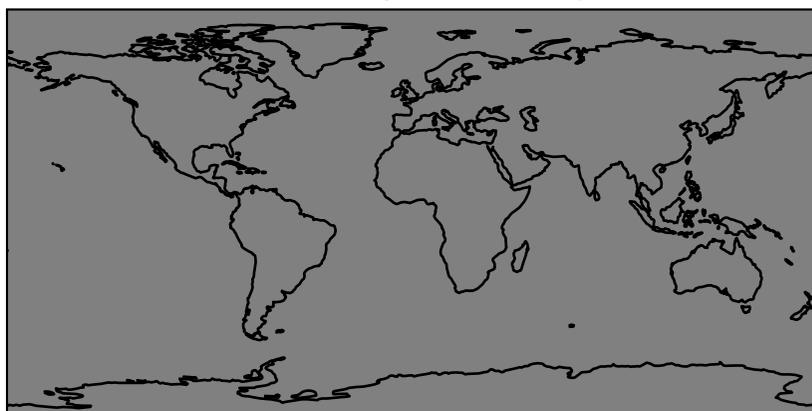
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



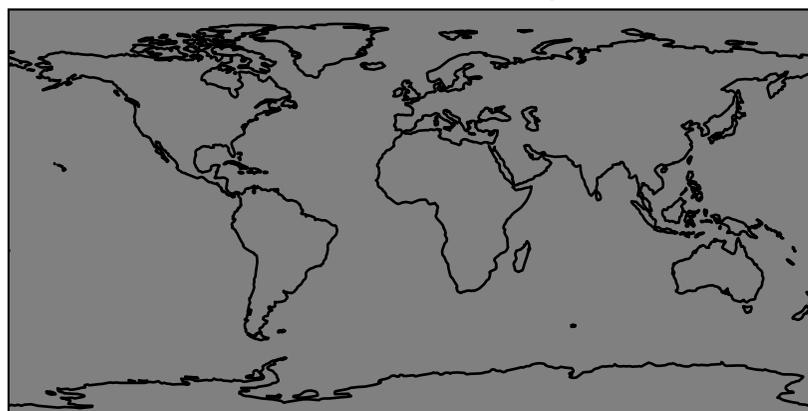
Zero throughout domain  
kg/m2/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

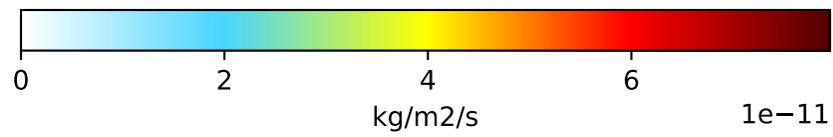
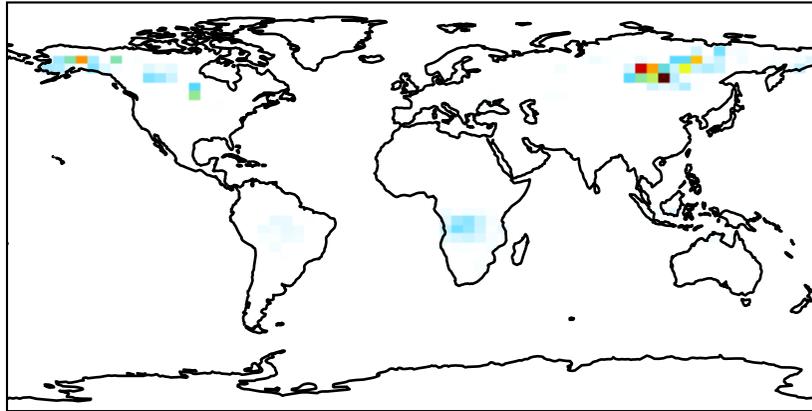
Ratio  
Dev/Ref, Fixed Range



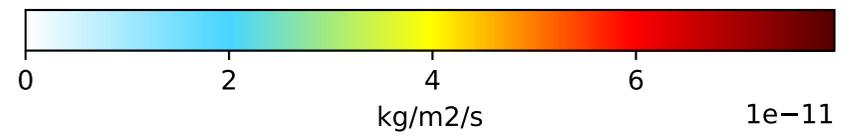
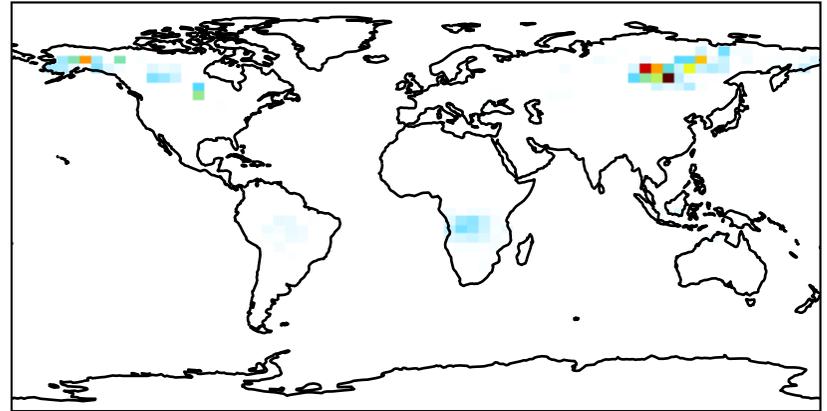
Undefined throughout domain  
unitless

# EmisACET\_BioBurn

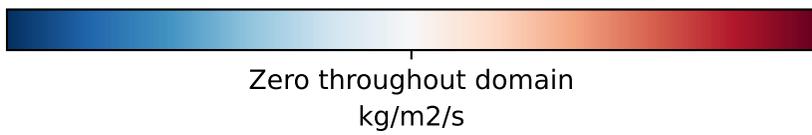
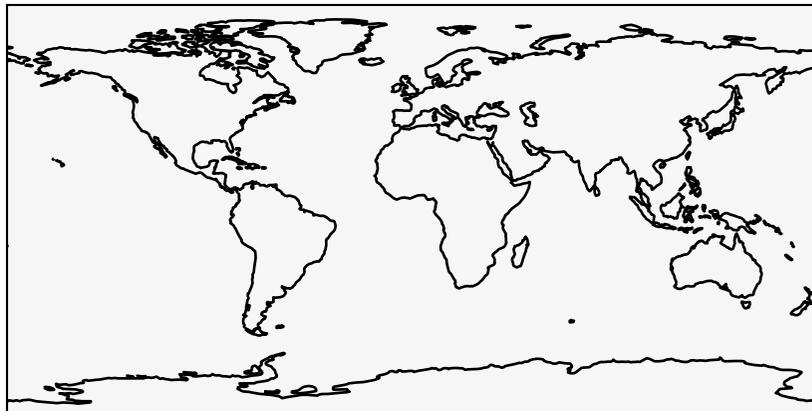
GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



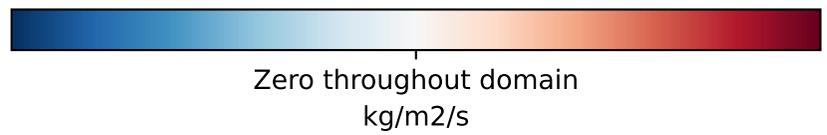
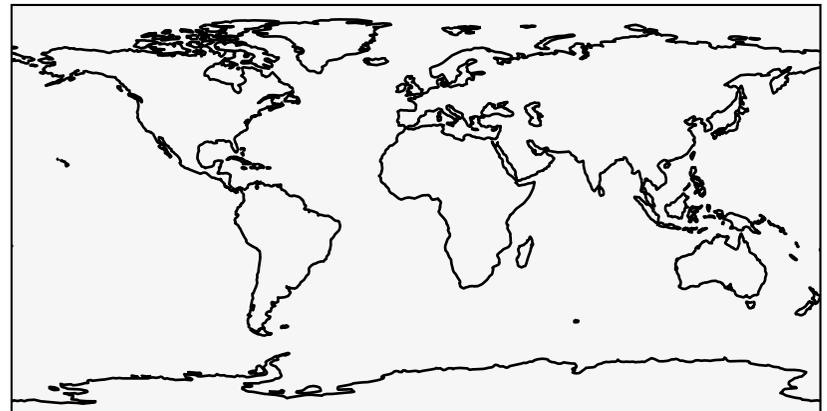
GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



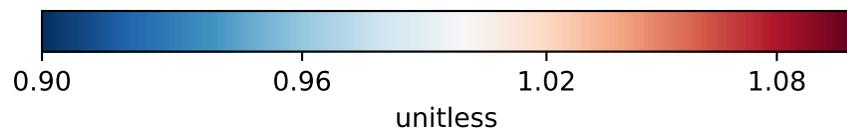
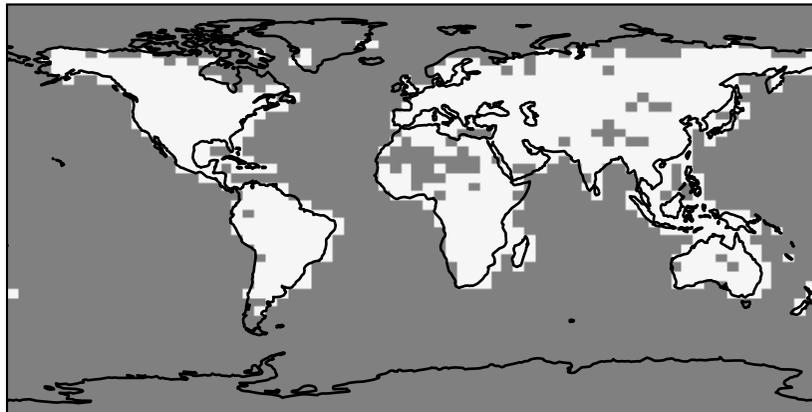
Difference  
Dev - Ref, Dynamic Range



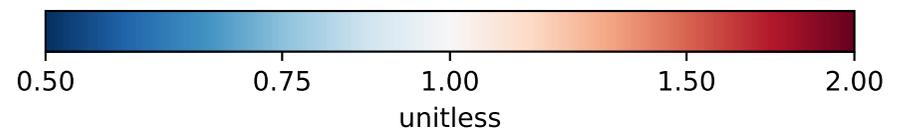
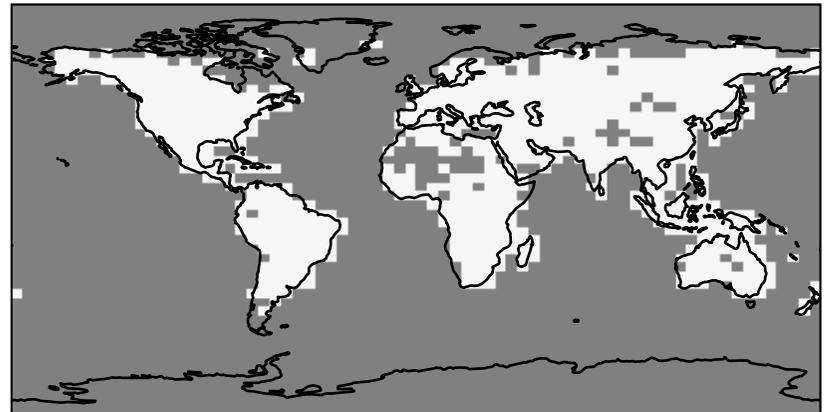
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

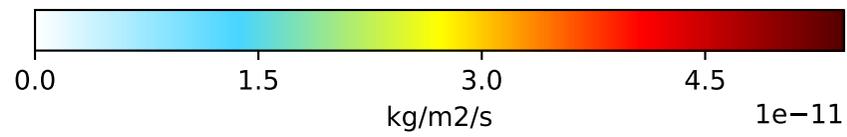
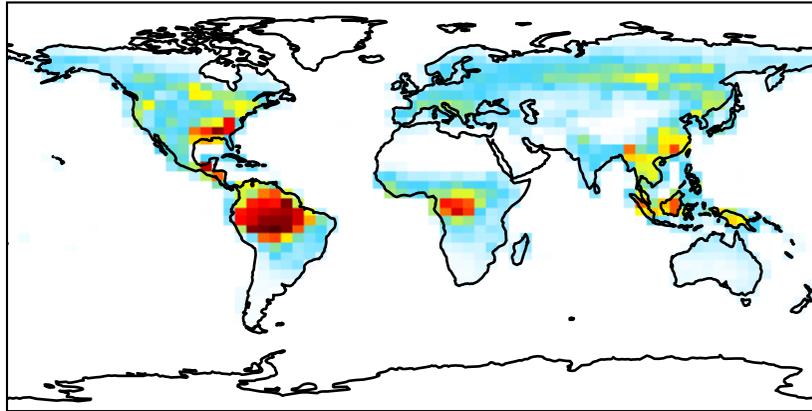


Ratio  
Dev/Ref, Fixed Range

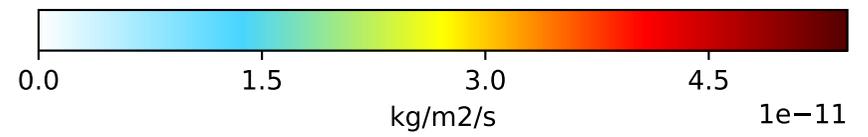
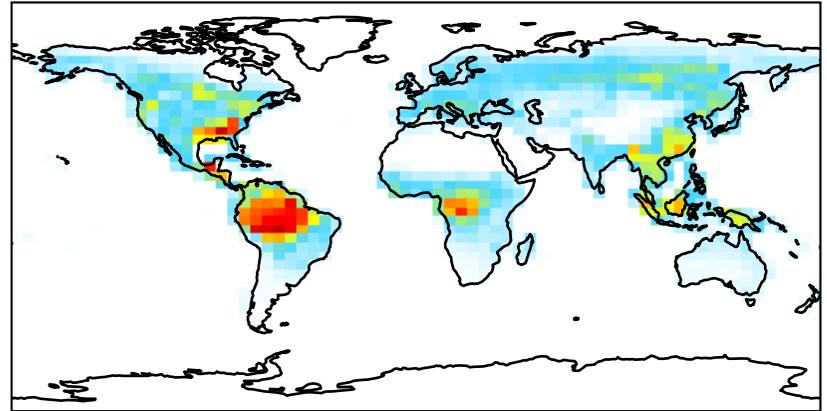


# EmisACET\_Biogenic

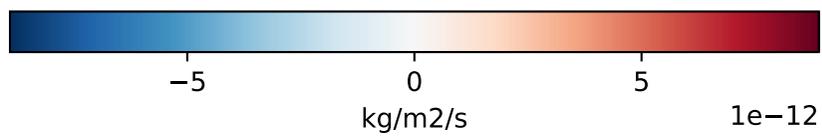
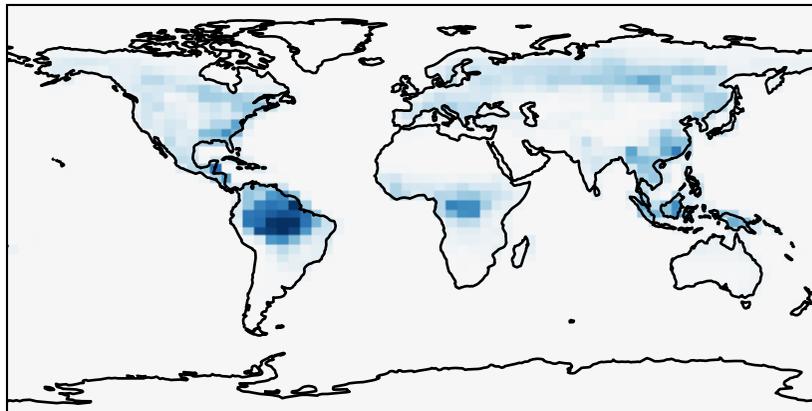
GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



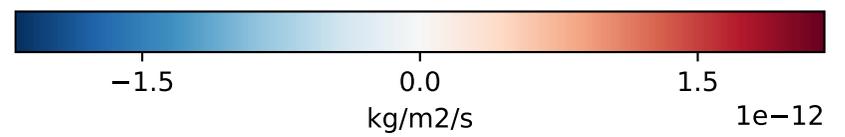
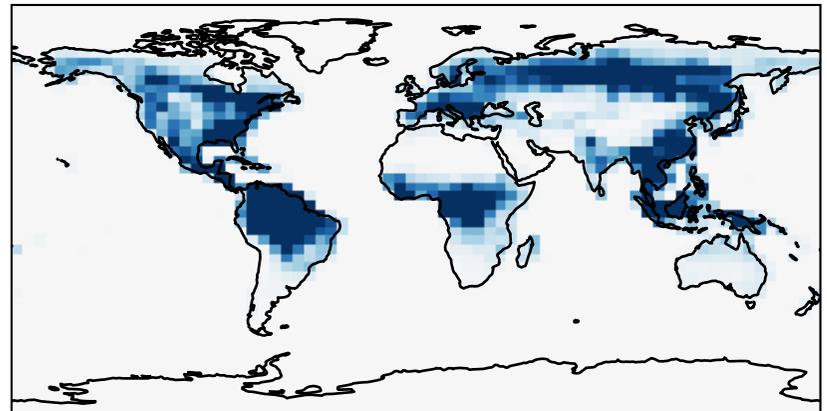
GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



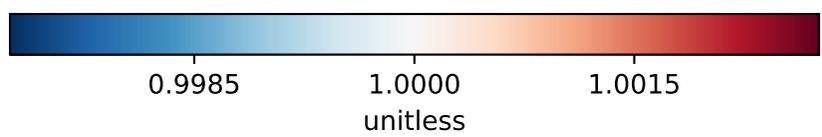
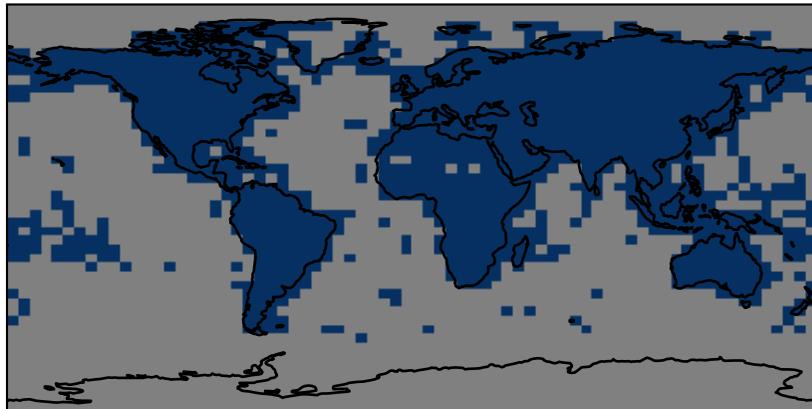
Difference  
Dev - Ref, Dynamic Range



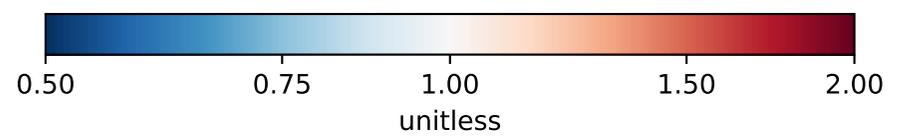
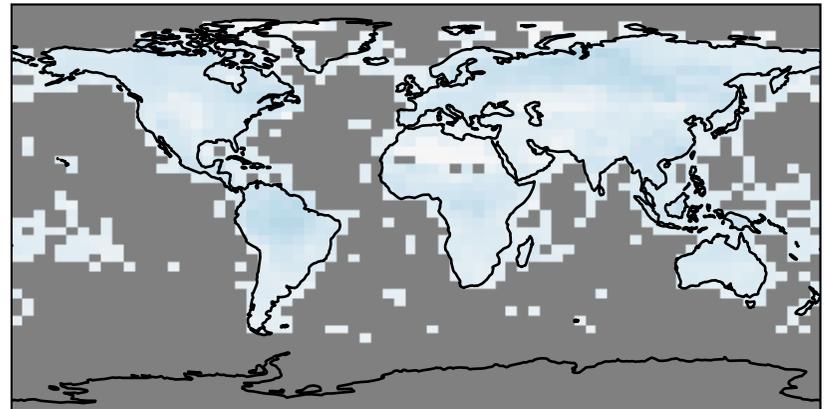
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

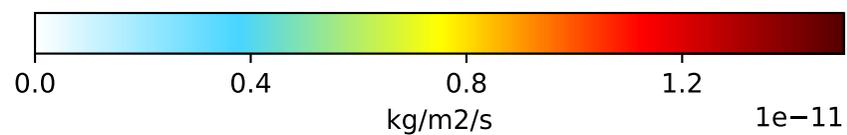
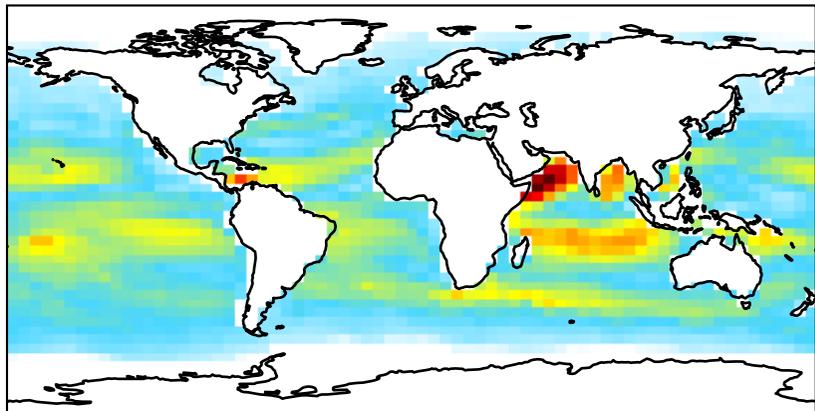


Ratio  
Dev/Ref, Fixed Range

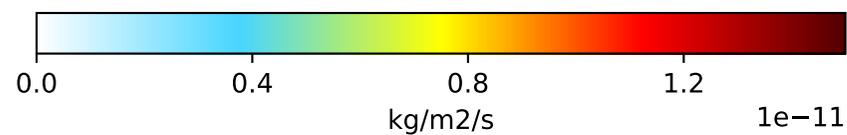
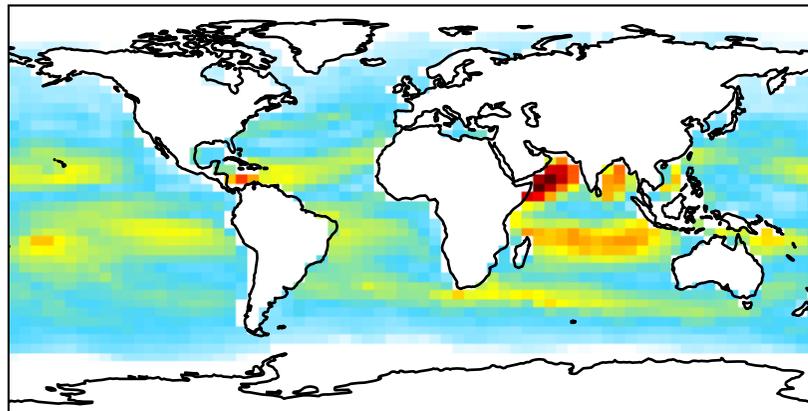


# EmisACET\_Ocean

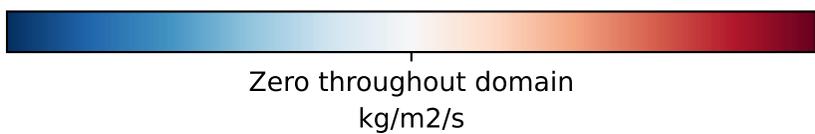
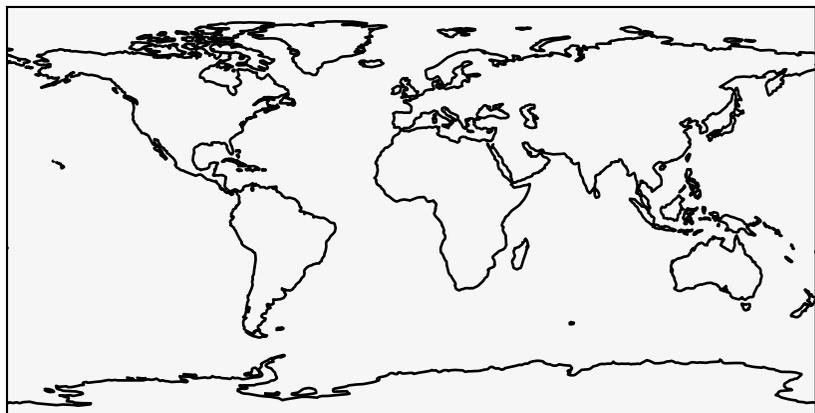
GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



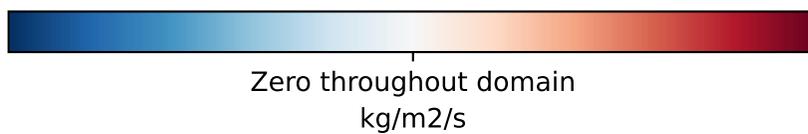
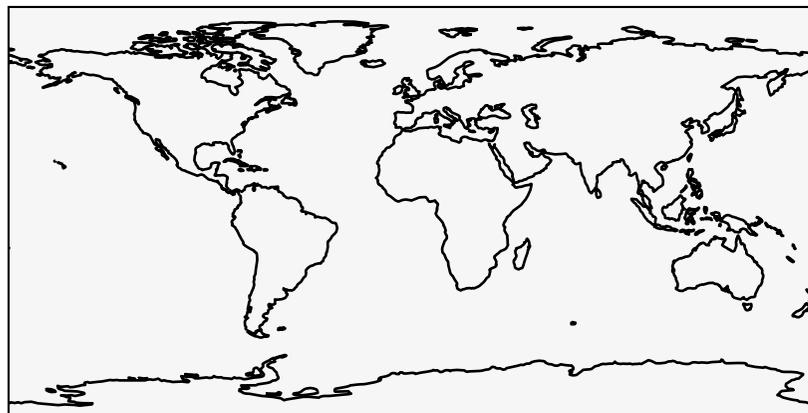
GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



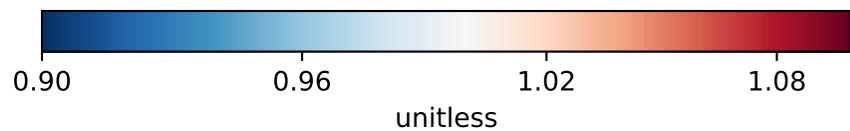
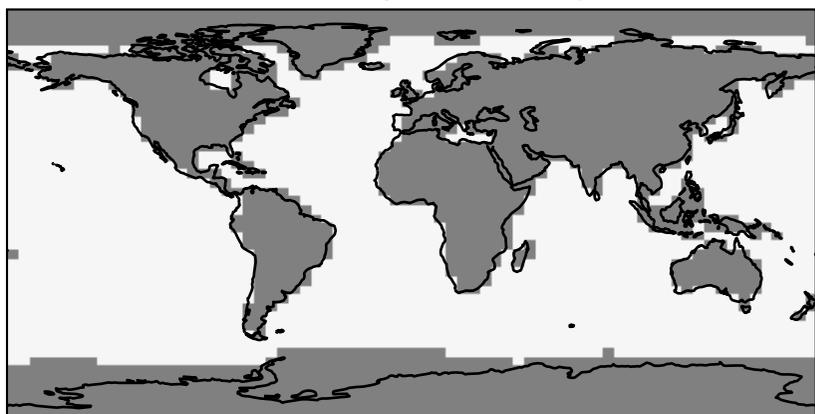
Difference  
Dev - Ref, Dynamic Range



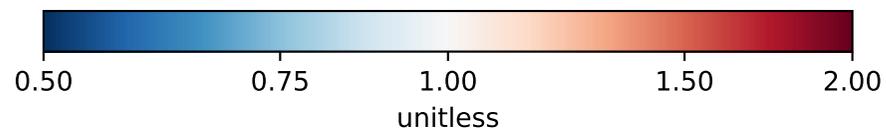
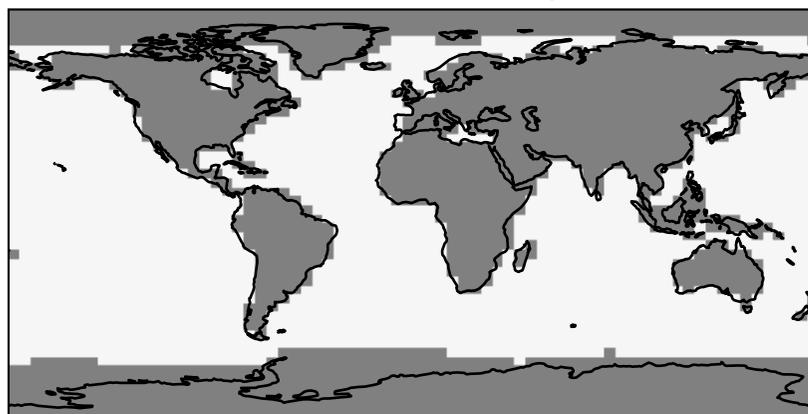
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

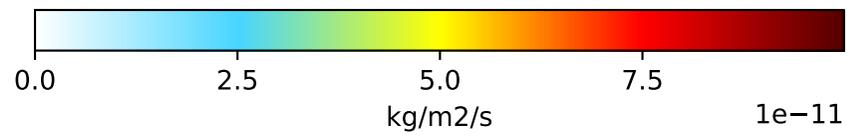
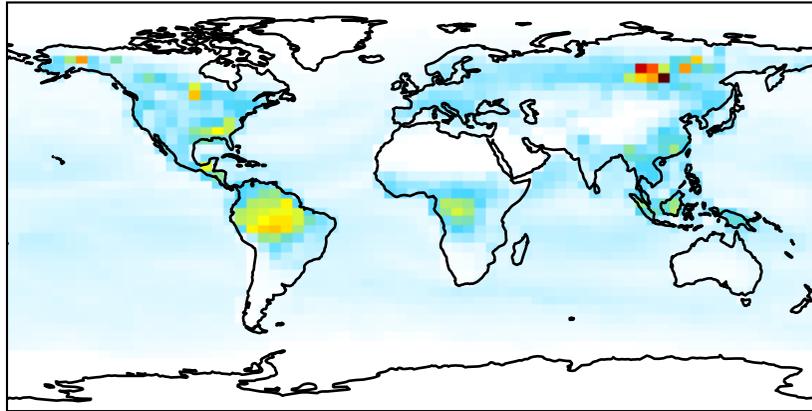


Ratio  
Dev/Ref, Fixed Range

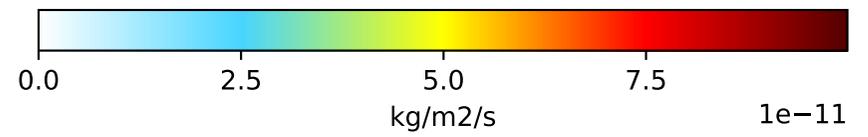
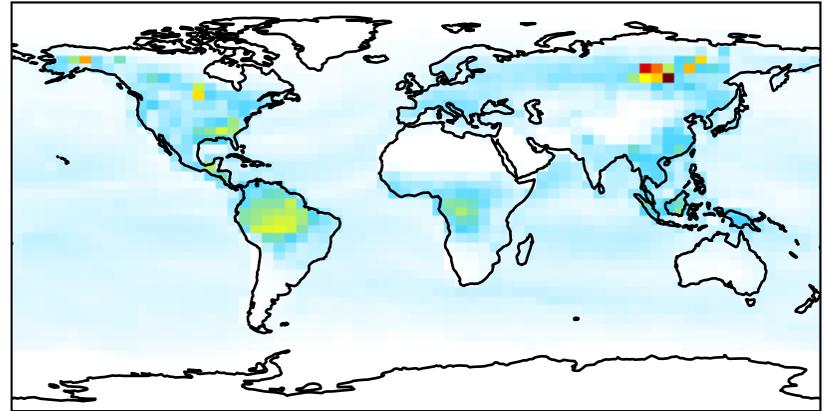


# EmisACET\_Total

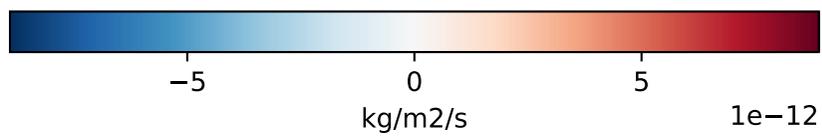
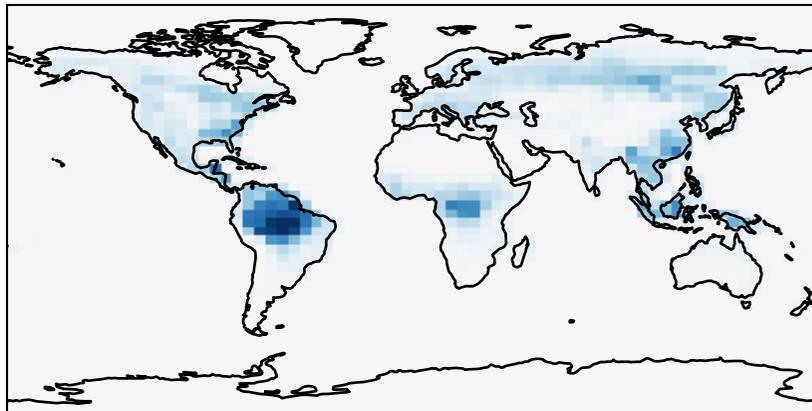
GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



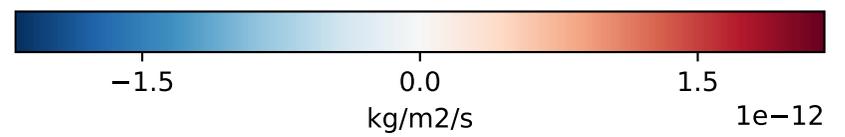
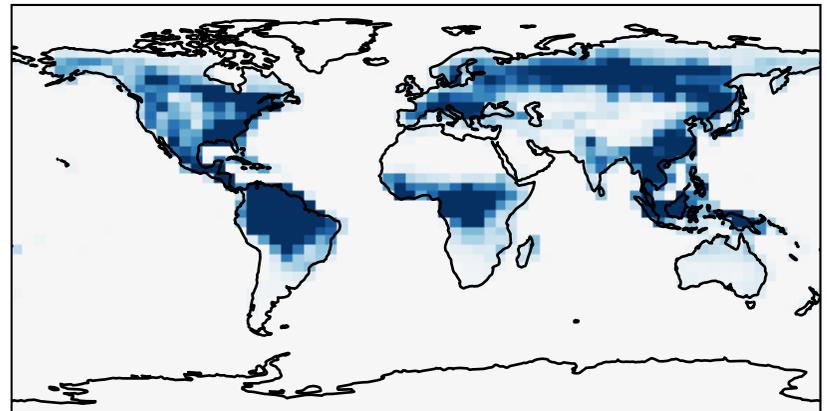
GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



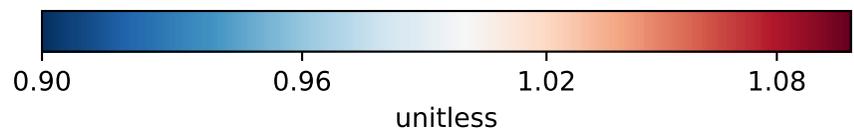
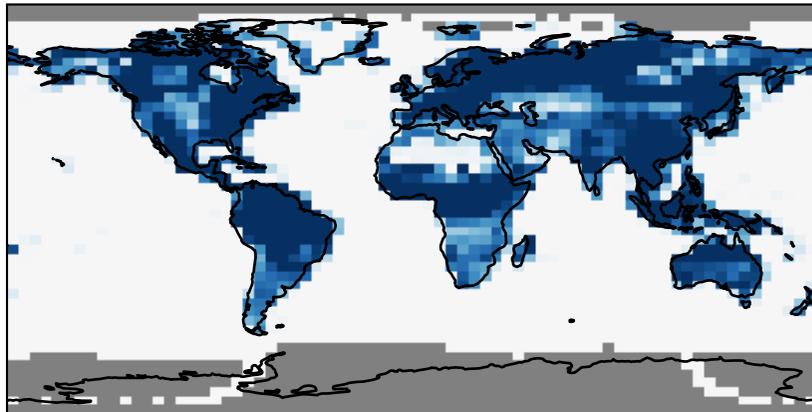
Difference  
Dev - Ref, Dynamic Range



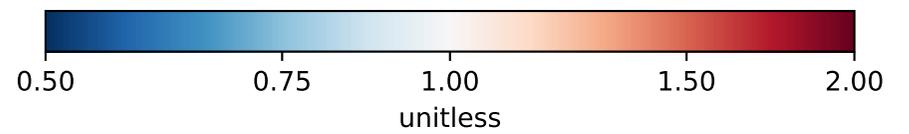
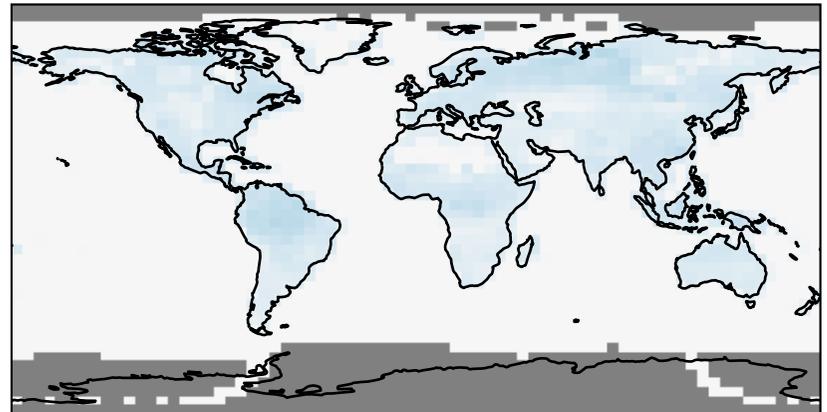
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

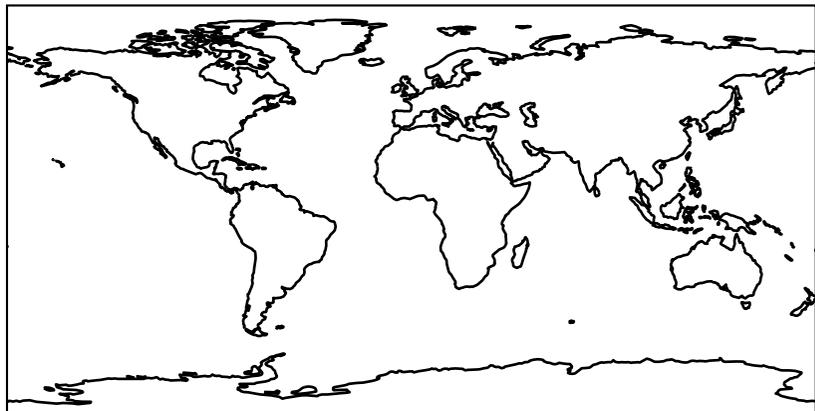


Ratio  
Dev/Ref, Fixed Range



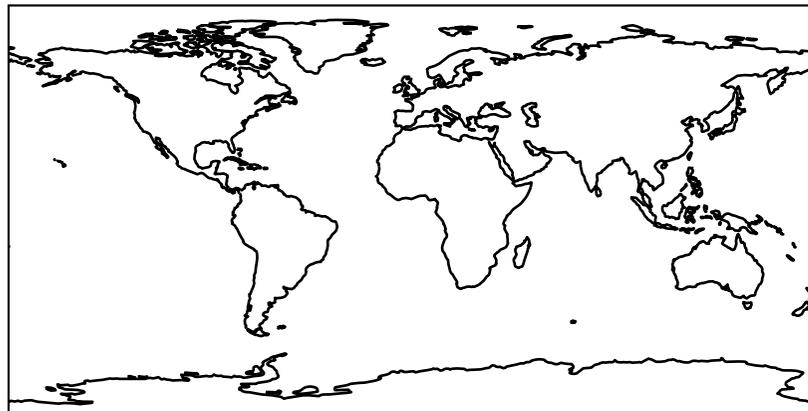
# EmisHAC\_Anthro

GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



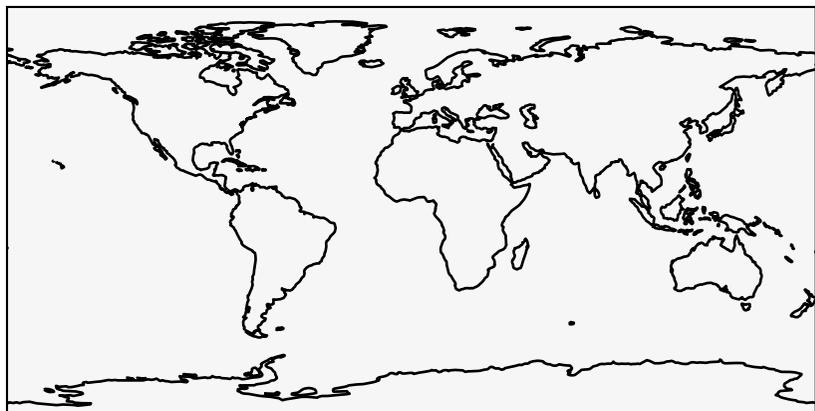
Zero throughout domain  
kg/m2/s

GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



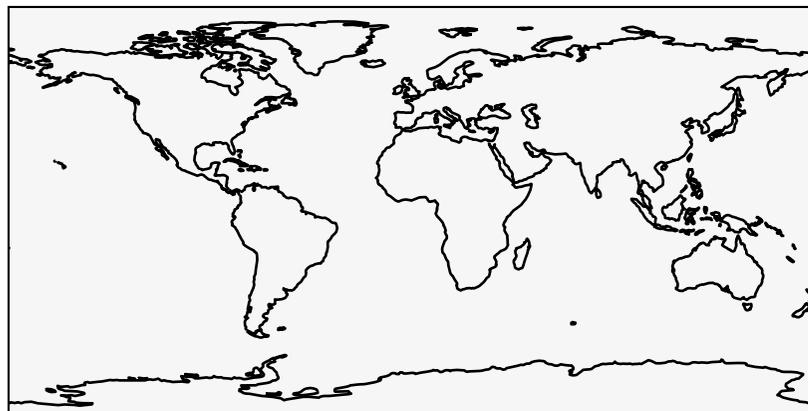
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Dynamic Range



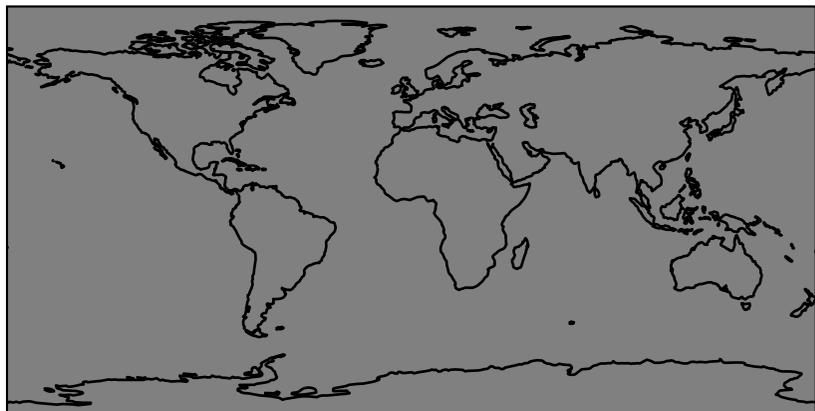
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



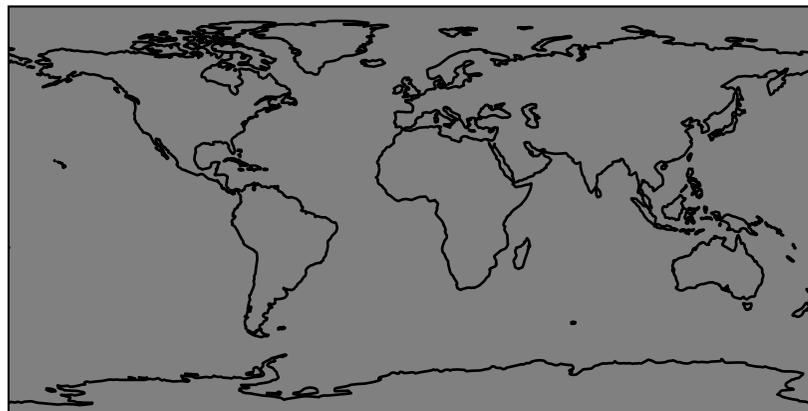
Zero throughout domain  
kg/m2/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

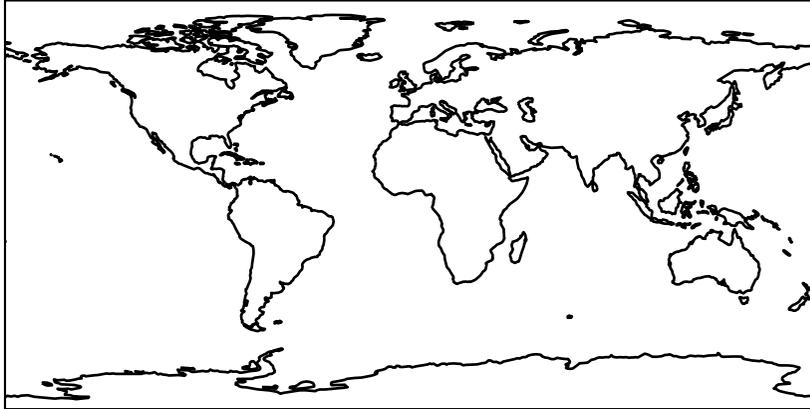
Ratio  
Dev/Ref, Fixed Range



Undefined throughout domain  
unitless

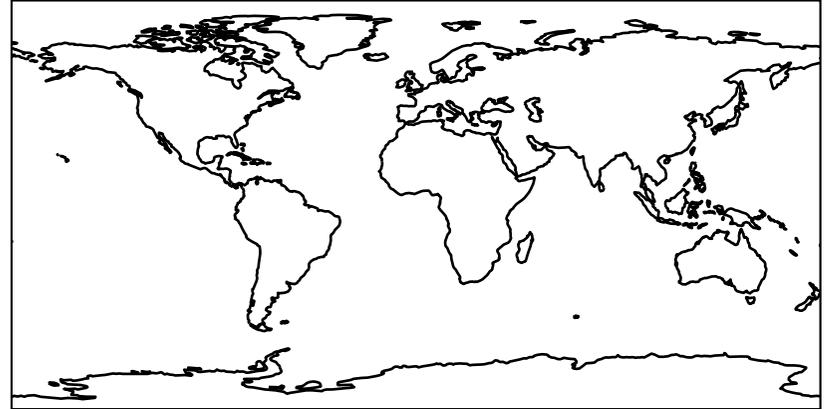
# EmisHAC\_Total

GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



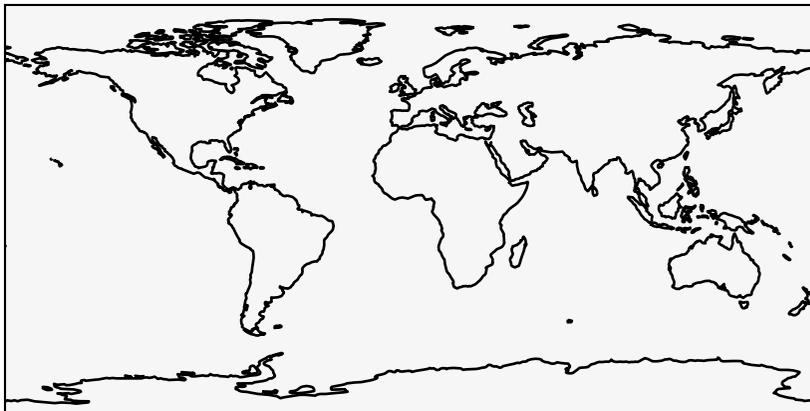
Zero throughout domain  
kg/m2/s

GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



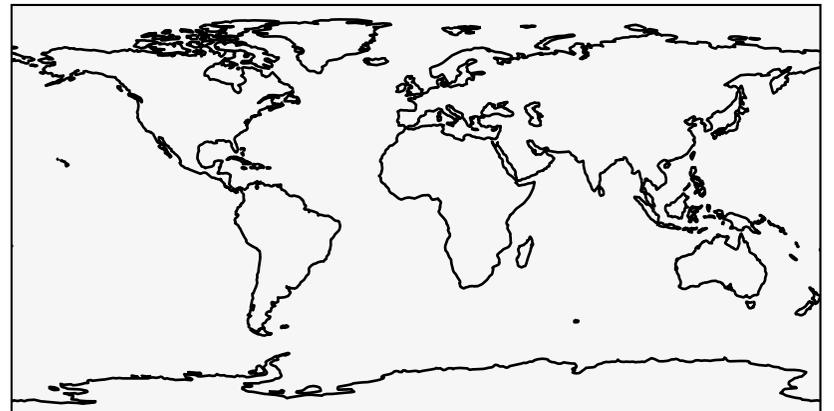
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Dynamic Range



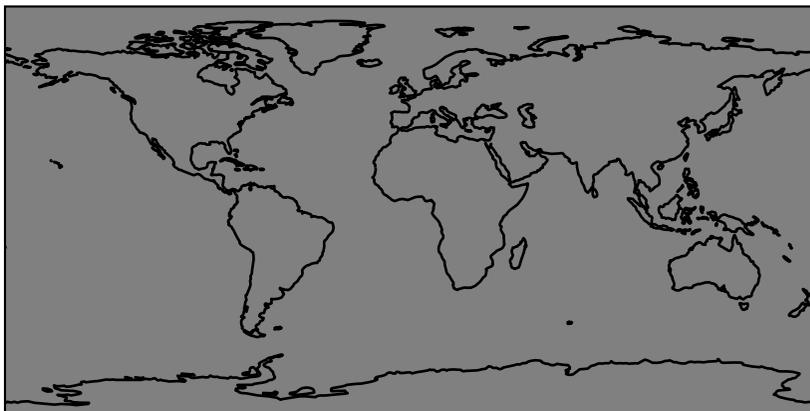
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



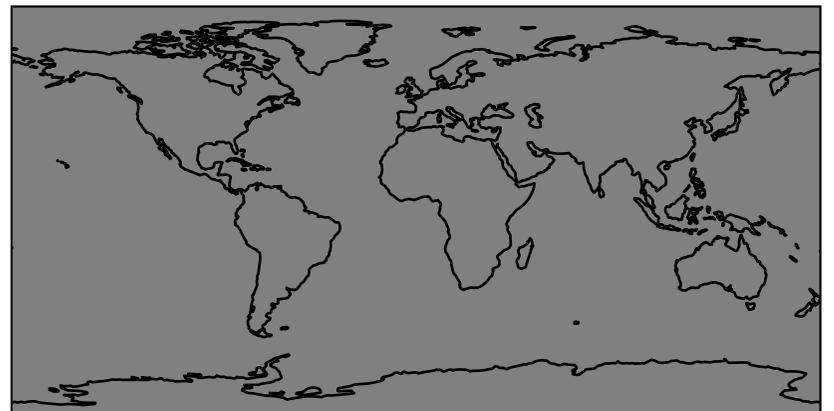
Zero throughout domain  
kg/m2/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

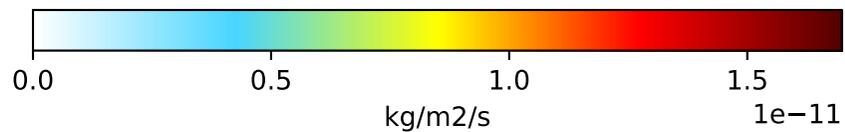
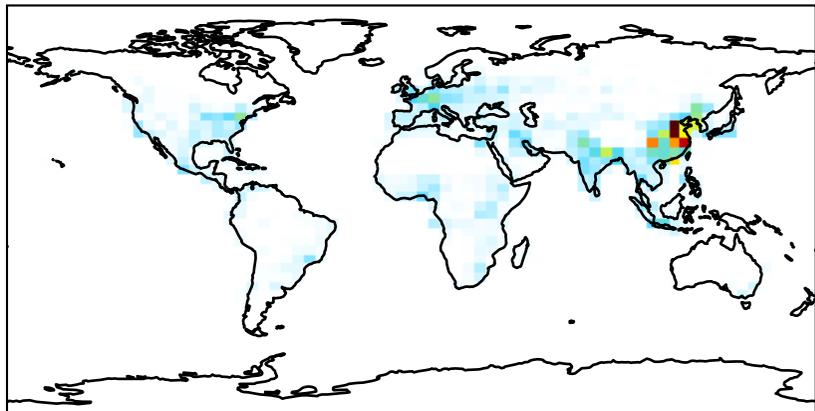
Ratio  
Dev/Ref, Fixed Range



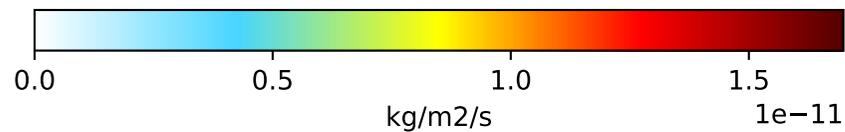
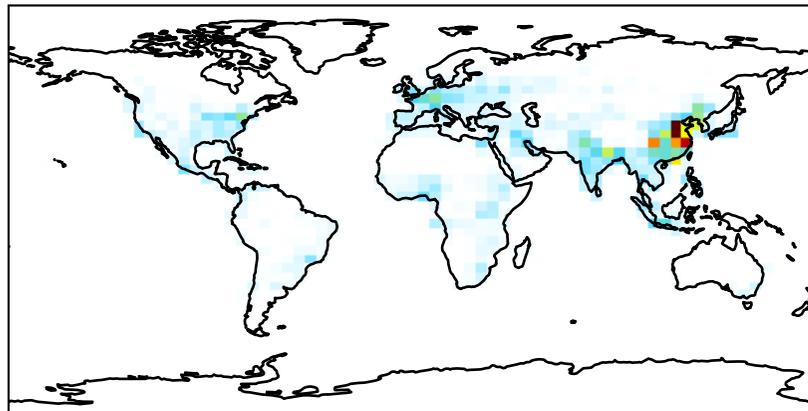
Undefined throughout domain  
unitless

# EmisMEK\_Anthro

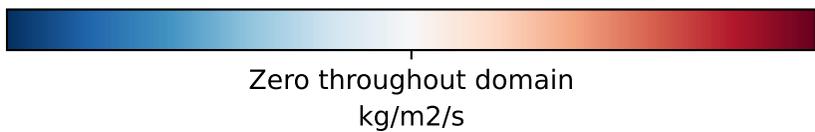
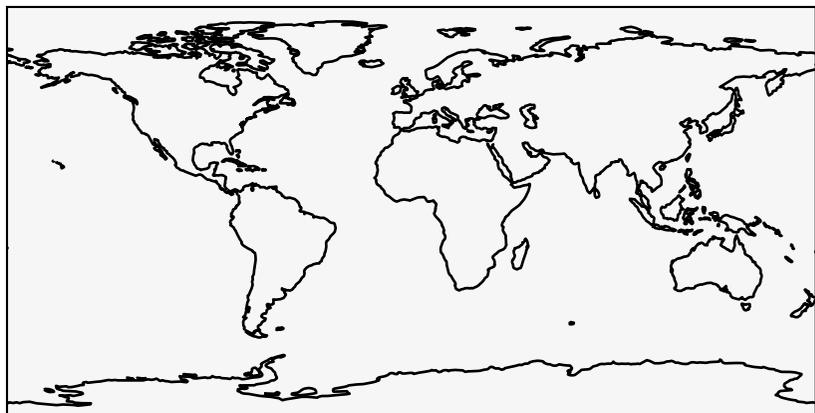
GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



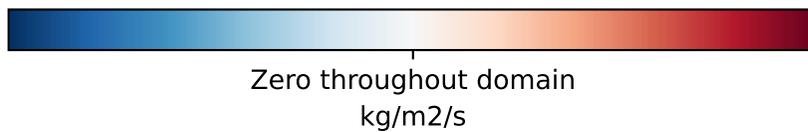
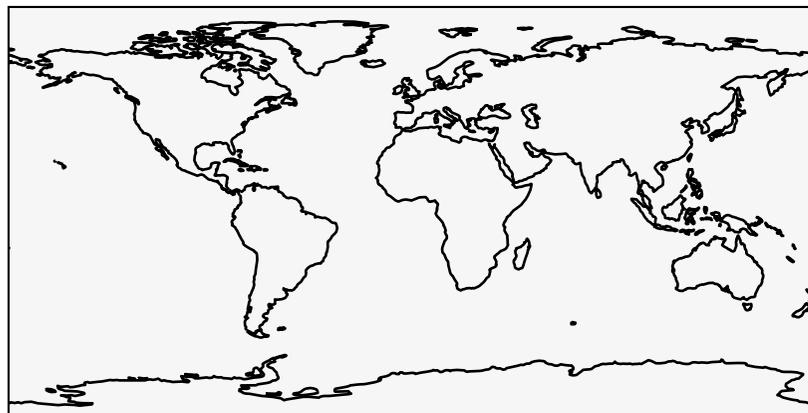
GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



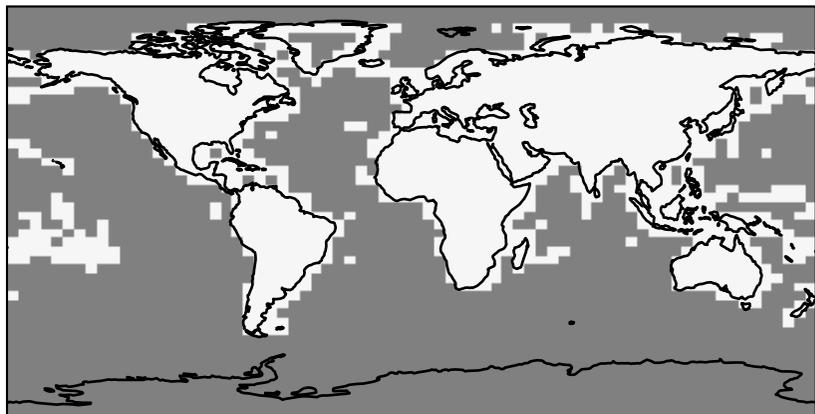
Difference  
Dev - Ref, Dynamic Range



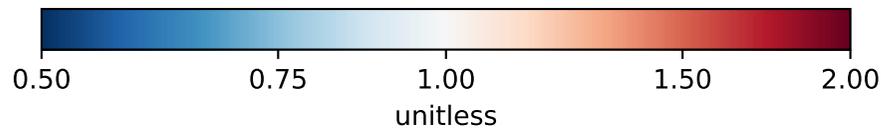
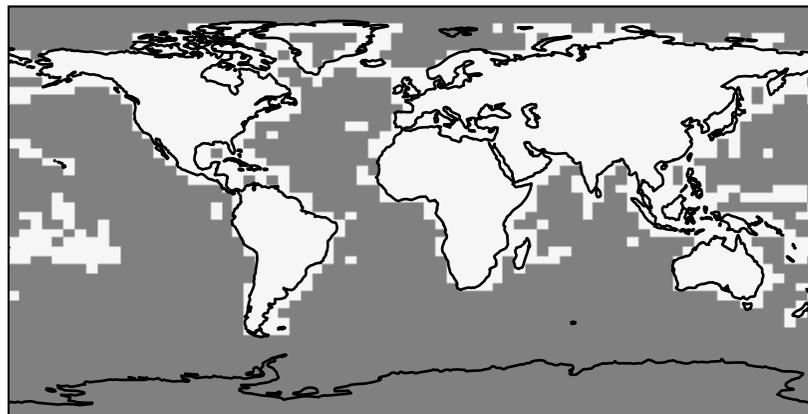
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

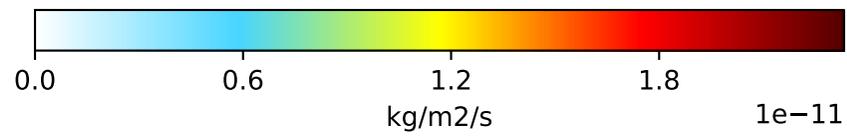
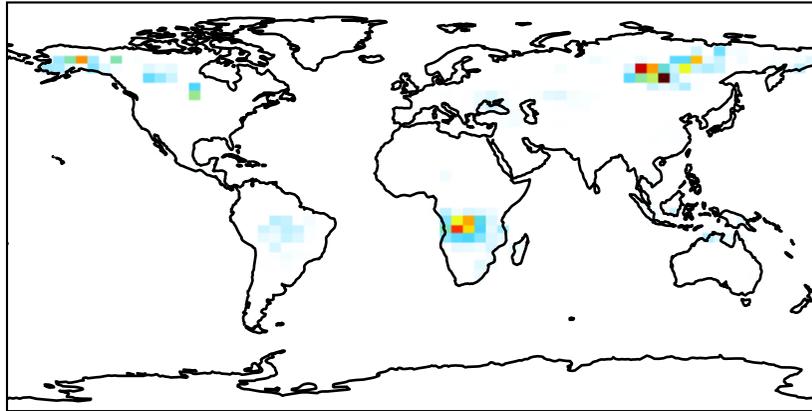


Ratio  
Dev/Ref, Fixed Range

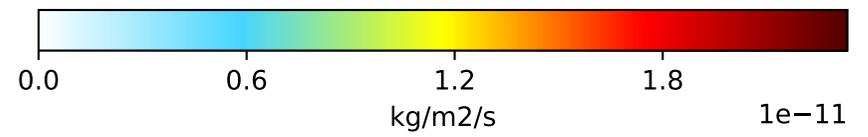
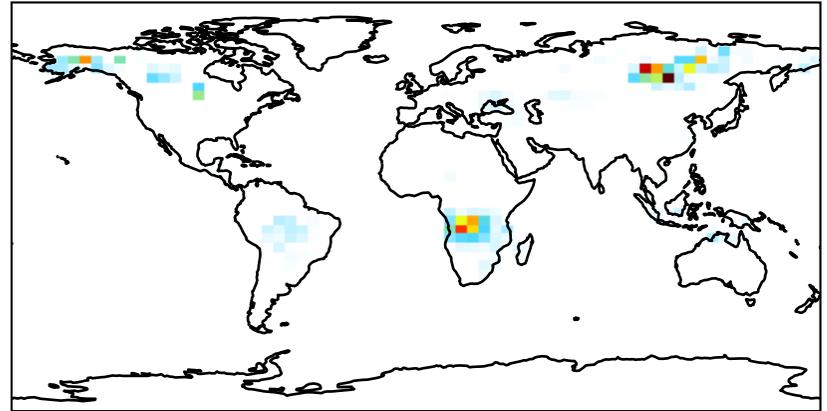


# EmisMEK\_BioBurn

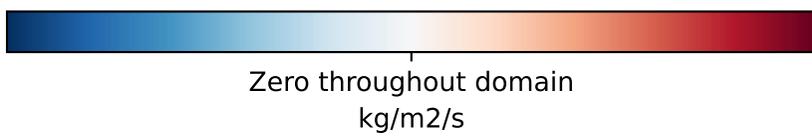
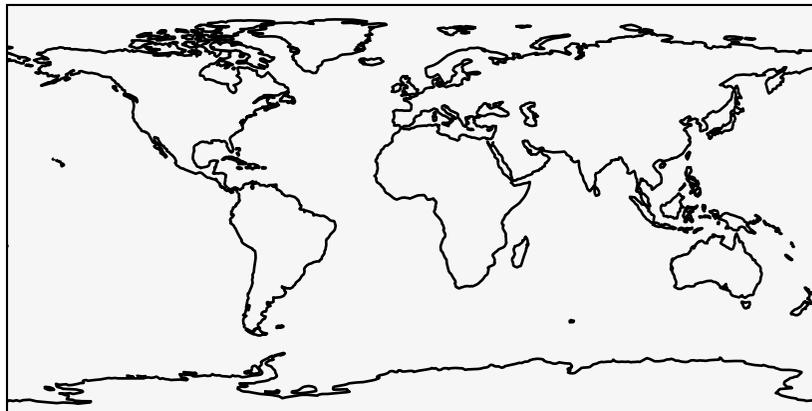
GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



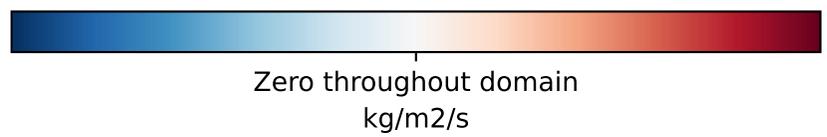
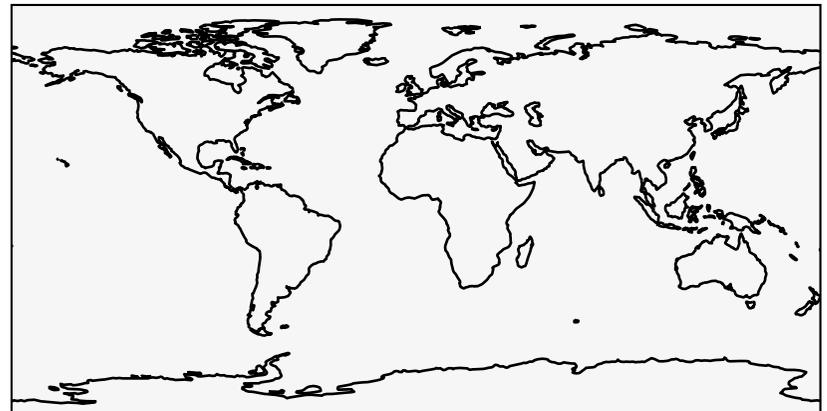
GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



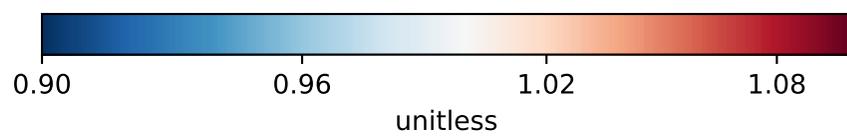
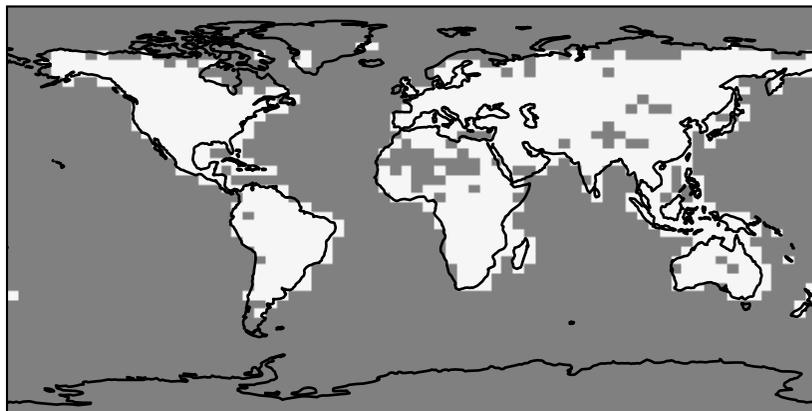
Difference  
Dev - Ref, Dynamic Range



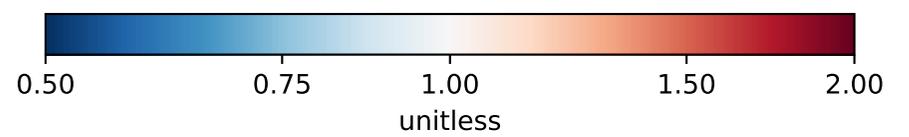
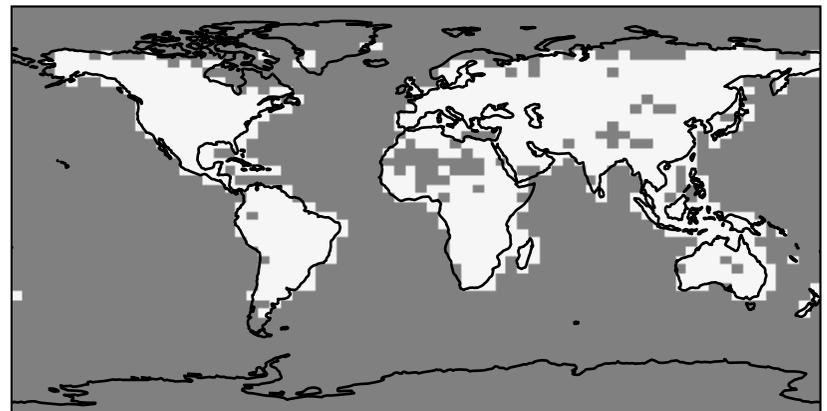
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

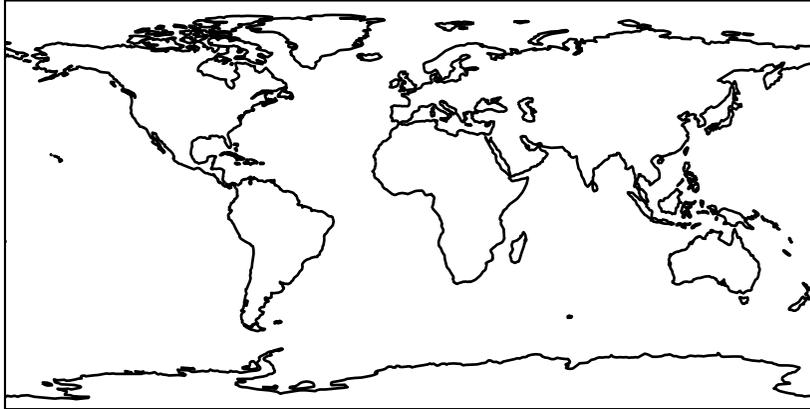


Ratio  
Dev/Ref, Fixed Range



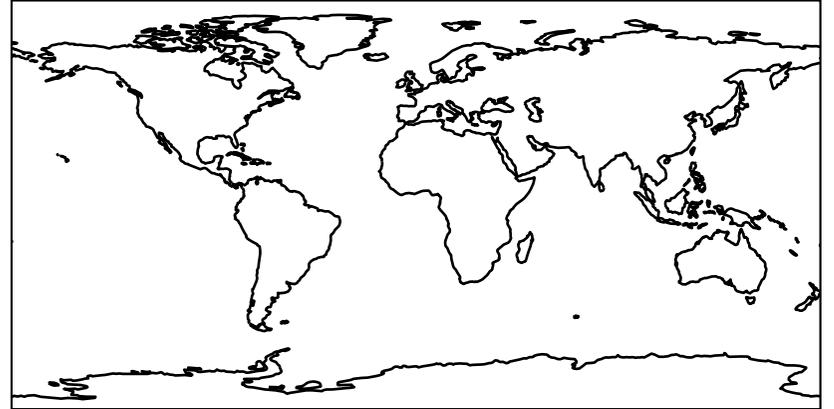
# EmisMEK\_Ship

GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



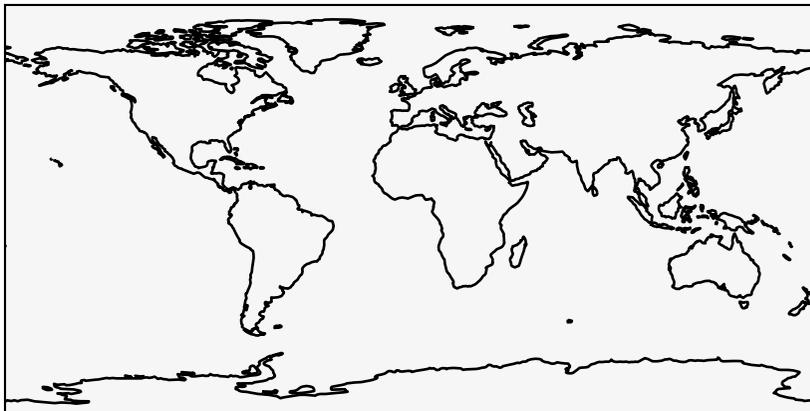
Zero throughout domain  
kg/m2/s

GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



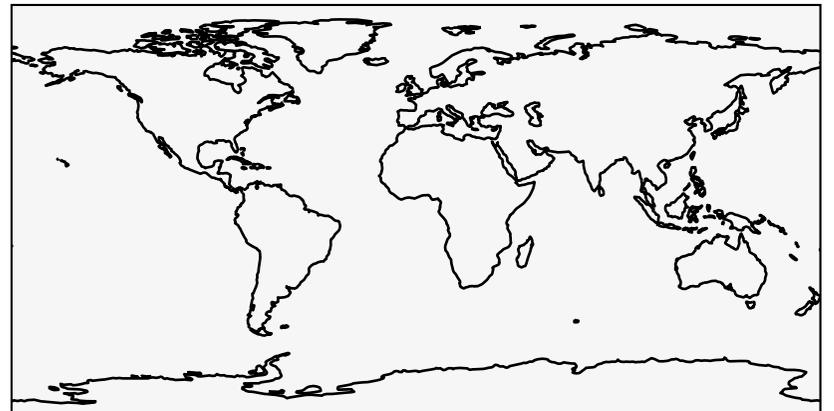
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Dynamic Range



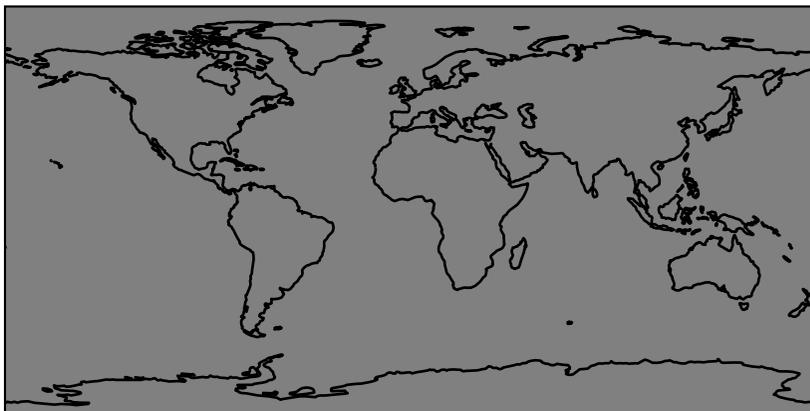
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



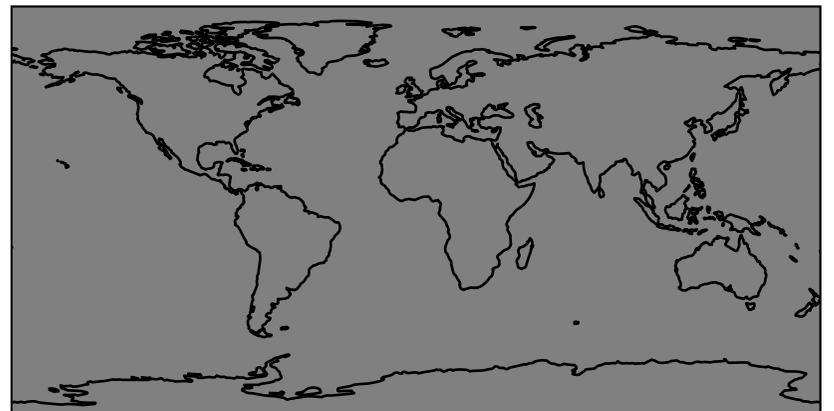
Zero throughout domain  
kg/m2/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

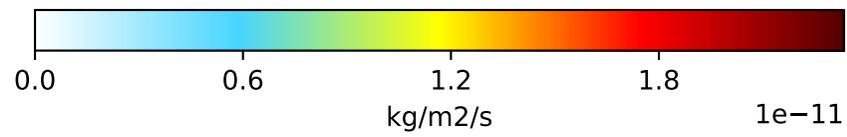
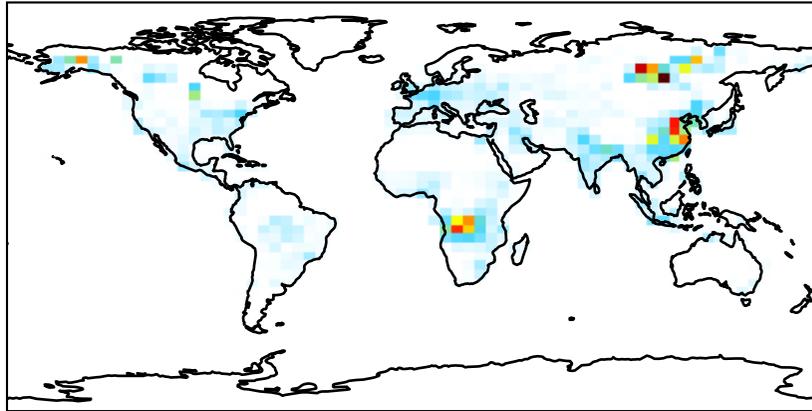
Ratio  
Dev/Ref, Fixed Range



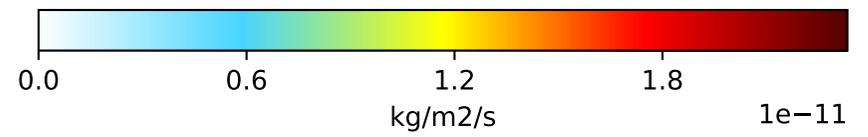
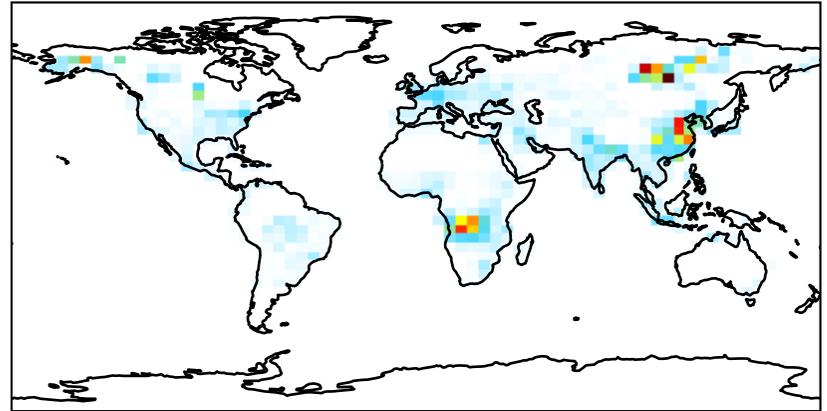
Undefined throughout domain  
unitless

# EmisMEK\_Total

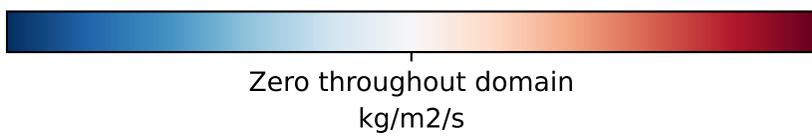
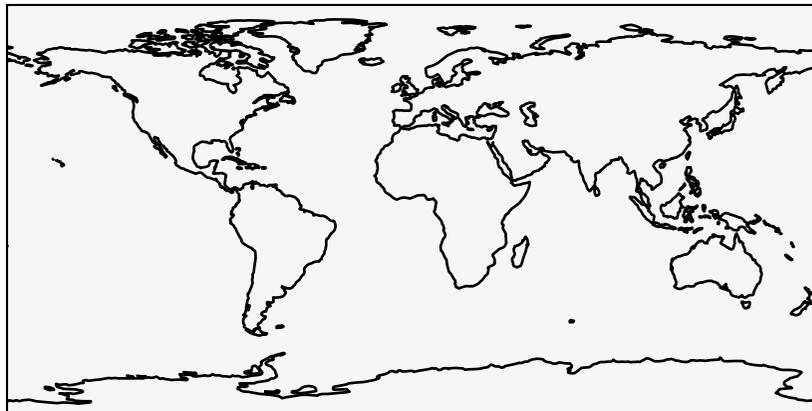
GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



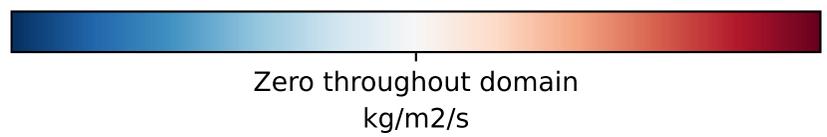
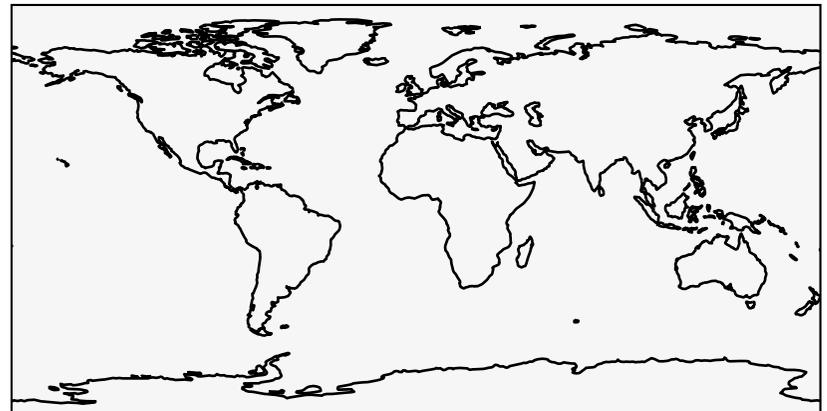
GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



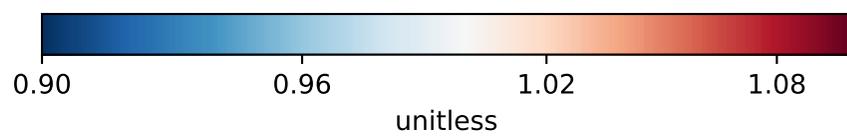
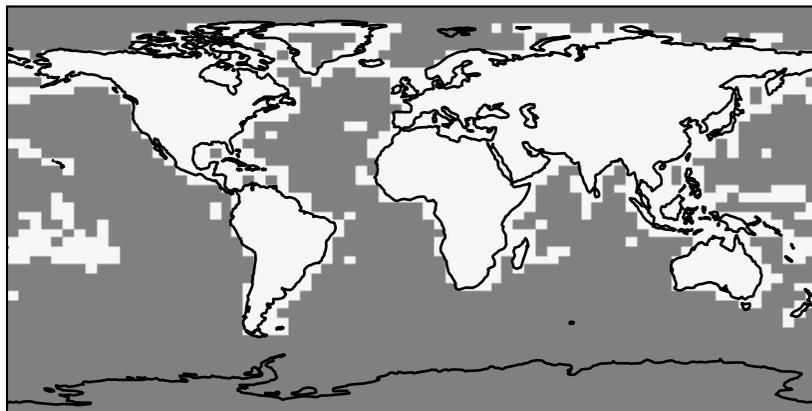
Difference  
Dev - Ref, Dynamic Range



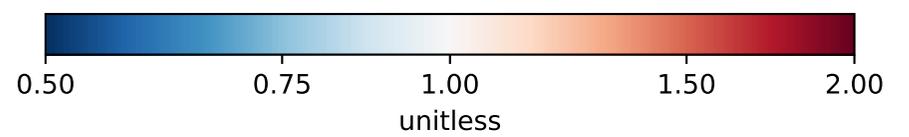
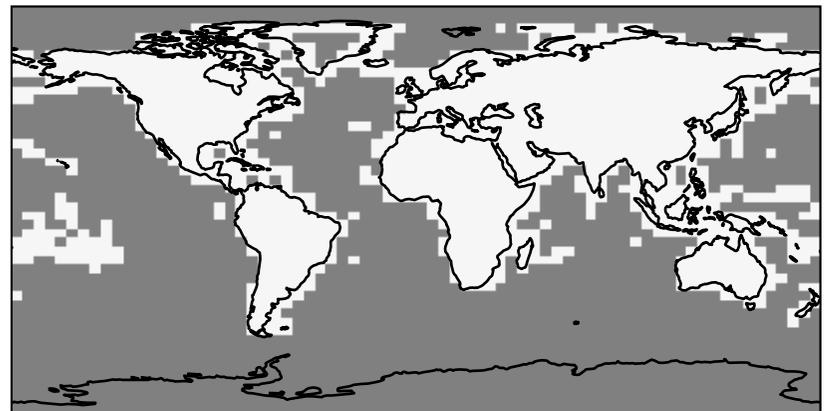
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

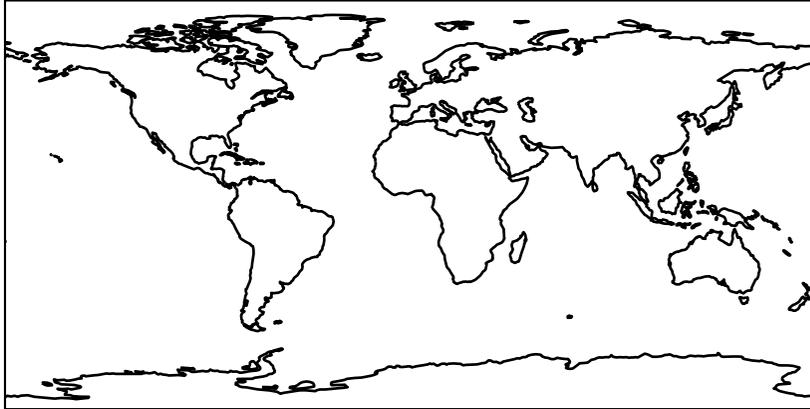


Ratio  
Dev/Ref, Fixed Range



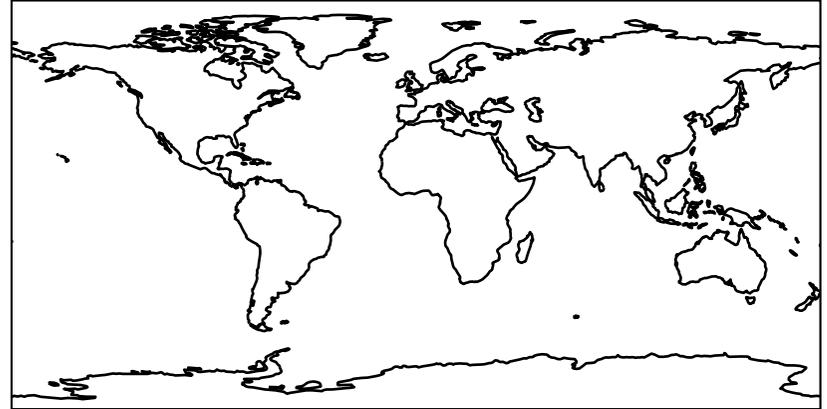
# EmisMVK\_Anthro

GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



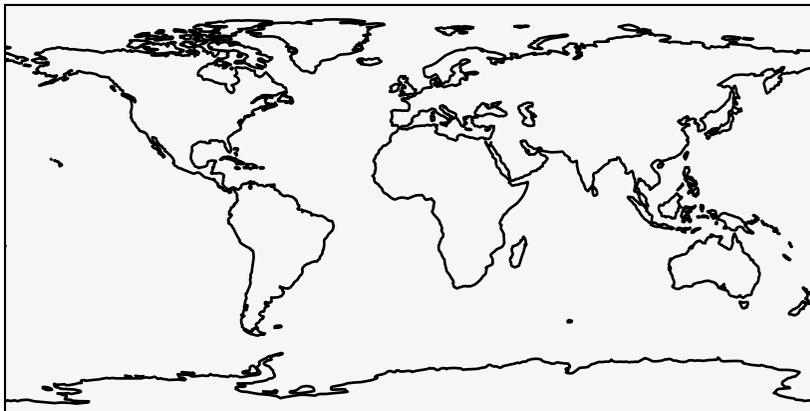
Zero throughout domain  
kg/m2/s

GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



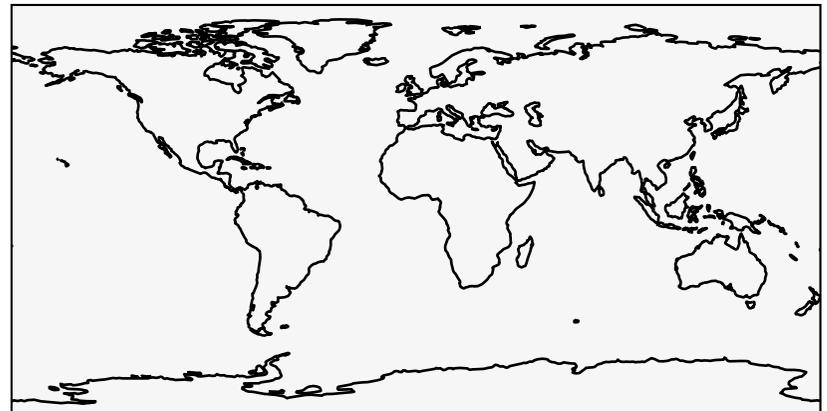
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Dynamic Range



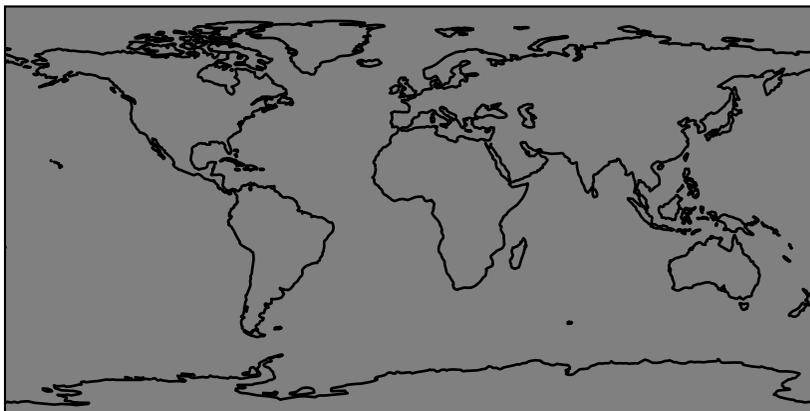
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



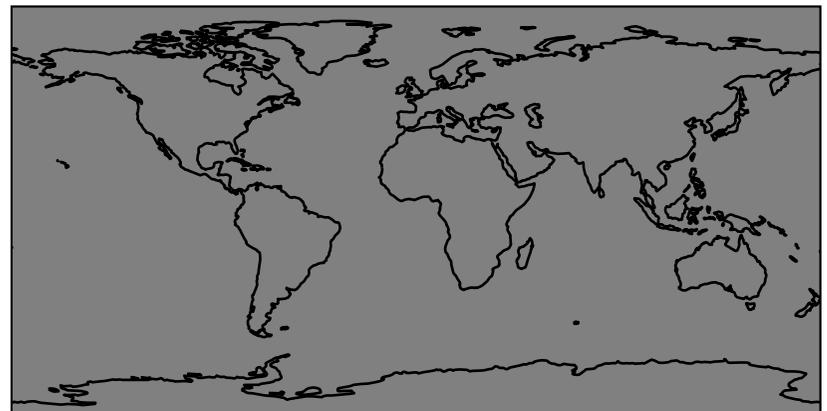
Zero throughout domain  
kg/m2/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

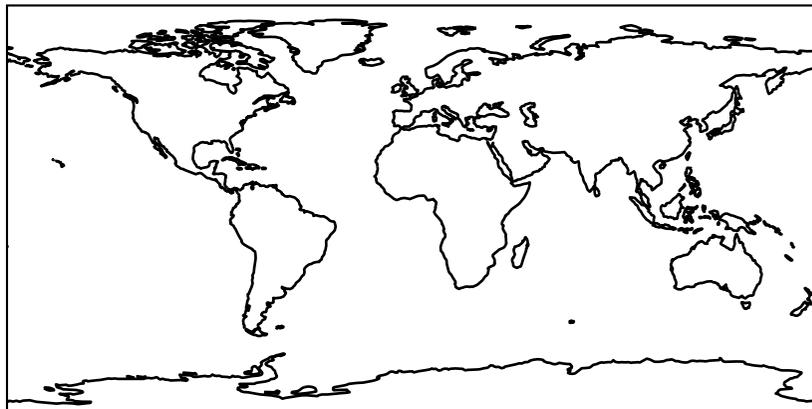
Ratio  
Dev/Ref, Fixed Range



Undefined throughout domain  
unitless

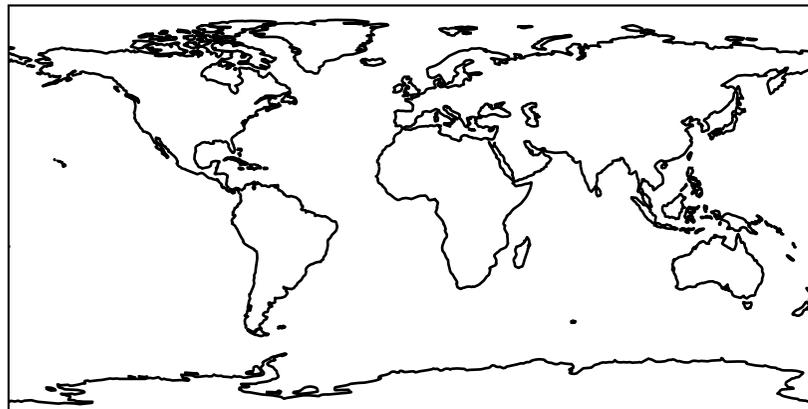
# EmisGLYC\_Anthro

GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



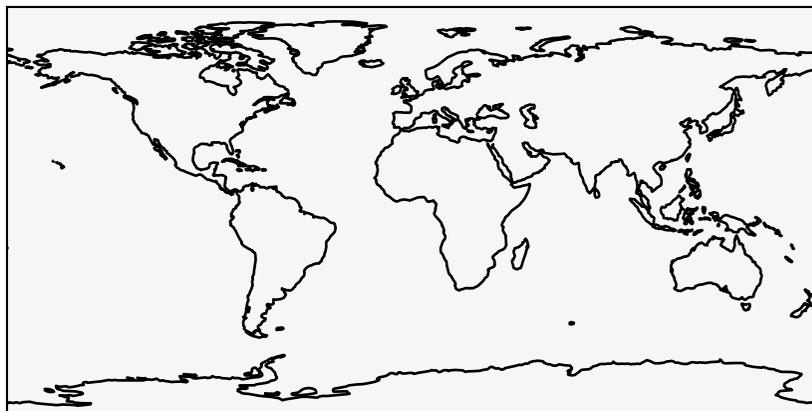
Zero throughout domain  
kg/m2/s

GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



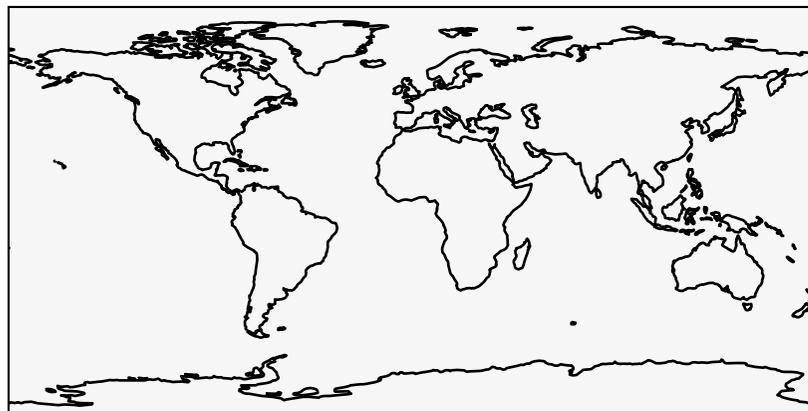
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Dynamic Range



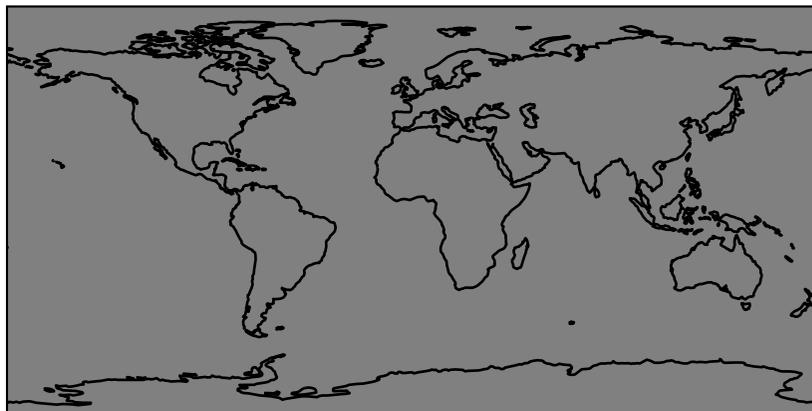
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



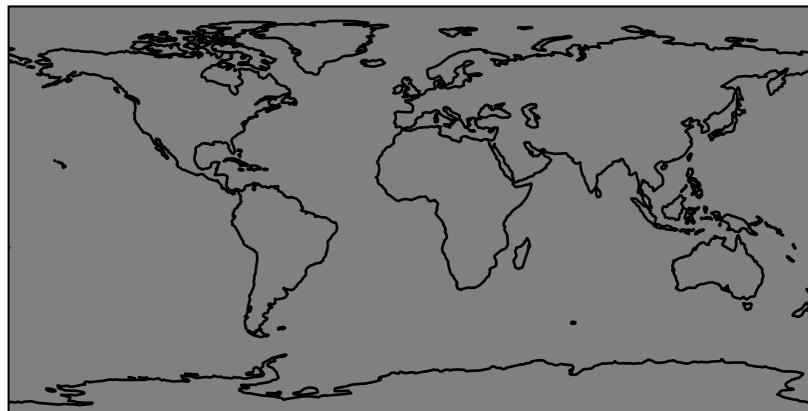
Zero throughout domain  
kg/m2/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

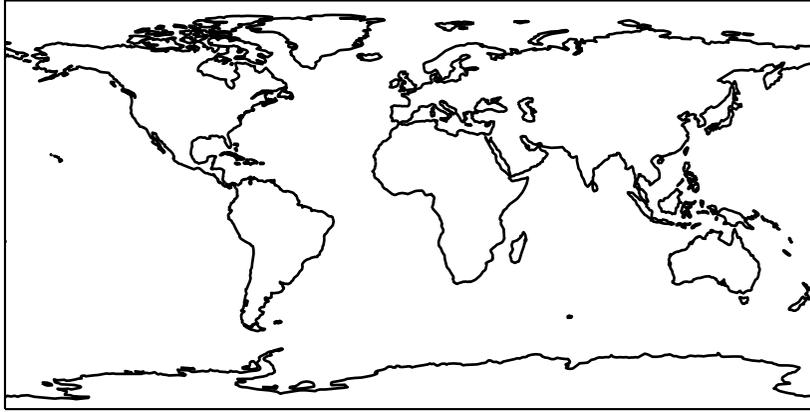
Ratio  
Dev/Ref, Fixed Range



Undefined throughout domain  
unitless

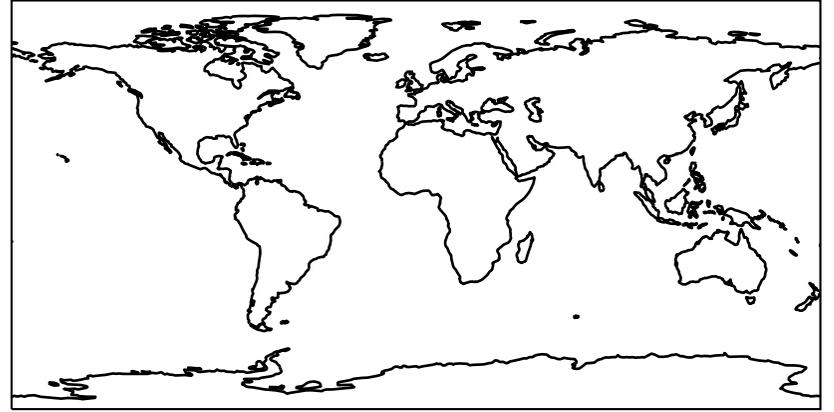
# EmisGLYC\_Total

GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



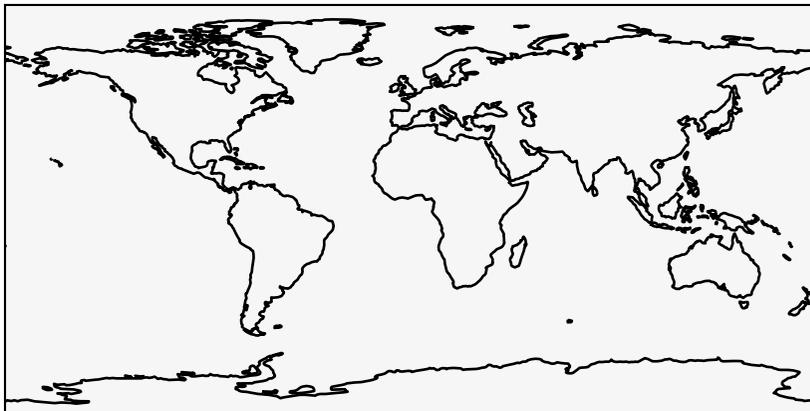
Zero throughout domain  
kg/m2/s

GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



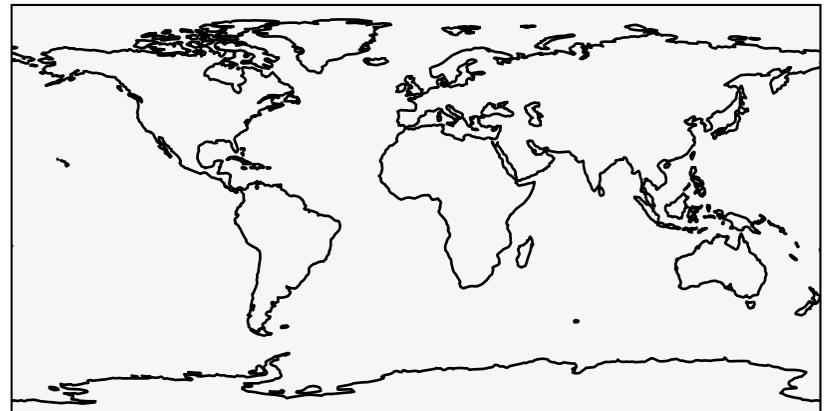
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Dynamic Range



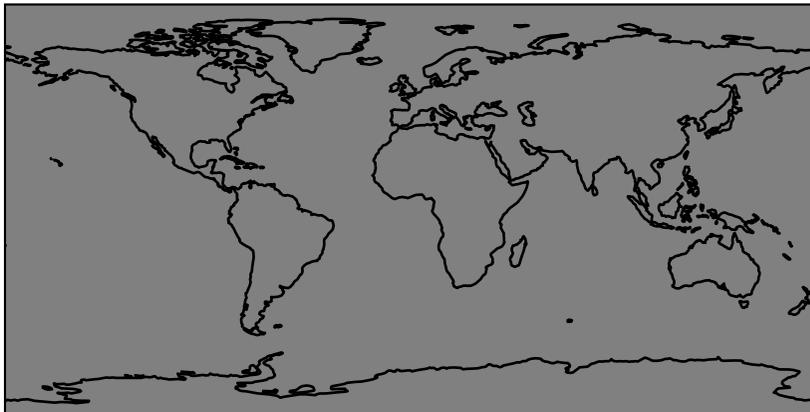
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



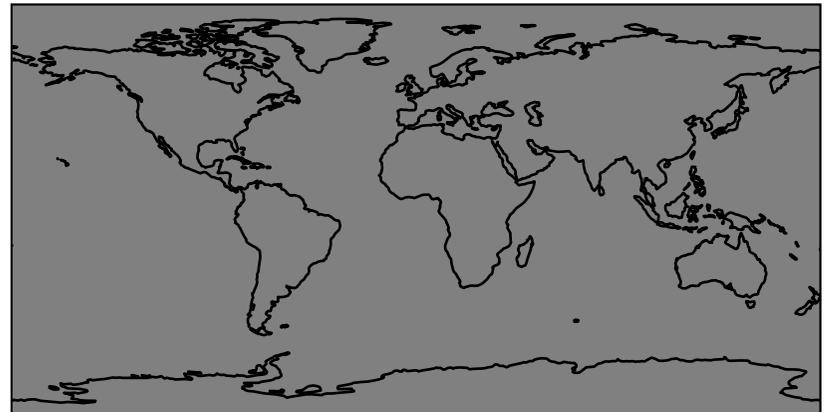
Zero throughout domain  
kg/m2/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

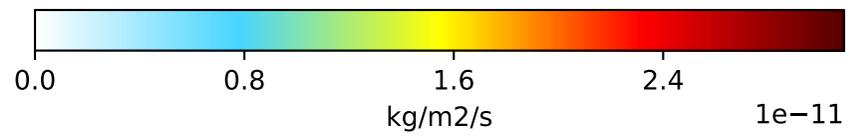
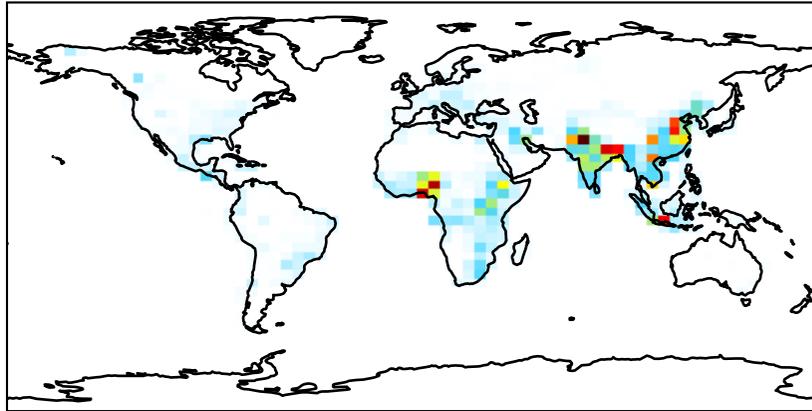
Ratio  
Dev/Ref, Fixed Range



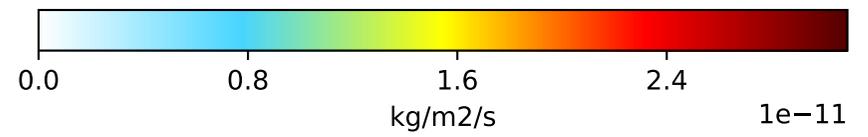
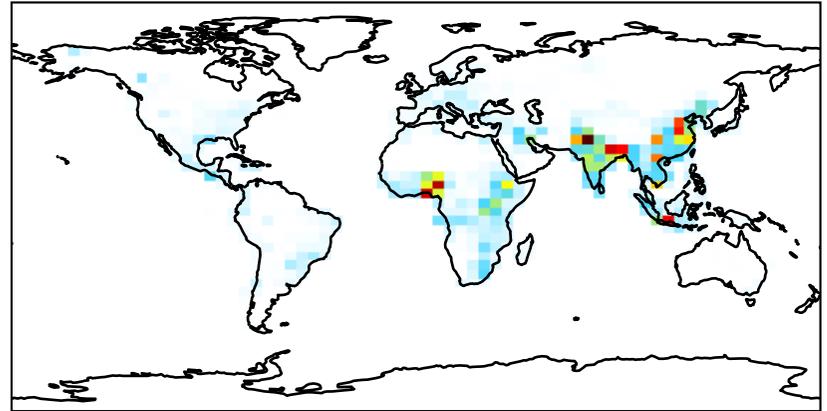
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unitless

# EmisHCOOH\_Anthro

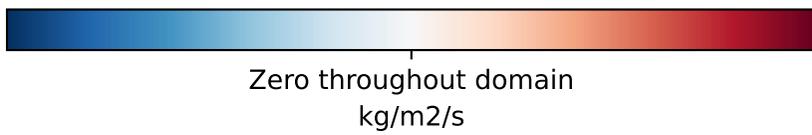
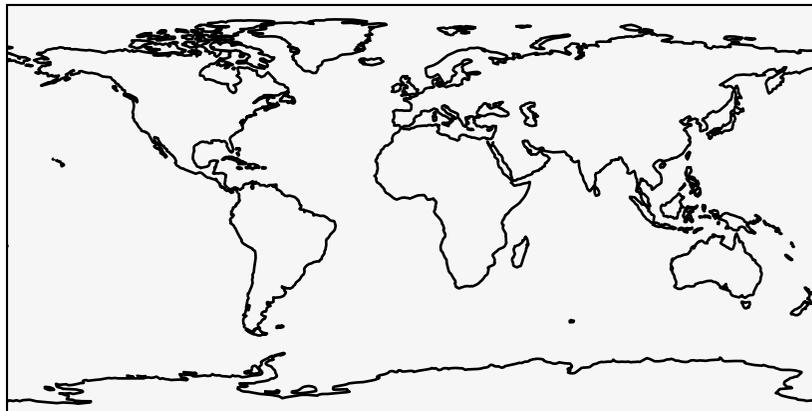
GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



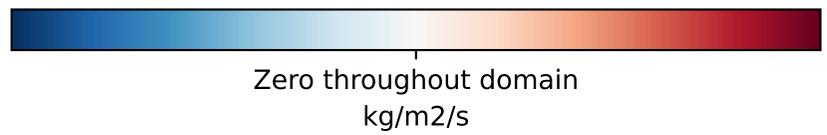
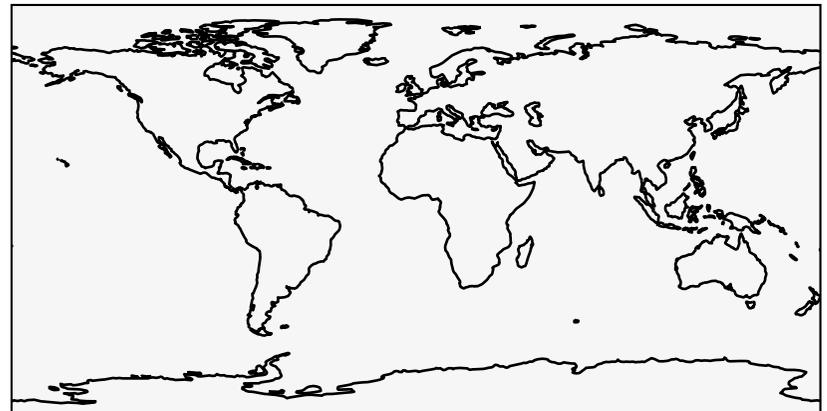
GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



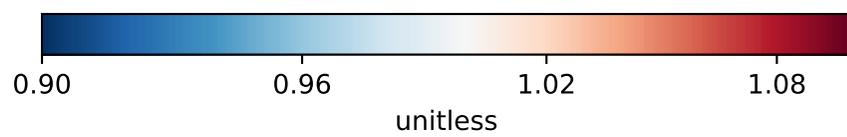
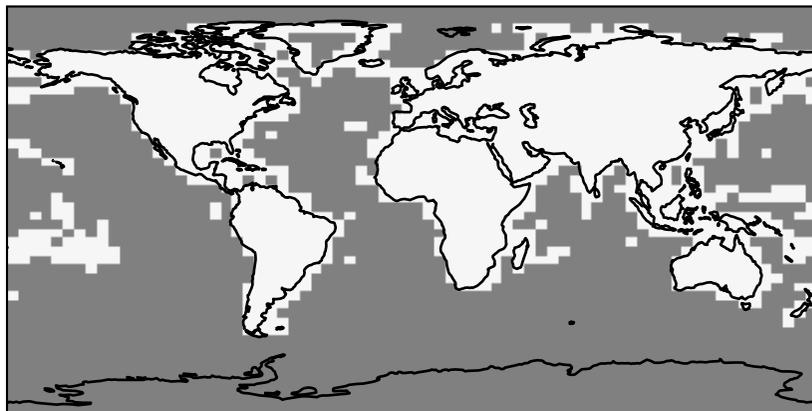
Difference  
Dev - Ref, Dynamic Range



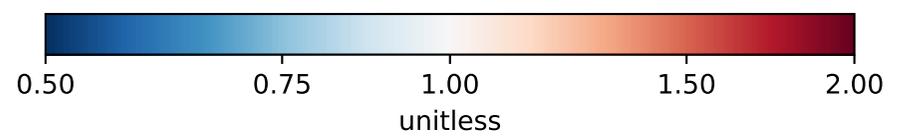
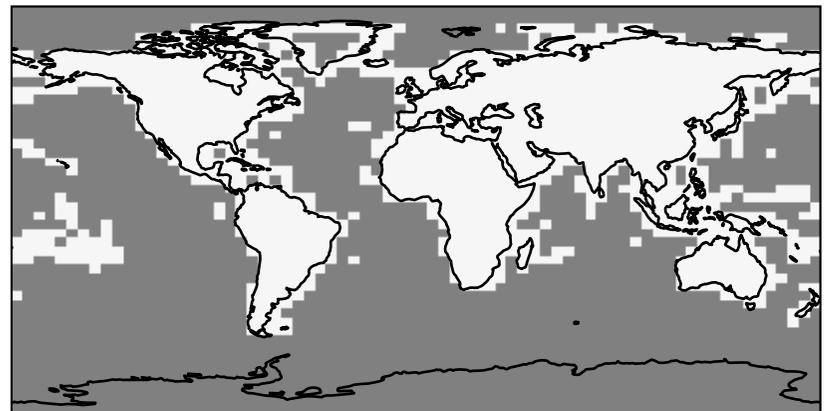
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

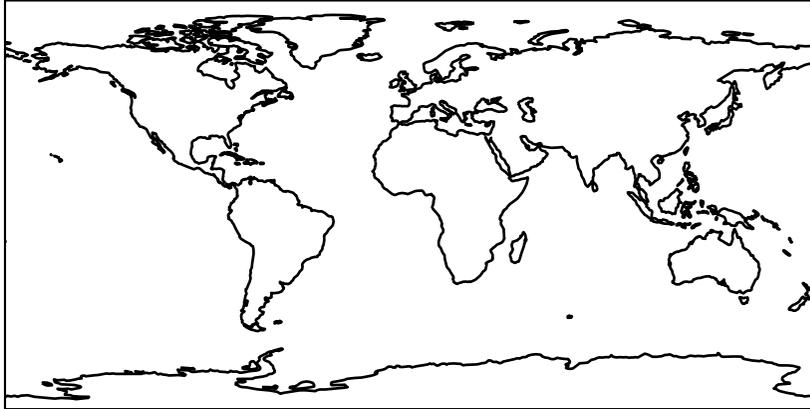


Ratio  
Dev/Ref, Fixed Range



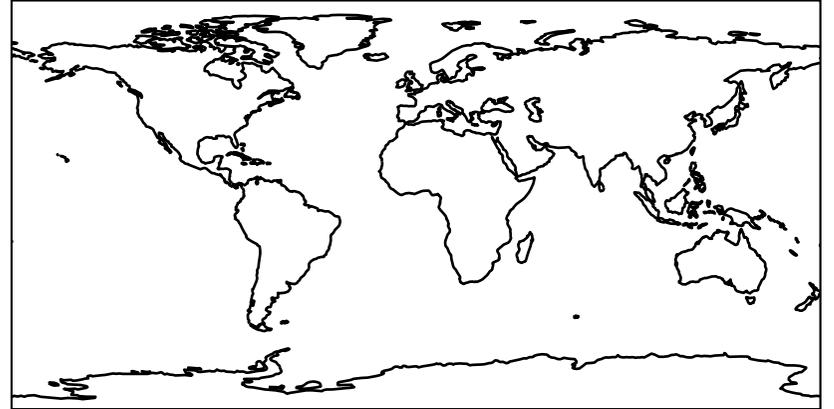
# EmisHCOOH\_Ship

GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



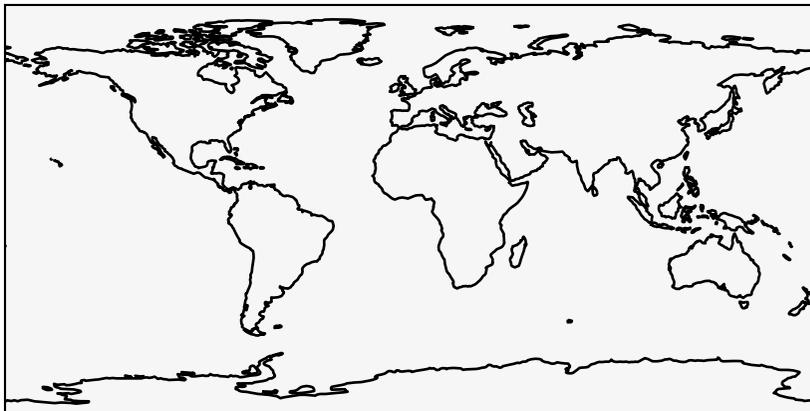
Zero throughout domain  
kg/m2/s

GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



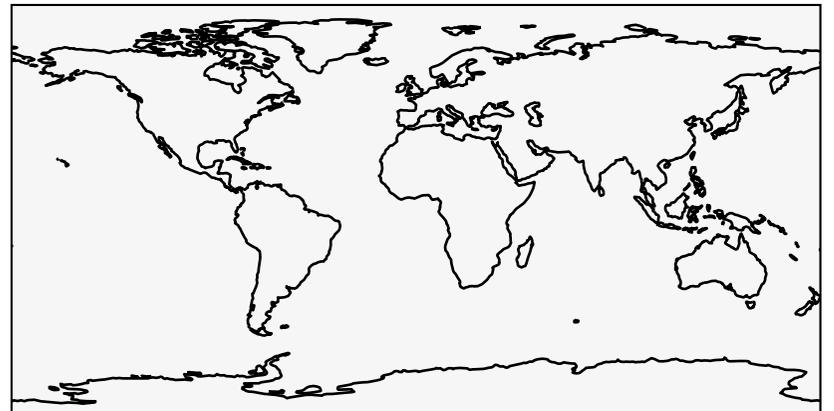
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Dynamic Range



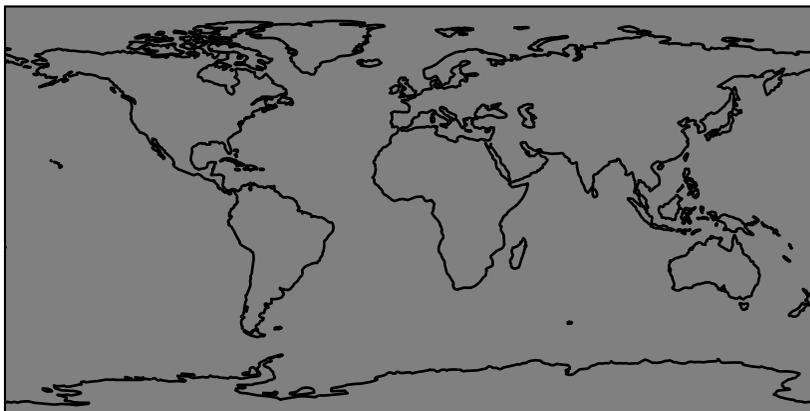
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



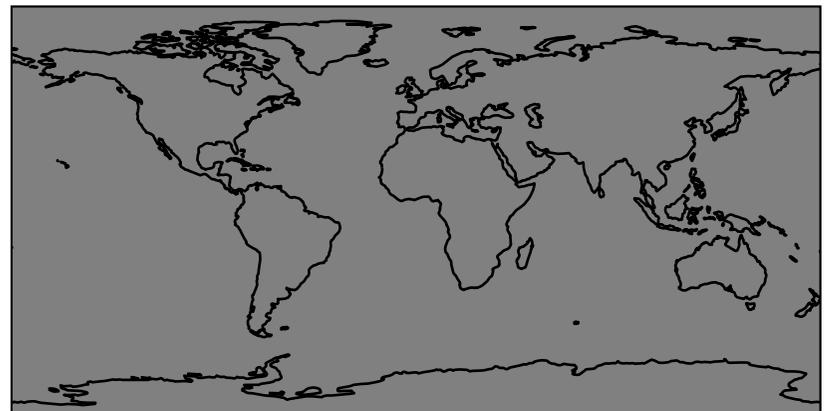
Zero throughout domain  
kg/m2/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

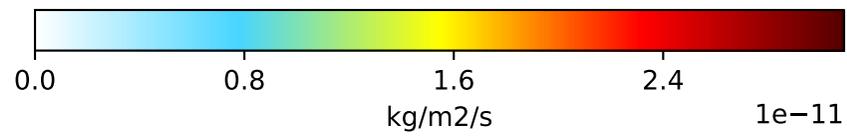
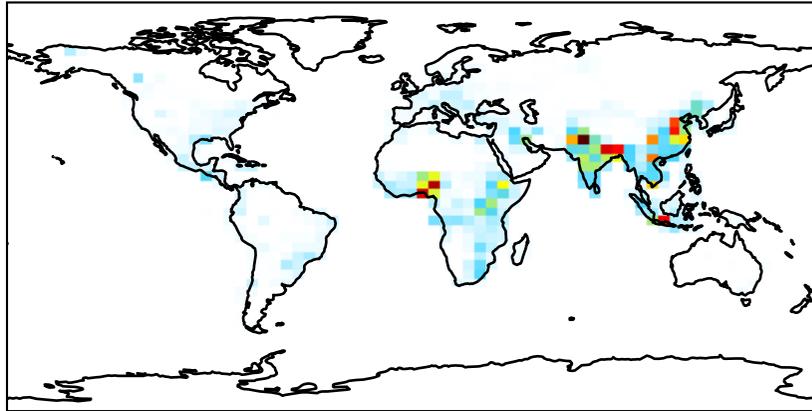
Ratio  
Dev/Ref, Fixed Range



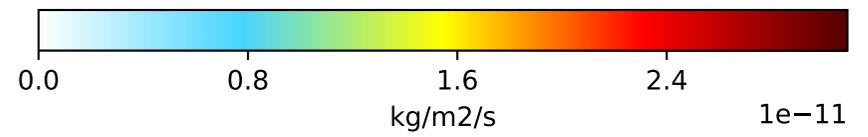
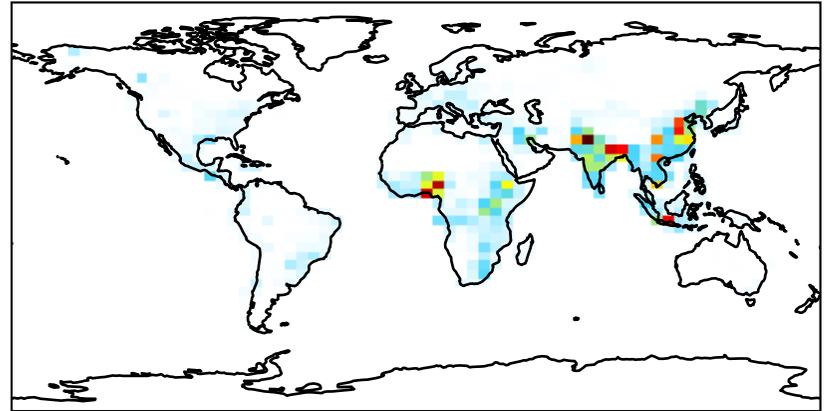
Undefined throughout domain  
unitless

# EmisHCOOH\_Total

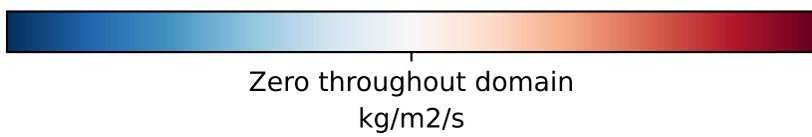
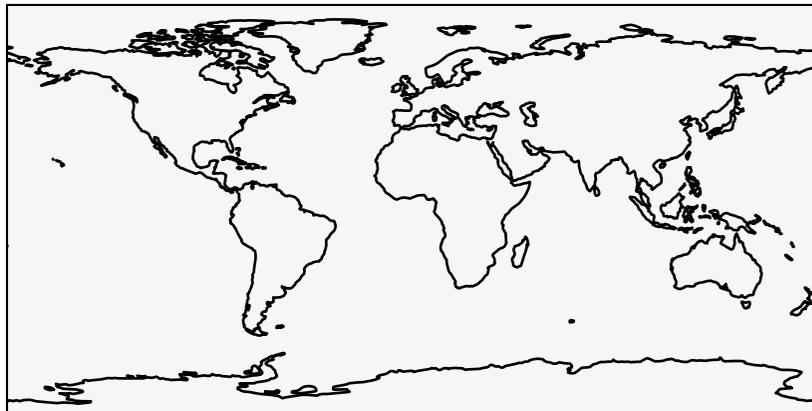
GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



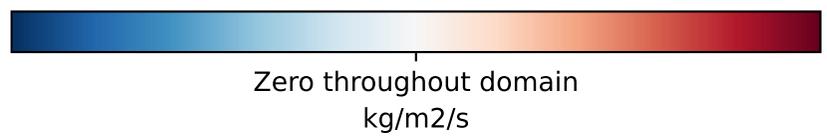
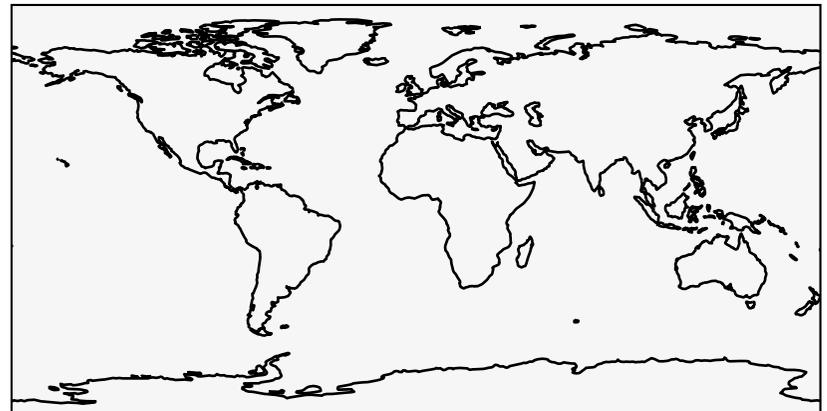
GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



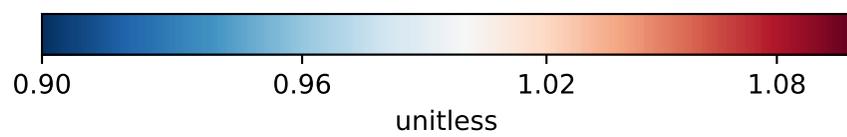
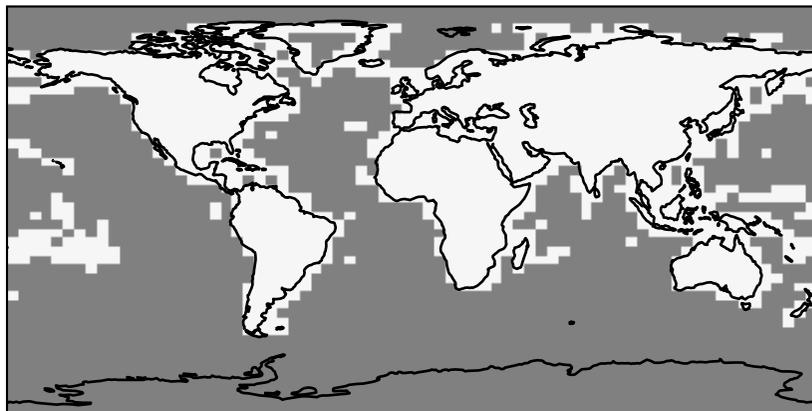
Difference  
Dev - Ref, Dynamic Range



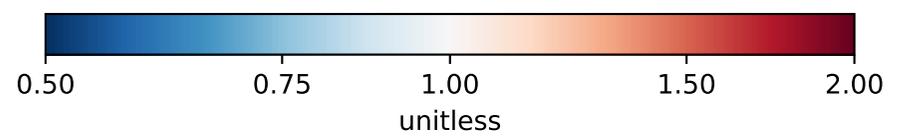
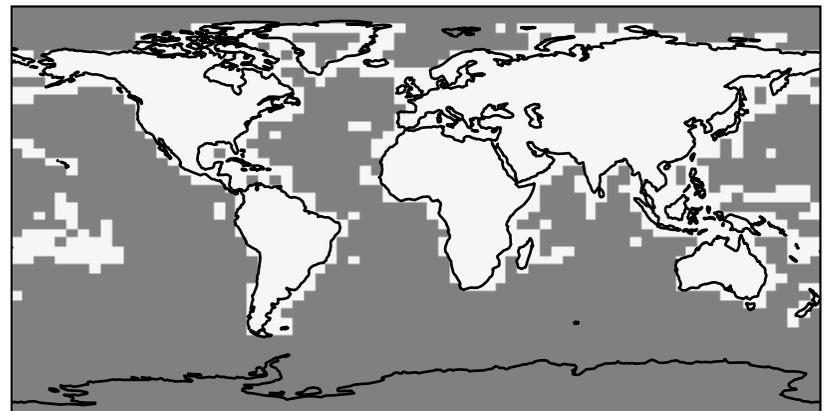
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

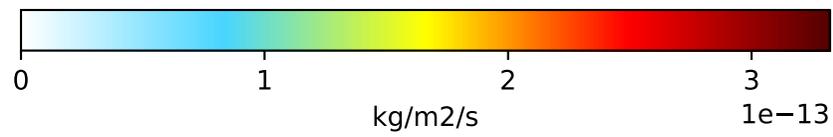
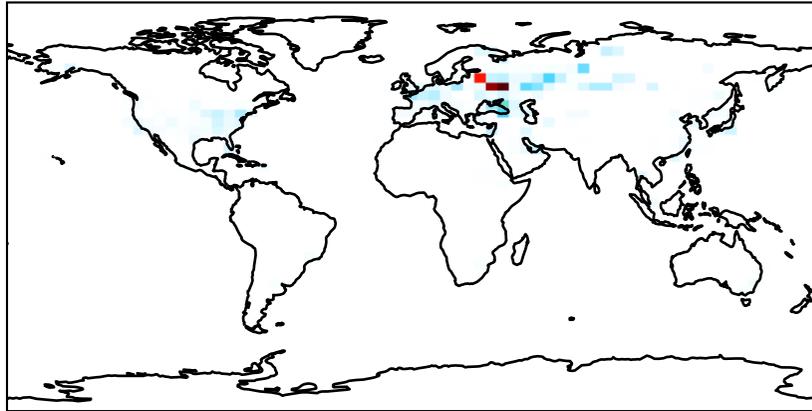


Ratio  
Dev/Ref, Fixed Range

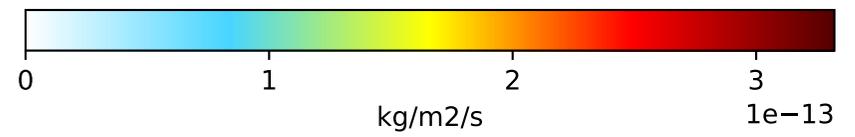
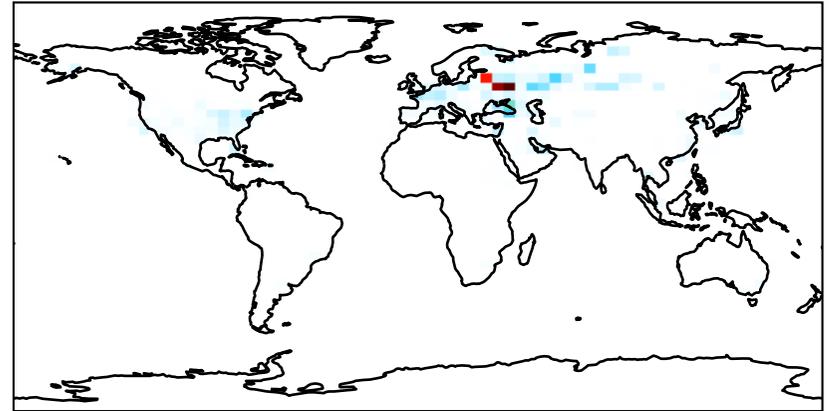


# EmisRCHO\_Aircraft

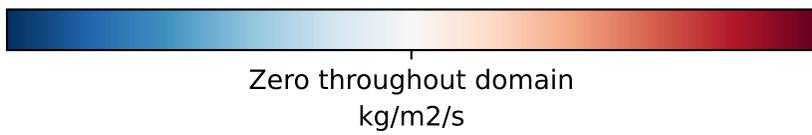
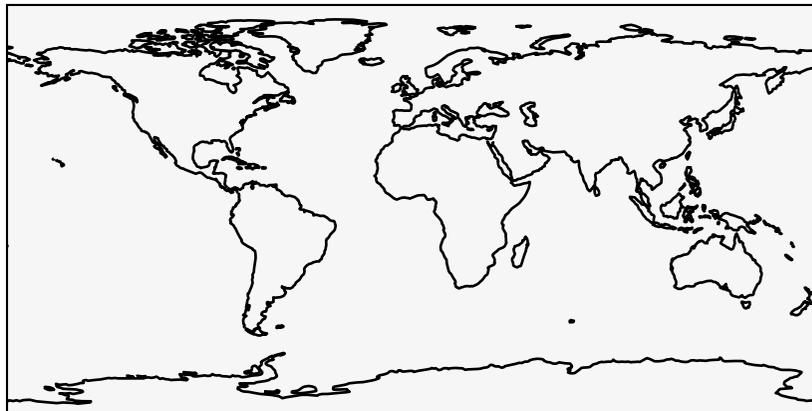
GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



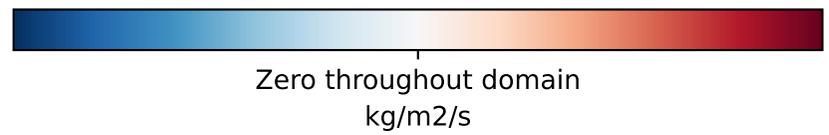
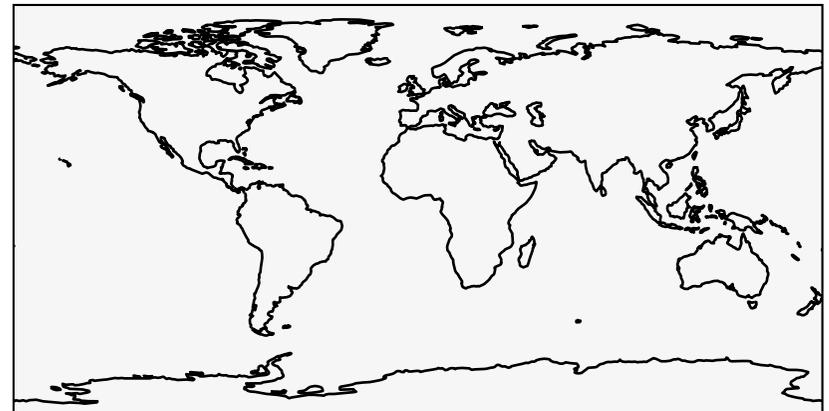
GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



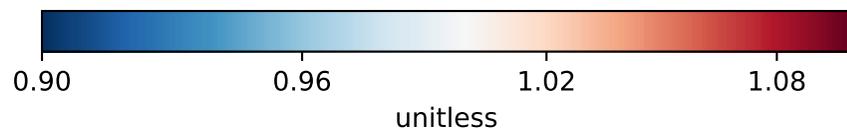
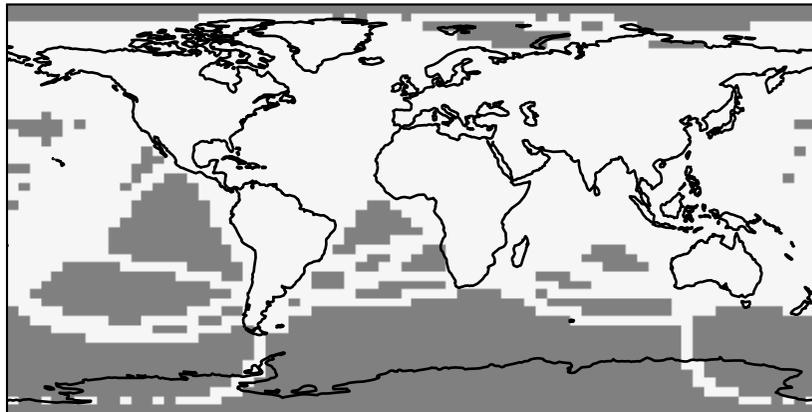
Difference  
Dev - Ref, Dynamic Range



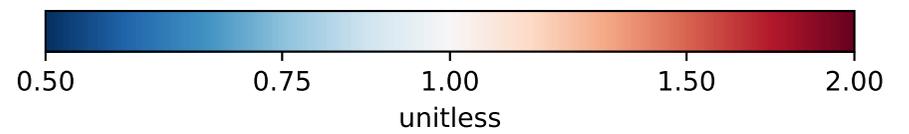
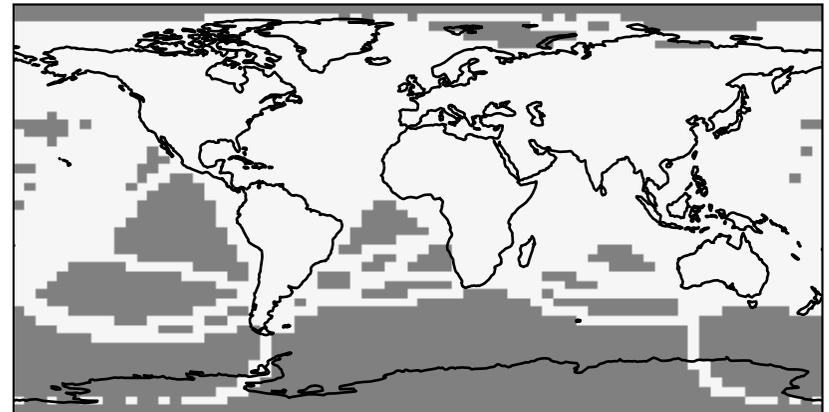
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

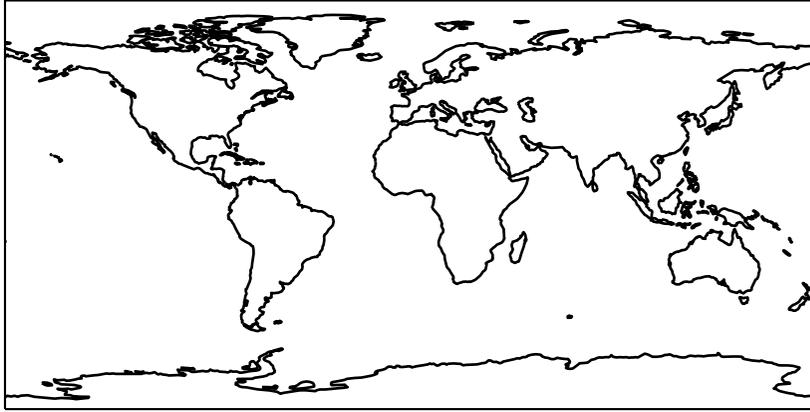


Ratio  
Dev/Ref, Fixed Range



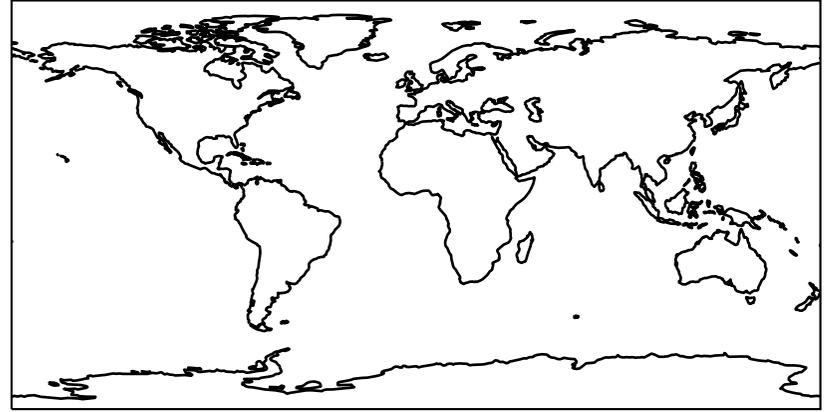
# EmisRCHO\_Anthro

GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



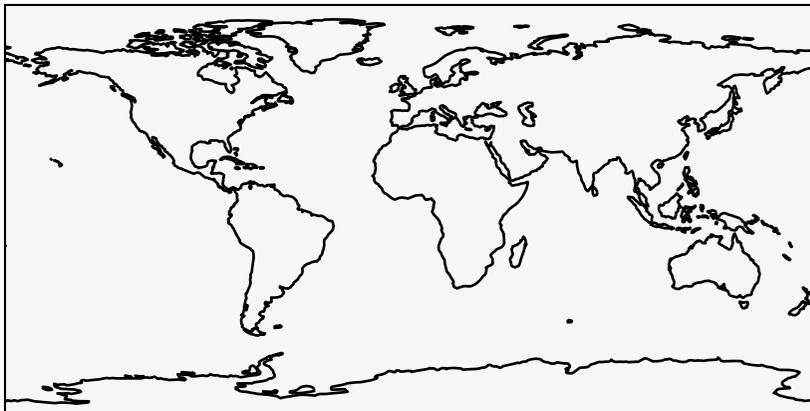
Zero throughout domain  
kg/m2/s

GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



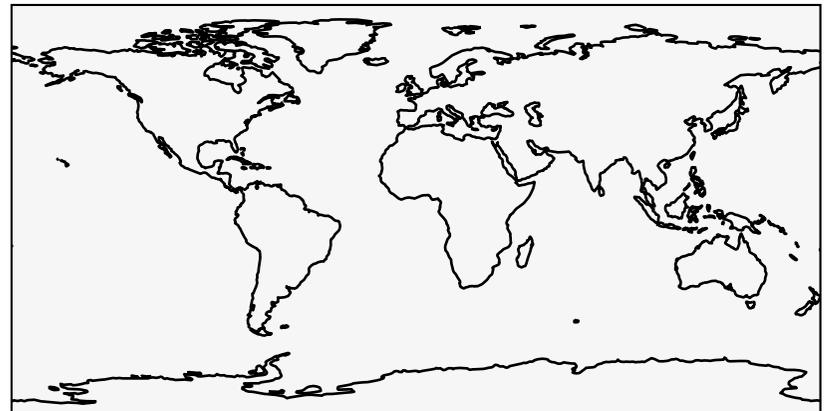
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Dynamic Range



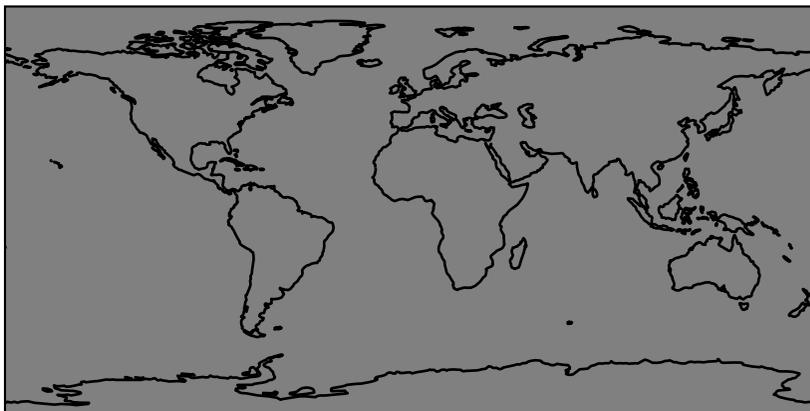
Zero throughout domain  
kg/m2/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



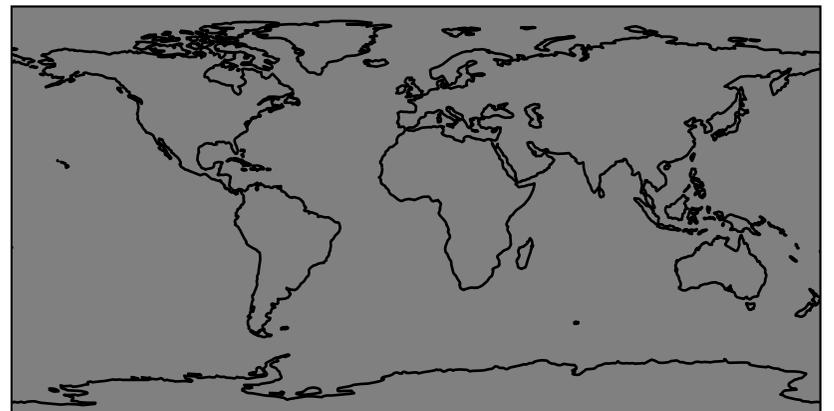
Zero throughout domain  
kg/m2/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

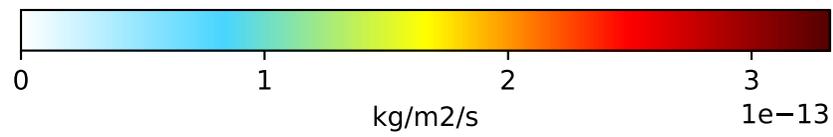
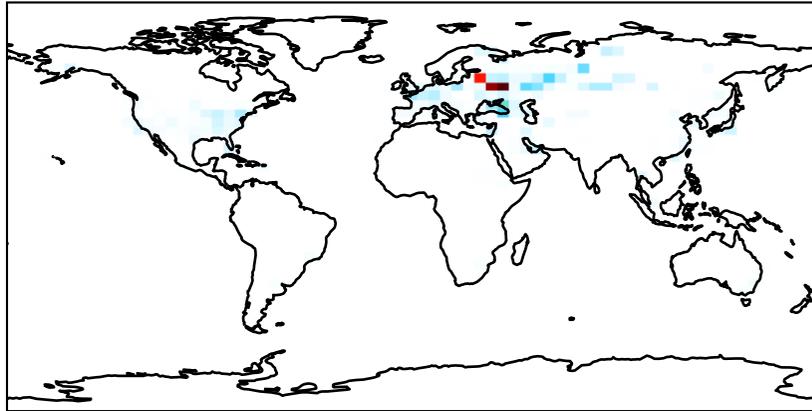
Ratio  
Dev/Ref, Fixed Range



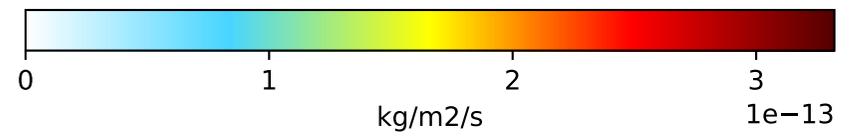
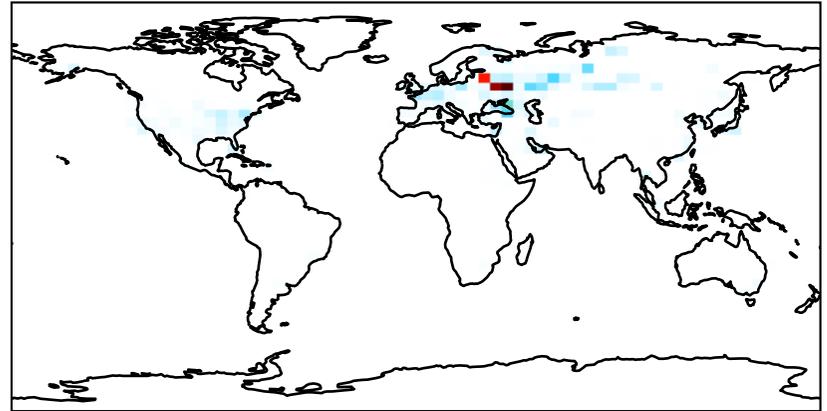
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unitless

# EmisRCHO\_Total

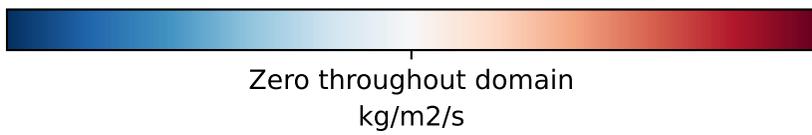
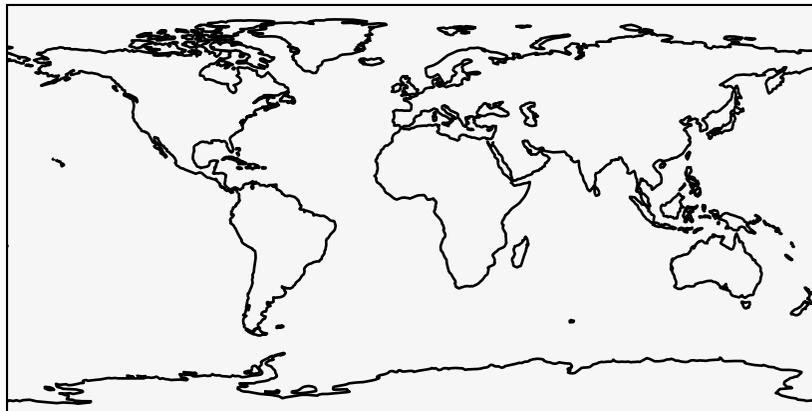
GCC\_13.2.0-beta.0 (Ref)  
4.0x5.0



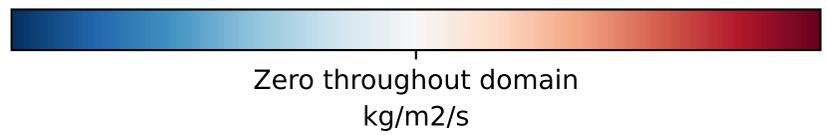
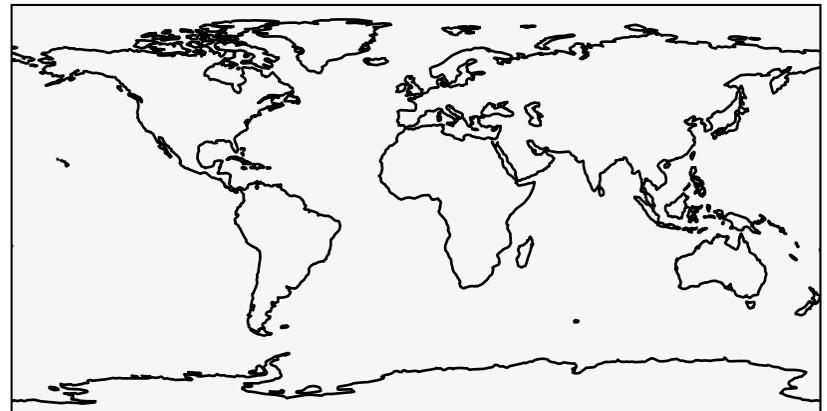
GCC\_13.3.0-rc.0 (Dev)  
4.0x5.0



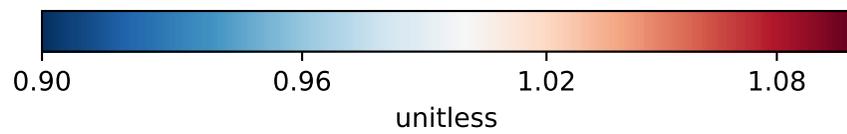
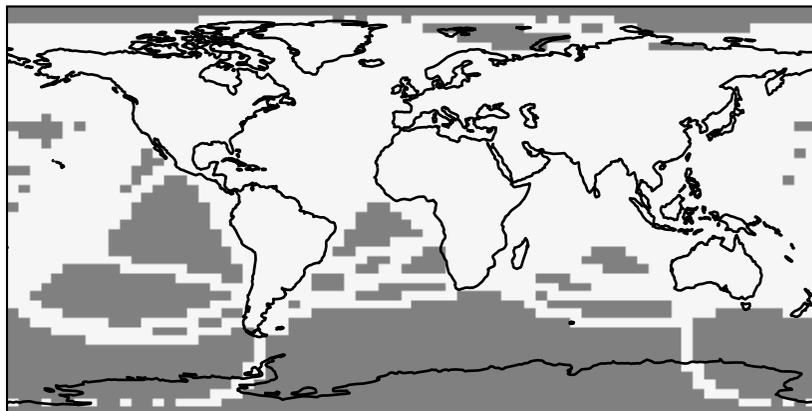
Difference  
Dev - Ref, Dynamic Range



Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

