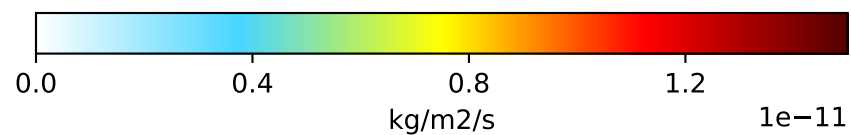
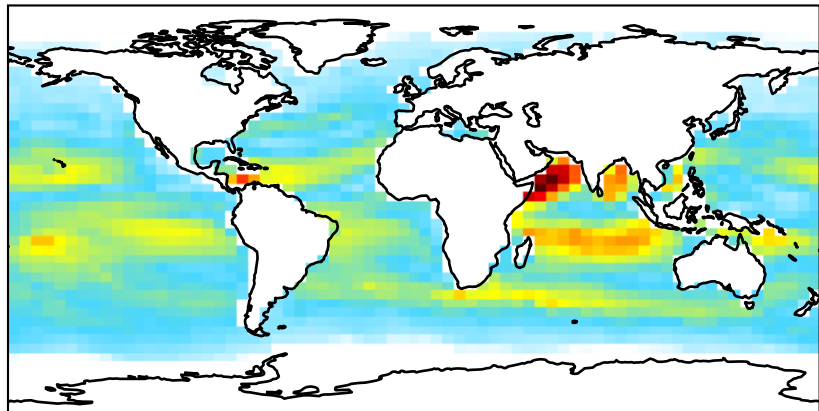
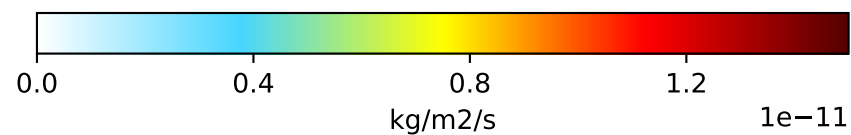
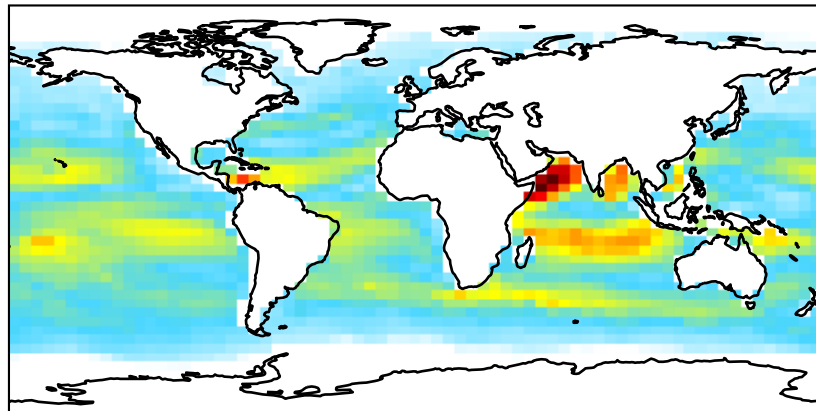


# EmisACET\_Ocean

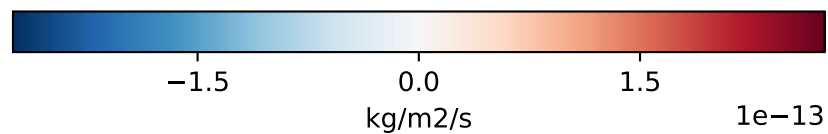
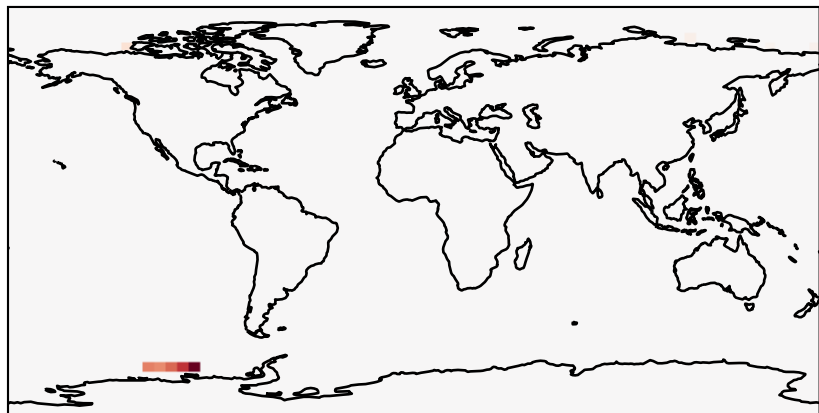
GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



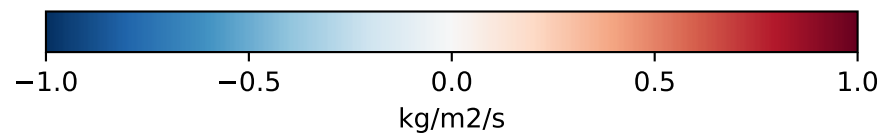
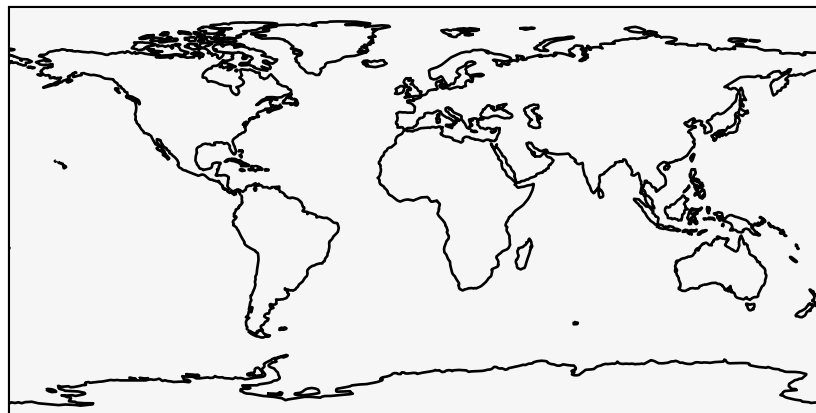
GCC\_13.4.0-rc.0 (Dev)  
4.0x5.0



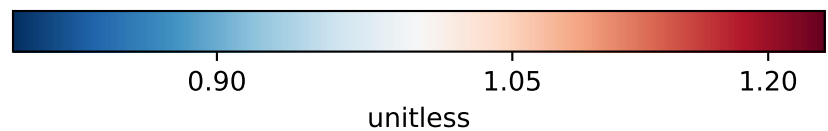
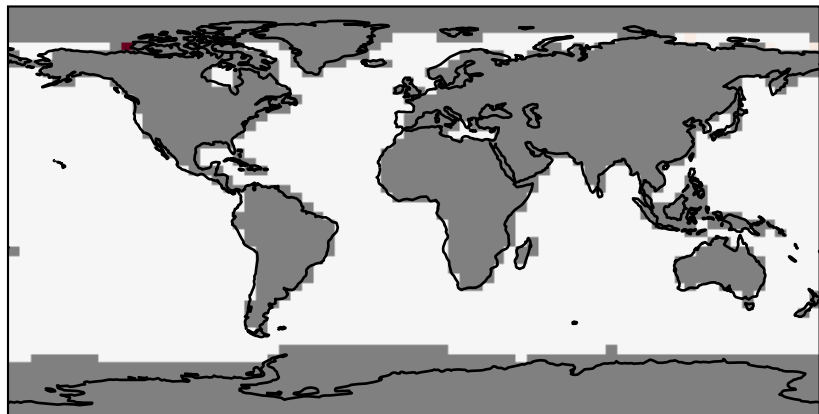
Difference  
Dev - Ref, Dynamic Range



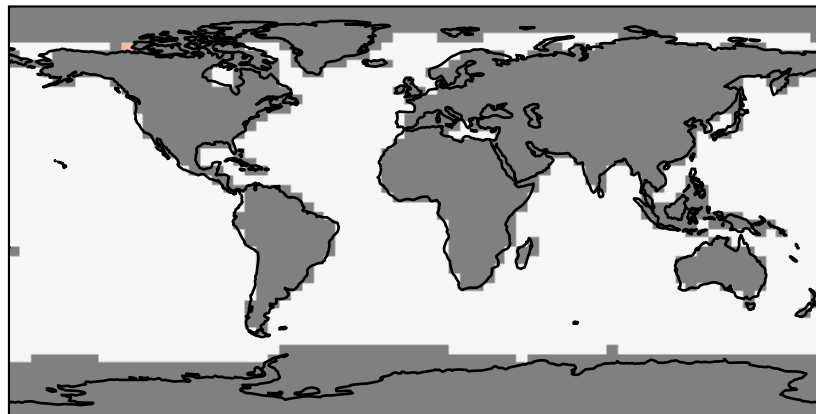
Difference  
Dev - Ref, Restricted Range [5%,95%]



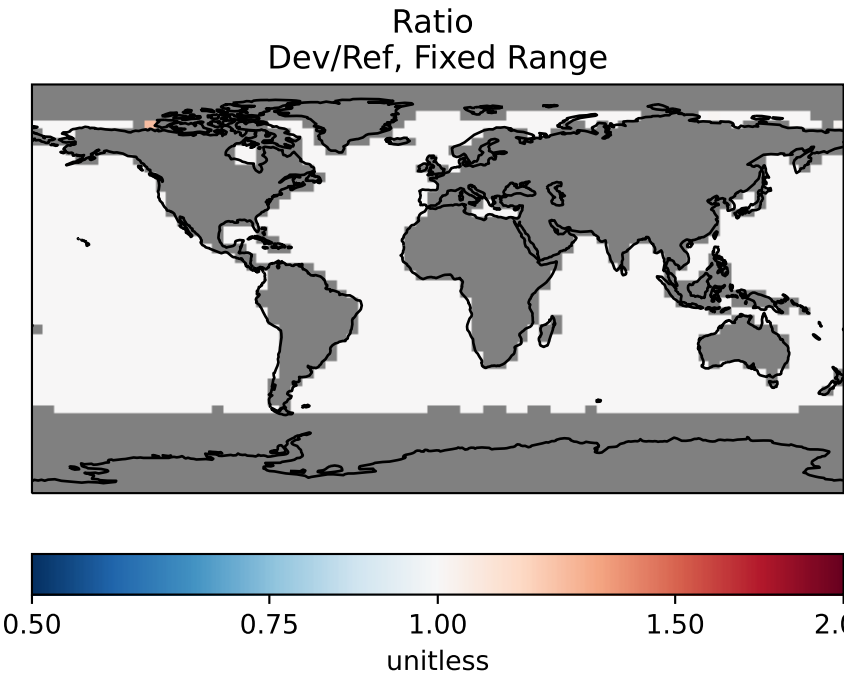
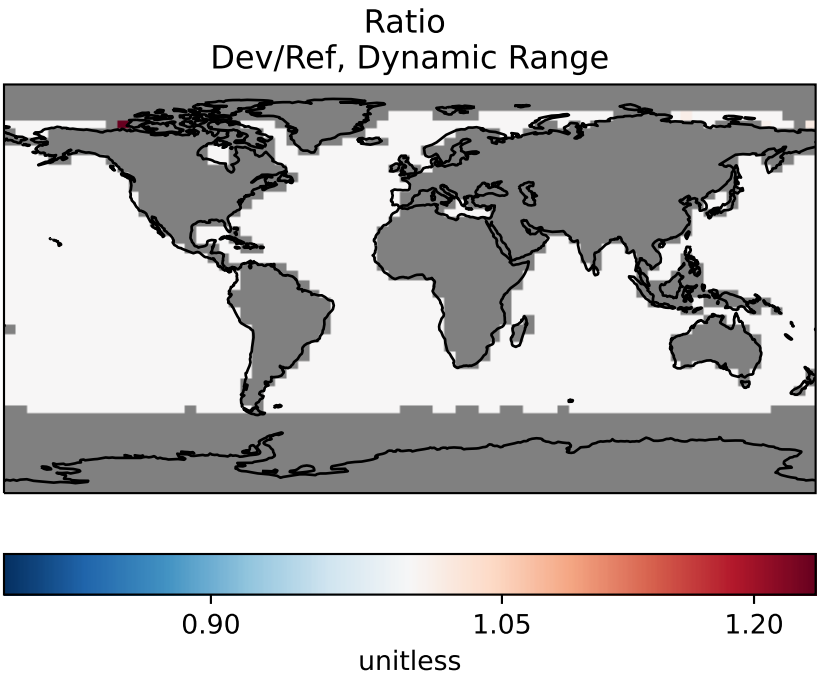
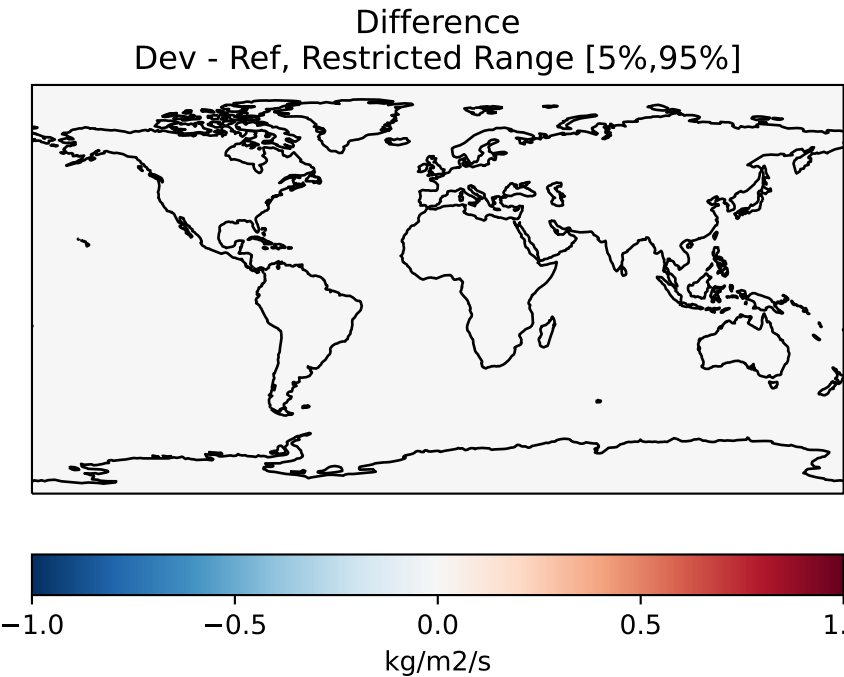
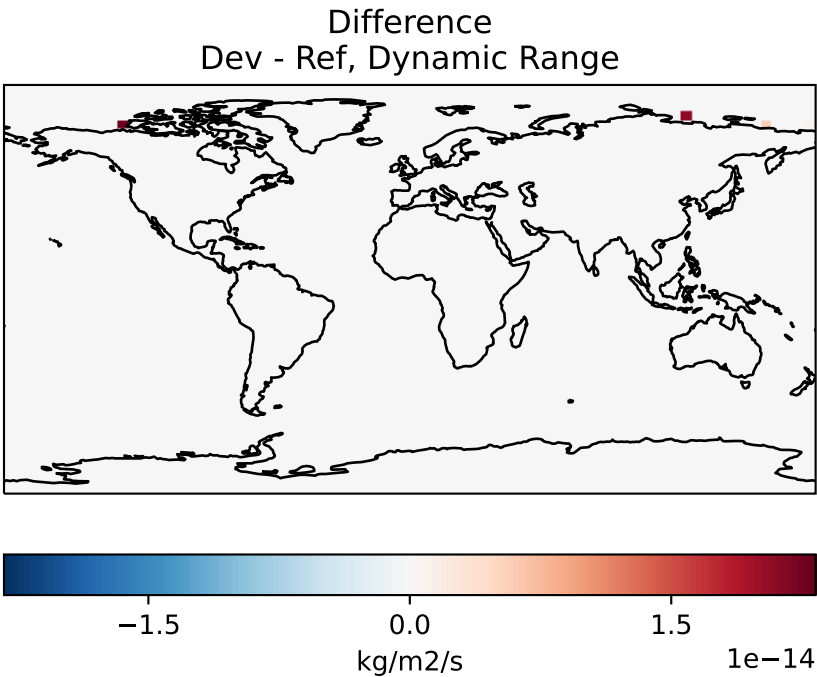
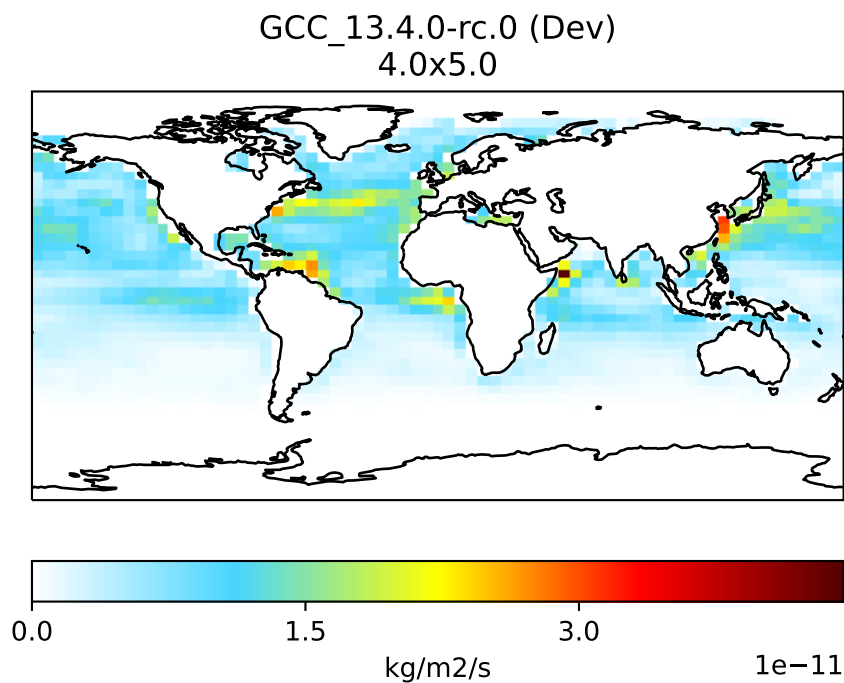
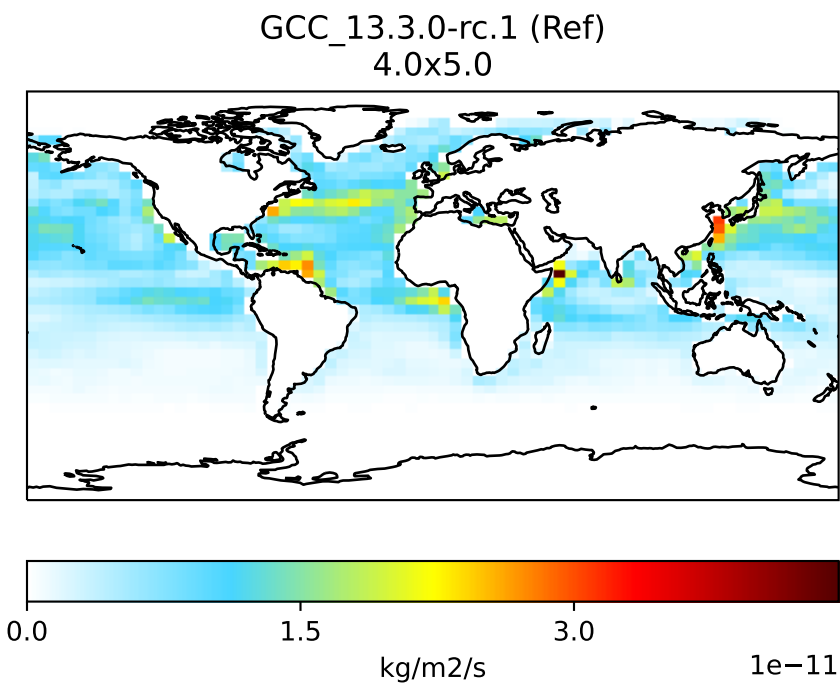
Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

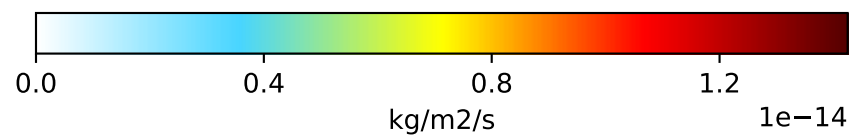
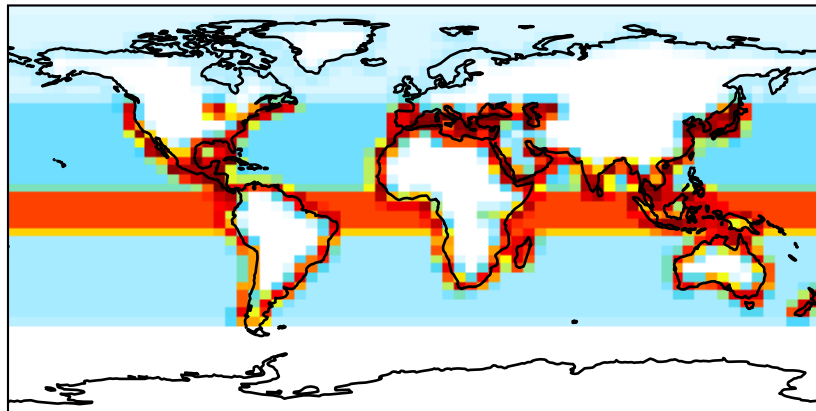


# EmisALD2\_Ocean

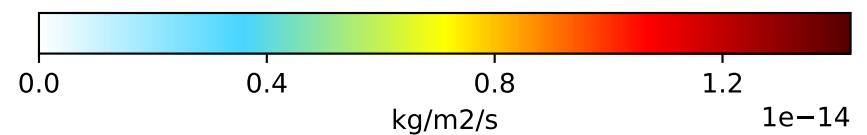
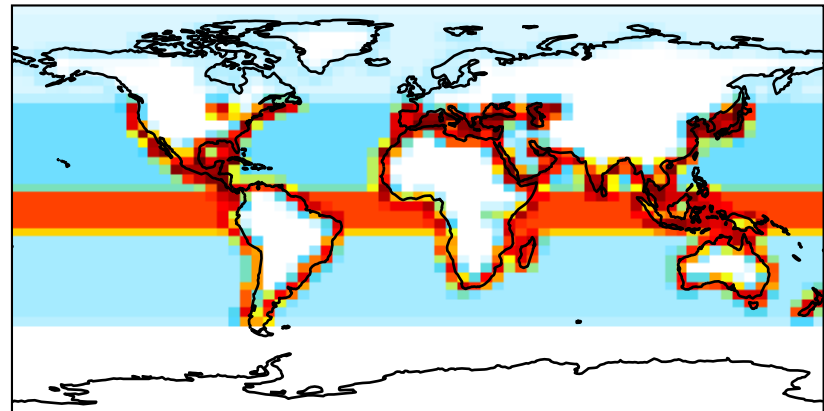


# EmisCH2Br2\_Ocean

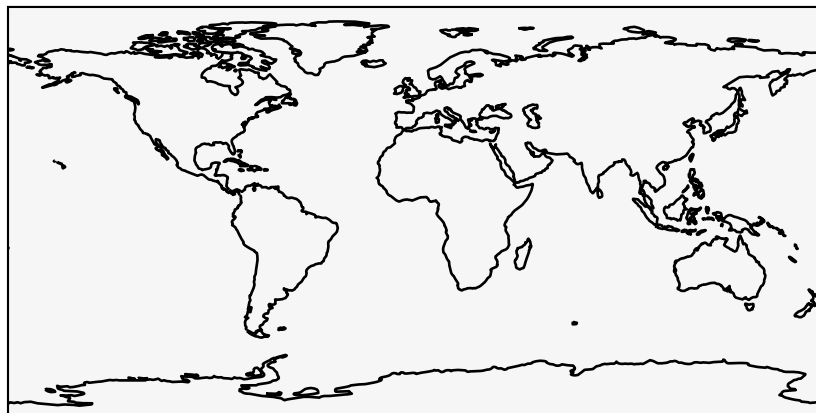
GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



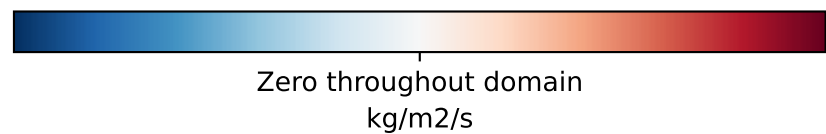
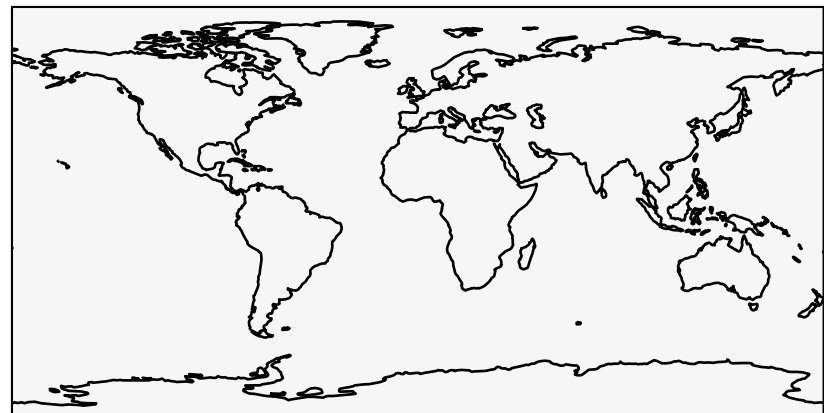
GCC\_13.4.0-rc.0 (Dev)  
4.0x5.0



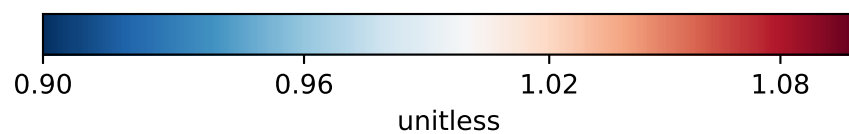
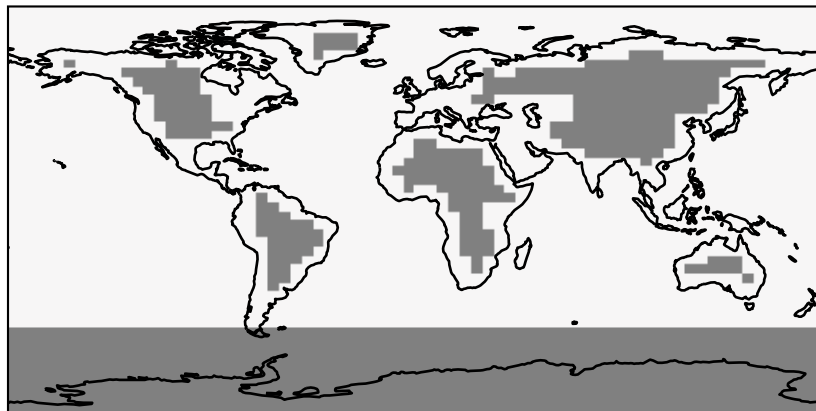
Difference  
Dev - Ref, Dynamic Range



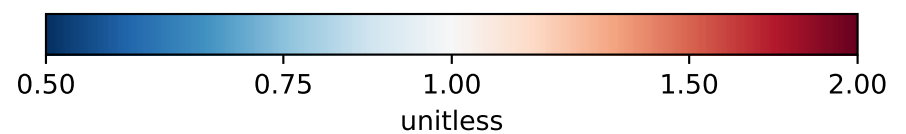
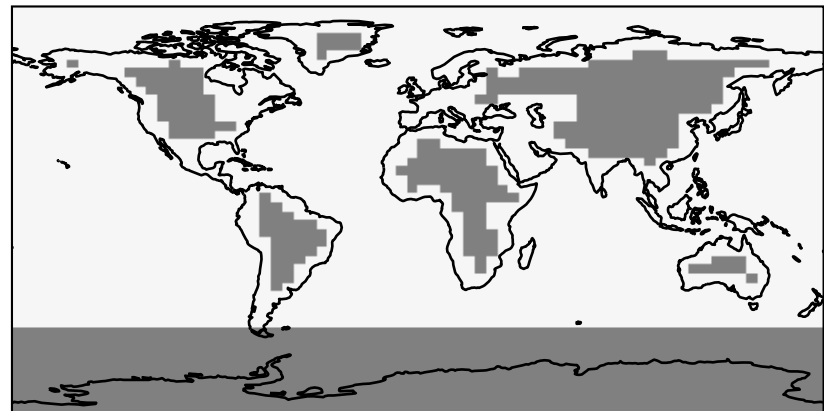
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

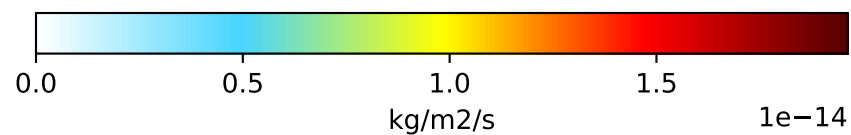
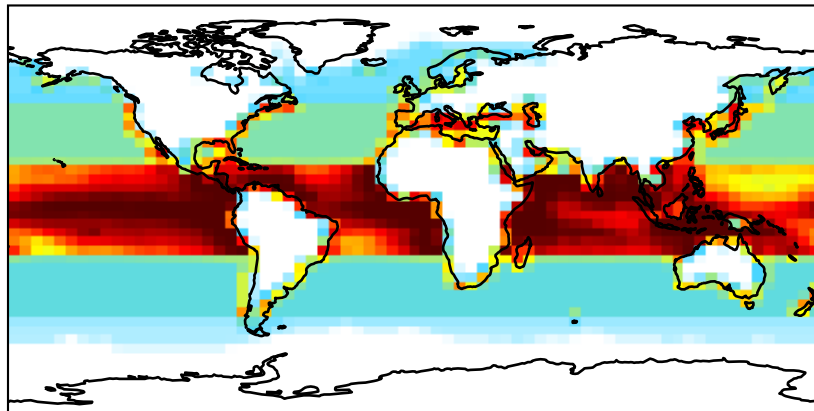


Ratio  
Dev/Ref, Fixed Range

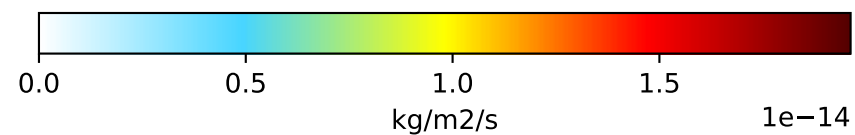
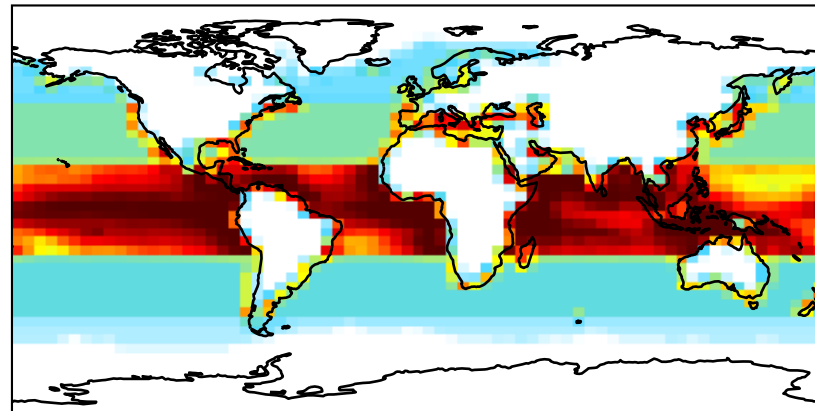


# EmisCH2I2\_Ocean

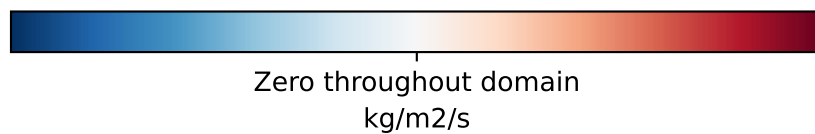
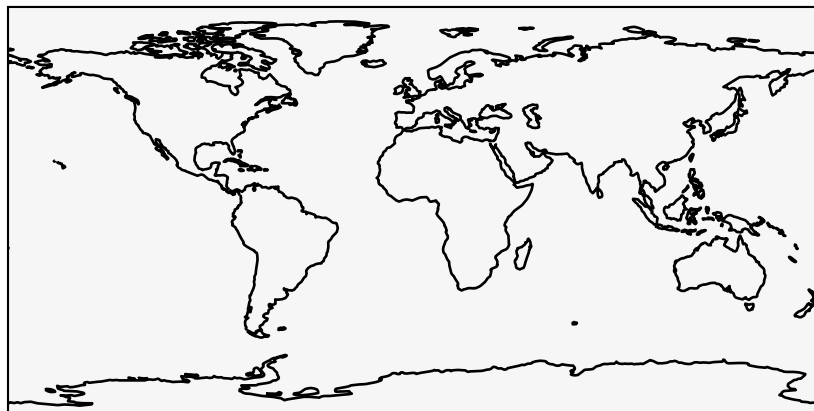
GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



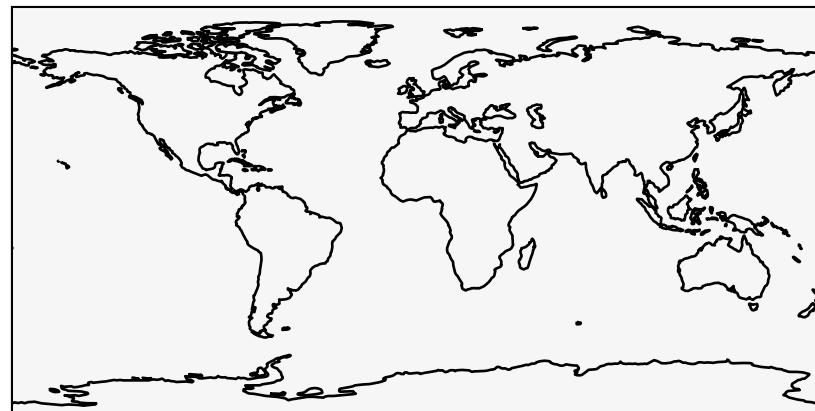
GCC\_13.4.0-rc.0 (Dev)  
4.0x5.0



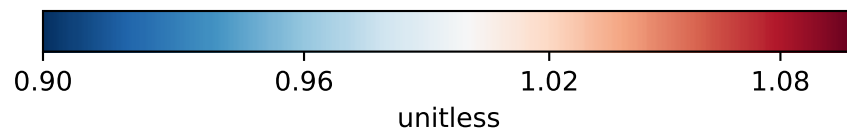
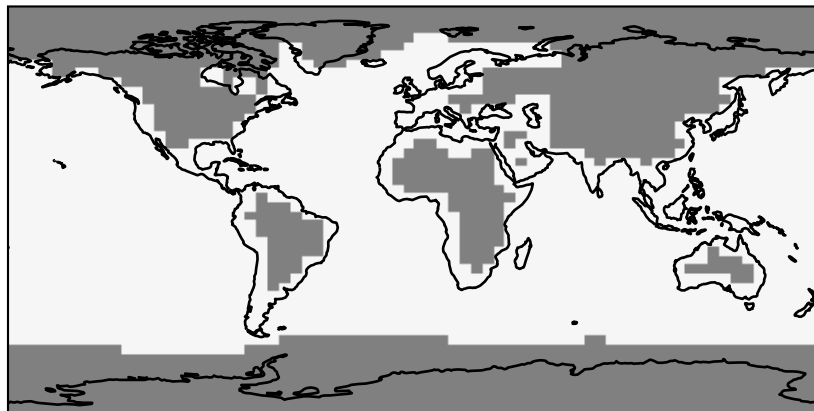
Difference  
Dev - Ref, Dynamic Range



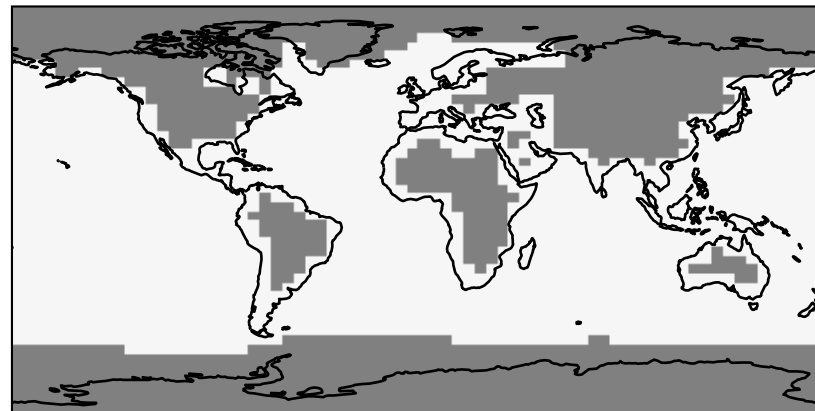
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

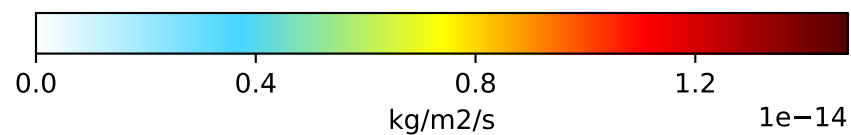
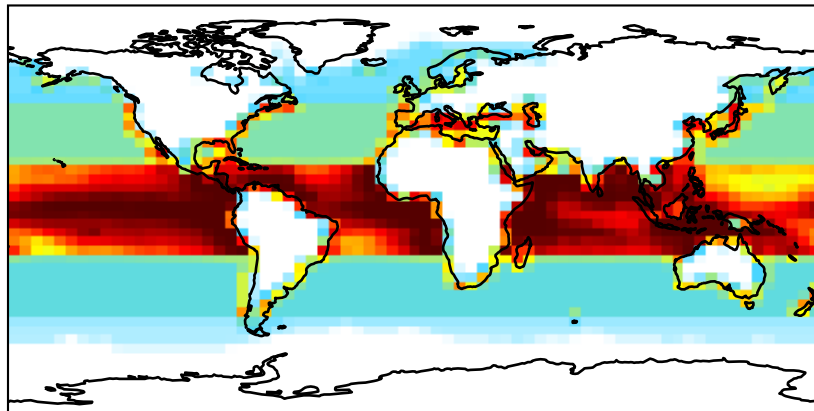


Ratio  
Dev/Ref, Fixed Range

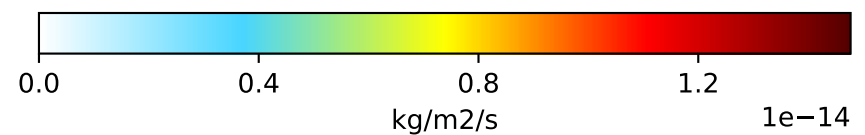
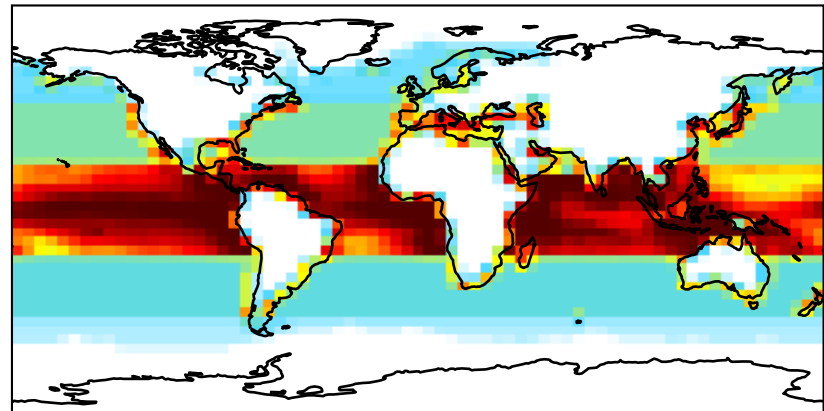


# EmisCH2IBr\_Ocean

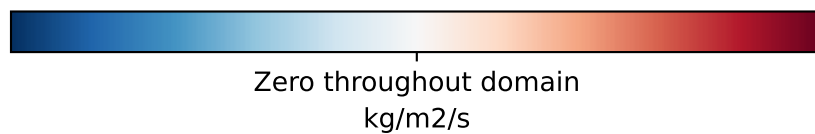
GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



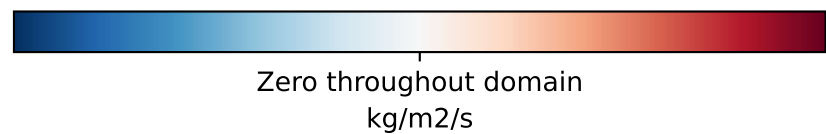
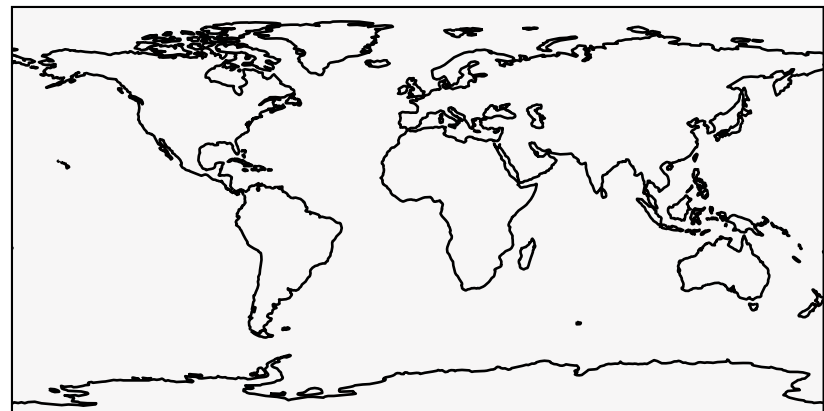
GCC\_13.4.0-rc.0 (Dev)  
4.0x5.0



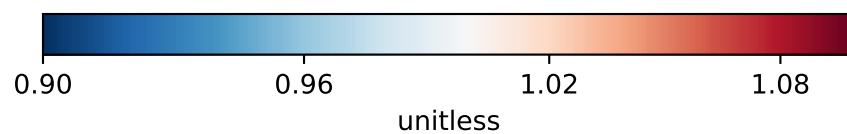
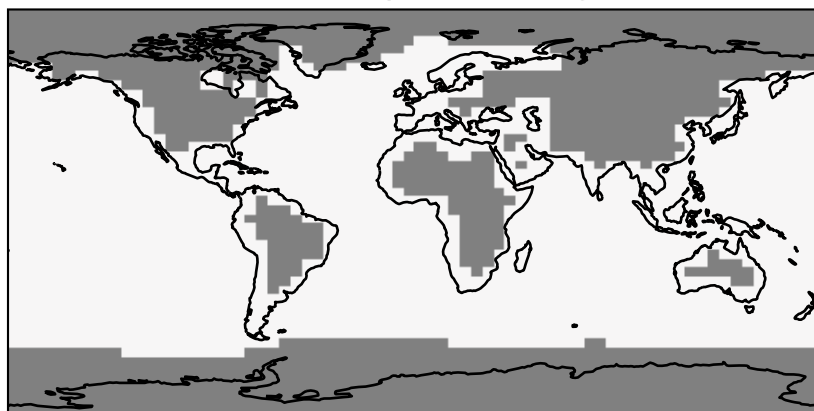
Difference  
Dev - Ref, Dynamic Range



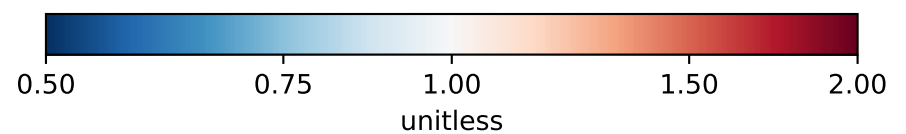
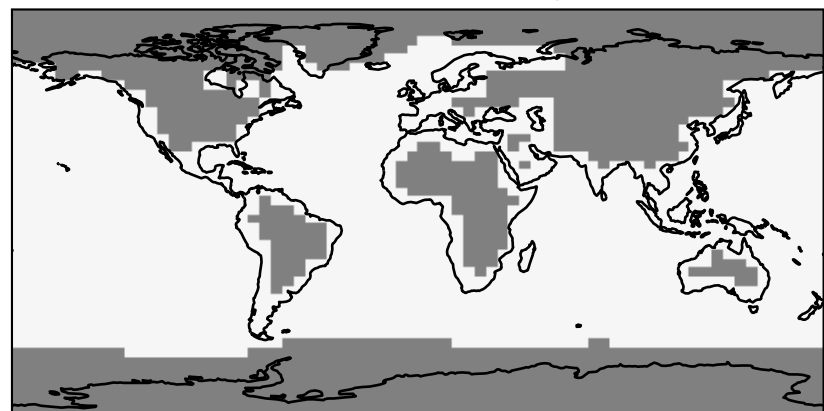
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



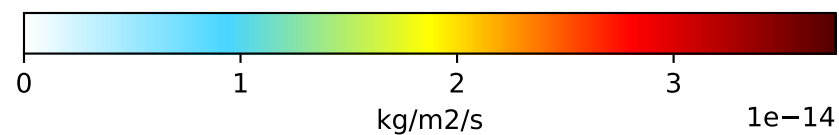
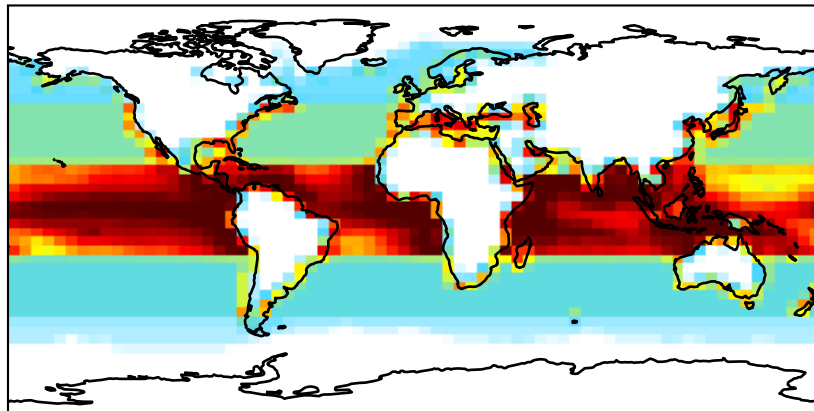
Ratio  
Dev/Ref, Fixed Range



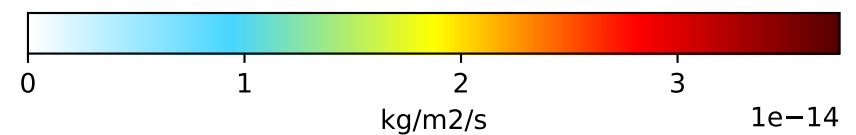
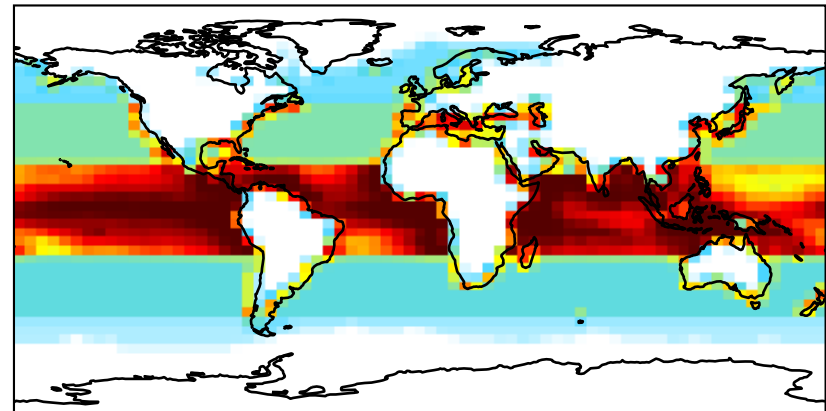


# EmisCH2ICl\_Ocean

GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



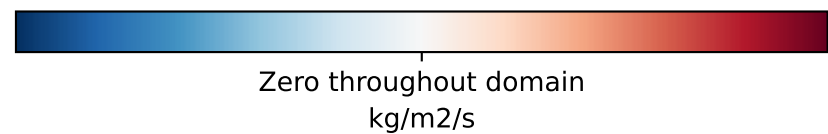
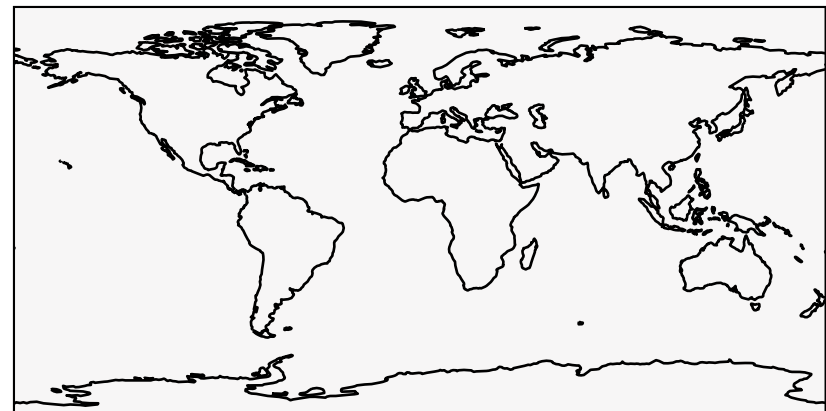
GCC\_13.4.0-rc.0 (Dev)  
4.0x5.0



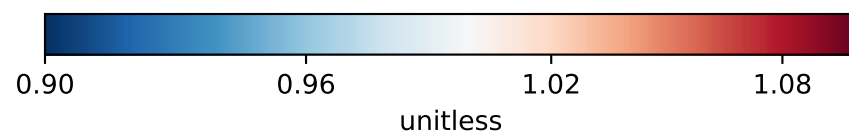
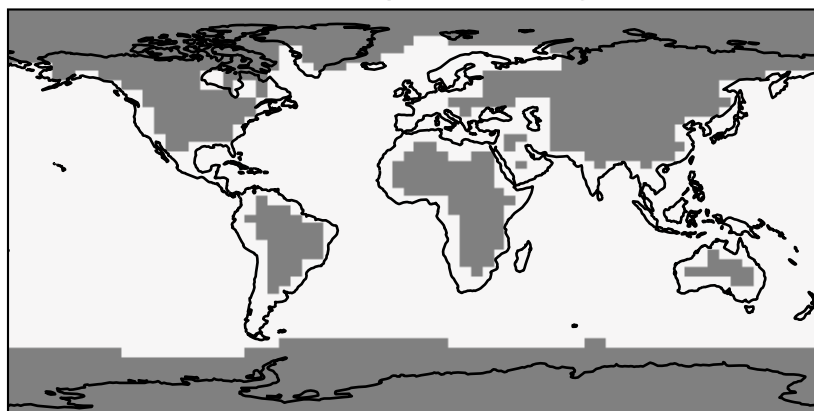
Difference  
Dev - Ref, Dynamic Range



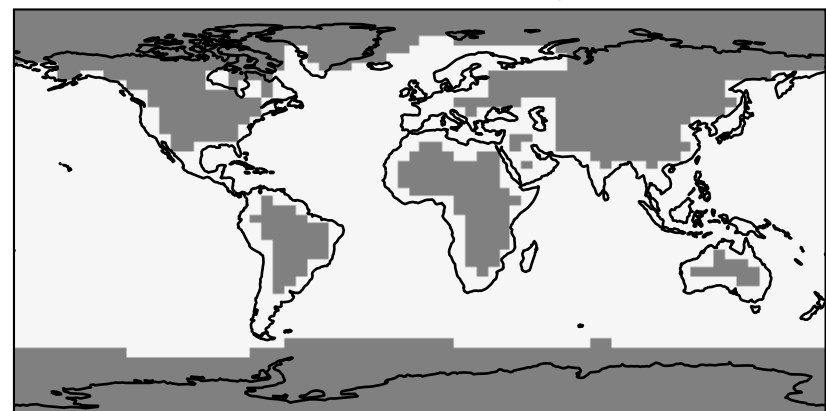
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

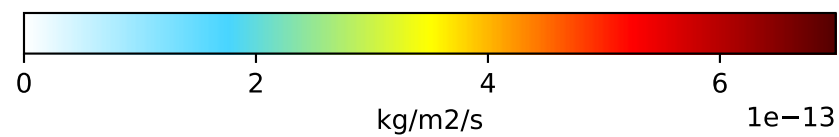
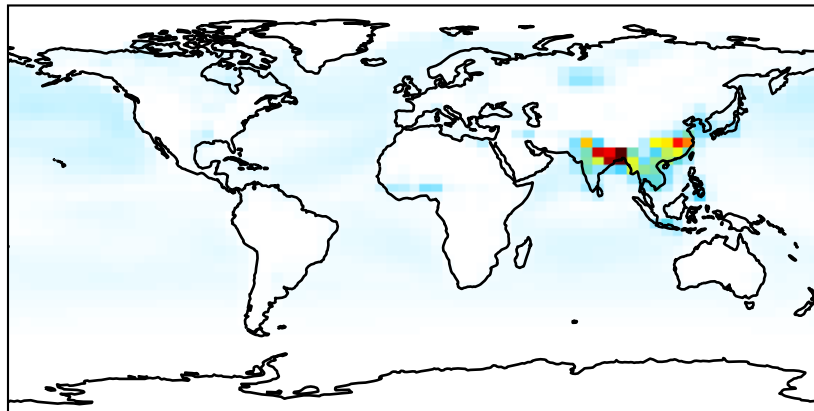


Ratio  
Dev/Ref, Fixed Range

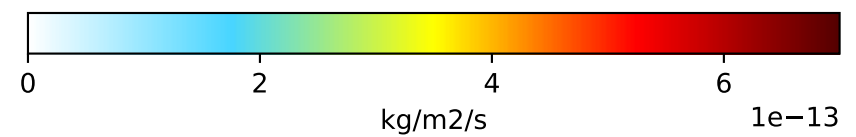
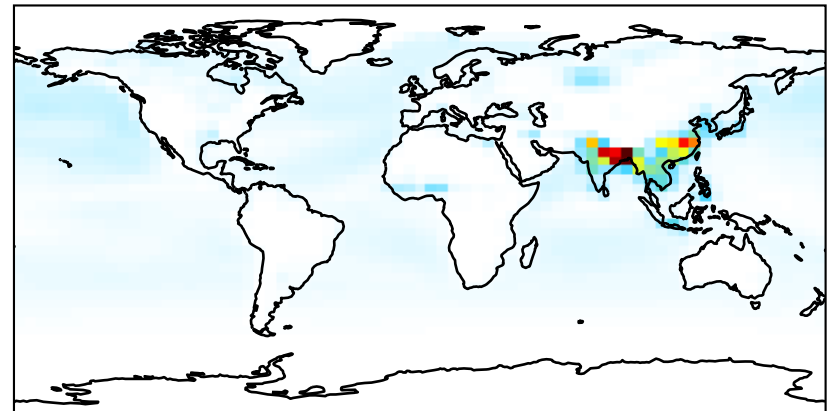


# EmisCH3I\_Ocean

GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



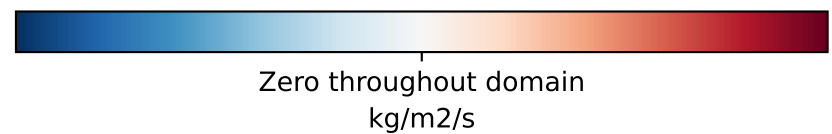
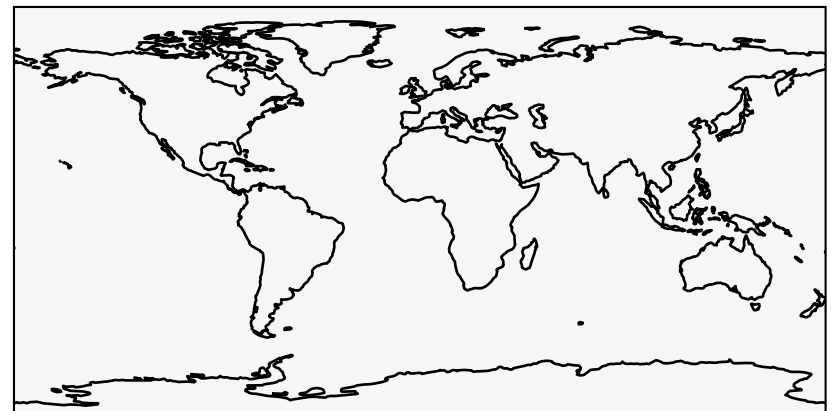
GCC\_13.4.0-rc.0 (Dev)  
4.0x5.0



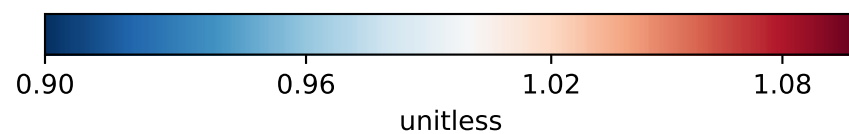
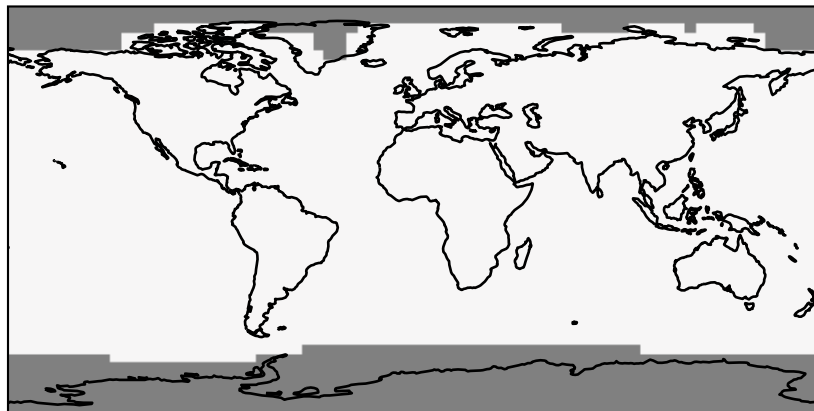
Difference  
Dev - Ref, Dynamic Range



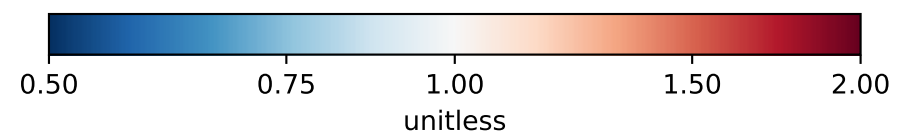
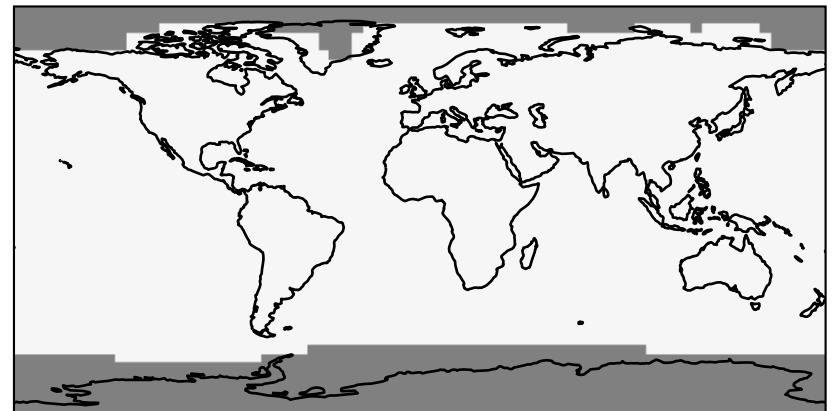
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

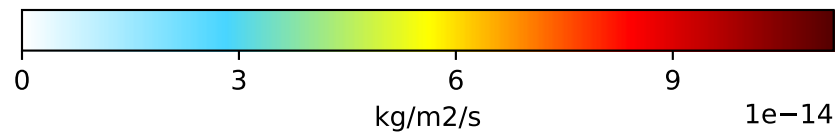
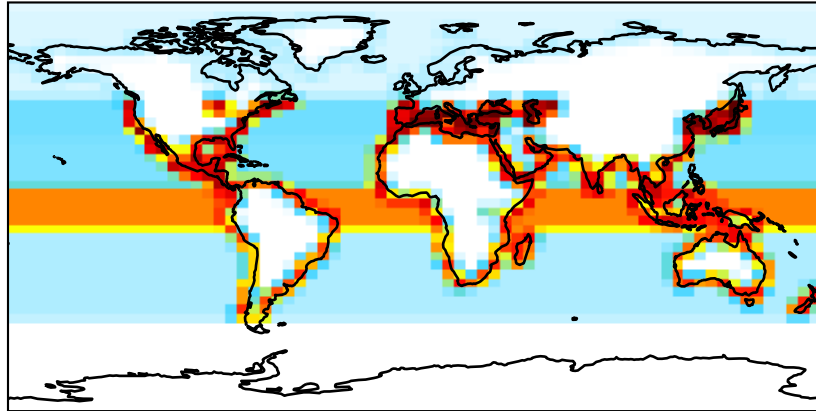


Ratio  
Dev/Ref, Fixed Range

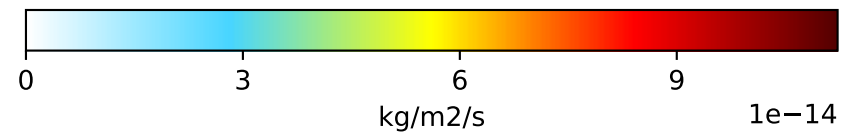
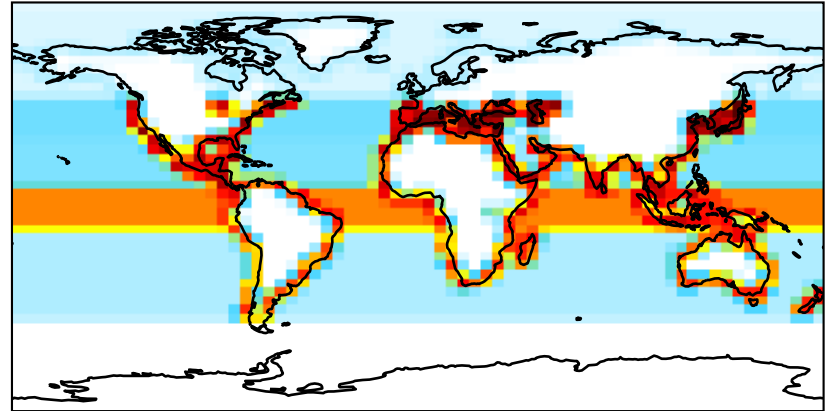


# EmisCHBr3\_Ocean

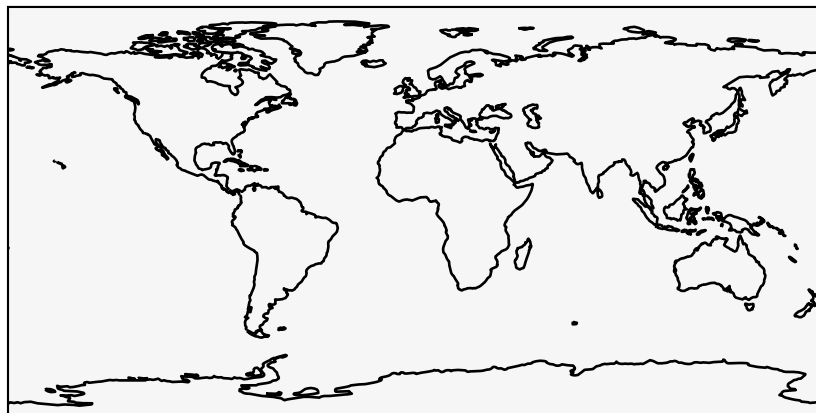
GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



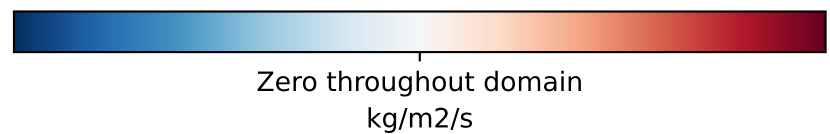
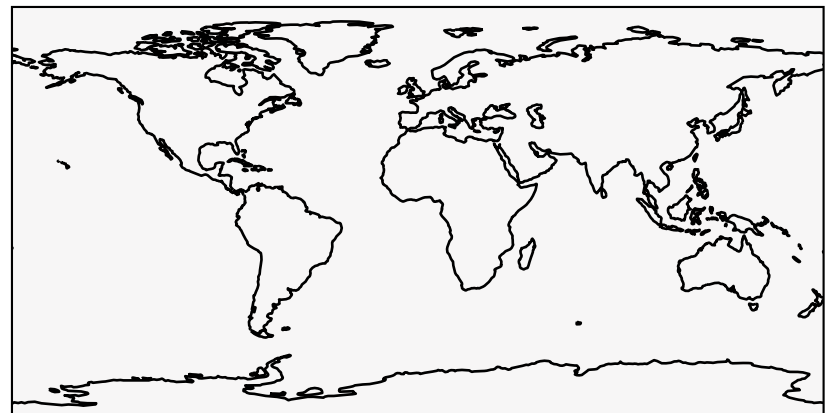
GCC\_13.4.0-rc.0 (Dev)  
4.0x5.0



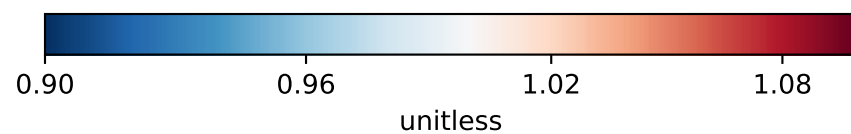
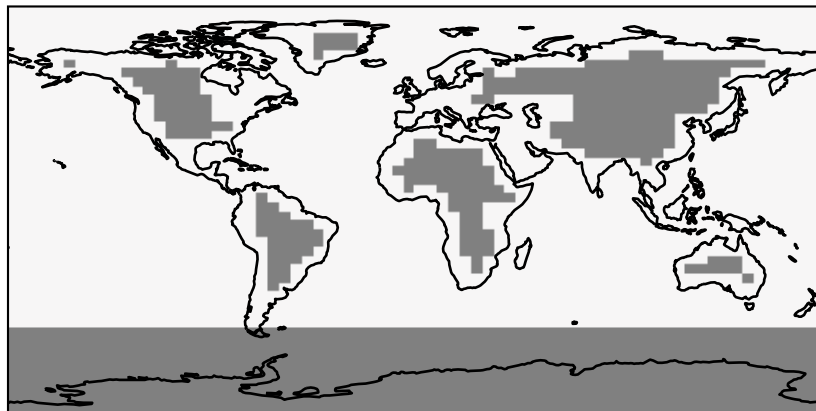
Difference  
Dev - Ref, Dynamic Range



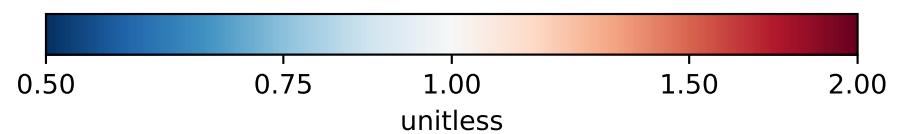
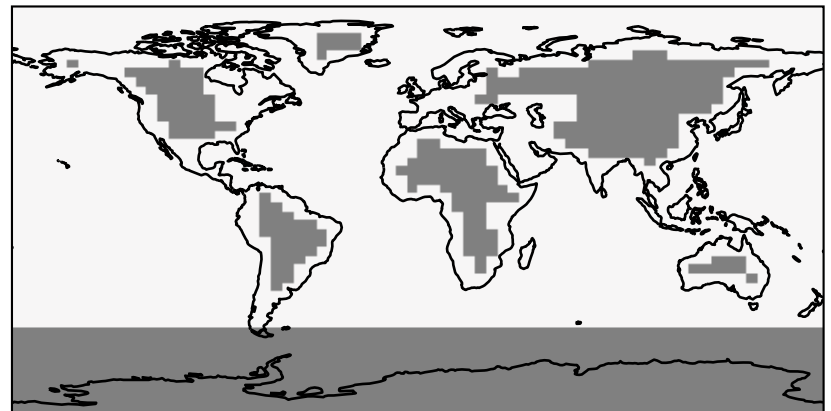
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



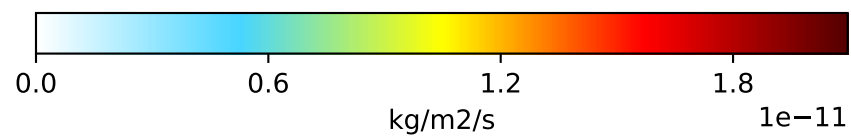
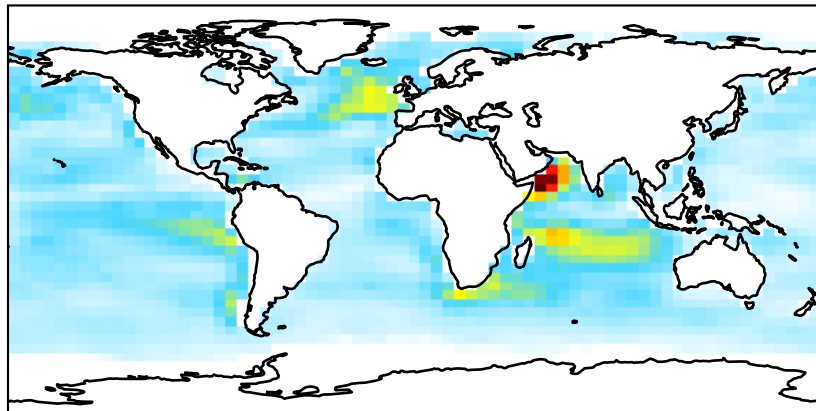
Ratio  
Dev/Ref, Fixed Range



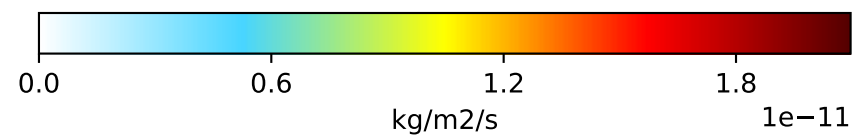
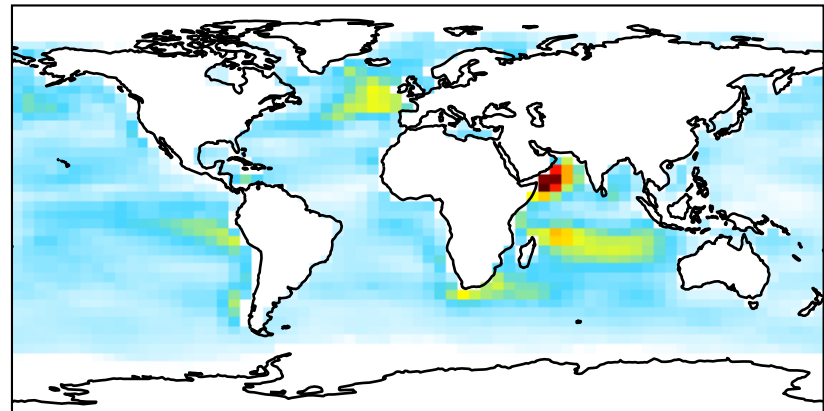


# EmisDMS\_Ocean

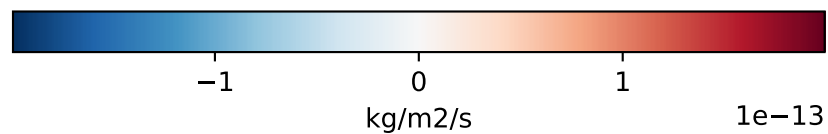
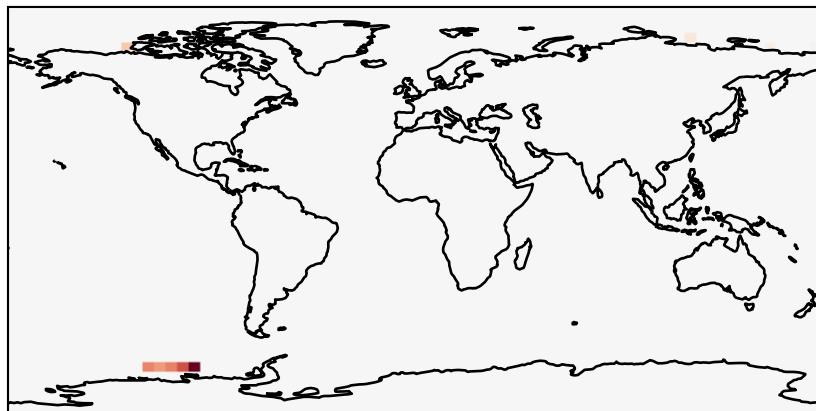
GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



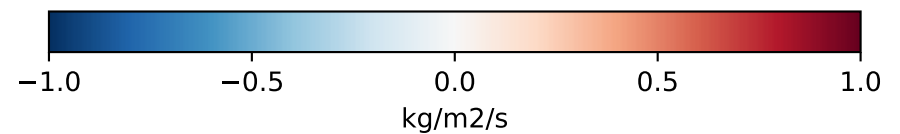
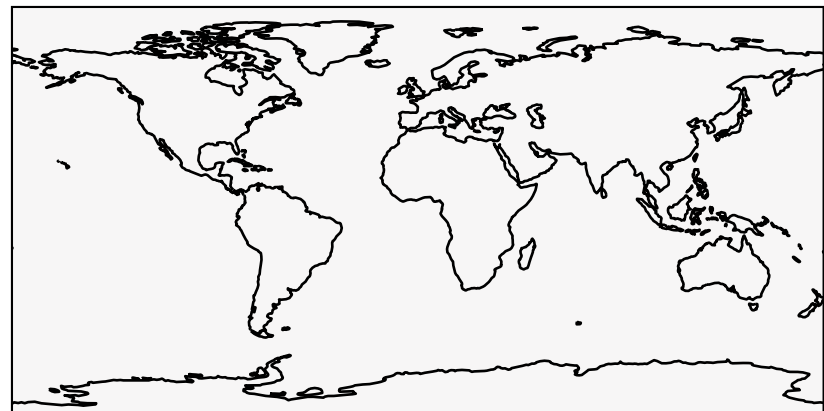
GCC\_13.4.0-rc.0 (Dev)  
4.0x5.0



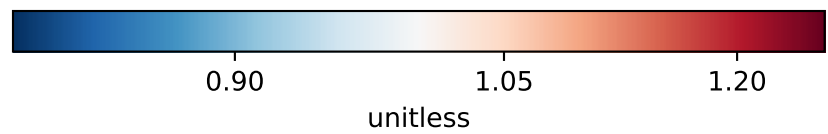
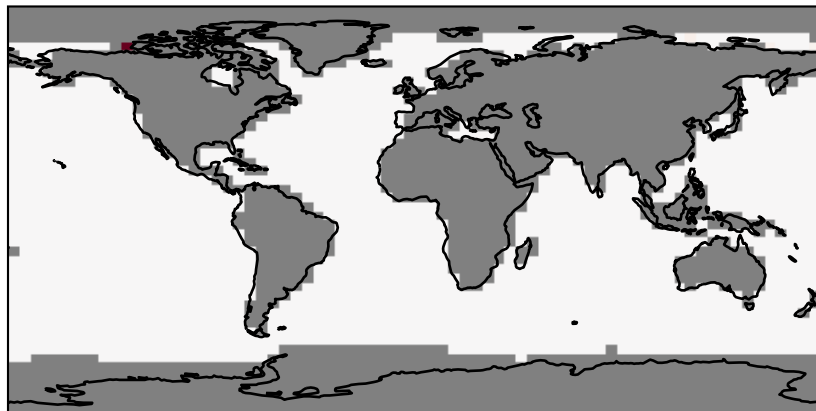
Difference  
Dev - Ref, Dynamic Range



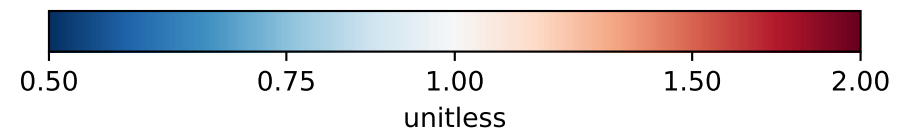
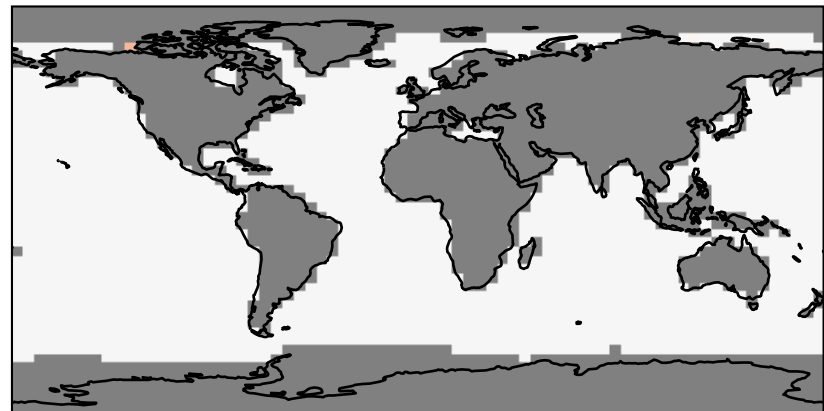
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

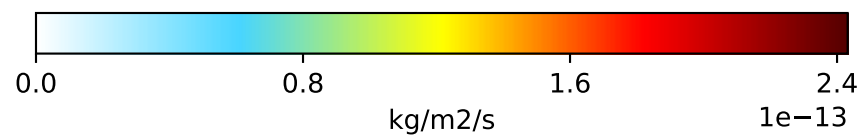
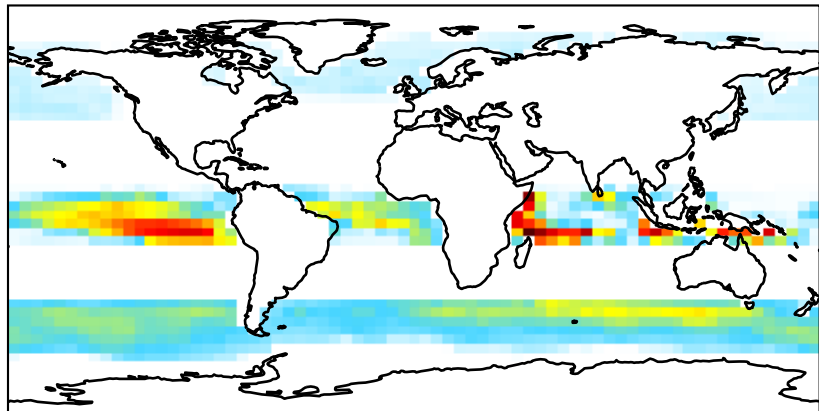


Ratio  
Dev/Ref, Fixed Range

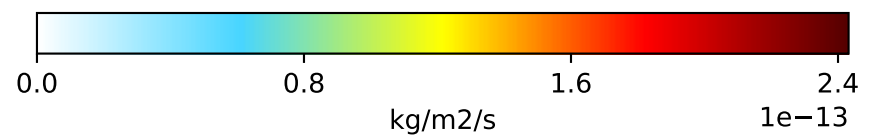
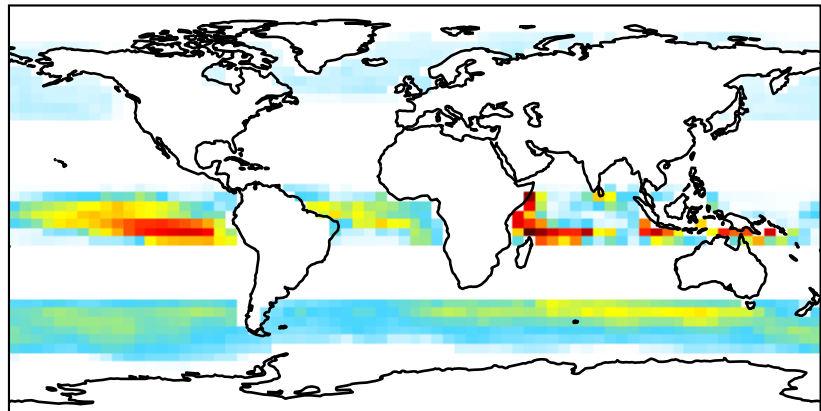


# EmisETNO3\_Ocean

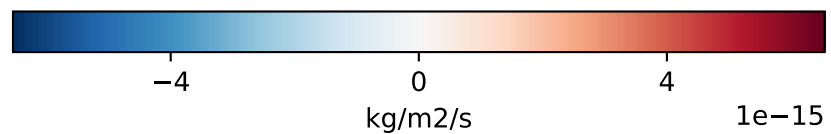
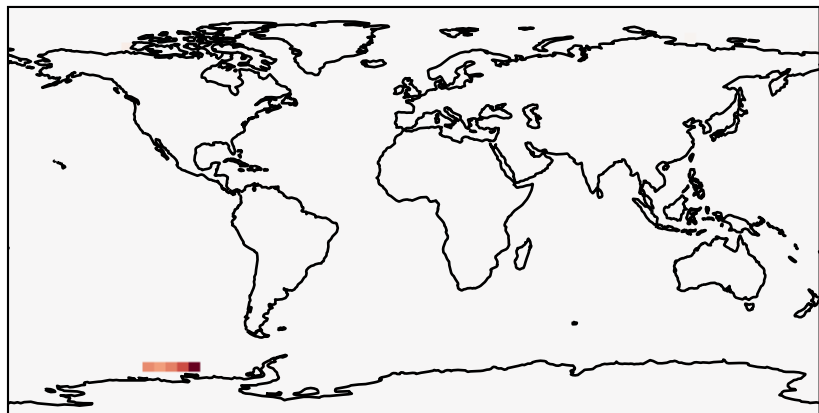
GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



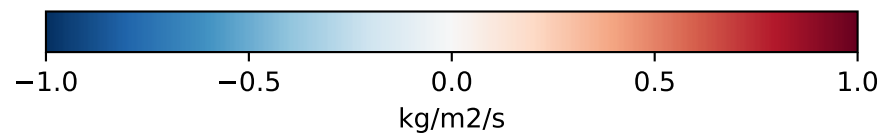
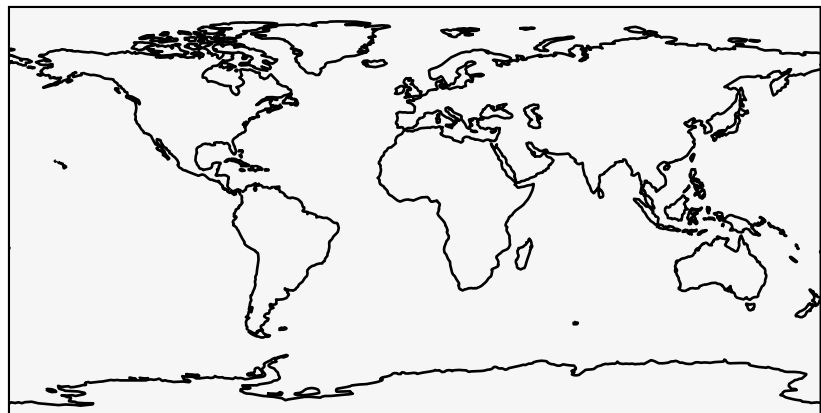
GCC\_13.4.0-rc.0 (Dev)  
4.0x5.0



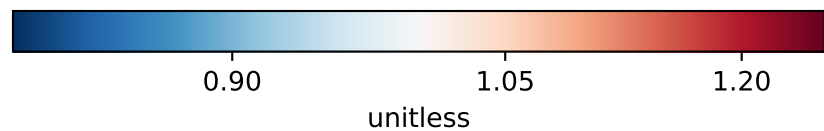
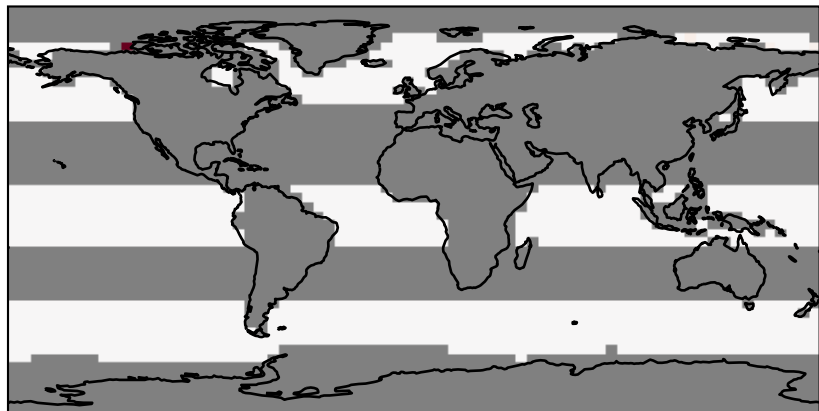
Difference  
Dev - Ref, Dynamic Range



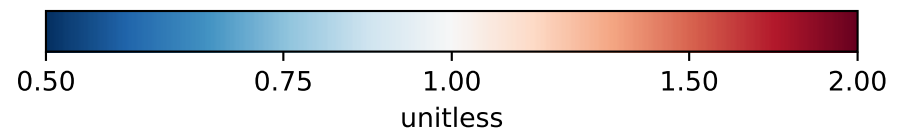
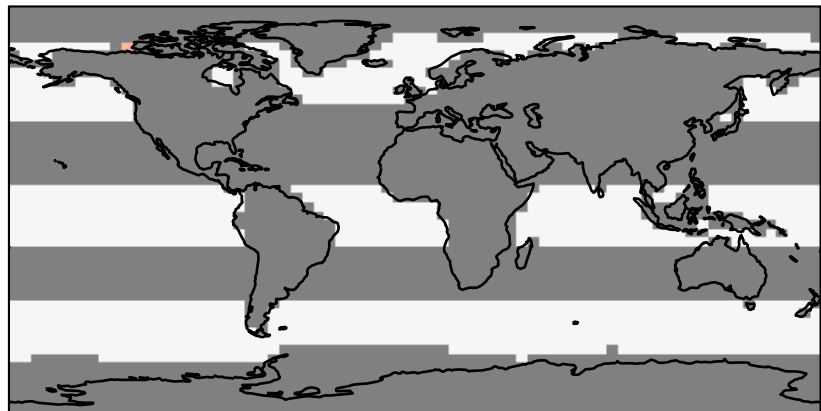
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

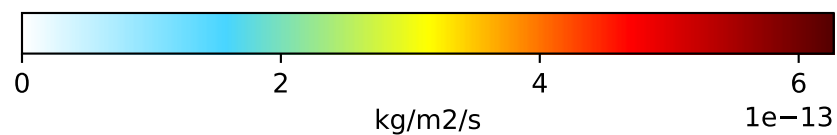
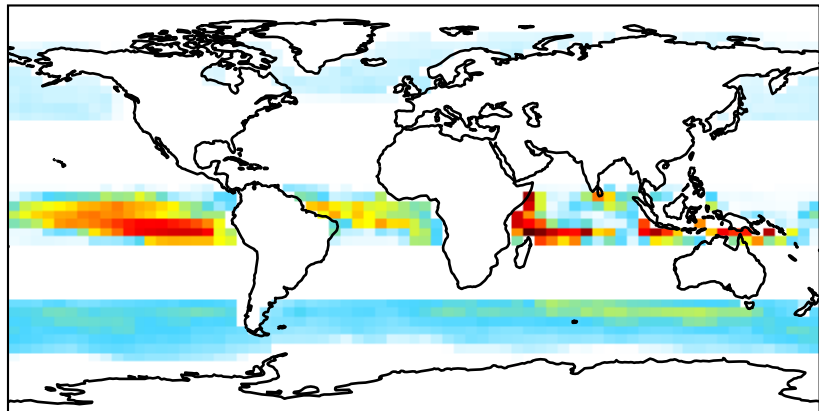


Ratio  
Dev/Ref, Fixed Range

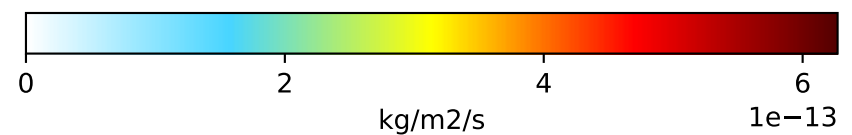
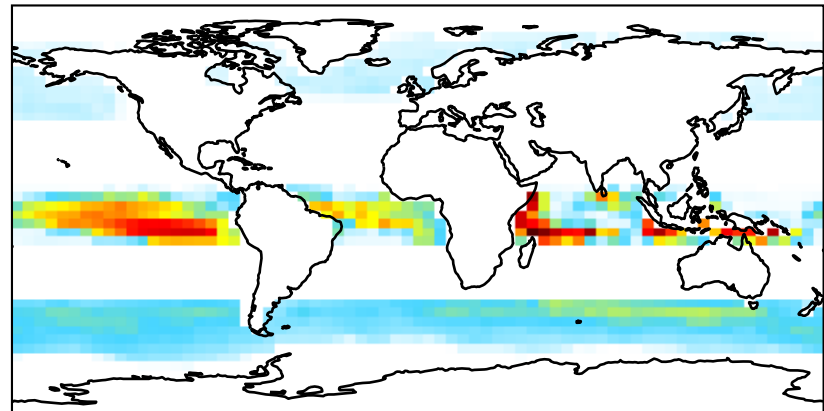


# EmisMEN03\_Ocean

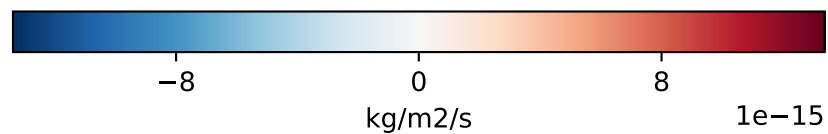
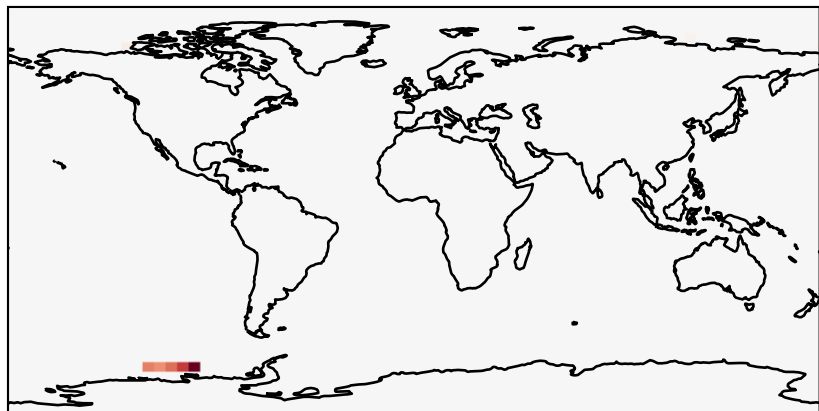
GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



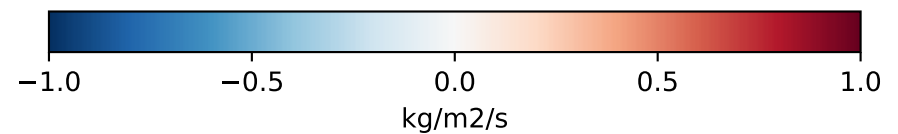
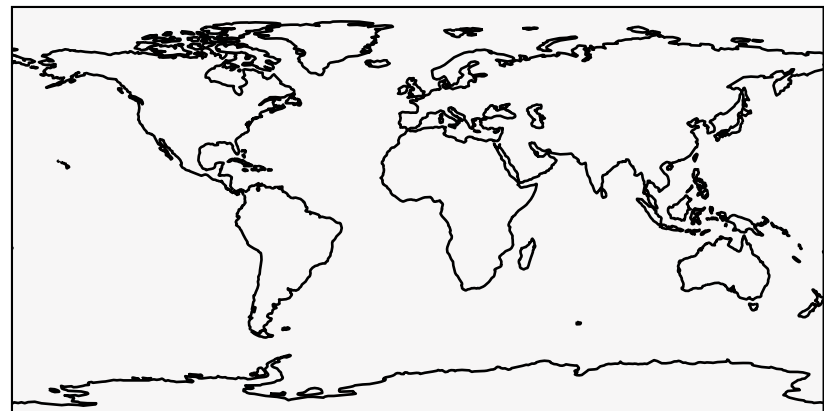
GCC\_13.4.0-rc.0 (Dev)  
4.0x5.0



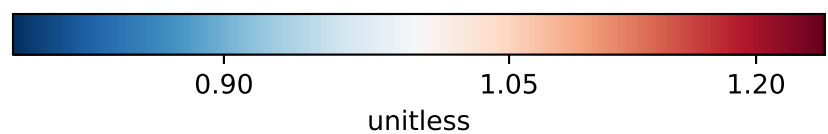
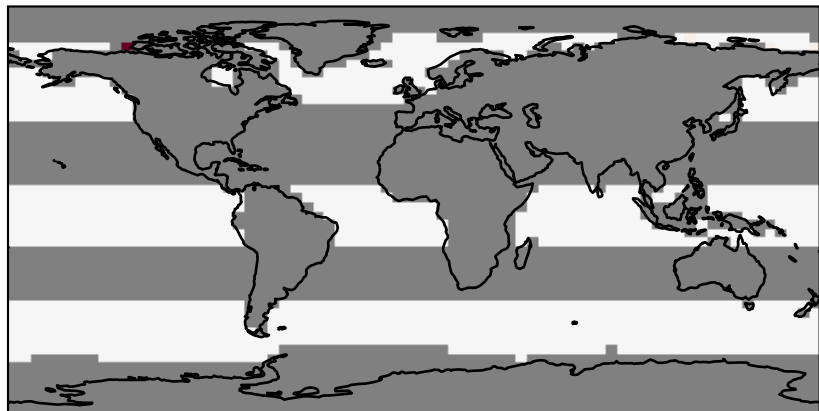
Difference  
Dev - Ref, Dynamic Range



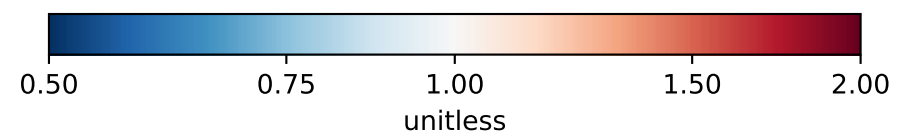
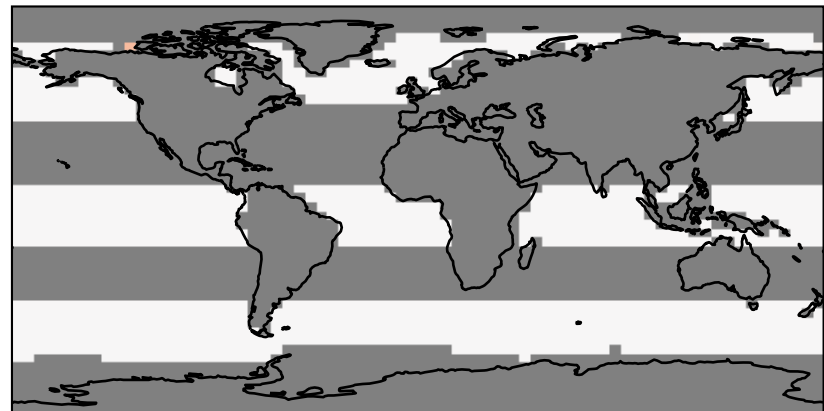
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

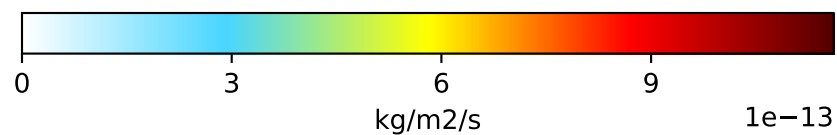
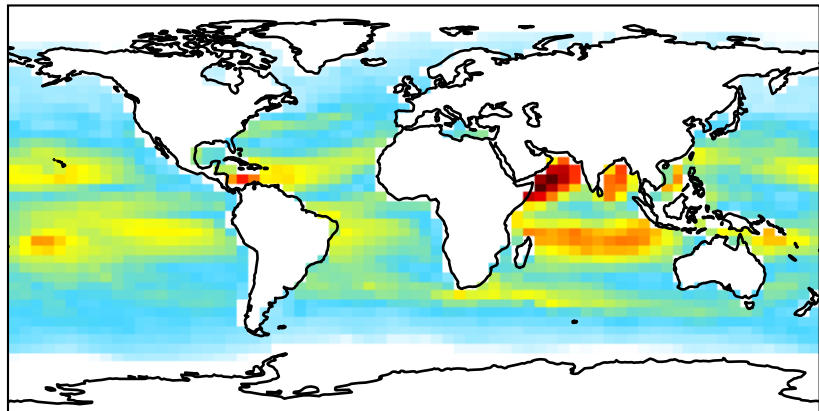


Ratio  
Dev/Ref, Fixed Range

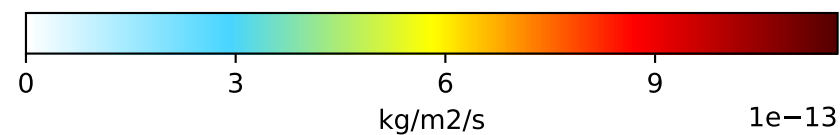
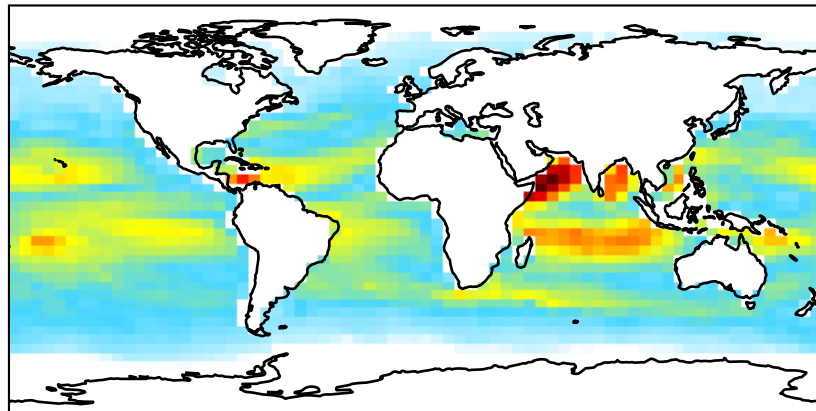


# EmisMOH\_Ocean

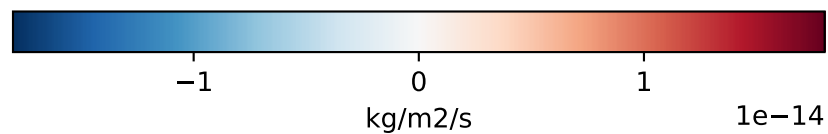
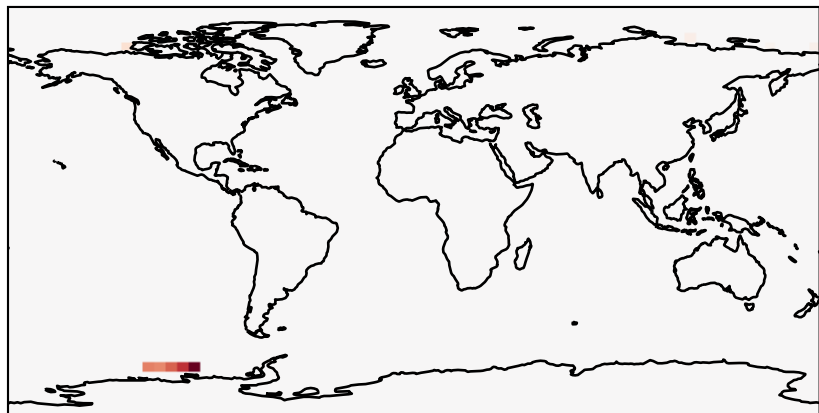
GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



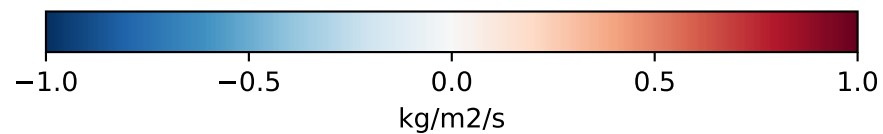
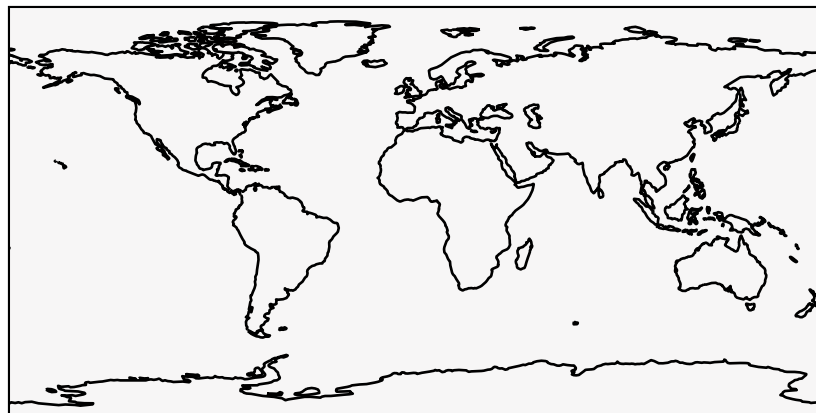
GCC\_13.4.0-rc.0 (Dev)  
4.0x5.0



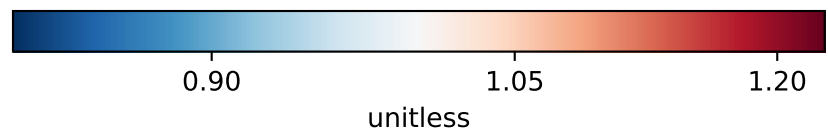
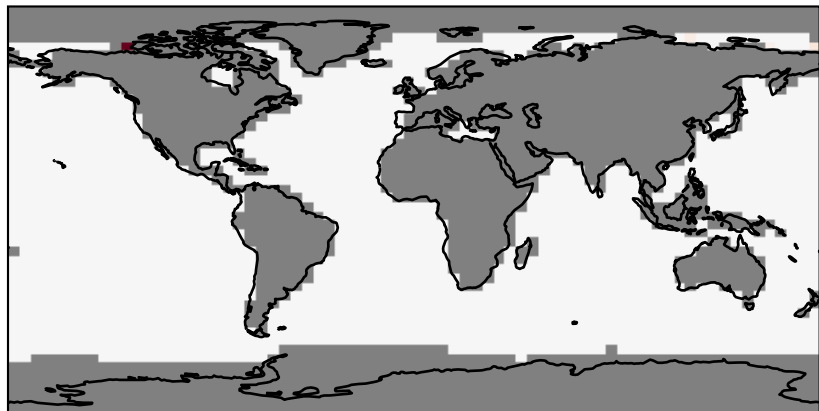
Difference  
Dev - Ref, Dynamic Range



Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

