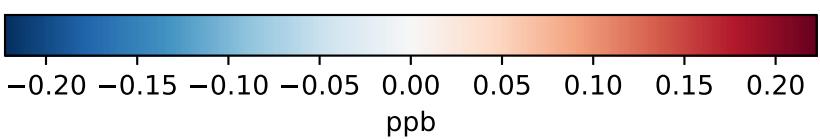
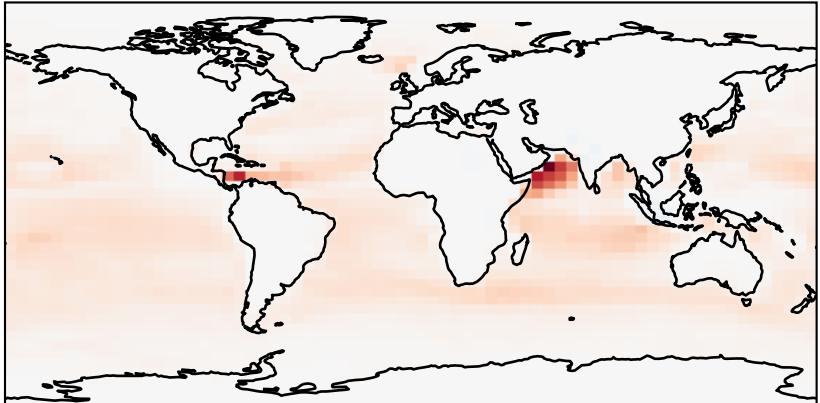
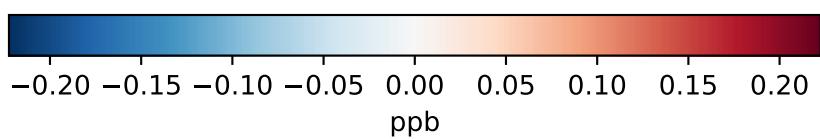
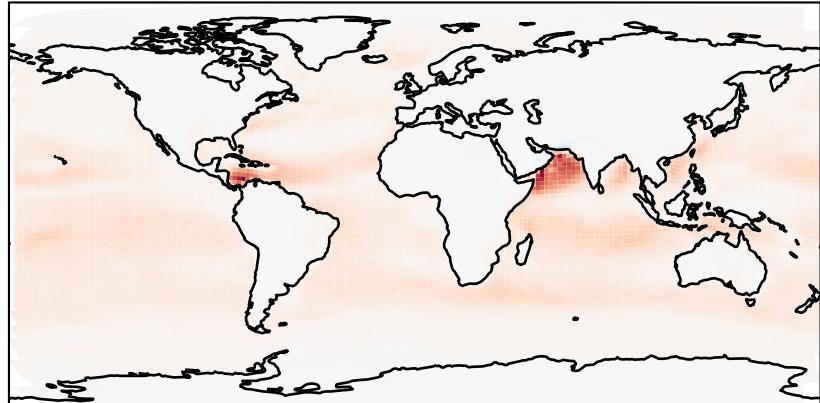


# SpeciesConc\_Cly

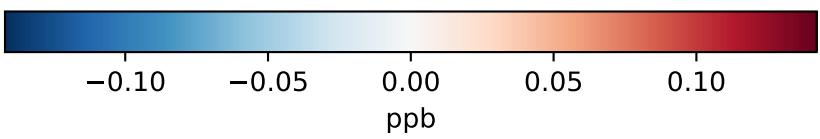
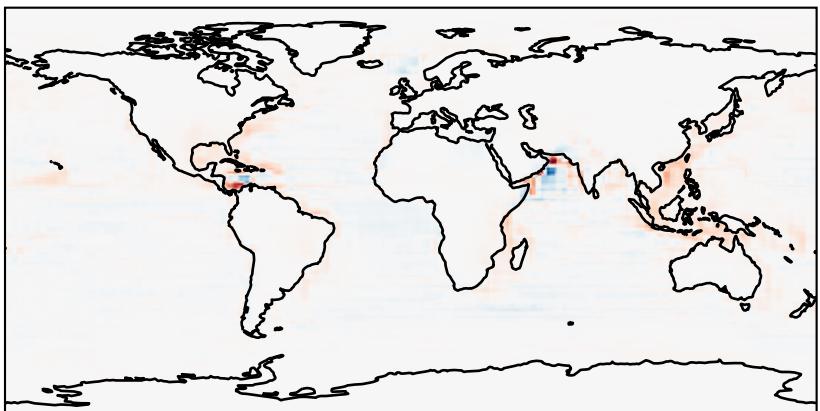
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



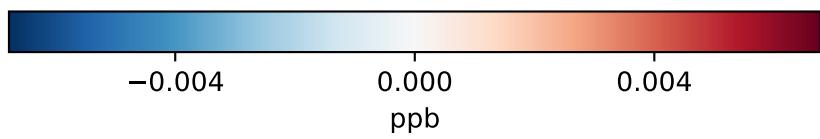
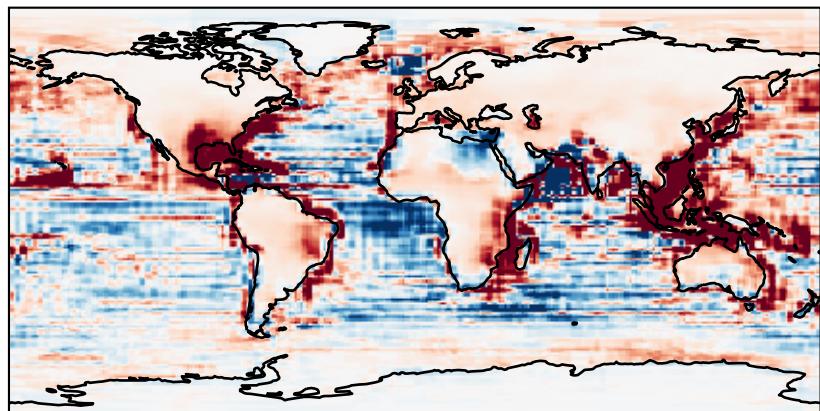
GCHP\_13.4.0-rc.1 - GCHP\_13.3.0-rc.1 (Dev)  
c48



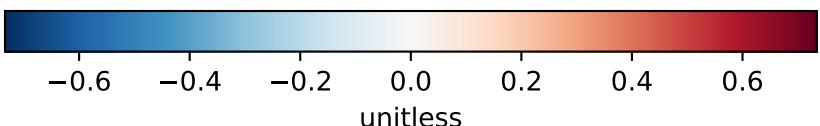
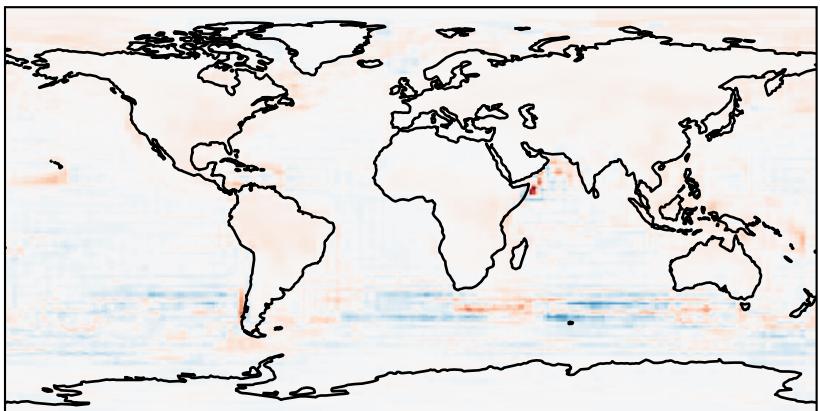
Difference (1x1.25)  
Dev - Ref, Dynamic Range



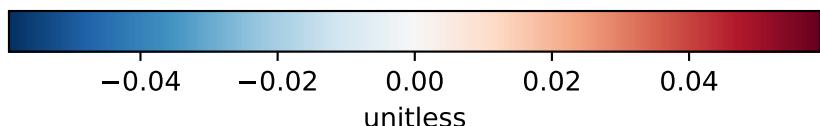
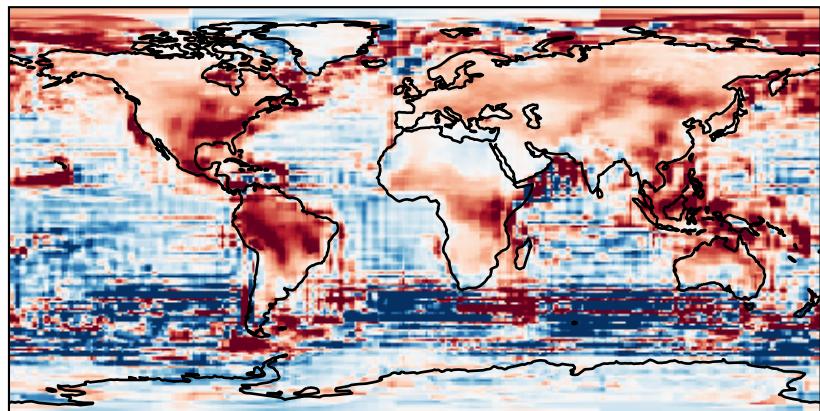
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

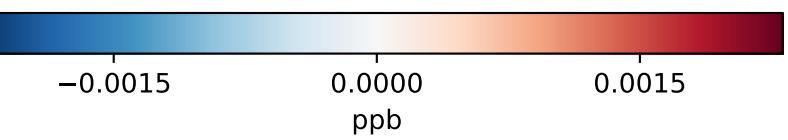
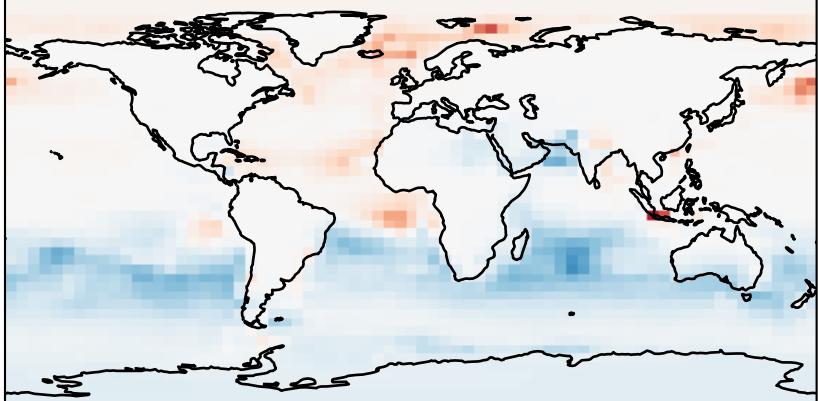


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

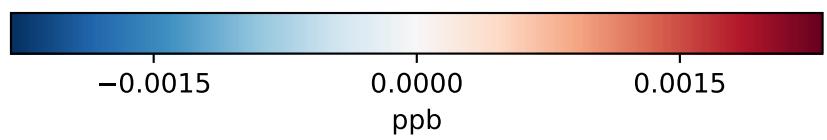
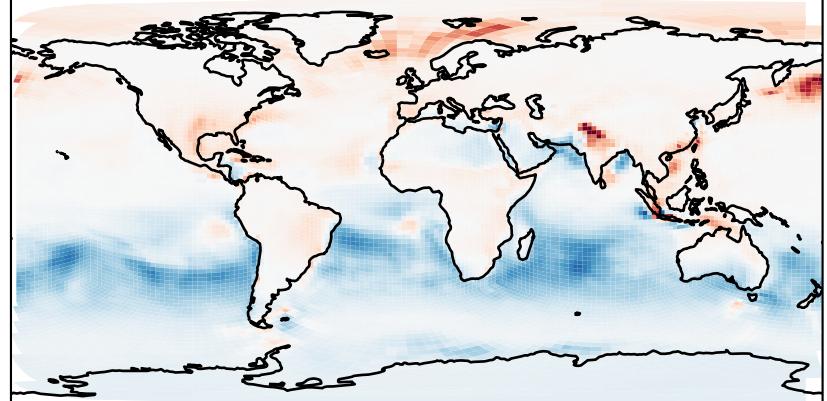


# SpeciesConc\_ClOx

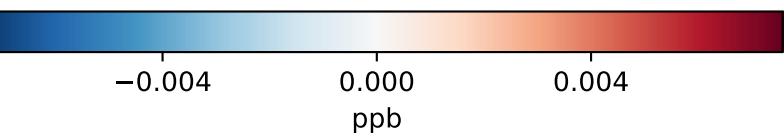
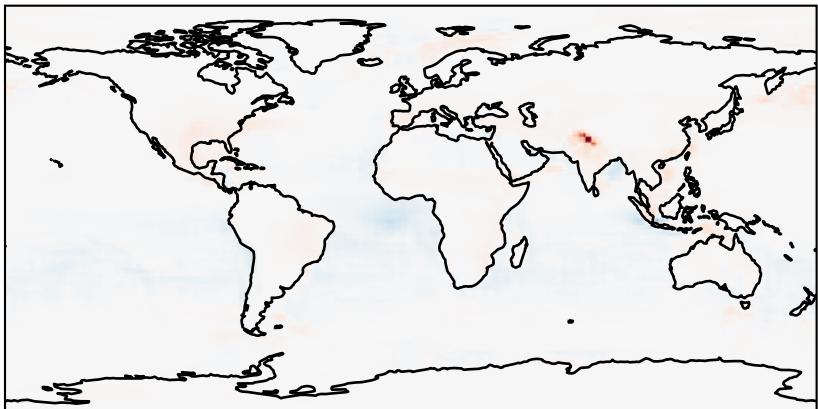
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



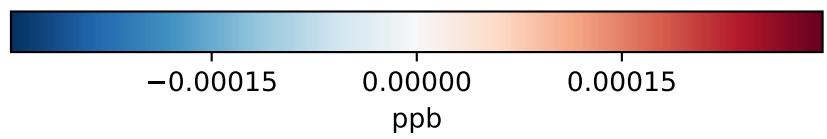
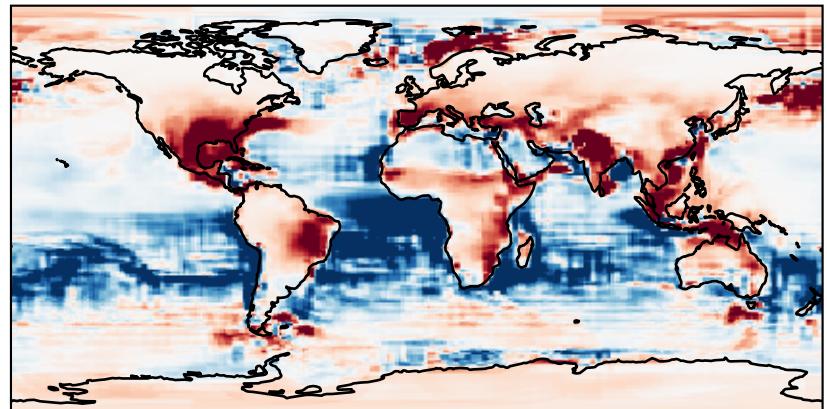
GCHP\_13.4.0-rc.1 - GCHP\_13.3.0-rc.1 (Dev)  
c48



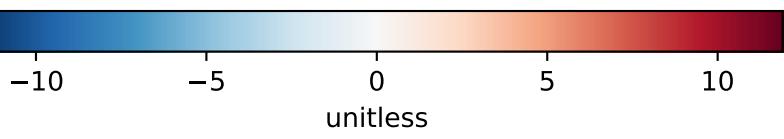
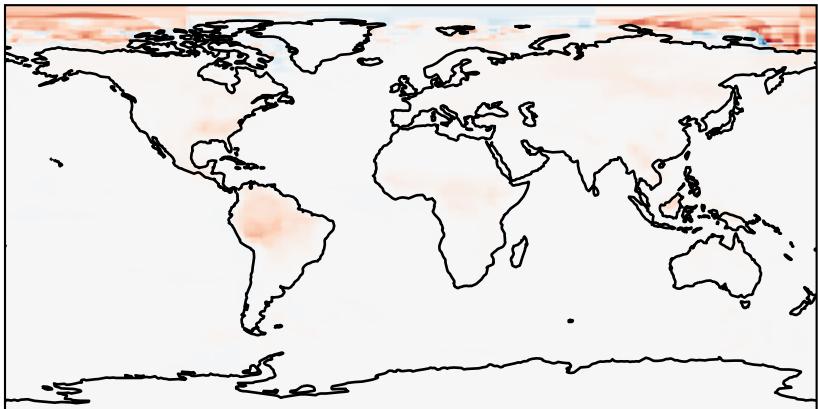
Difference (1x1.25)  
Dev - Ref, Dynamic Range



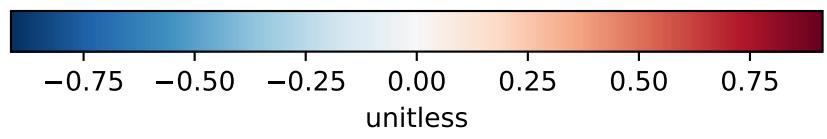
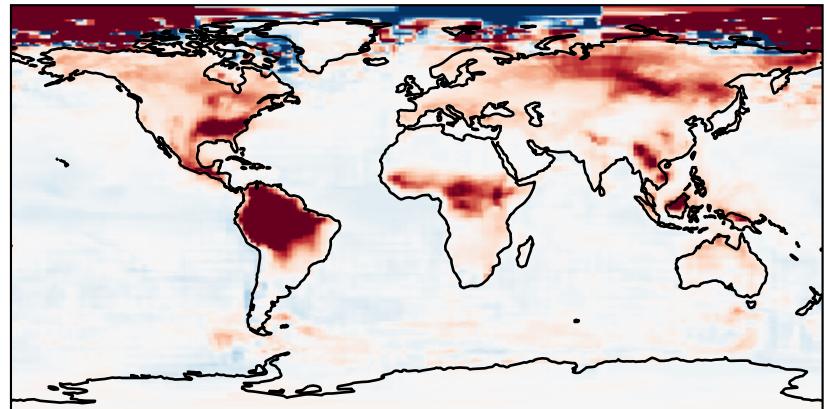
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

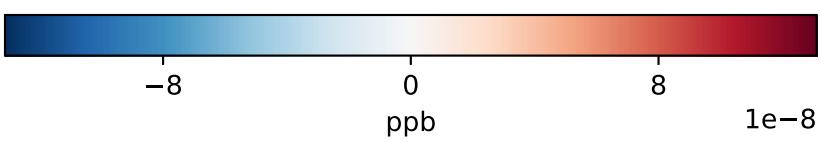
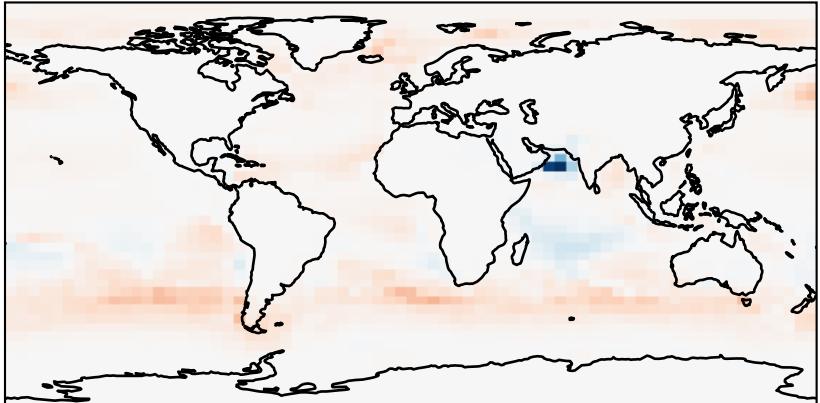


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

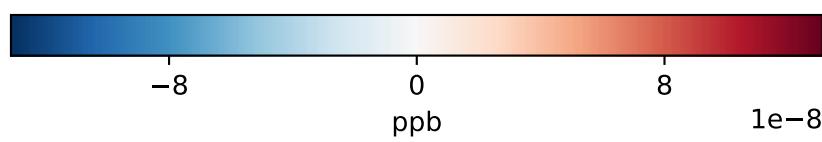
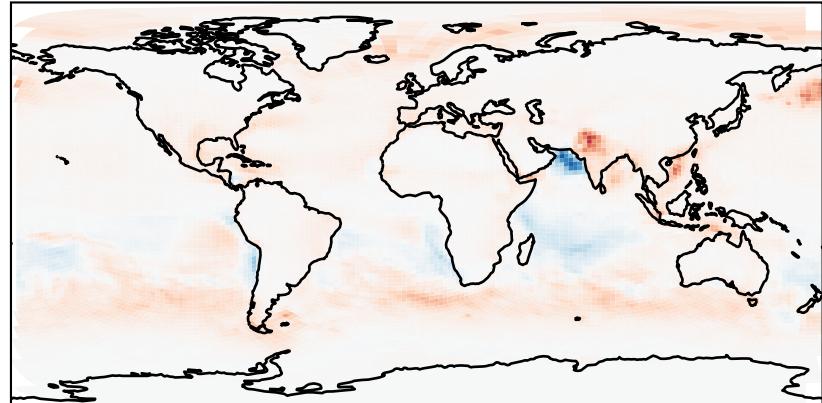


# SpeciesConc\_Cl

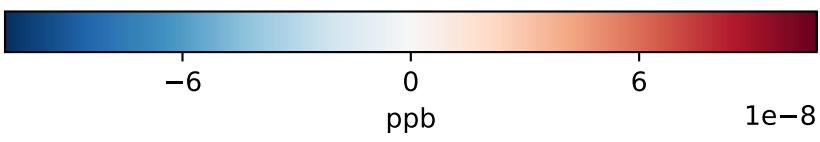
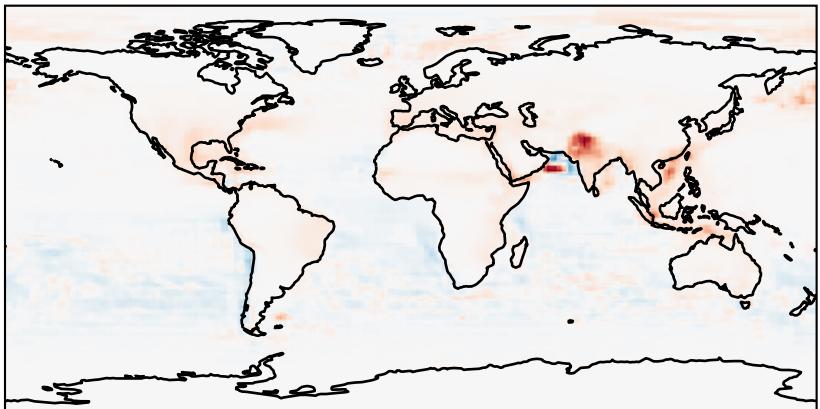
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



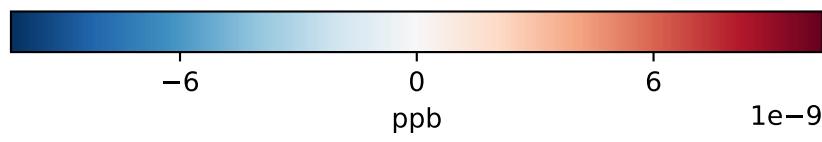
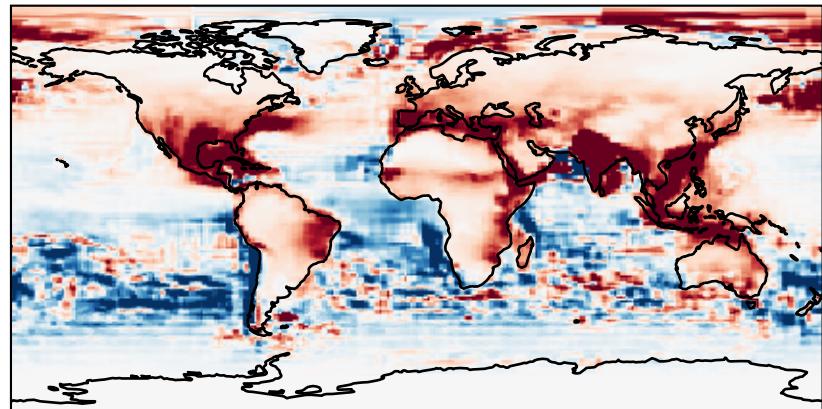
GCHP\_13.4.0-rc.1 - GCHP\_13.3.0-rc.1 (Dev)  
c48



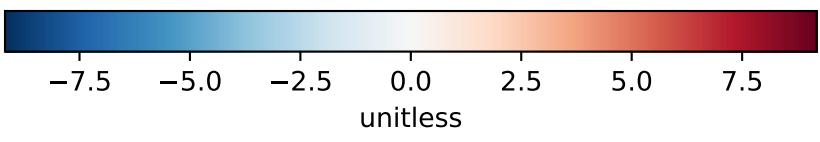
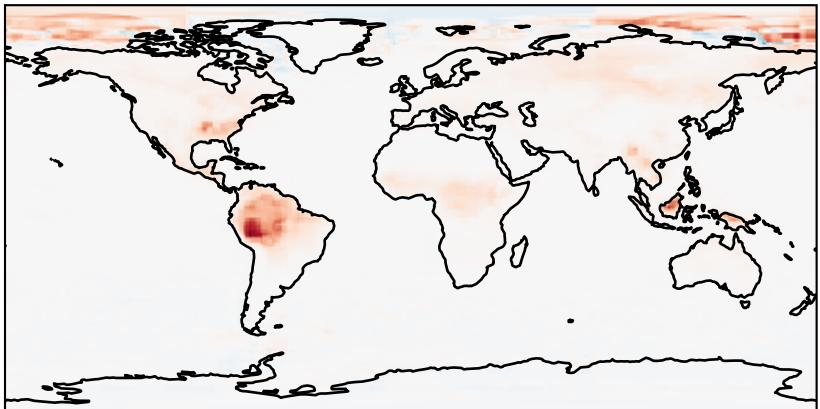
Difference (1x1.25)  
Dev - Ref, Dynamic Range



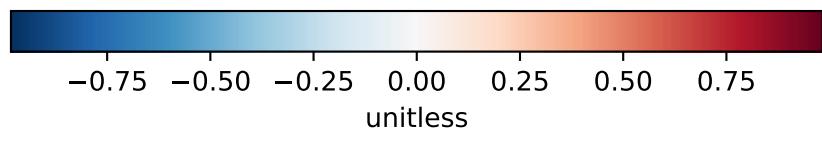
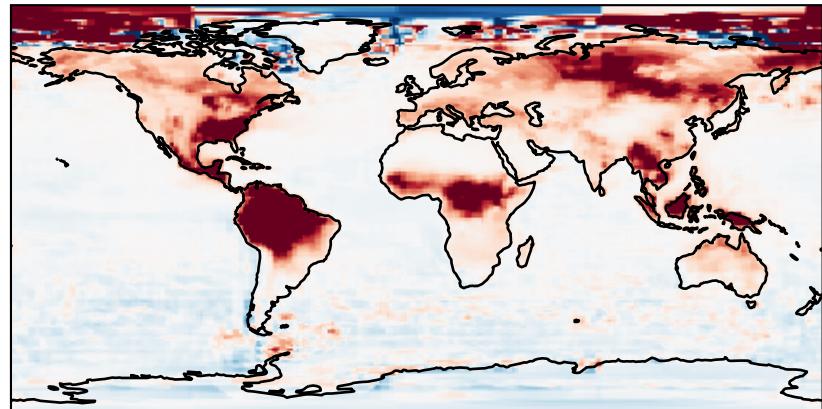
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

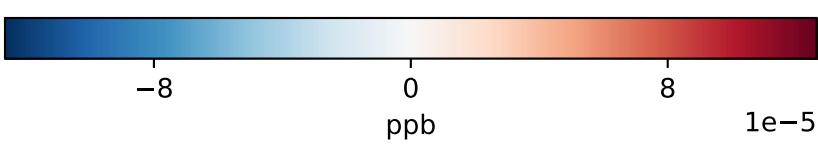
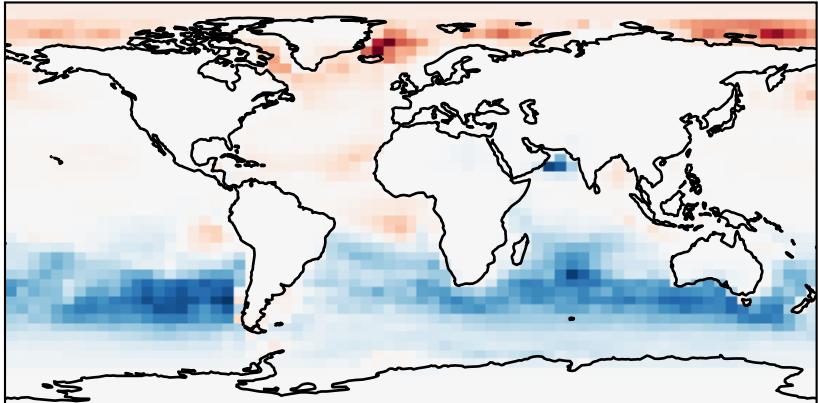


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

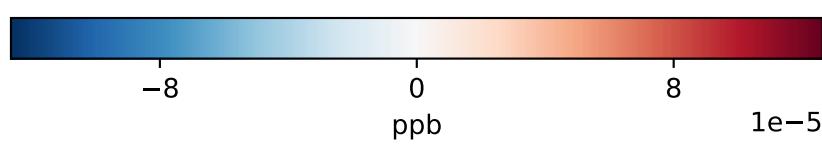
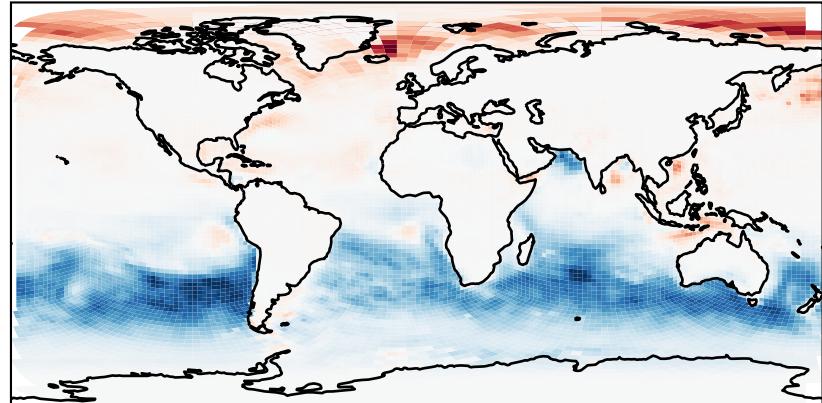


# SpeciesConc\_CIO

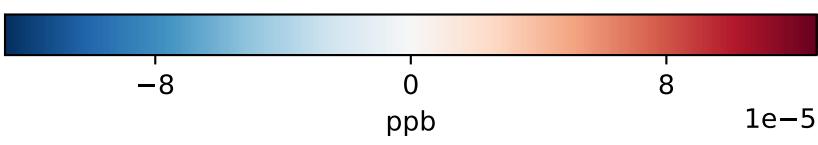
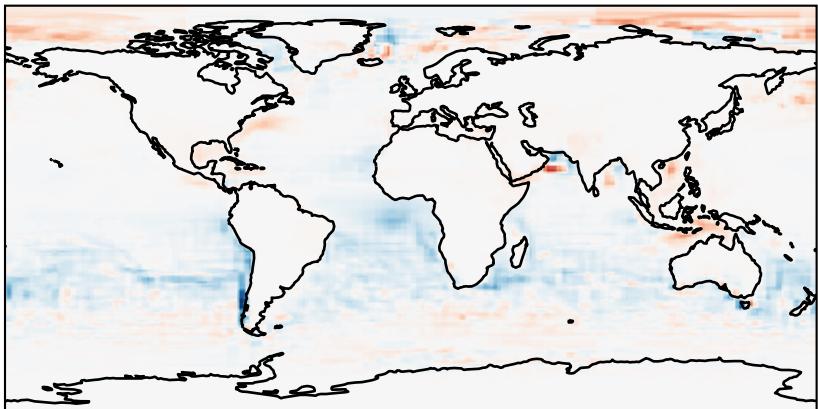
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



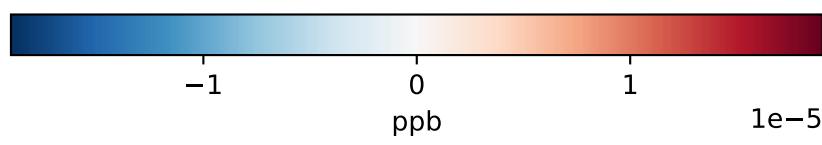
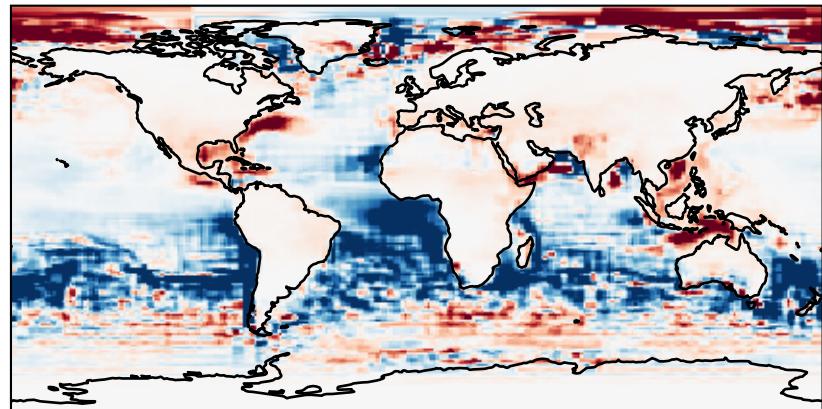
GCHP\_13.4.0-rc.1 - GCHP\_13.3.0-rc.1 (Dev)  
c48



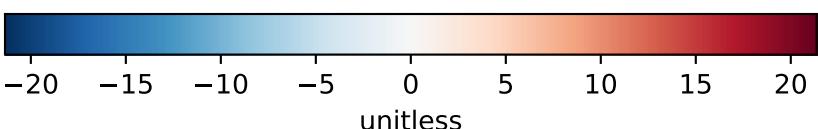
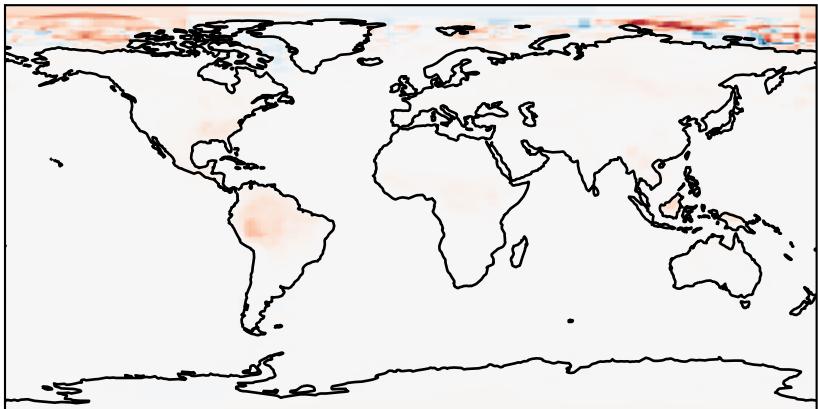
Difference (1x1.25)  
Dev - Ref, Dynamic Range



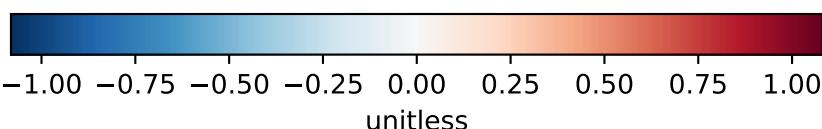
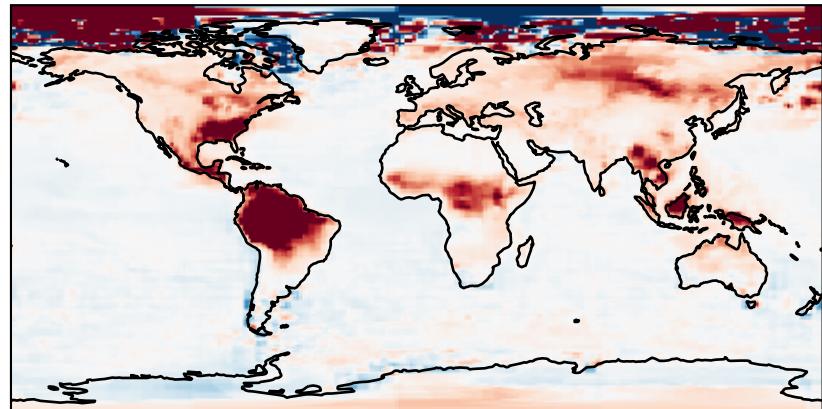
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

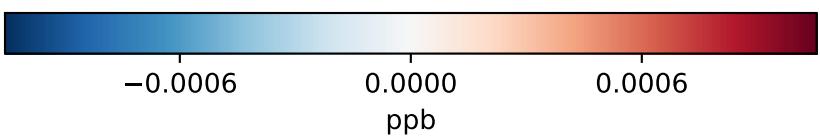
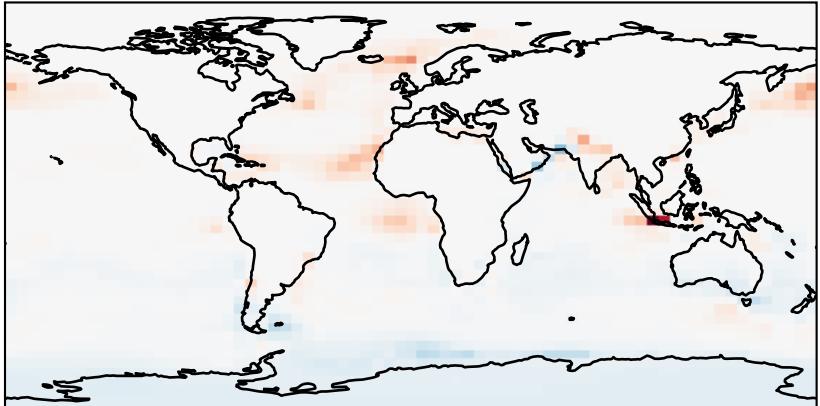


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

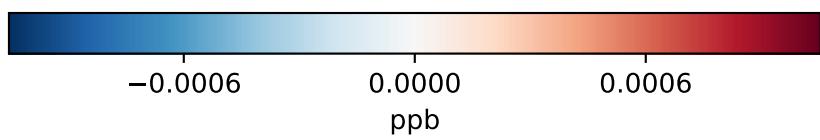
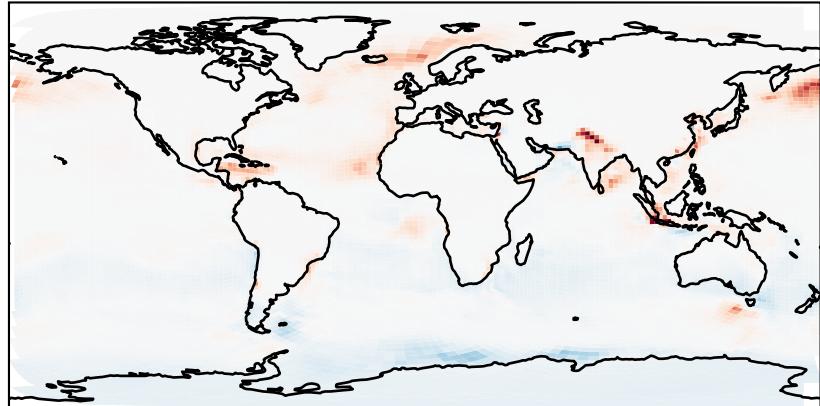


# SpeciesConc\_CI2

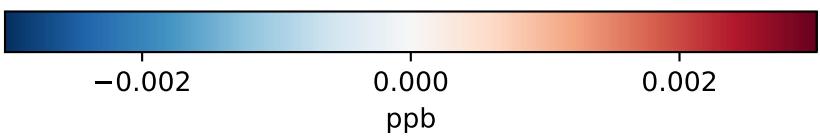
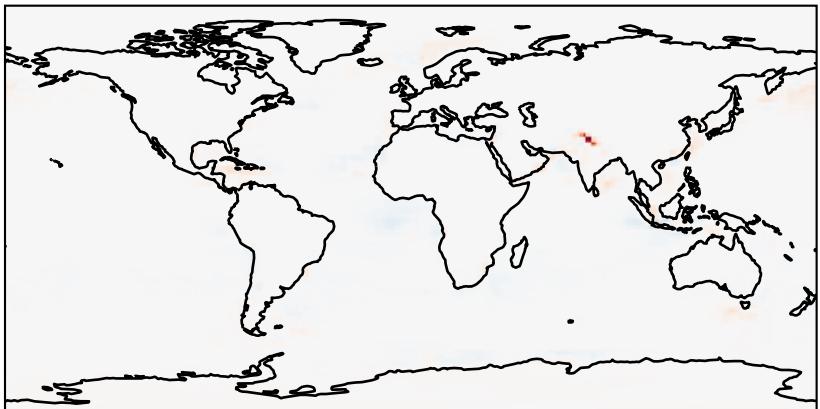
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



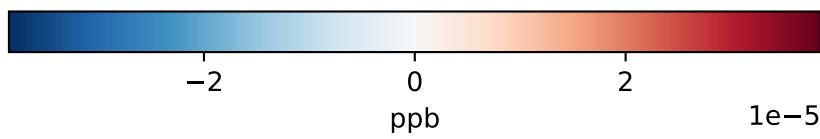
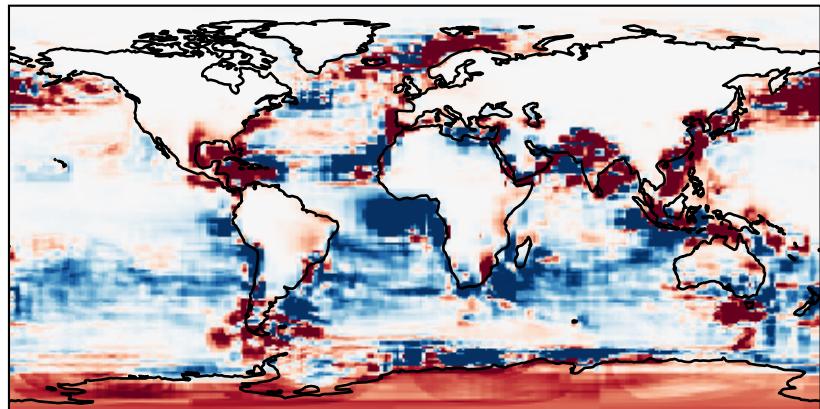
GCHP\_13.4.0-rc.1 - GCHP\_13.3.0-rc.1 (Dev)  
c48



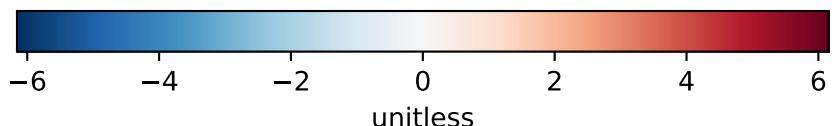
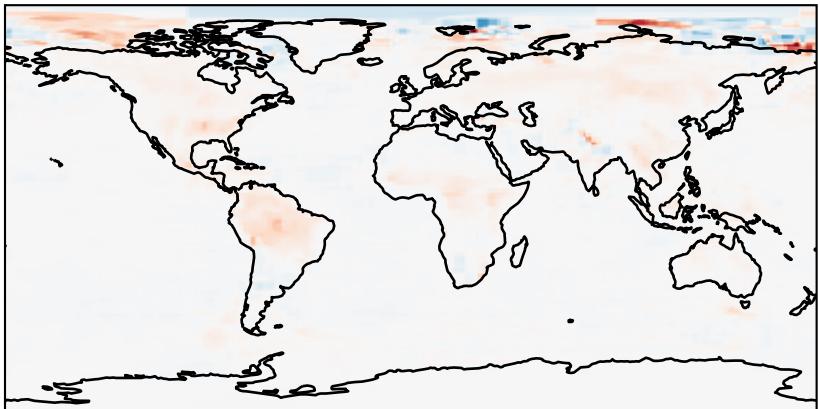
Difference (1x1.25)  
Dev - Ref, Dynamic Range



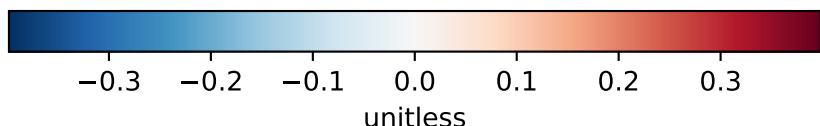
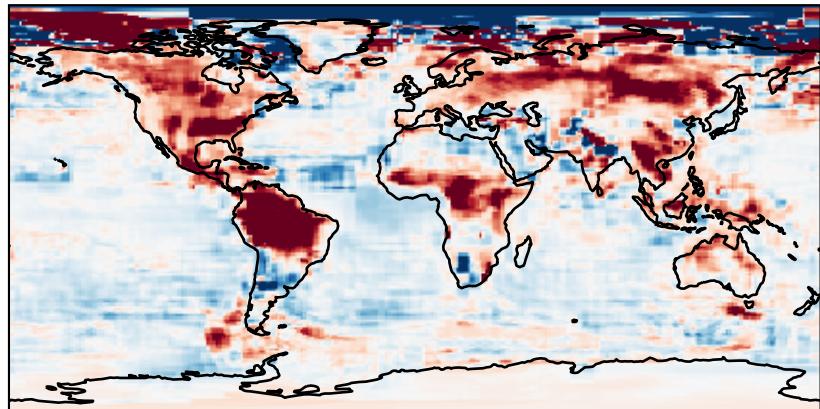
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

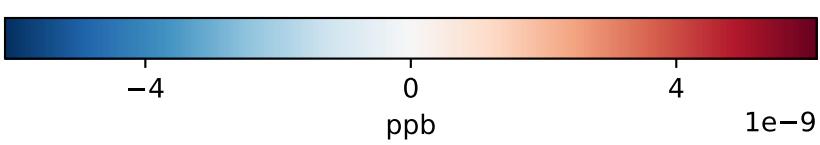
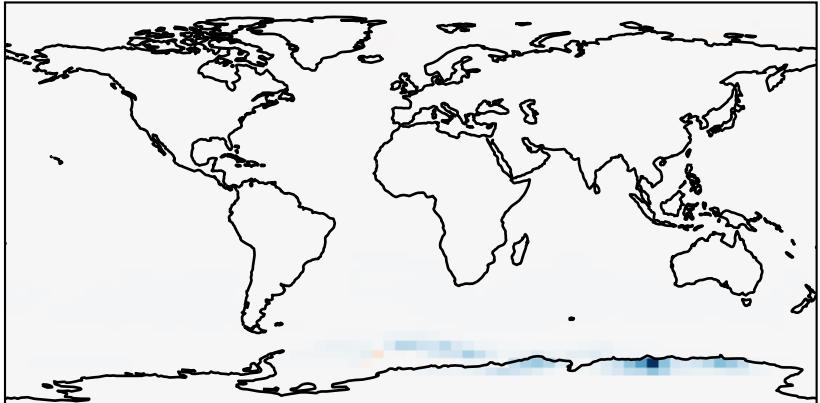


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

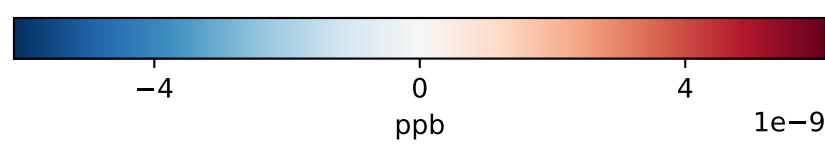
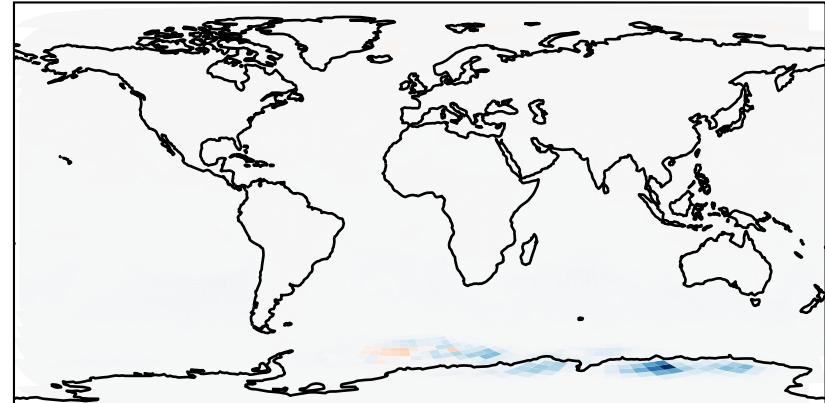


# SpeciesConc\_Cl202

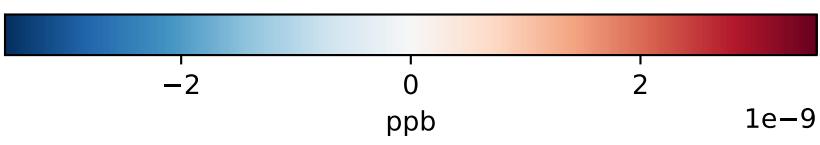
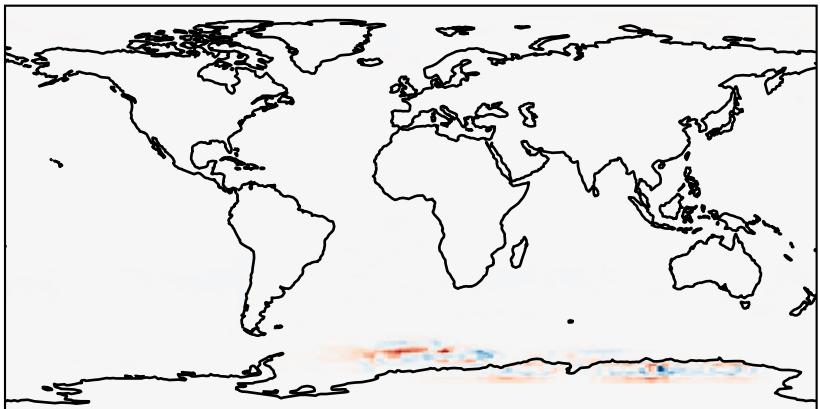
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



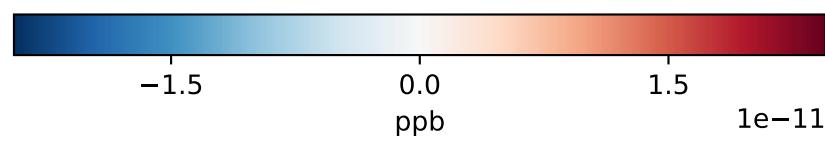
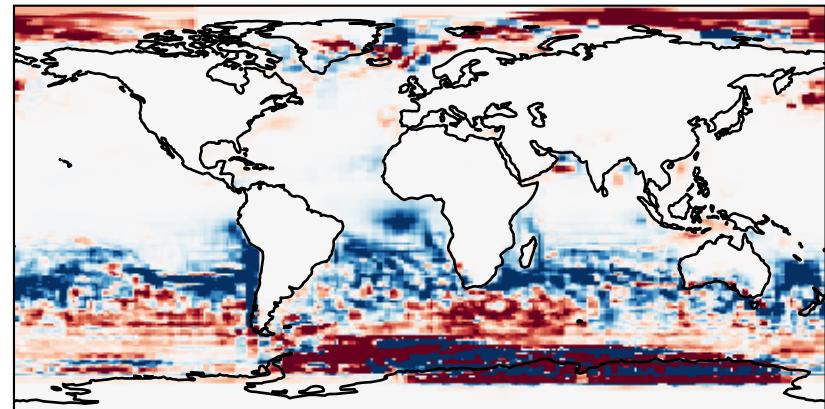
GCHP\_13.4.0-rc.1 - GCHP\_13.3.0-rc.1 (Dev)  
c48



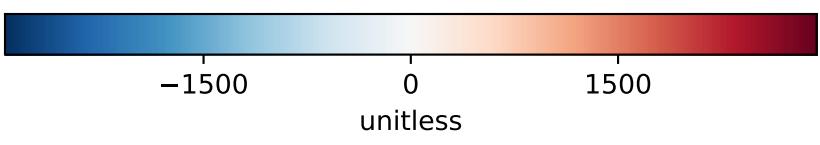
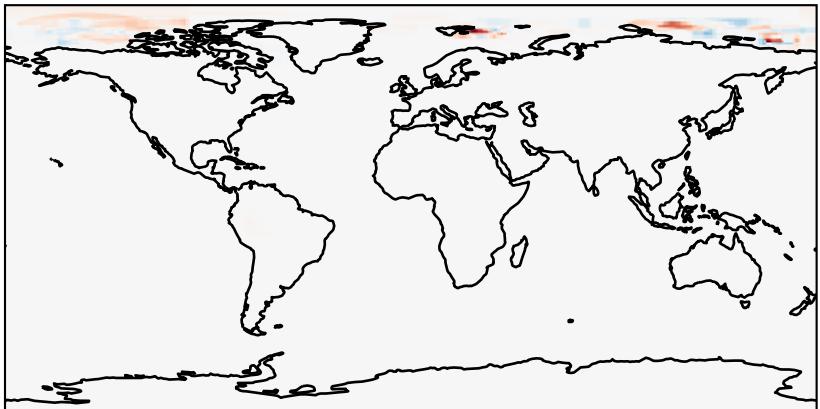
Difference (1x1.25)  
Dev - Ref, Dynamic Range



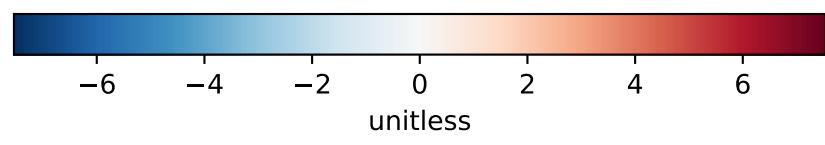
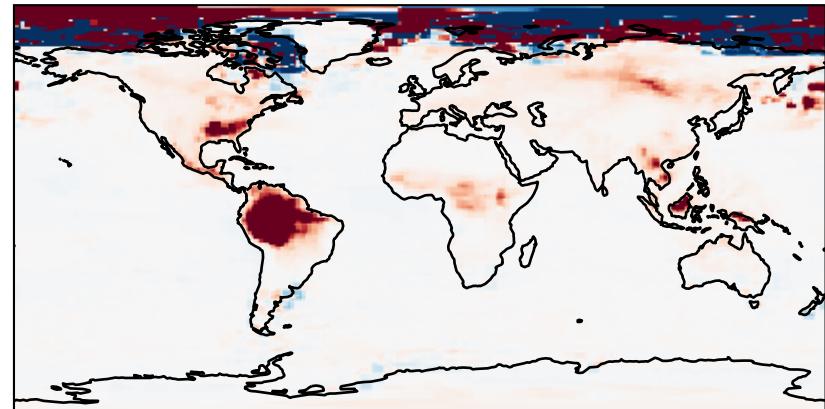
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

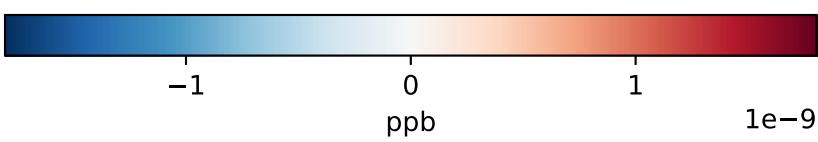
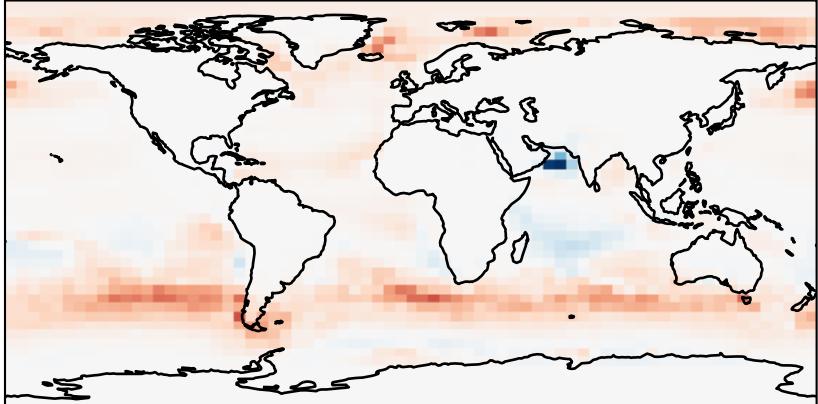


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

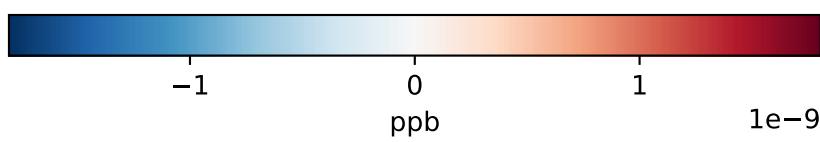
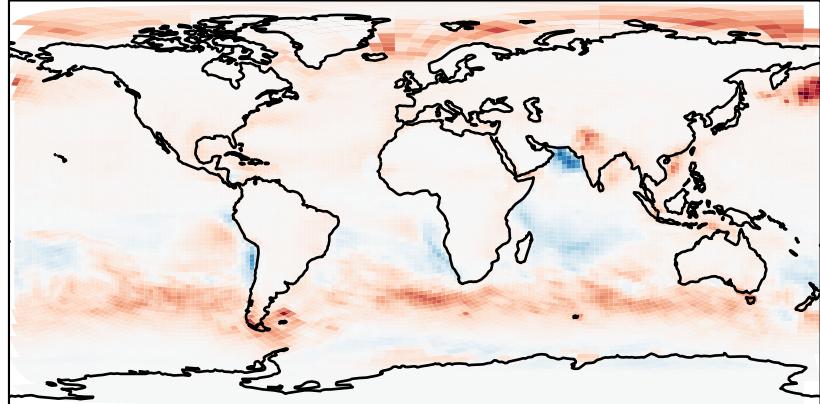


# SpeciesConc\_ClOO

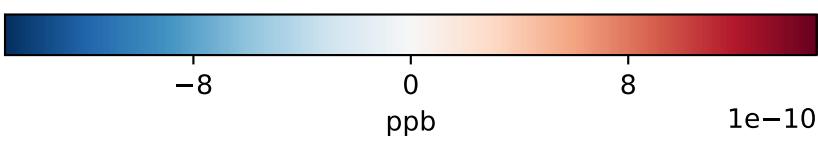
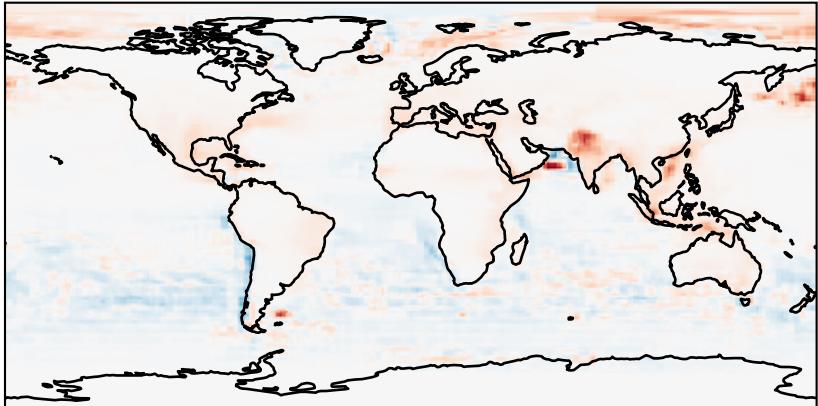
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



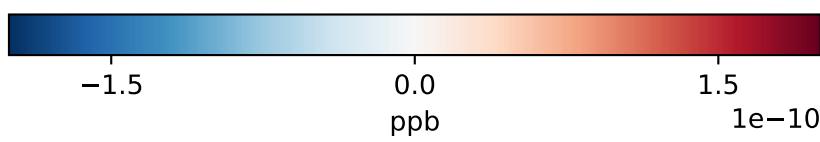
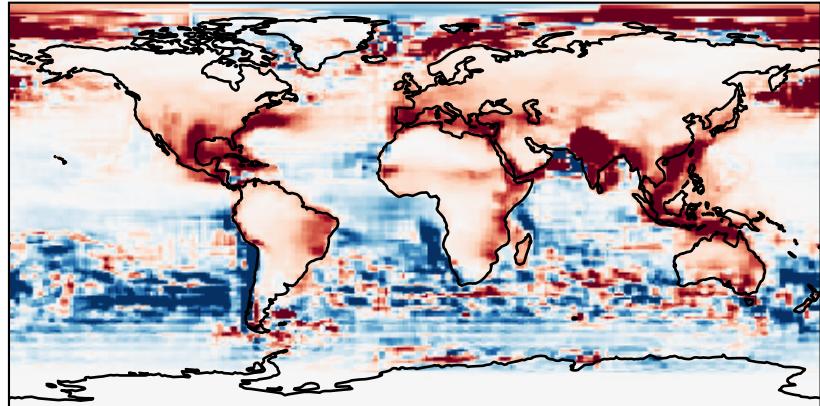
GCHP\_13.4.0-rc.1 - GCHP\_13.3.0-rc.1 (Dev)  
c48



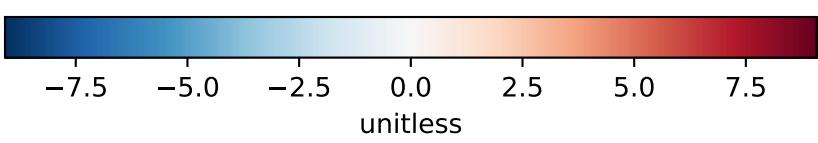
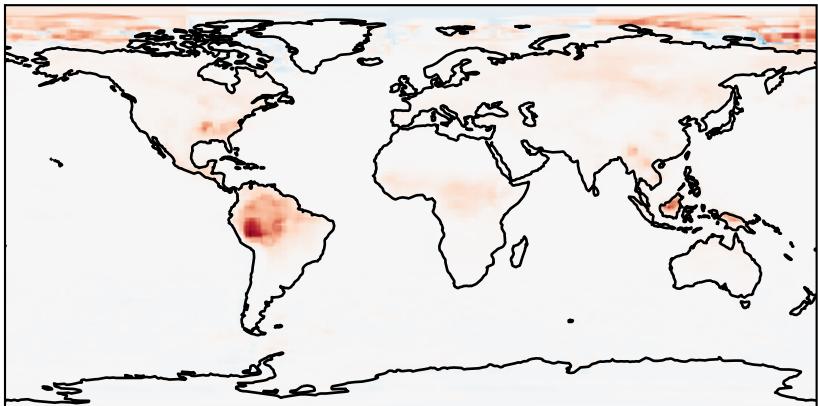
Difference (1x1.25)  
Dev - Ref, Dynamic Range



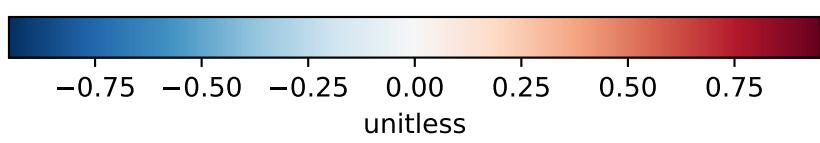
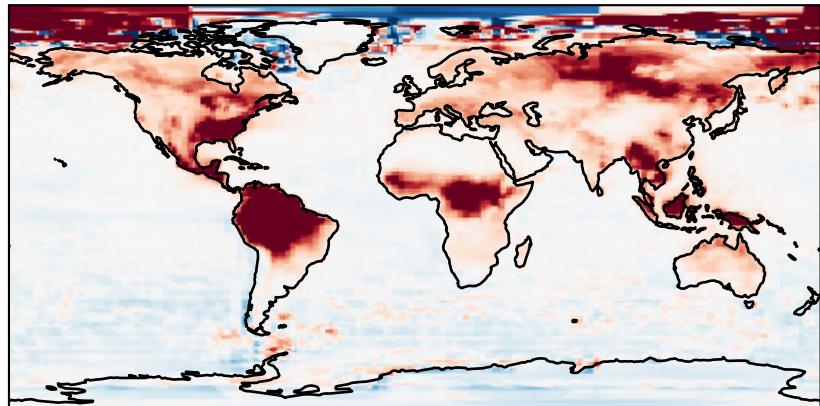
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

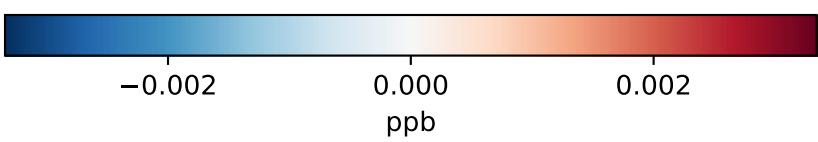
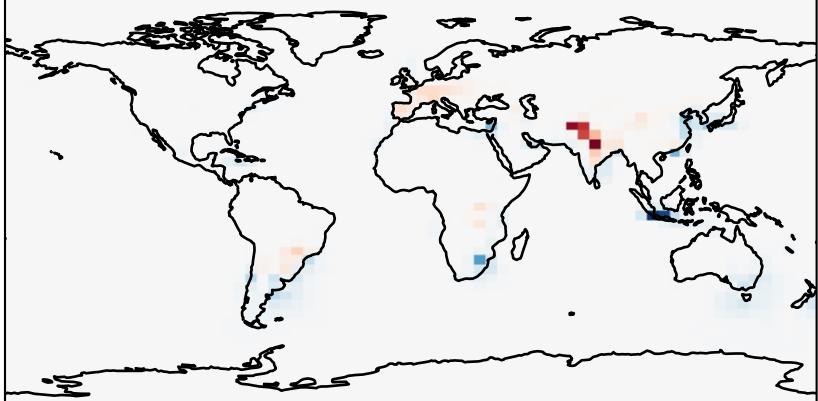


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

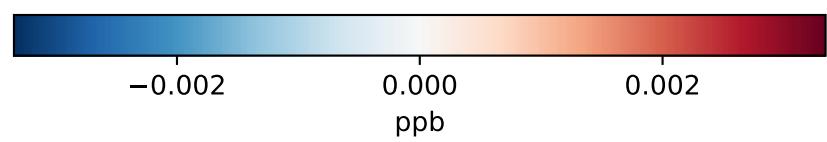
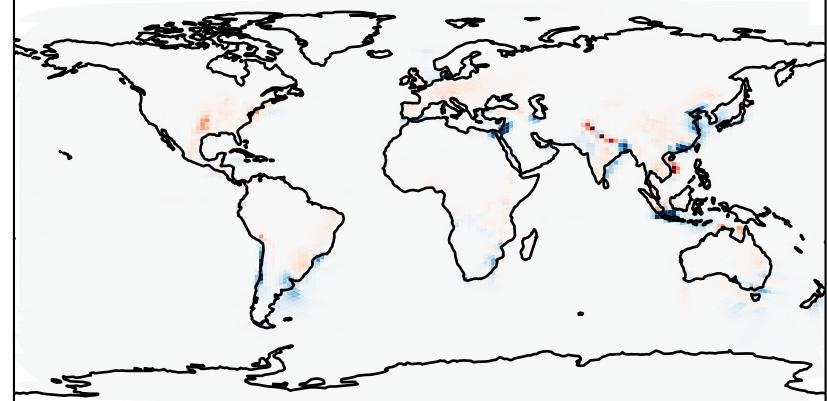


# SpeciesConc\_ClNO<sub>2</sub>

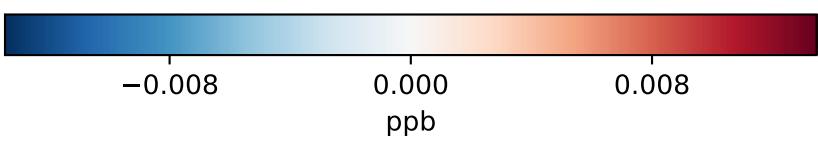
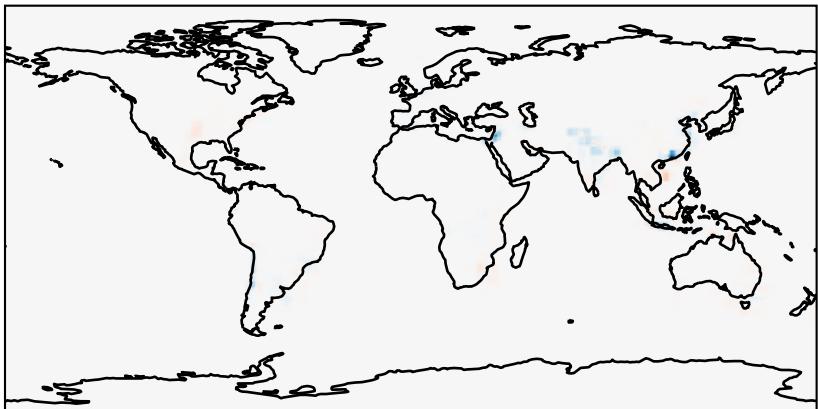
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



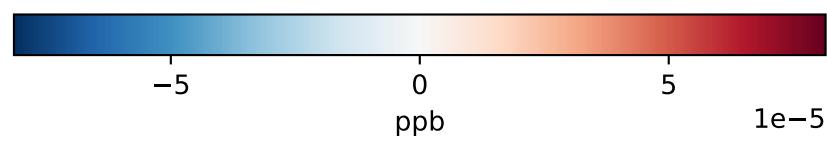
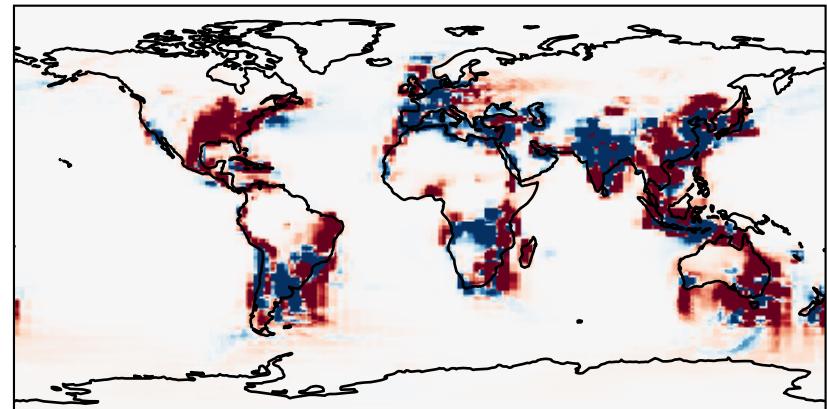
GCHP\_13.4.0-rc.1 - GCHP\_13.3.0-rc.1 (Dev)  
c48



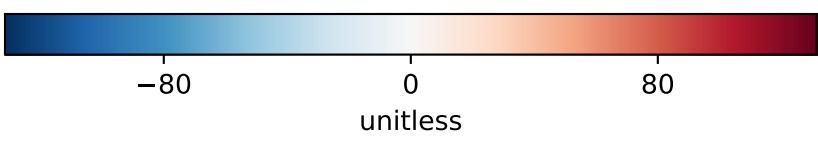
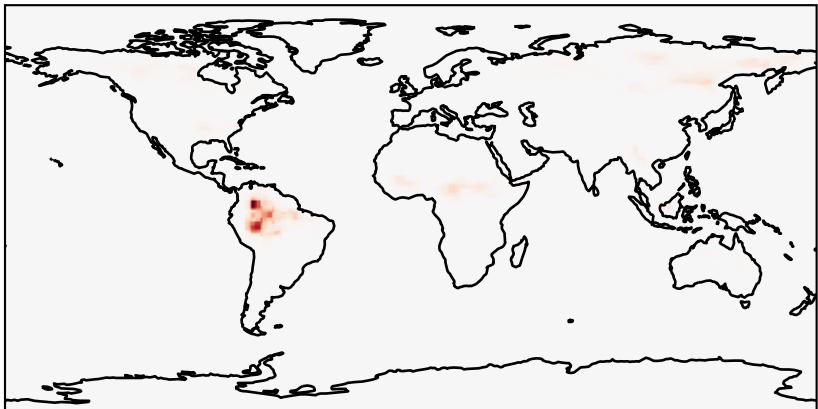
Difference (1x1.25)  
Dev - Ref, Dynamic Range



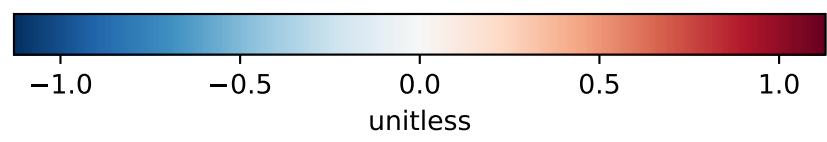
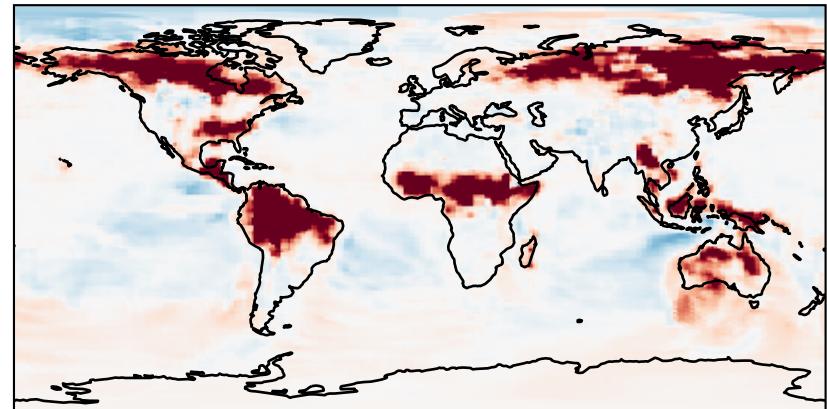
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

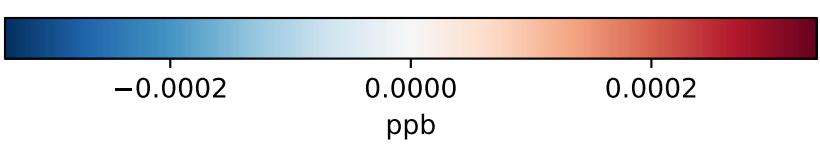
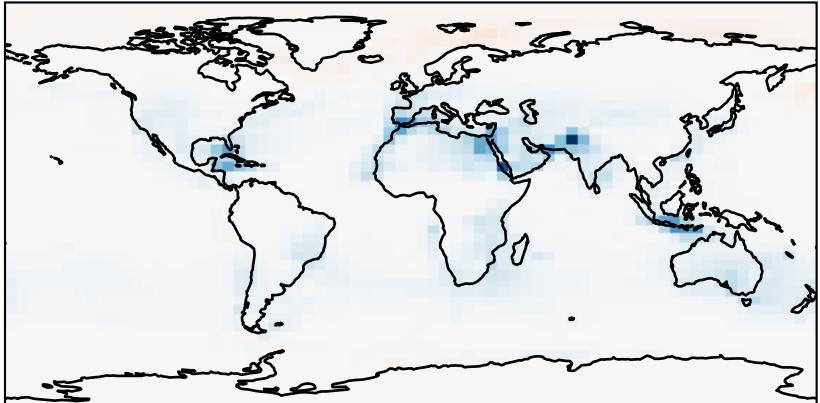


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

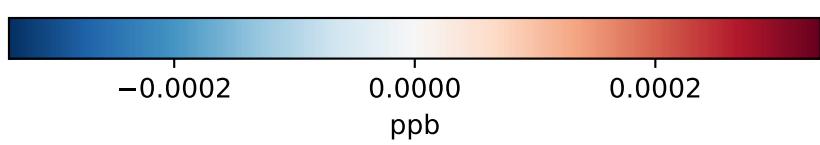
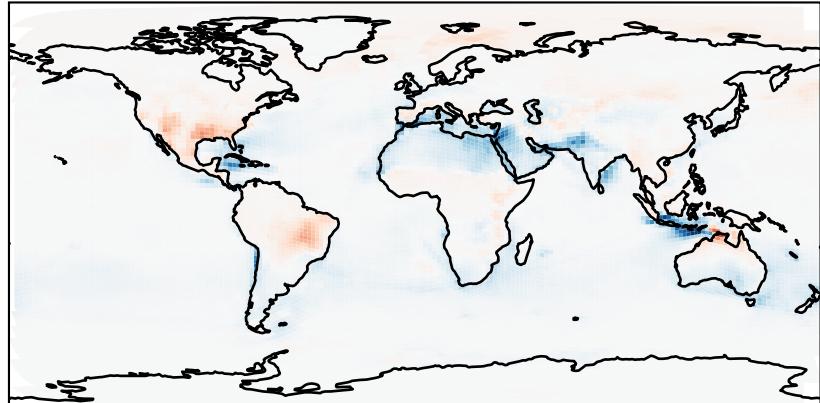


# SpeciesConc\_CINO3

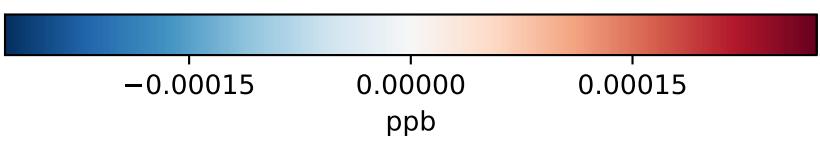
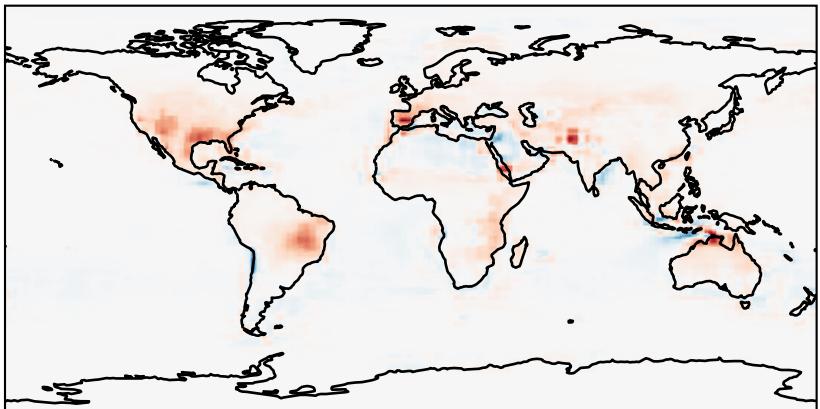
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



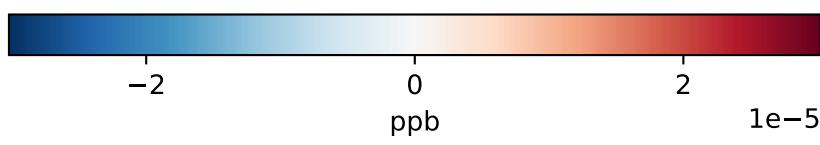
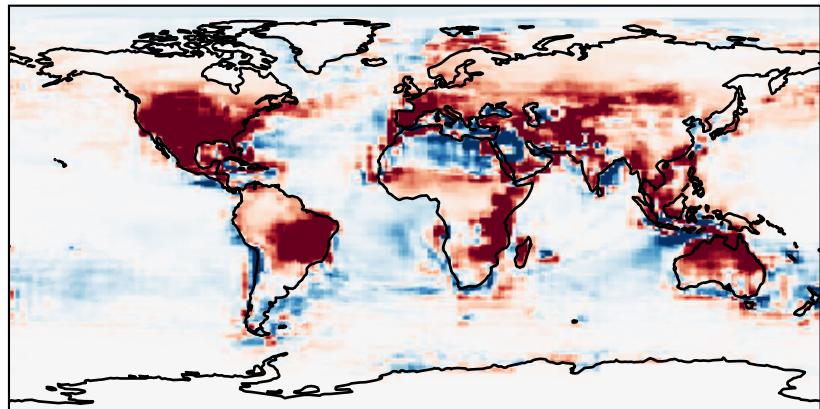
GCHP\_13.4.0-rc.1 - GCHP\_13.3.0-rc.1 (Dev)  
c48



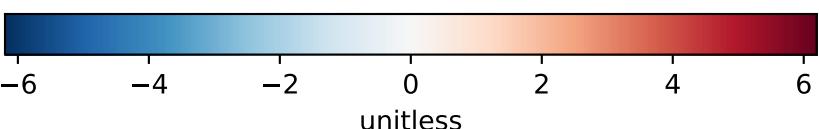
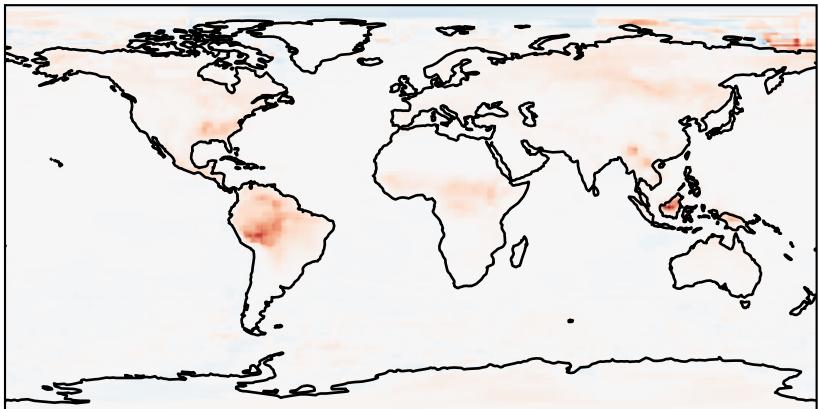
Difference (1x1.25)  
Dev - Ref, Dynamic Range



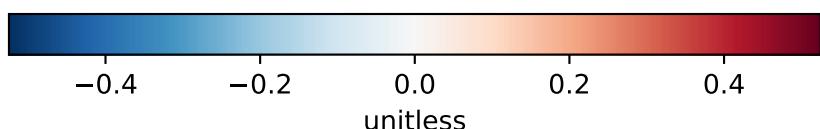
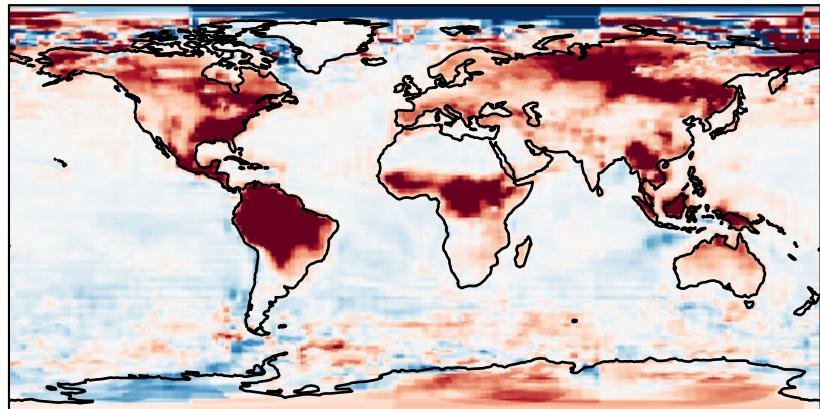
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

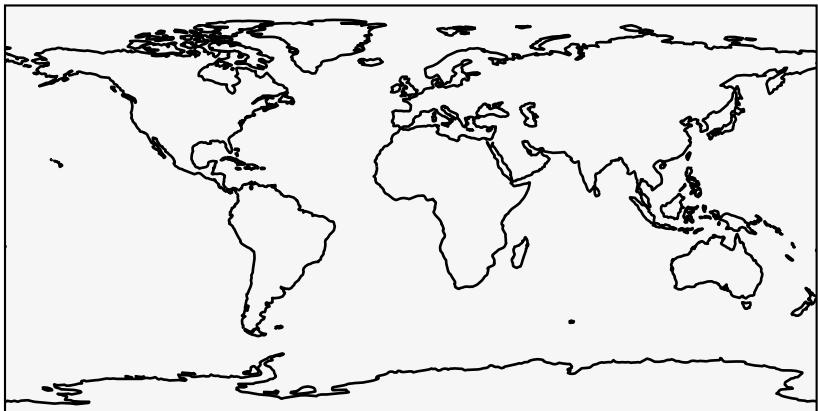


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref



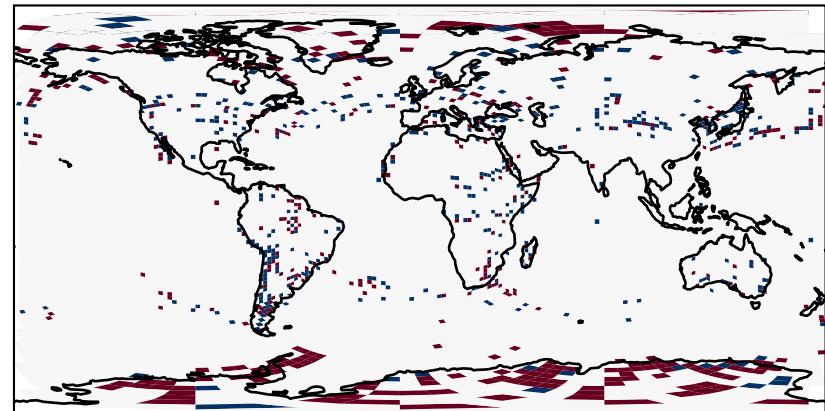
# SpeciesConc\_CCI4

GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



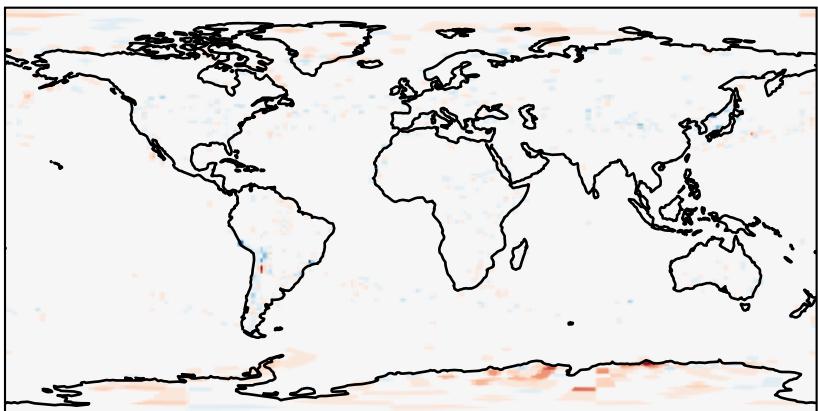
Zero throughout domain  
ppb

GCHP\_13.4.0-rc.1 - GCHP\_13.3.0-rc.1 (Dev)  
c48



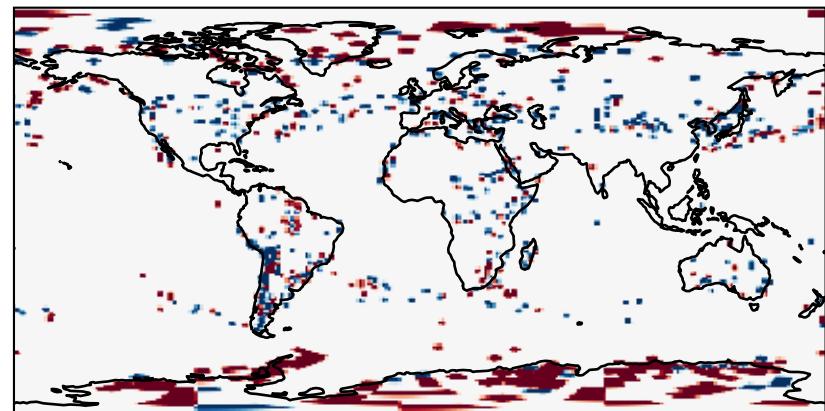
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ppb

Difference (1x1.25)  
Dev - Ref, Dynamic Range



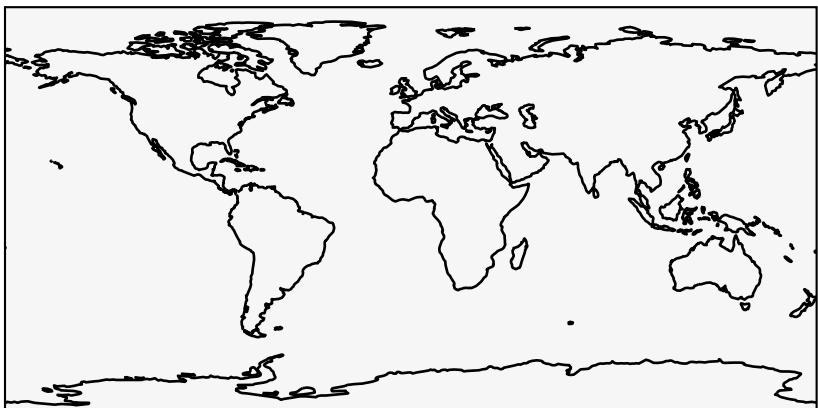
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ppb

Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



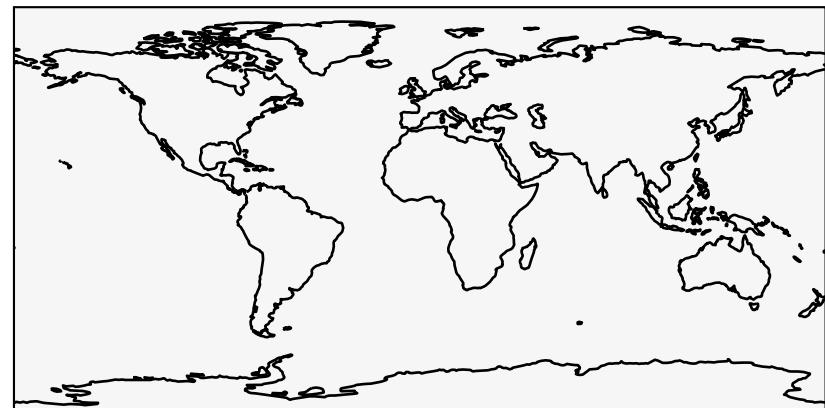
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Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref



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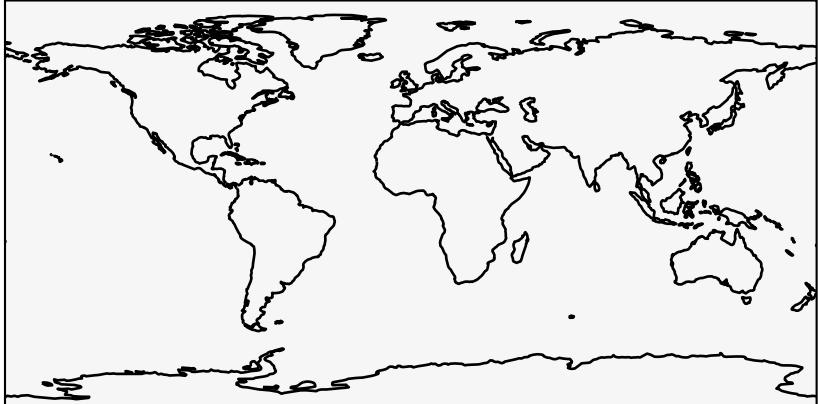
Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref



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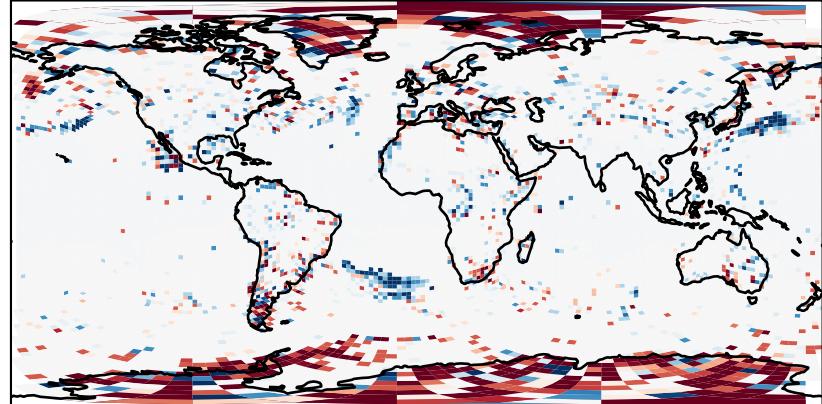
# SpeciesConc\_CFCs

GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



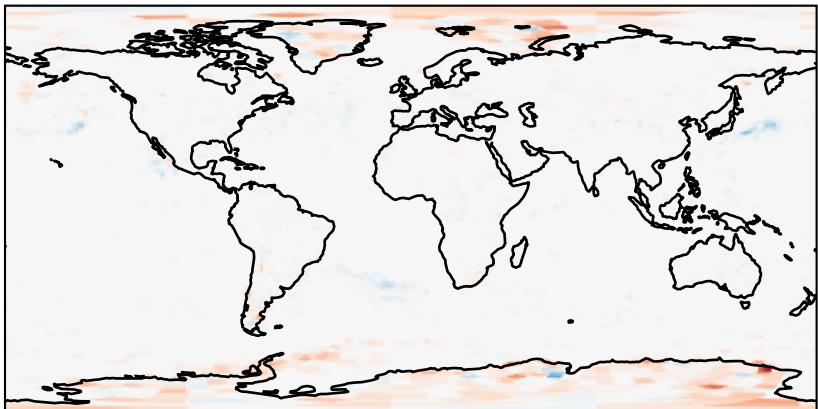
Zero throughout domain  
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GCHP\_13.4.0-rc.1 - GCHP\_13.3.0-rc.1 (Dev)  
c48



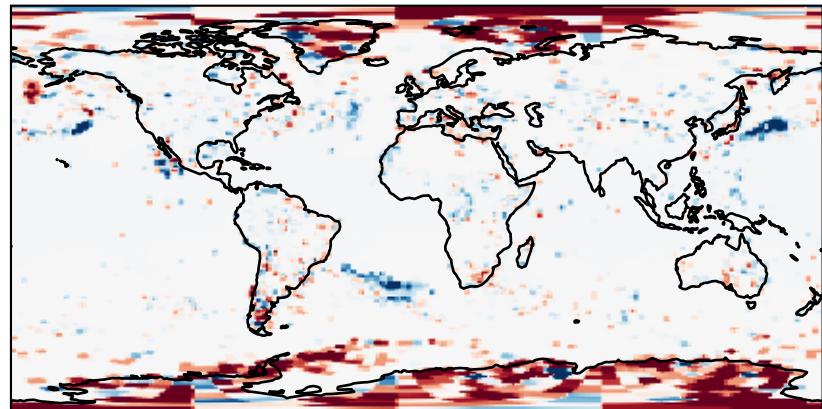
-5 0 5 1e-8  
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Difference (1x1.25)  
Dev - Ref, Dynamic Range



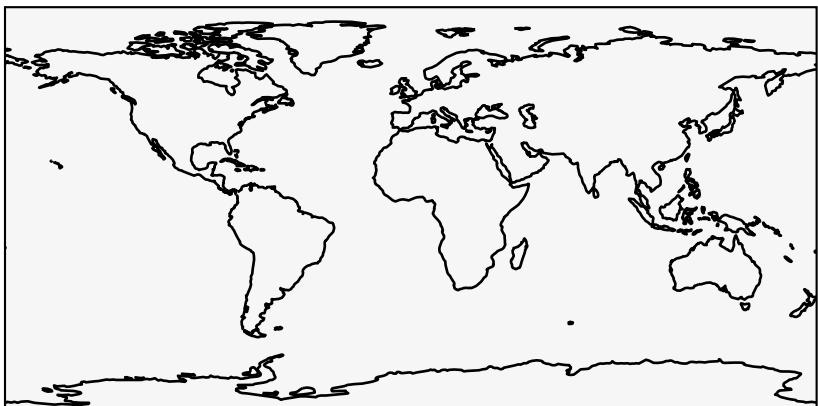
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Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



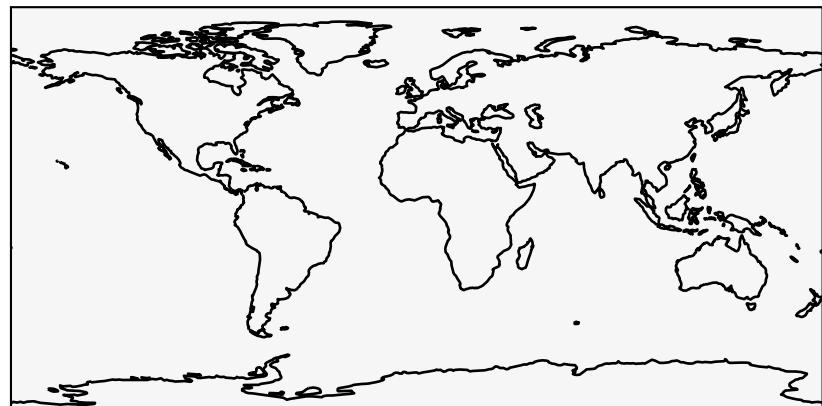
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Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref



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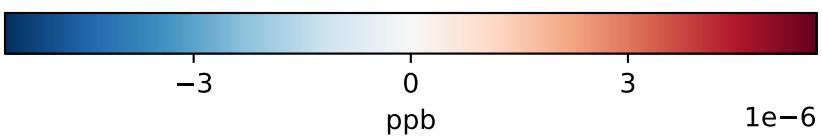
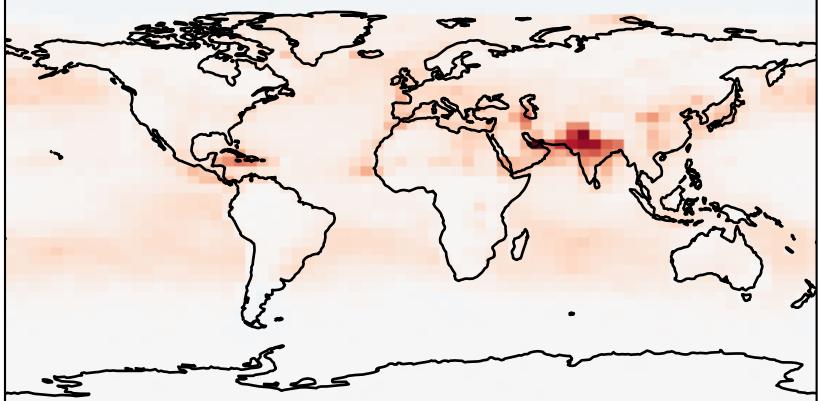
Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref



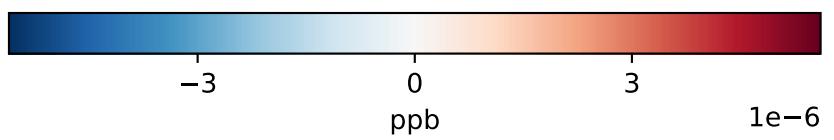
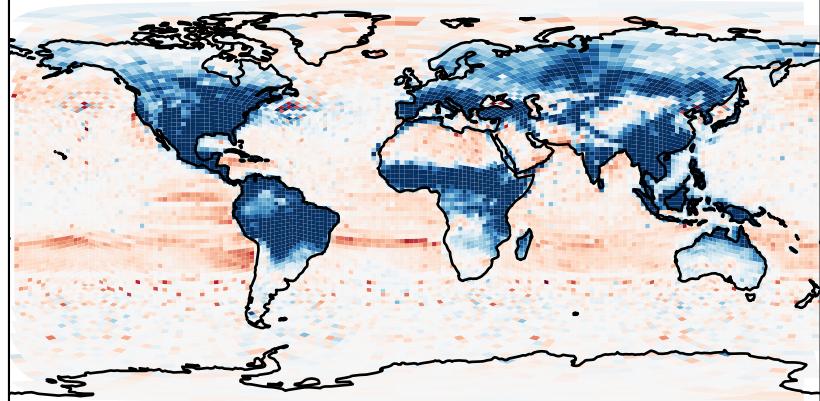
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# SpeciesConc\_CH3Cl

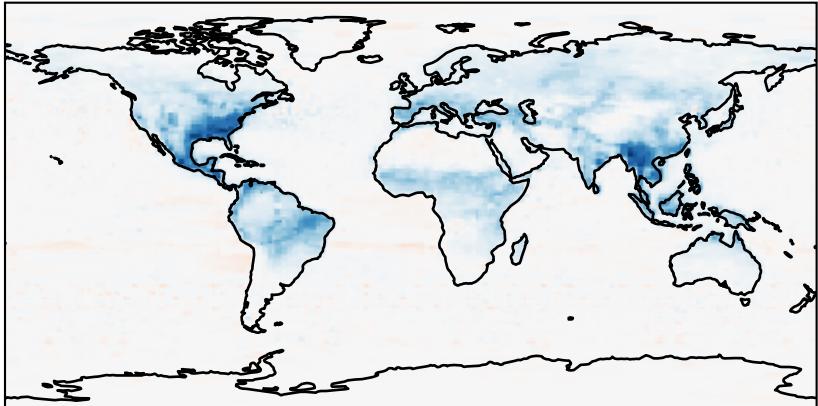
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



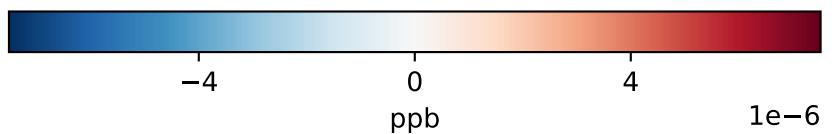
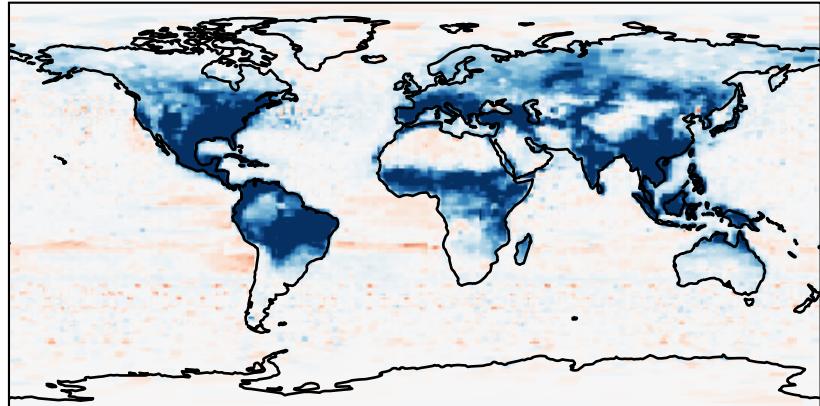
GCHP\_13.4.0-rc.1 - GCHP\_13.3.0-rc.1 (Dev)  
c48



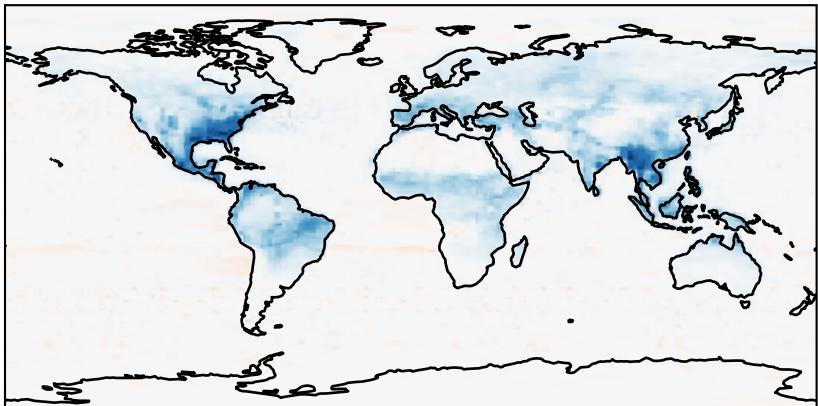
Difference (1x1.25)  
Dev - Ref, Dynamic Range



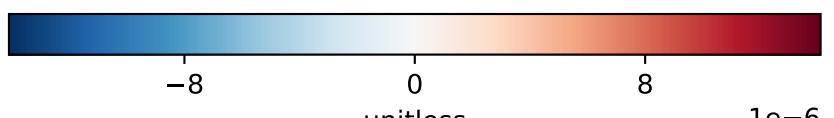
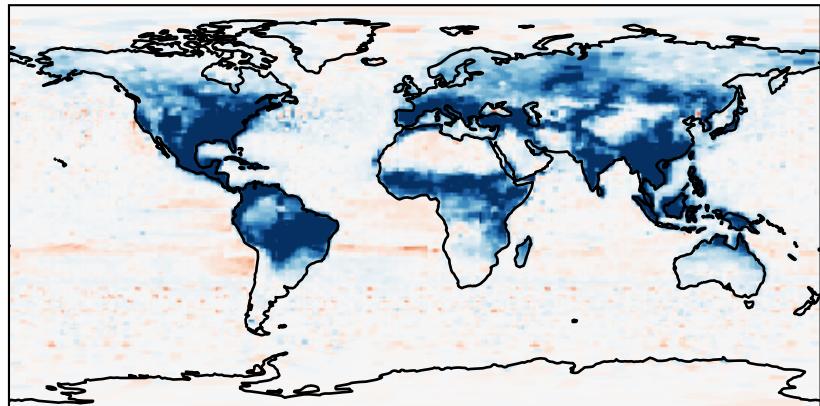
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

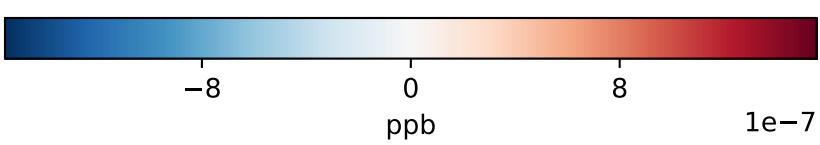
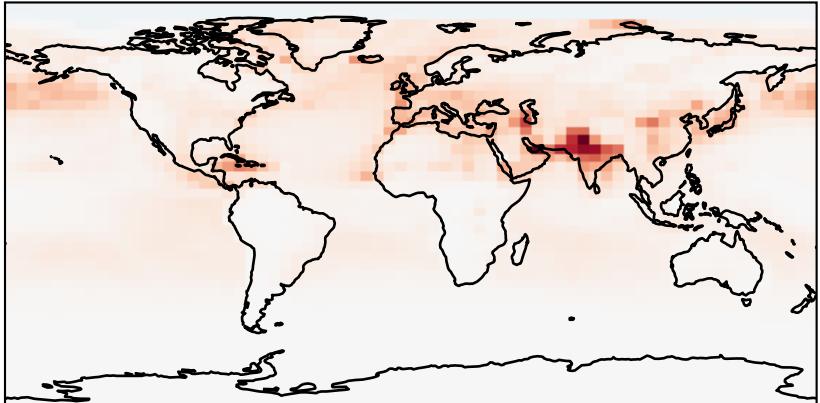


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

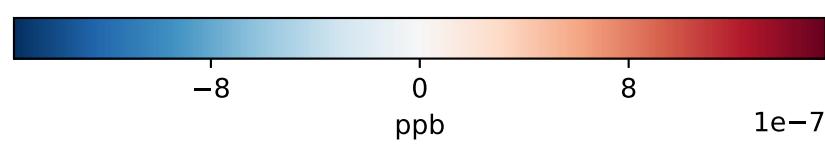
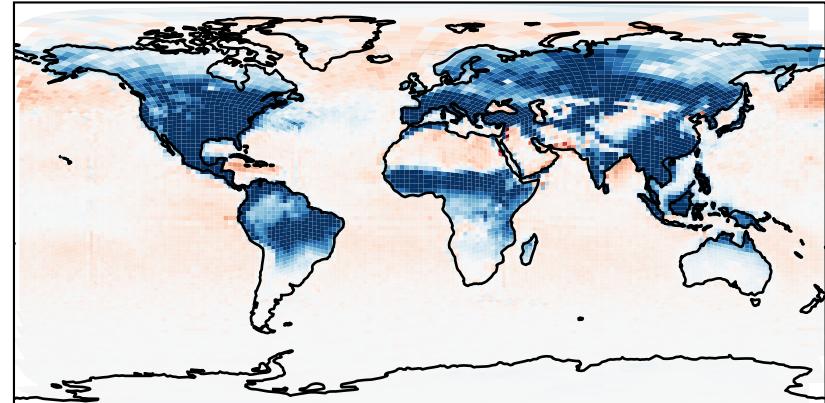


# SpeciesConc\_CH2Cl2

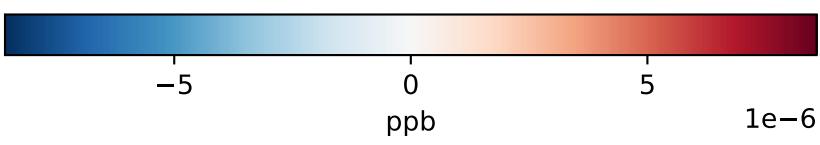
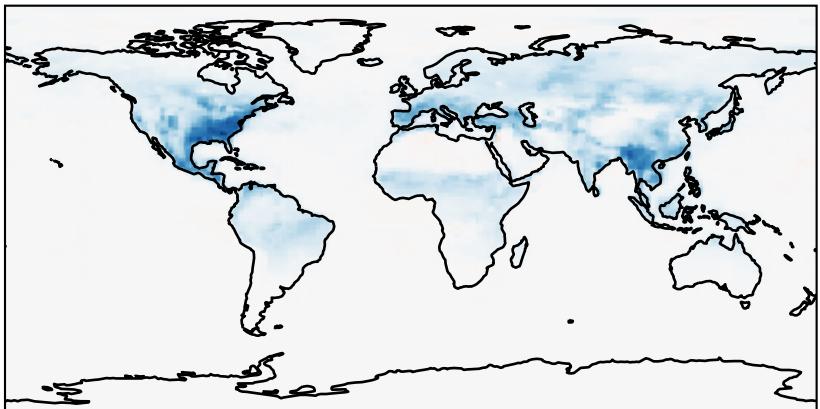
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



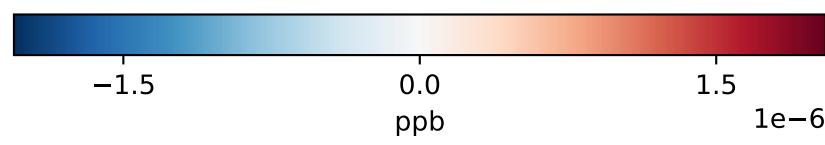
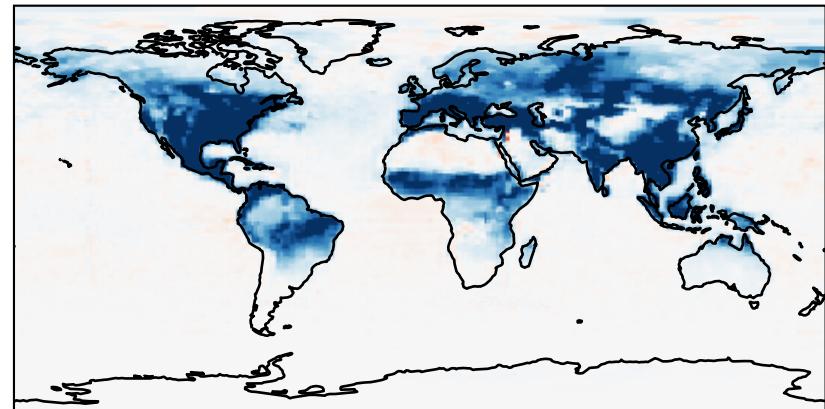
GCHP\_13.4.0-rc.1 - GCHP\_13.3.0-rc.1 (Dev)  
c48



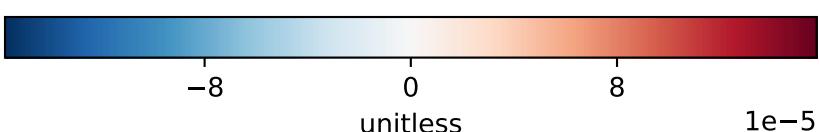
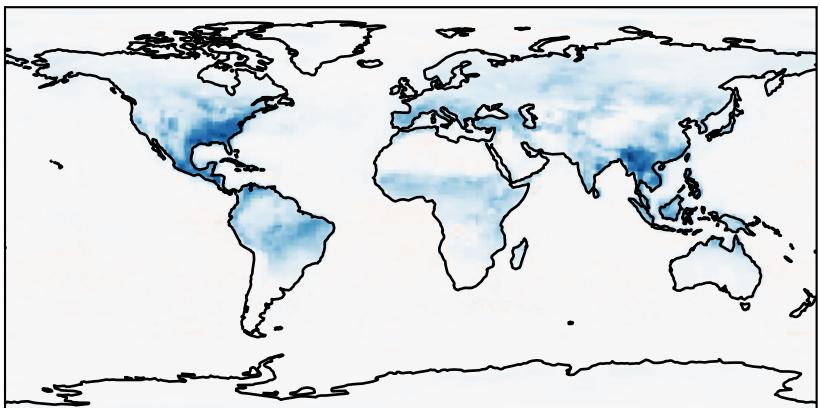
Difference (1x1.25)  
Dev - Ref, Dynamic Range



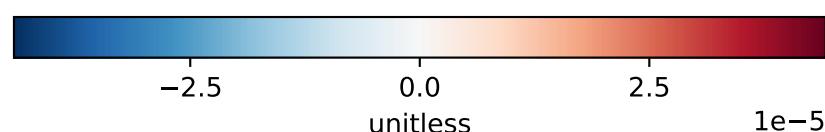
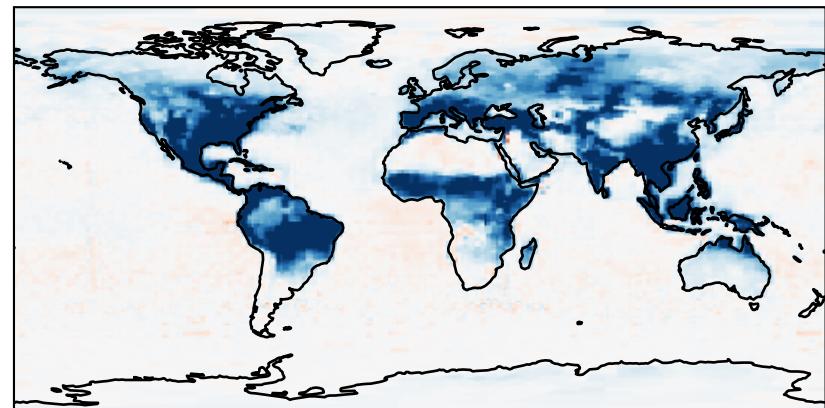
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

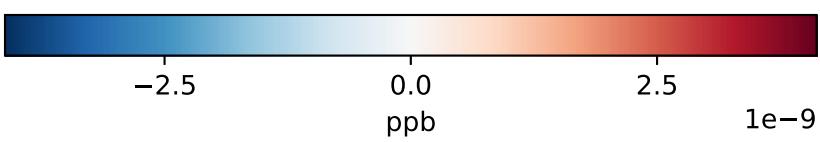
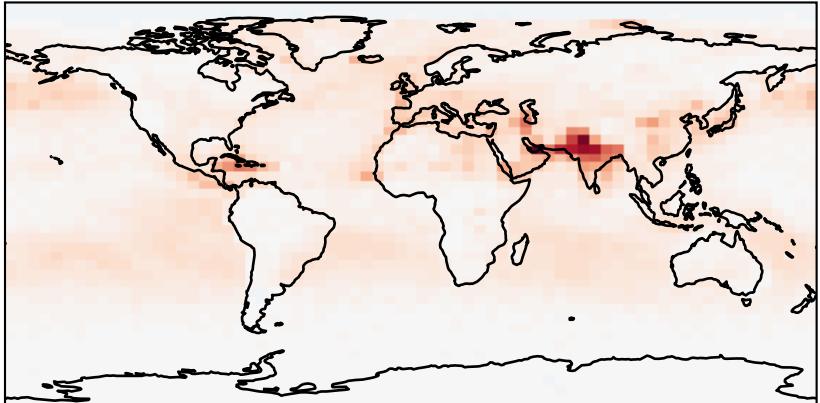


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

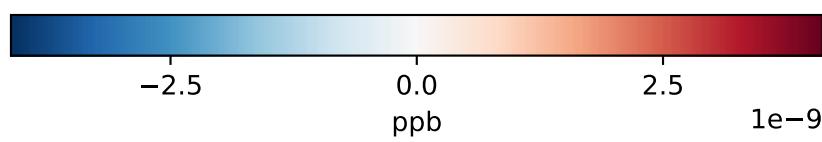
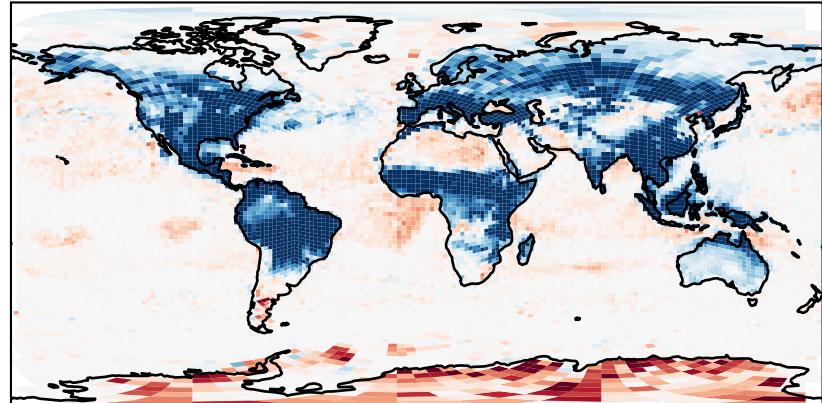


# SpeciesConc\_CH3CCl3

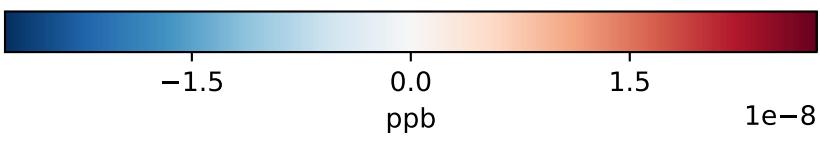
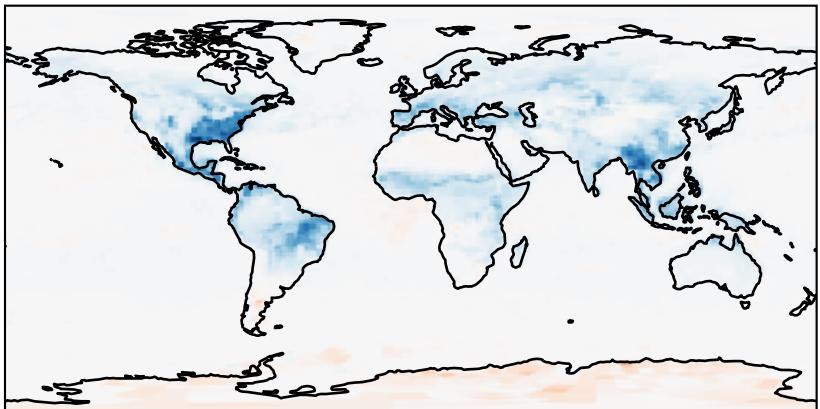
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



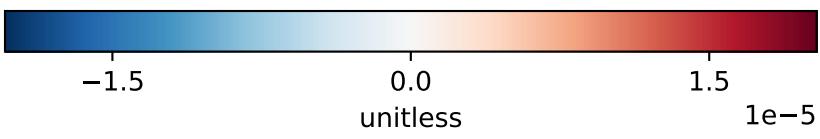
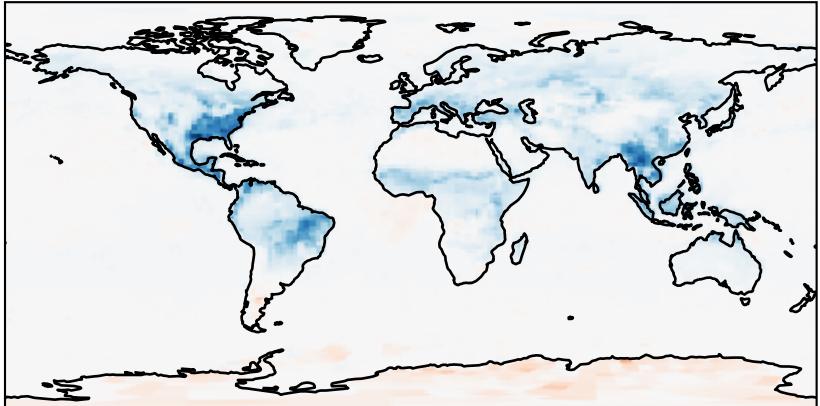
GCHP\_13.4.0-rc.1 - GCHP\_13.3.0-rc.1 (Dev)  
c48



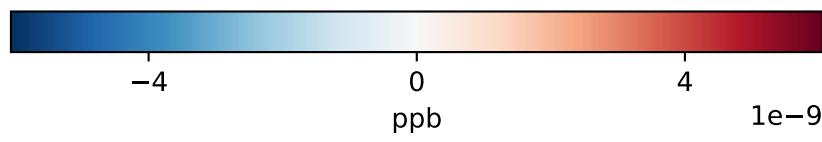
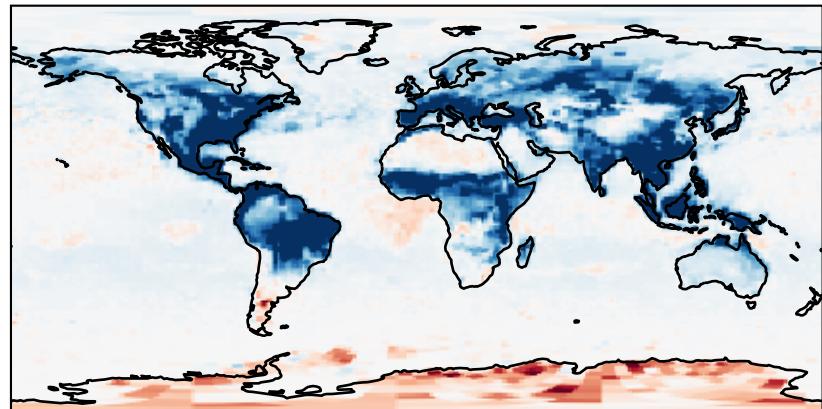
Difference (1x1.25)  
Dev - Ref, Dynamic Range



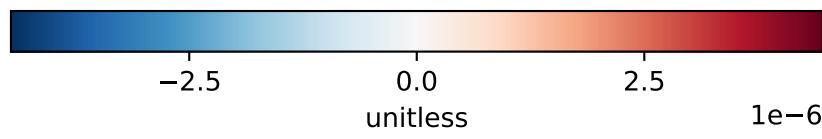
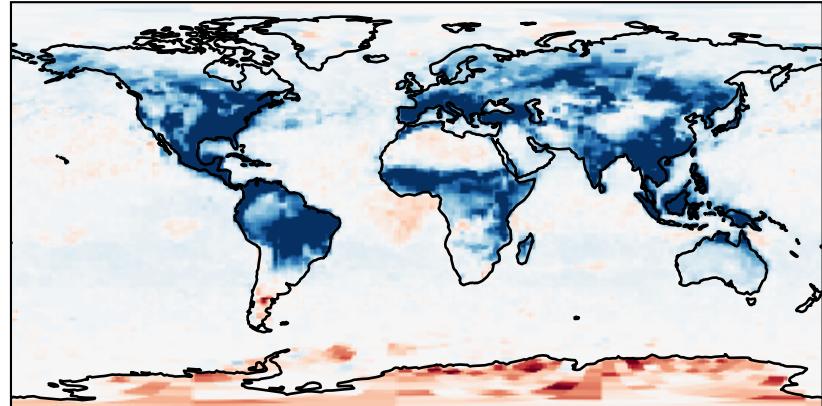
Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref



Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]

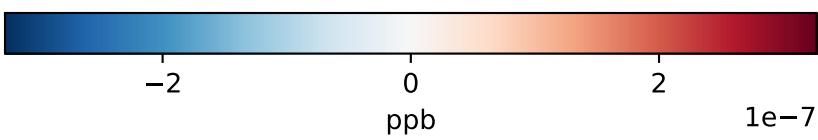
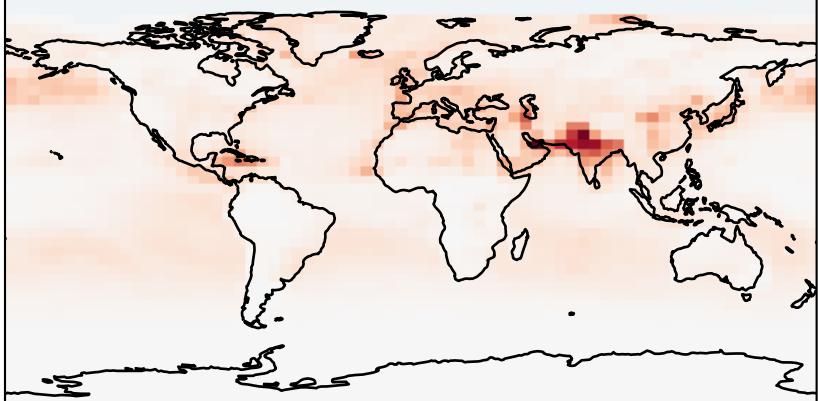


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

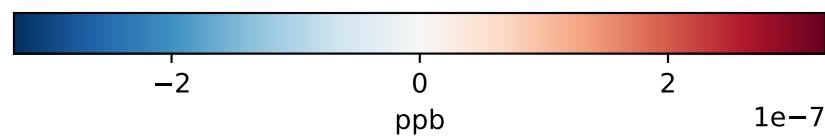
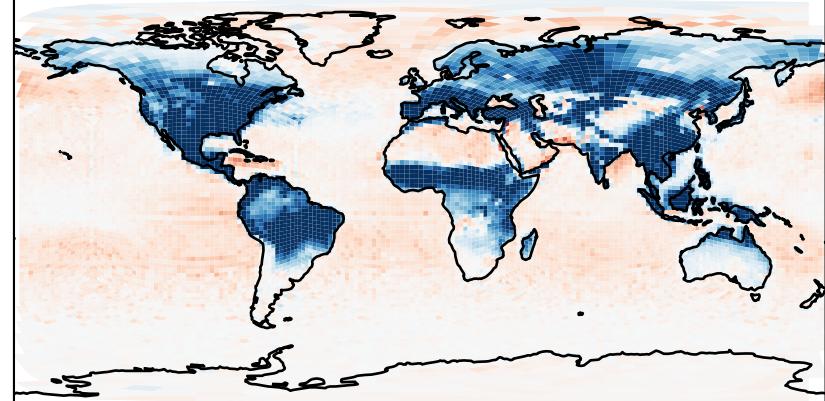


# SpeciesConc\_CHCl3

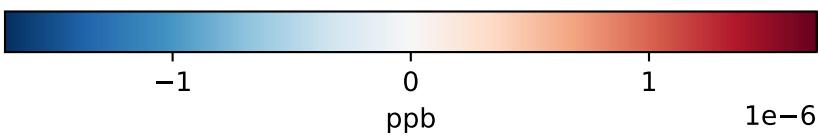
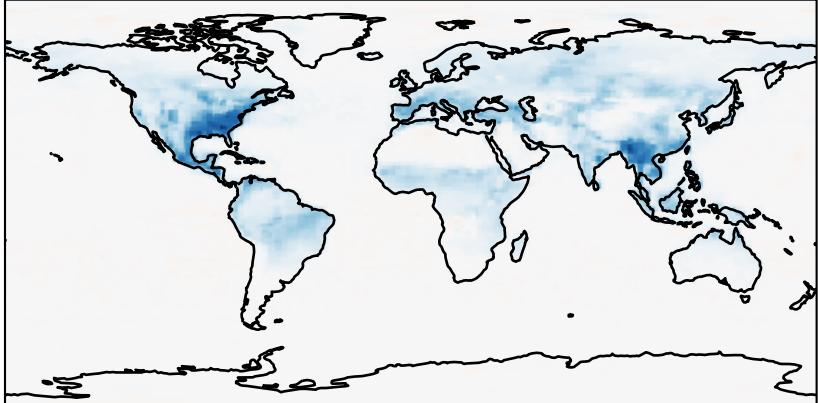
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



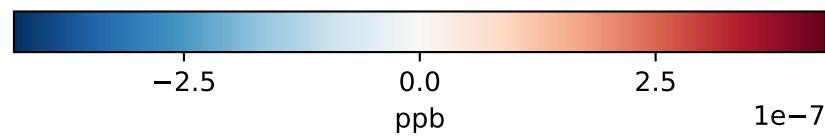
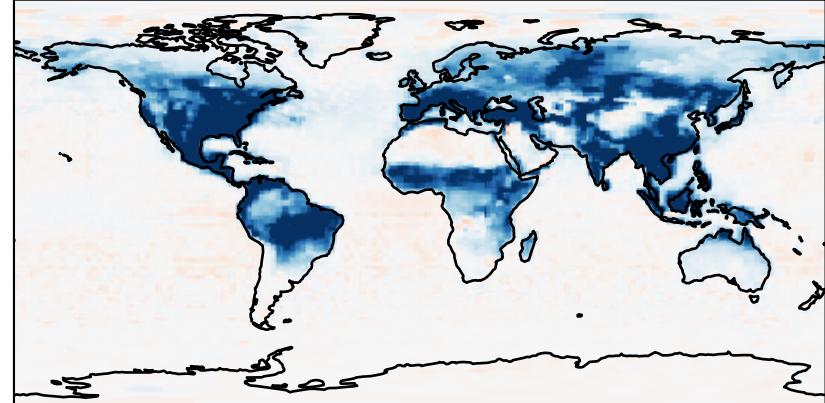
GCHP\_13.4.0-rc.1 - GCHP\_13.3.0-rc.1 (Dev)  
c48



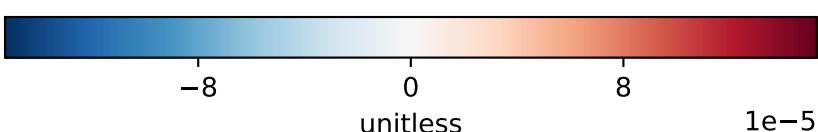
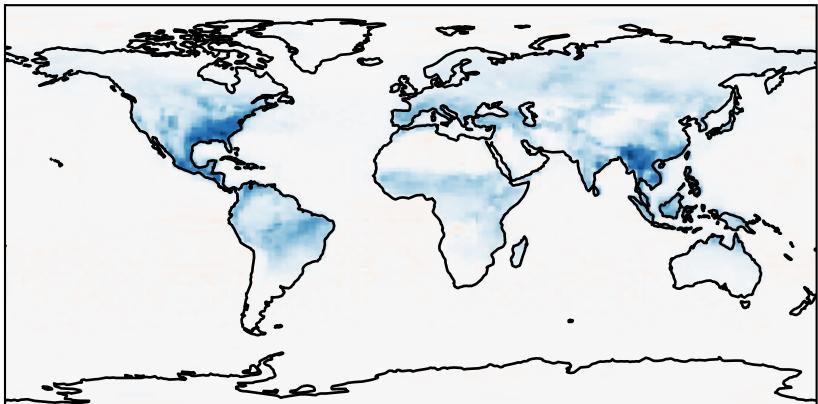
Difference (1x1.25)  
Dev - Ref, Dynamic Range



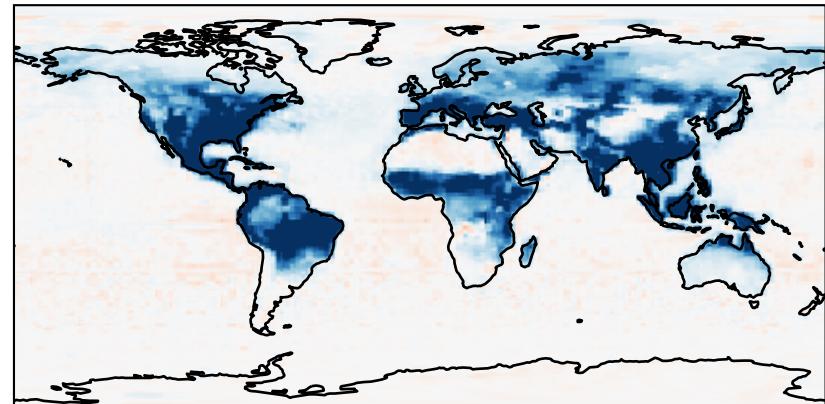
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

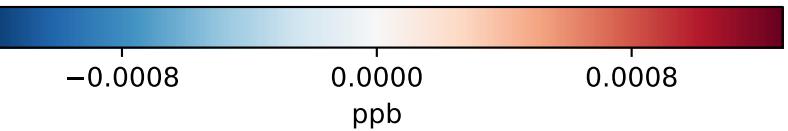
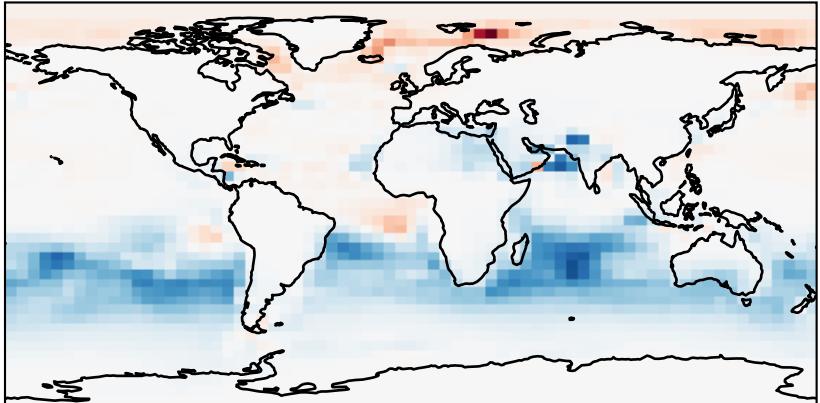


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

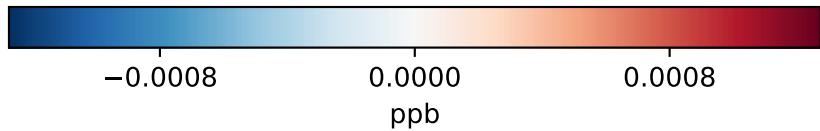
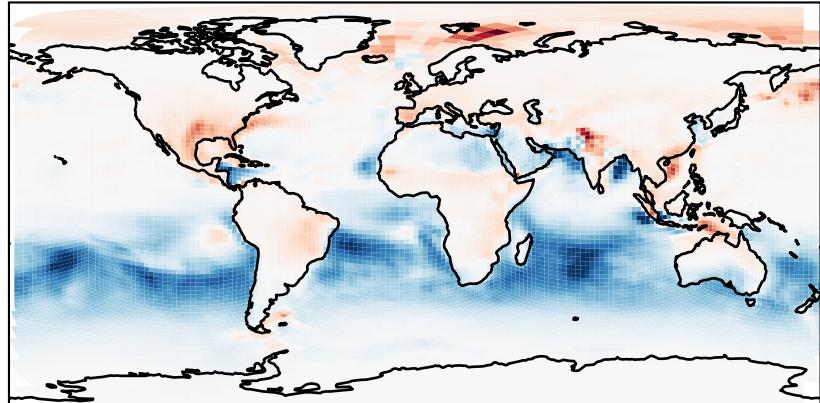


# SpeciesConc\_HOCl

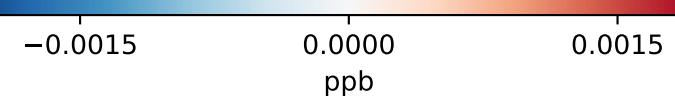
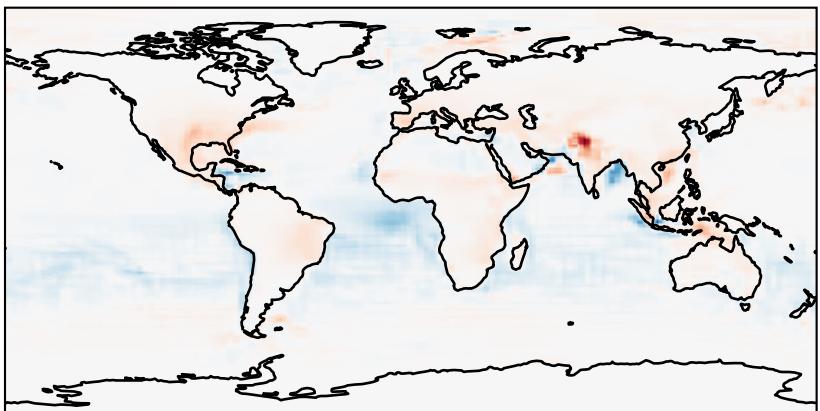
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



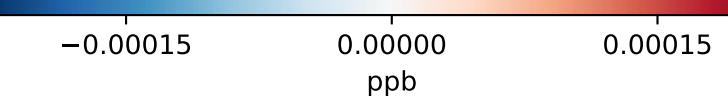
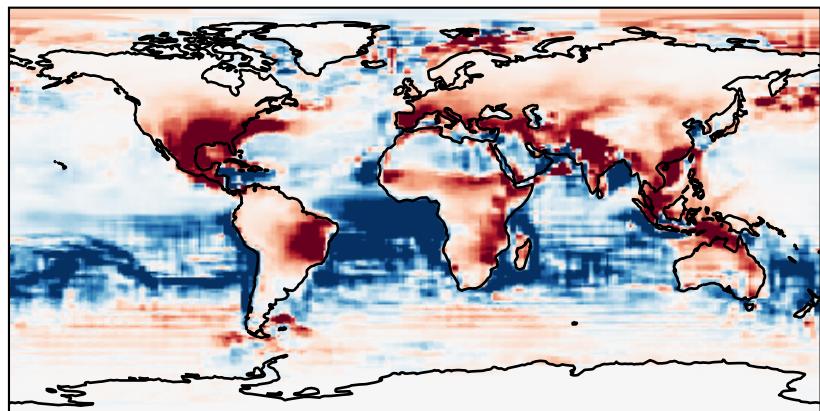
GCHP\_13.4.0-rc.1 - GCHP\_13.3.0-rc.1 (Dev)  
c48



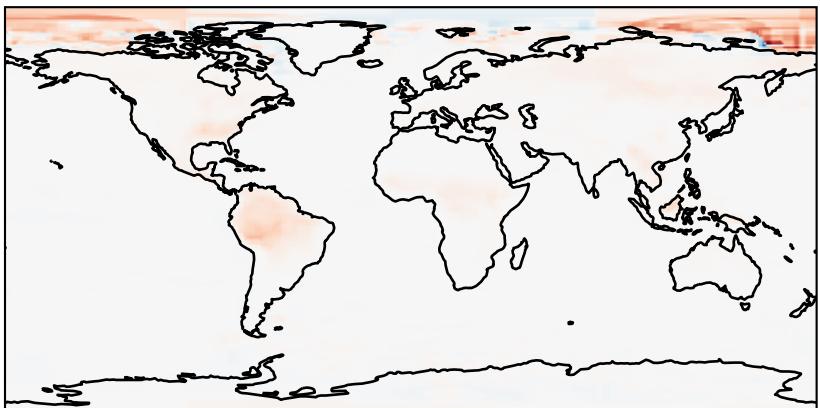
Difference (1x1.25)  
Dev - Ref, Dynamic Range



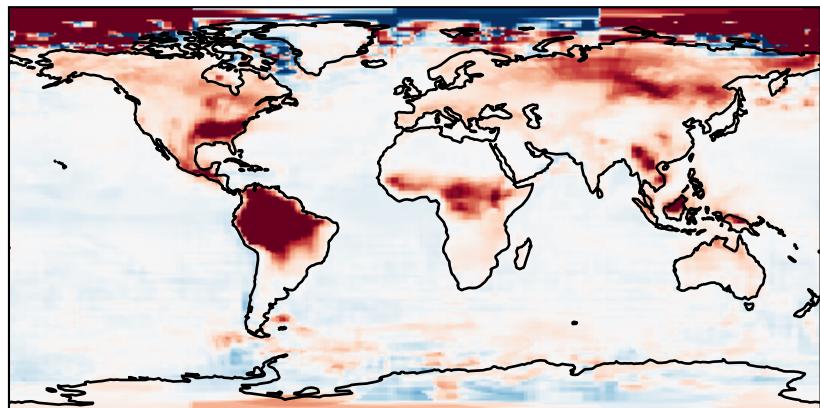
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

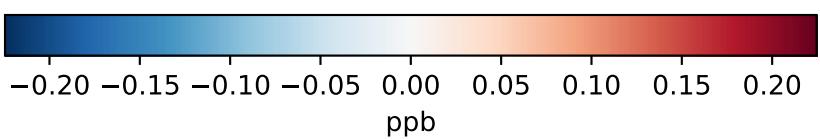
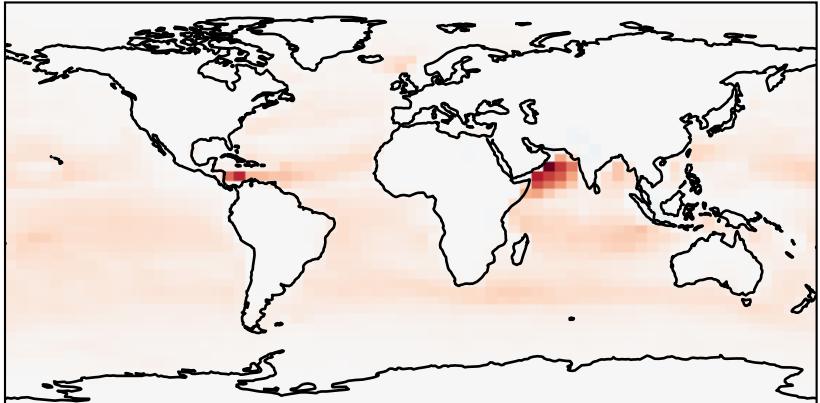


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

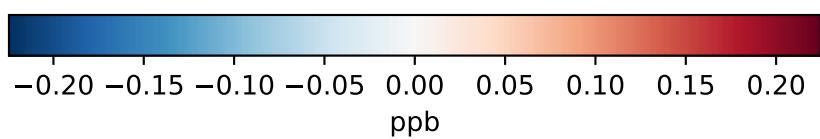
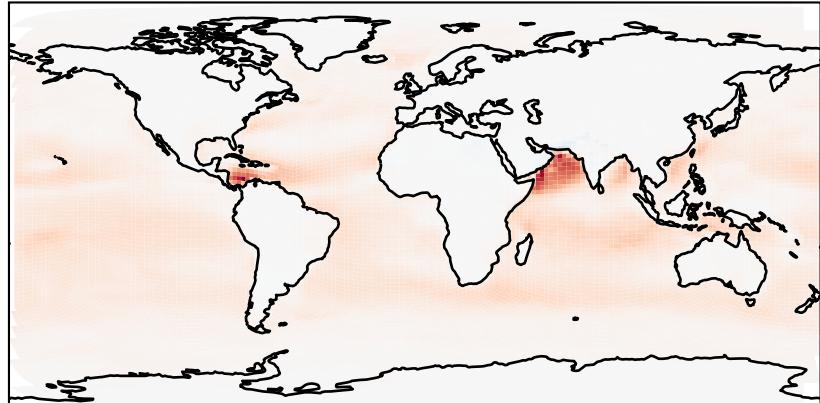


# SpeciesConc\_HCl

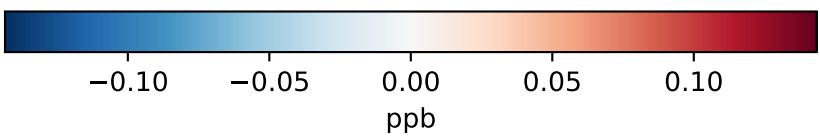
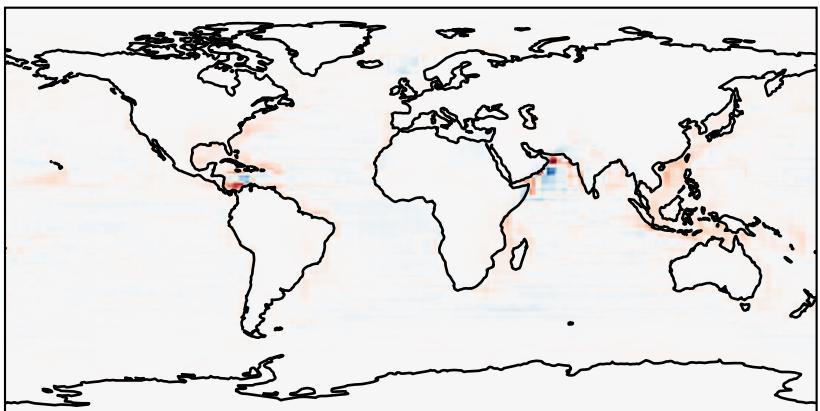
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



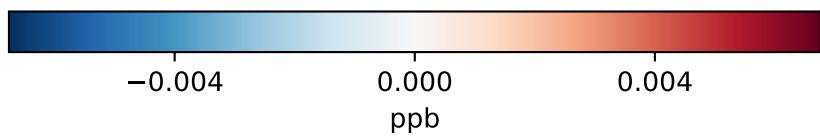
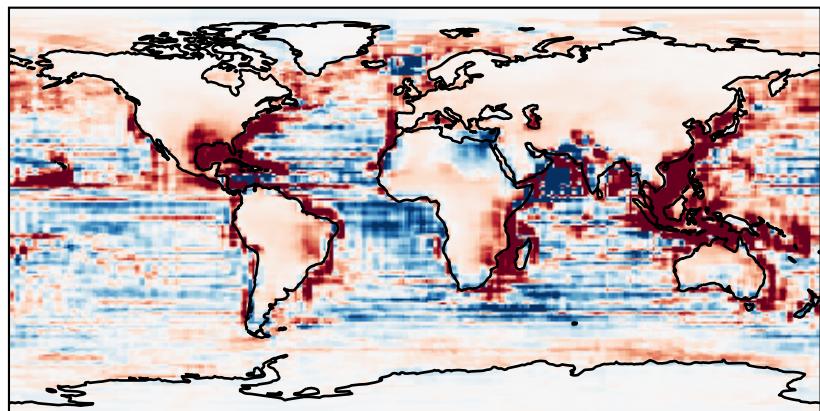
GCHP\_13.4.0-rc.1 - GCHP\_13.3.0-rc.1 (Dev)  
c48



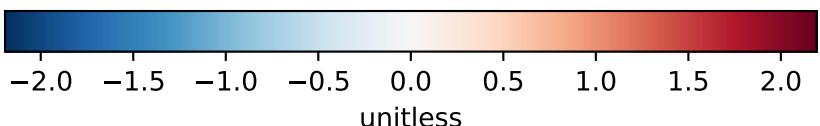
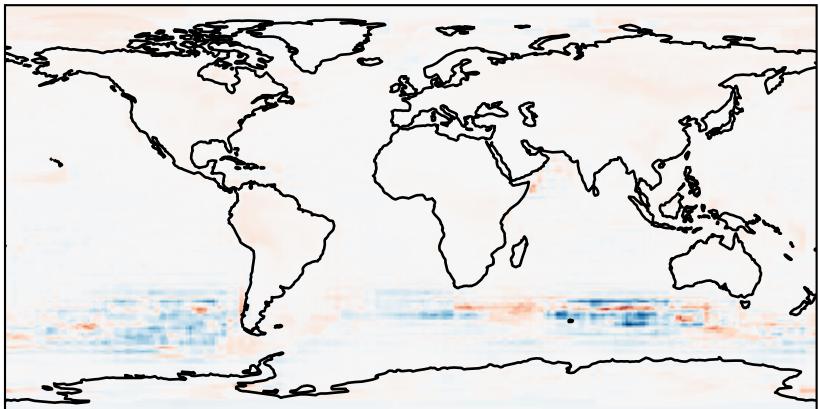
Difference (1x1.25)  
Dev - Ref, Dynamic Range



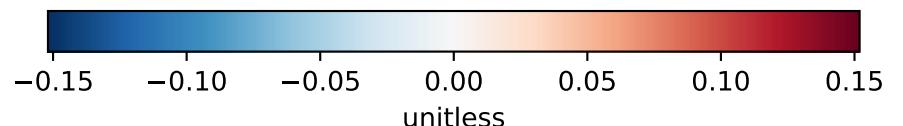
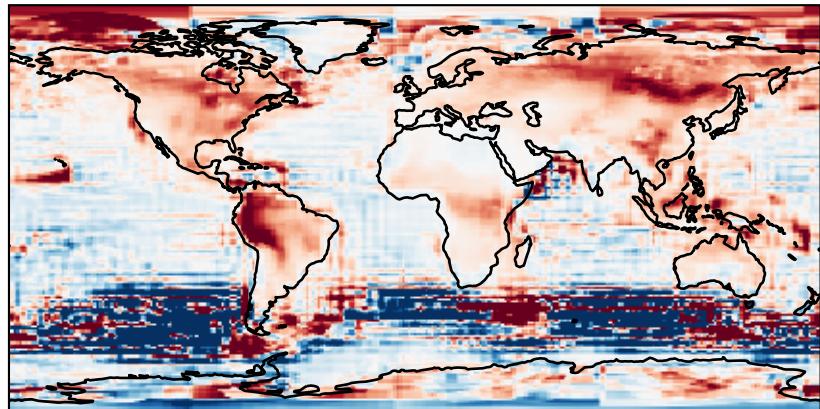
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

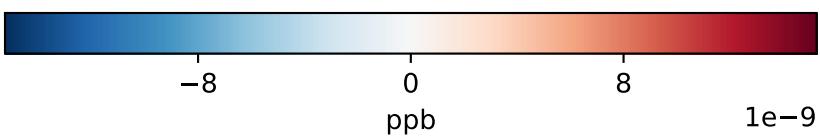
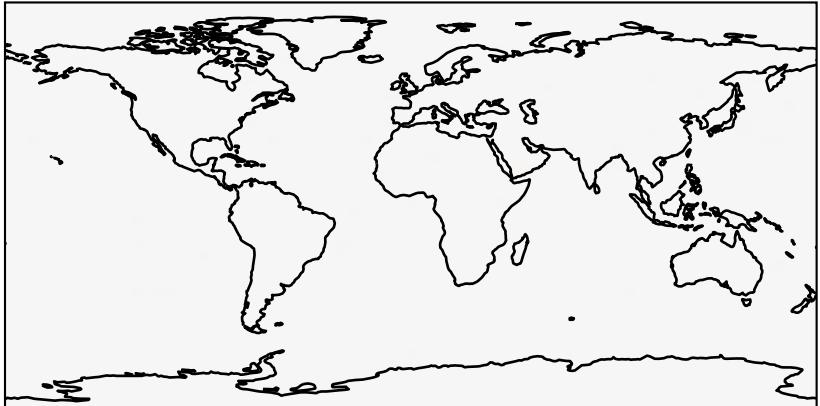


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

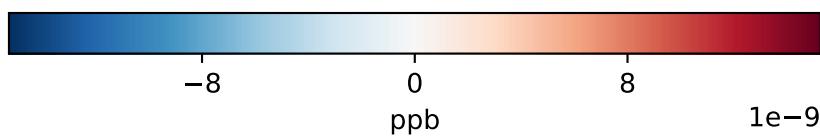
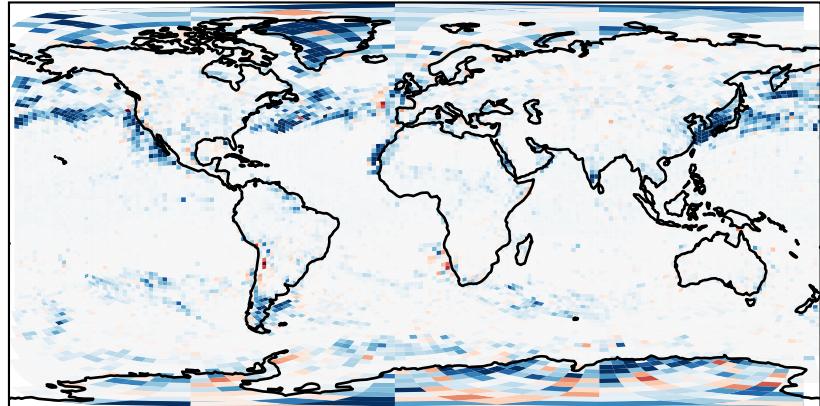


# SpeciesConc\_Halons

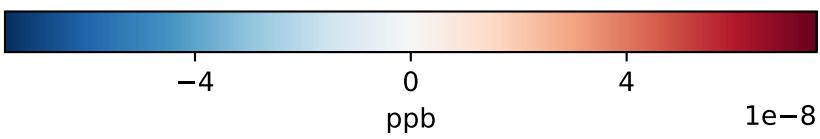
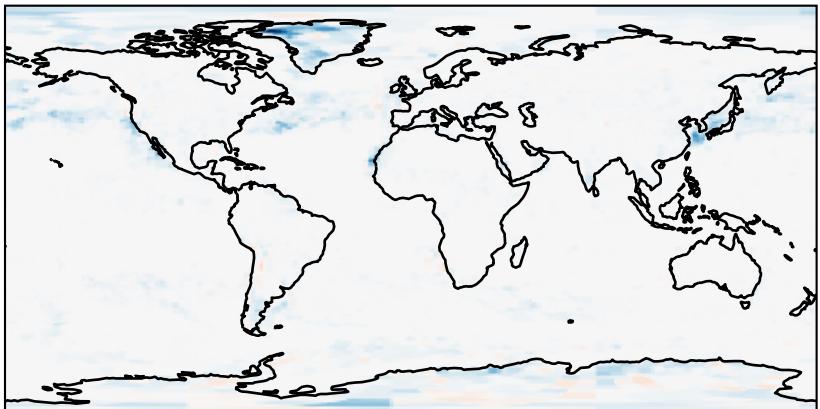
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



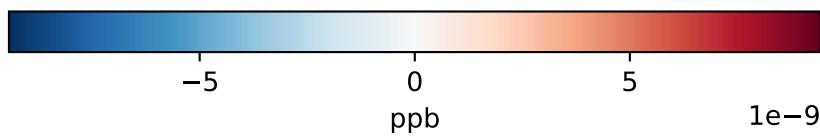
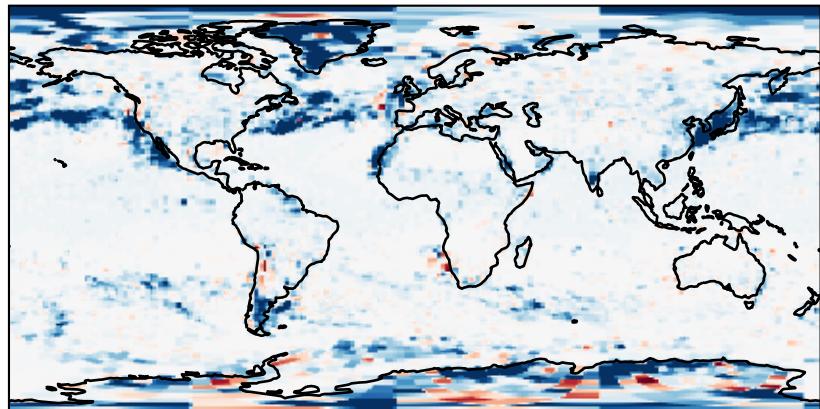
GCHP\_13.4.0-rc.1 - GCHP\_13.3.0-rc.1 (Dev)  
c48



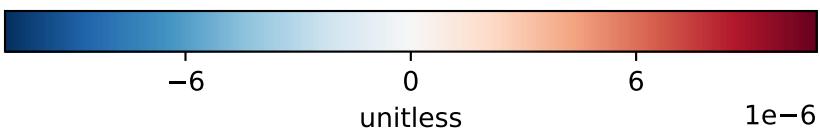
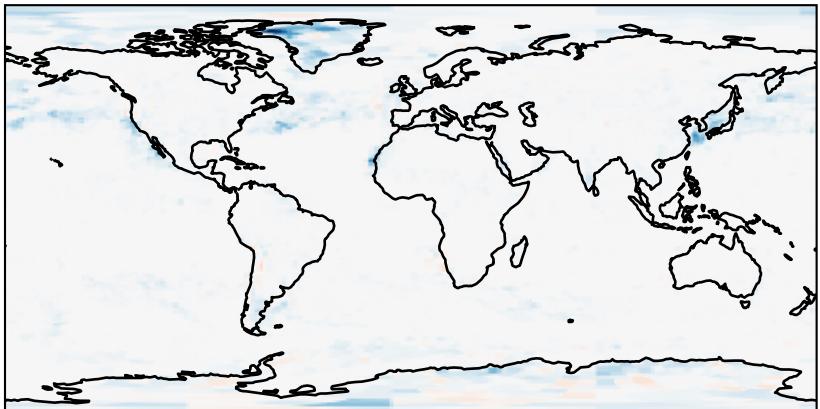
Difference (1x1.25)  
Dev - Ref, Dynamic Range



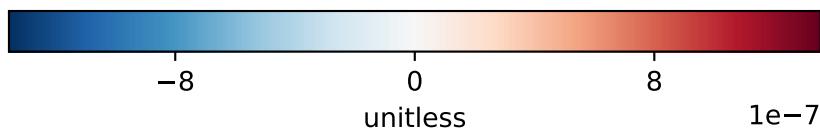
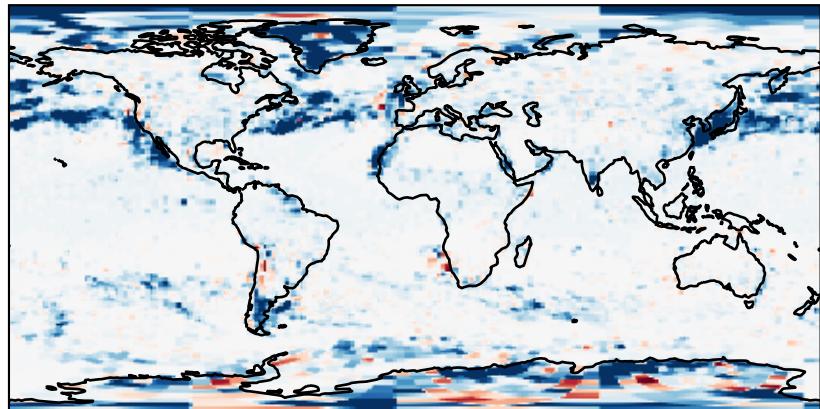
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

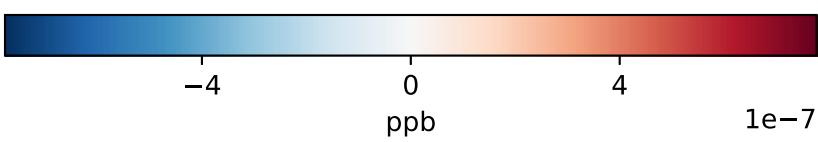
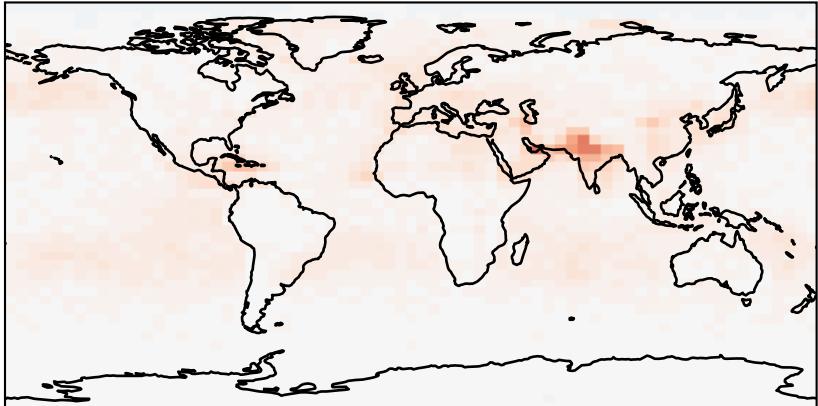


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

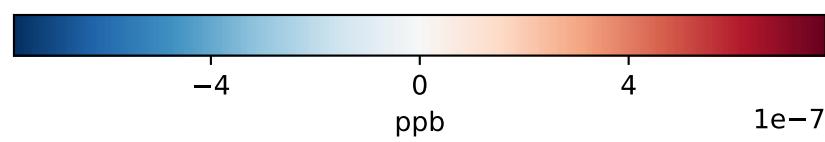
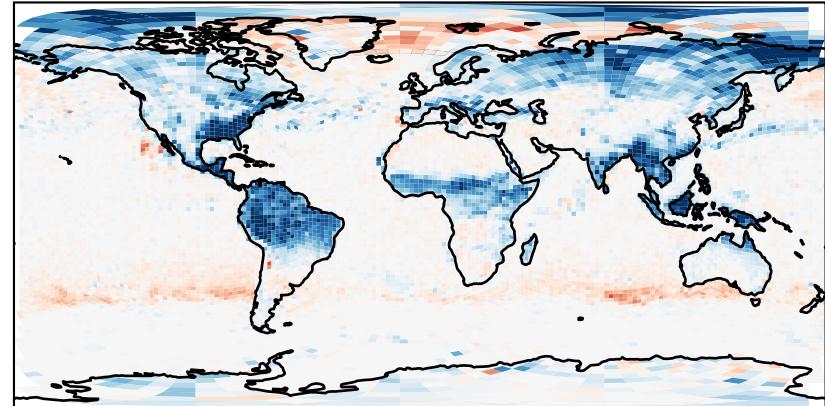


# SpeciesConc\_HCFCs

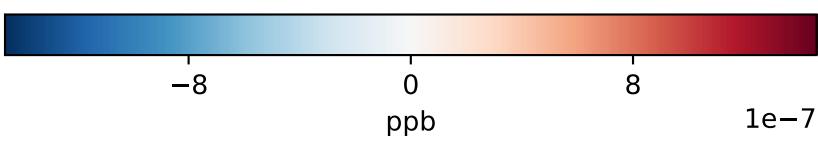
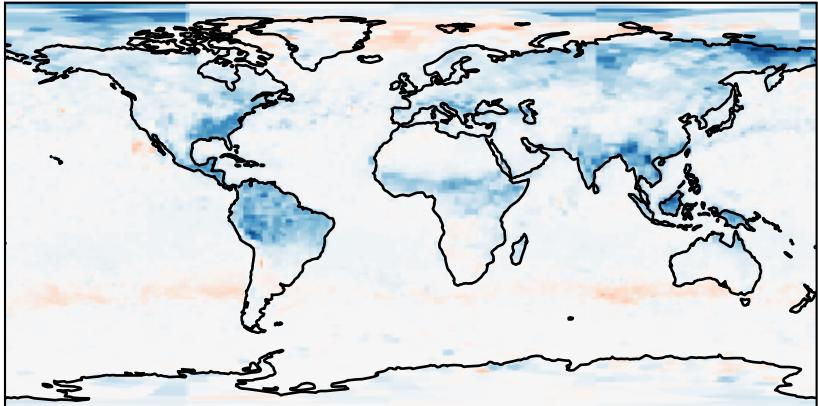
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



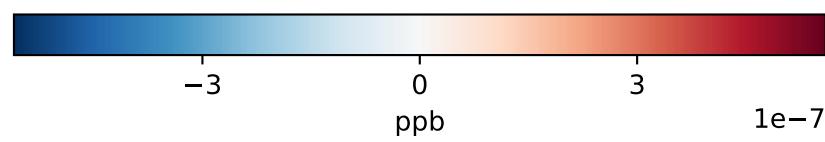
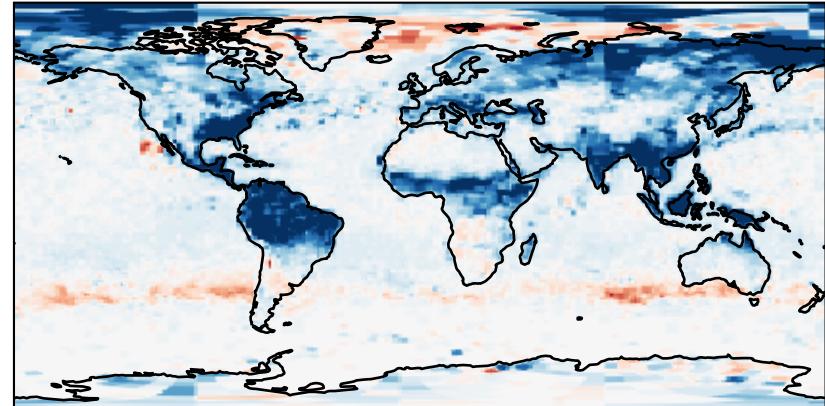
GCHP\_13.4.0-rc.1 - GCHP\_13.3.0-rc.1 (Dev)  
c48



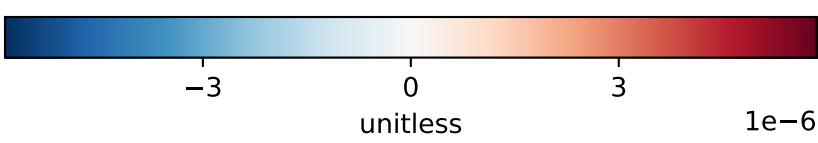
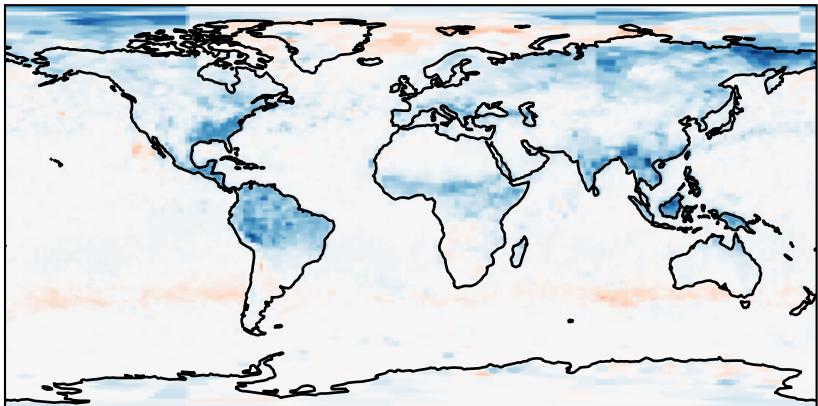
Difference (1x1.25)  
Dev - Ref, Dynamic Range



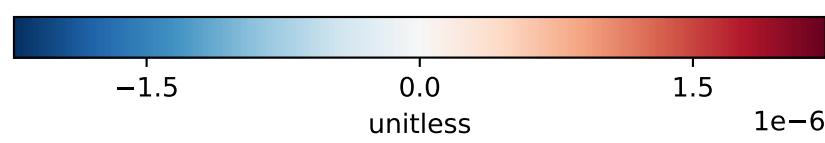
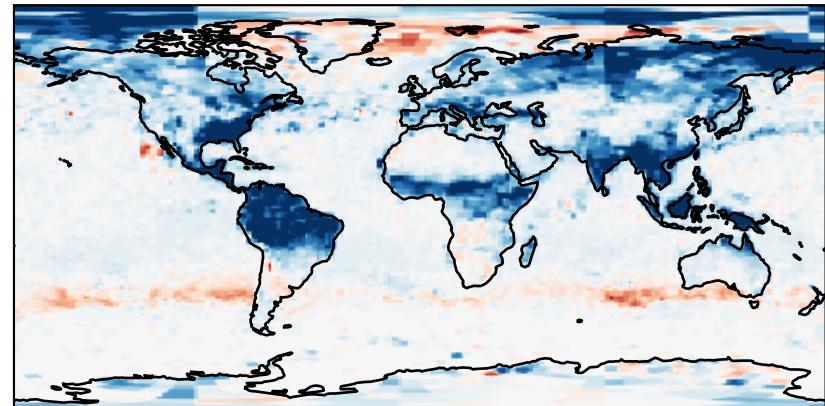
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

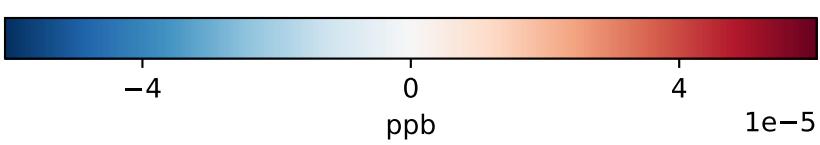
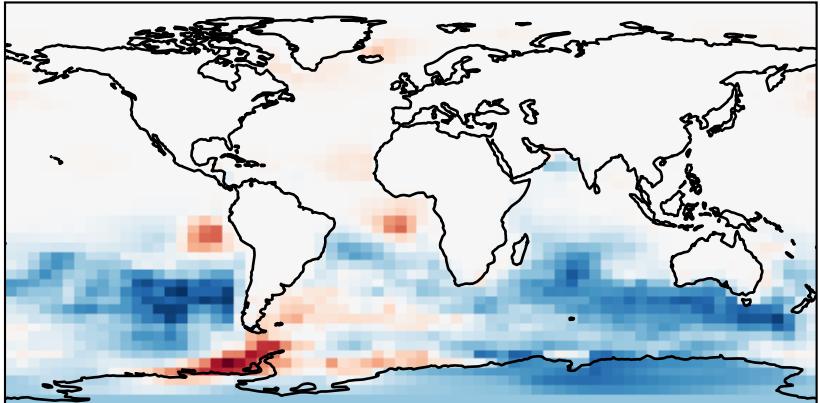


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

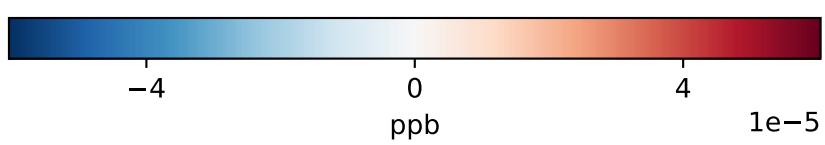
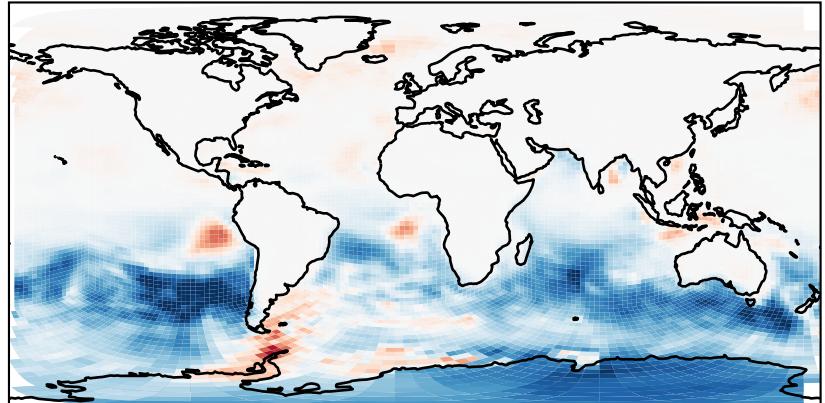


# SpeciesConc\_OClO

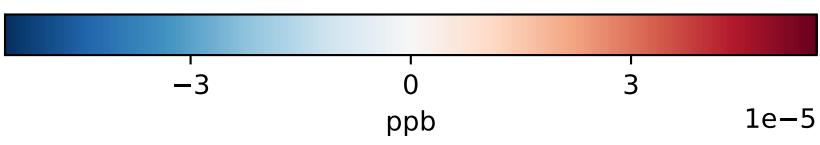
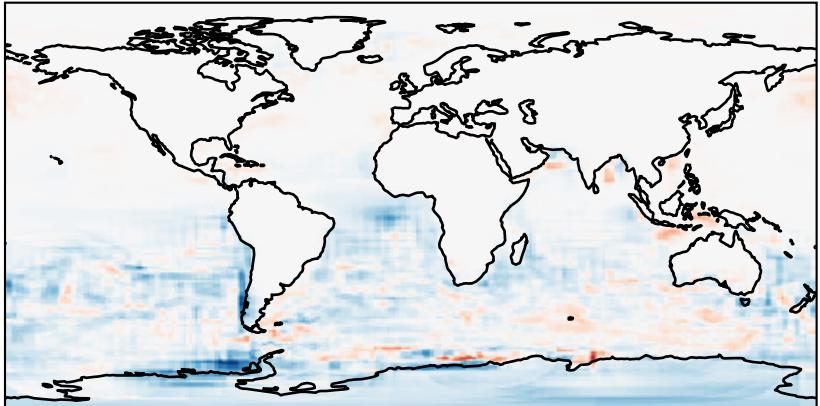
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



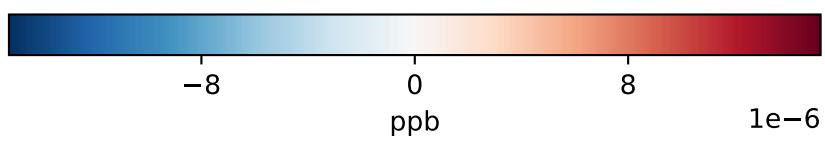
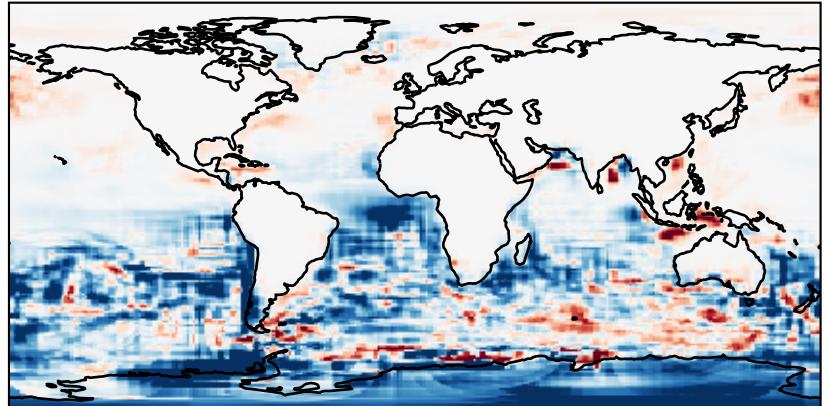
GCHP\_13.4.0-rc.1 - GCHP\_13.3.0-rc.1 (Dev)  
c48



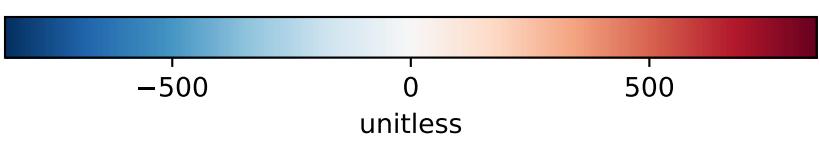
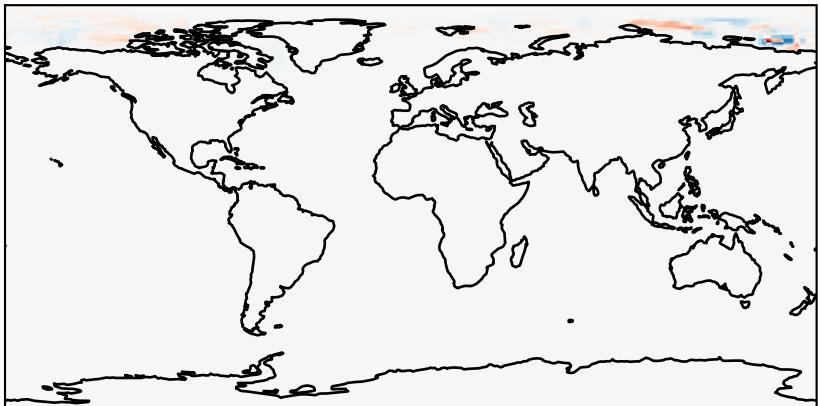
Difference (1x1.25)  
Dev - Ref, Dynamic Range



Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref



Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

