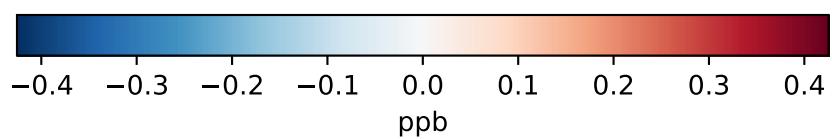
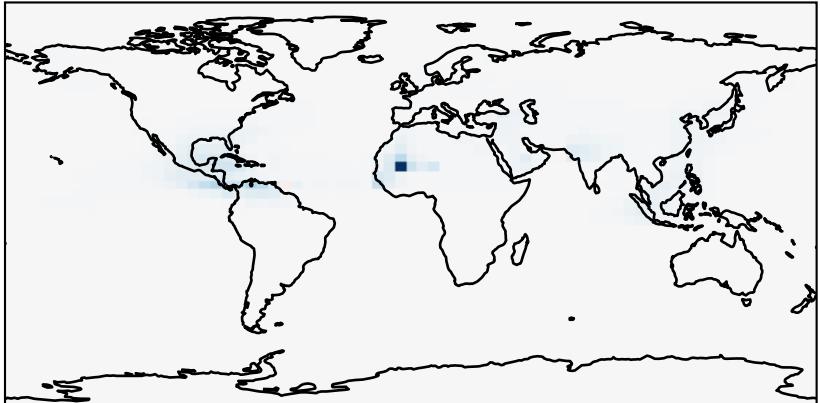
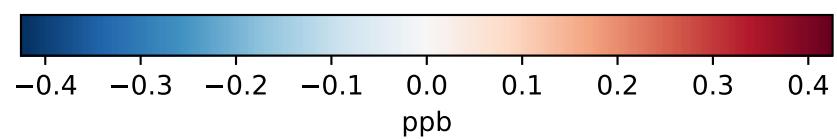
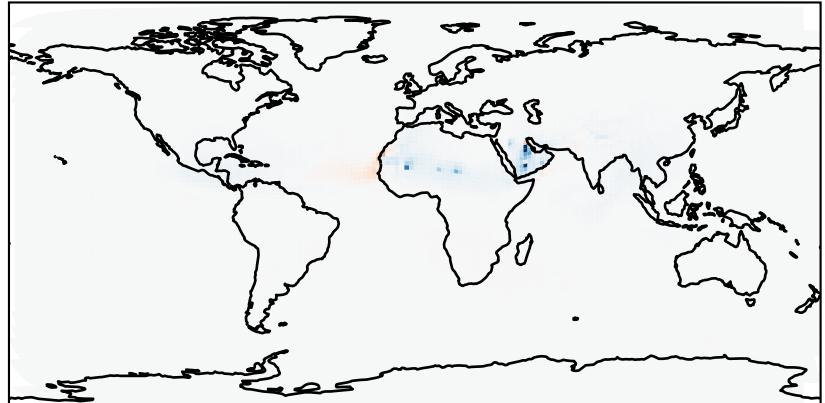


# SpeciesConc\_DST1

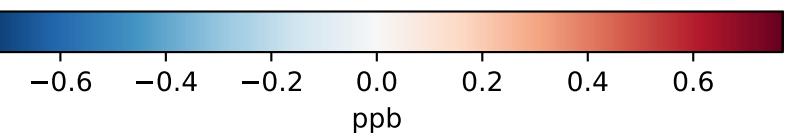
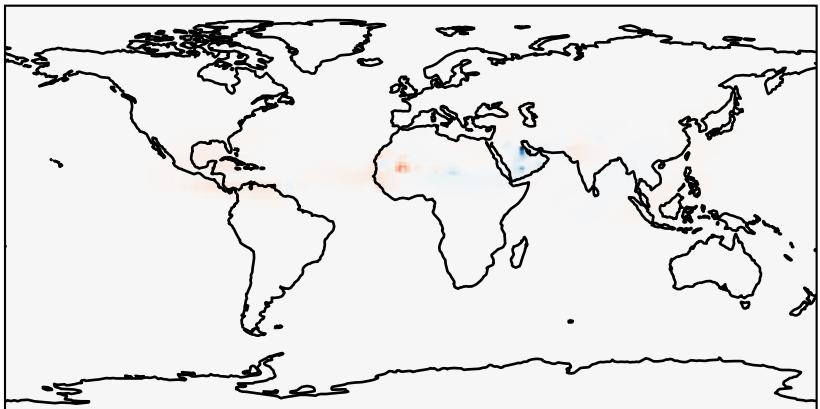
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



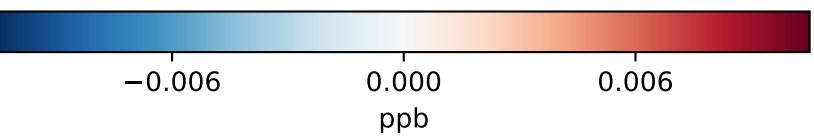
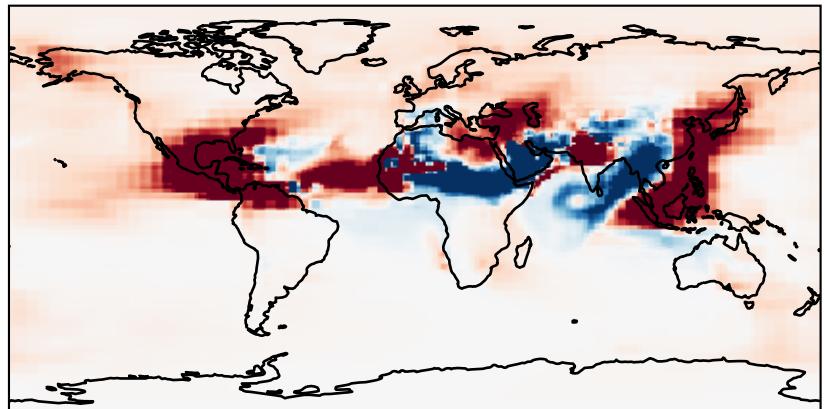
GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48



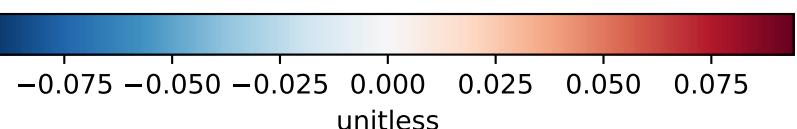
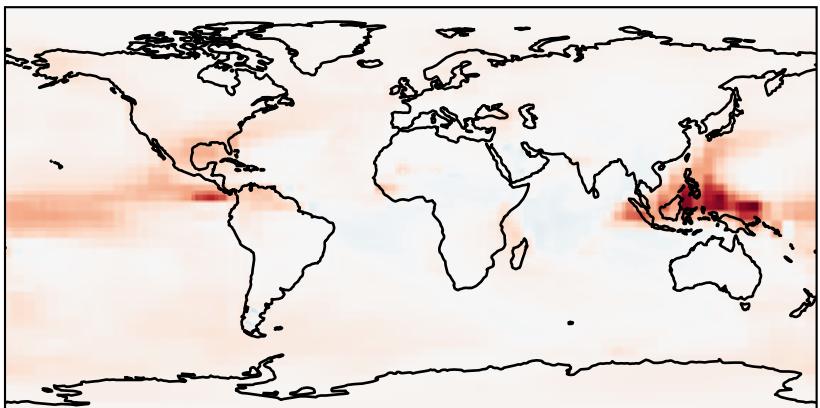
Difference (1x1.25)  
Dev - Ref, Dynamic Range



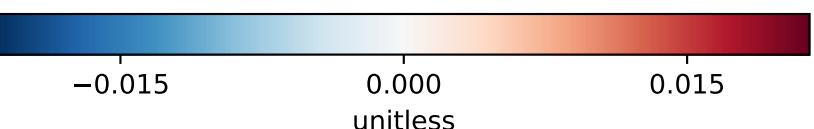
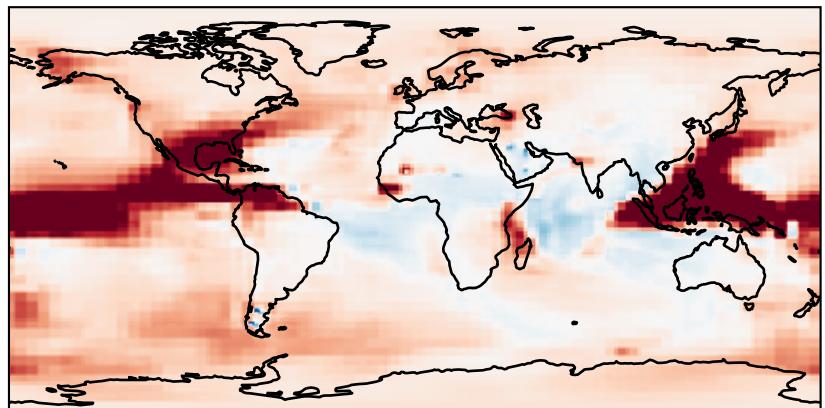
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

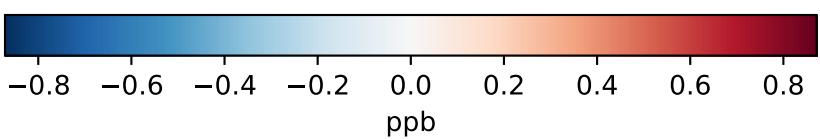
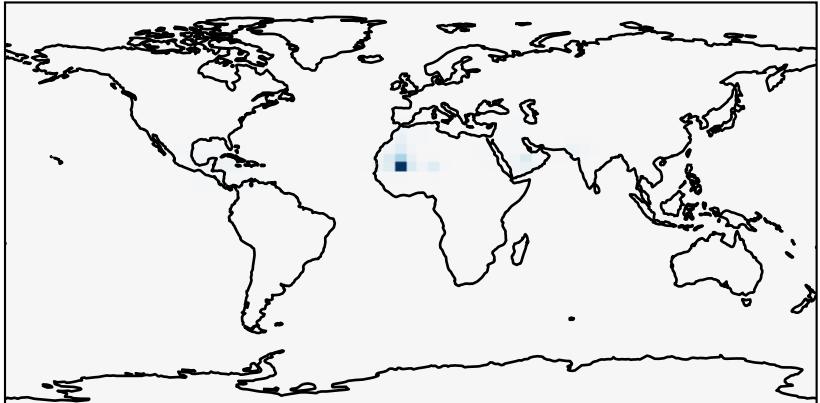


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

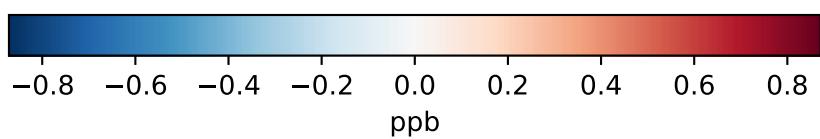
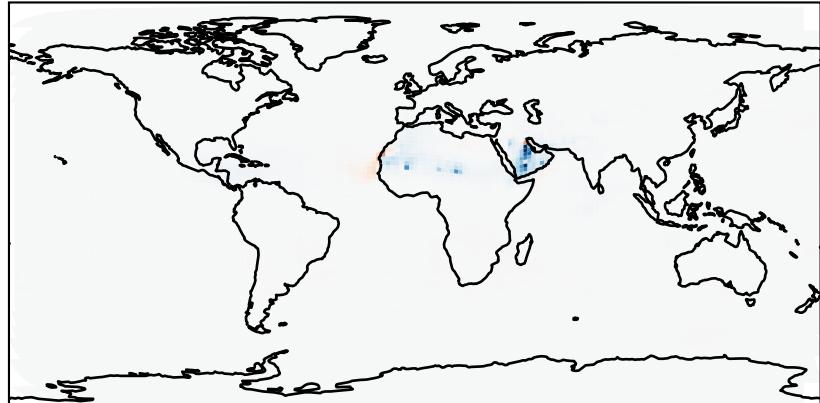


# SpeciesConc\_DST2

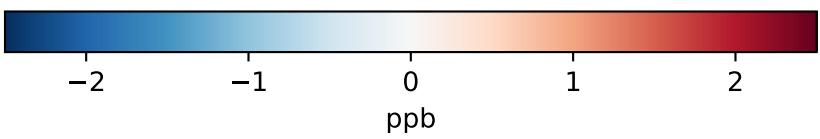
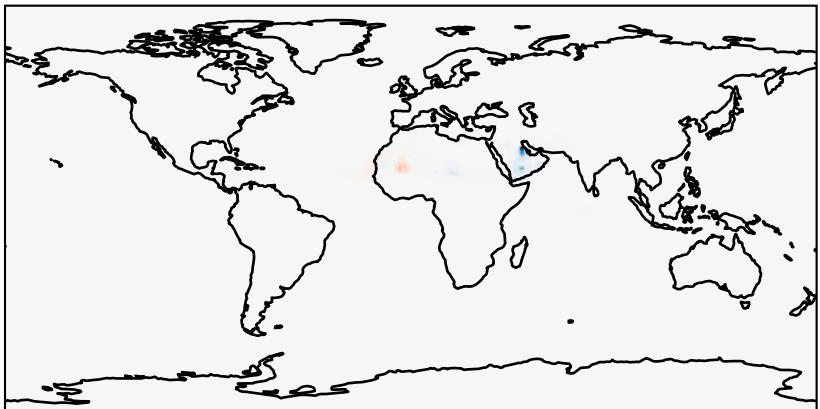
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



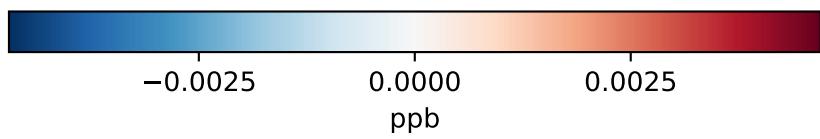
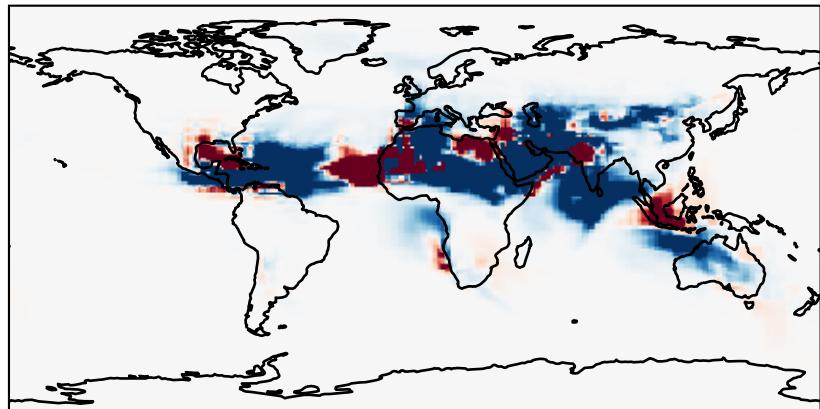
GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48



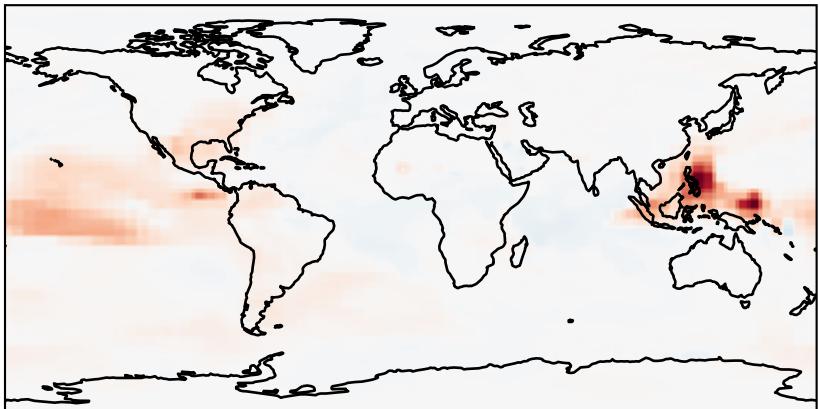
Difference (1x1.25)  
Dev - Ref, Dynamic Range



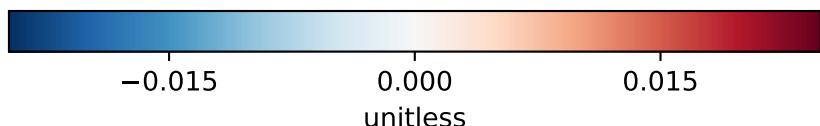
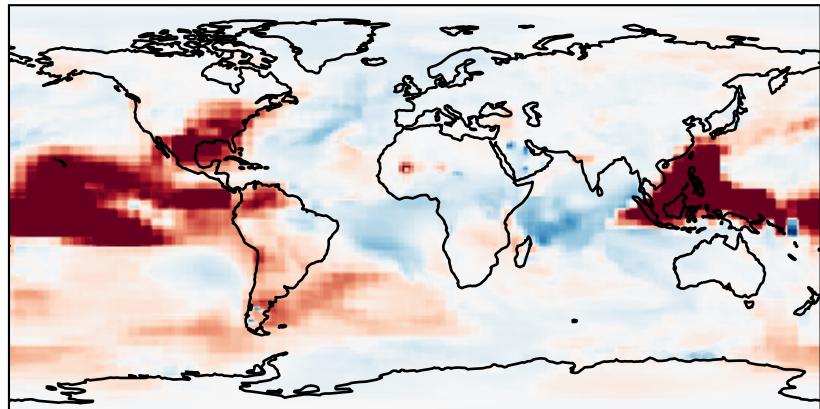
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

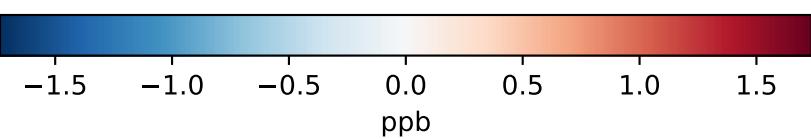
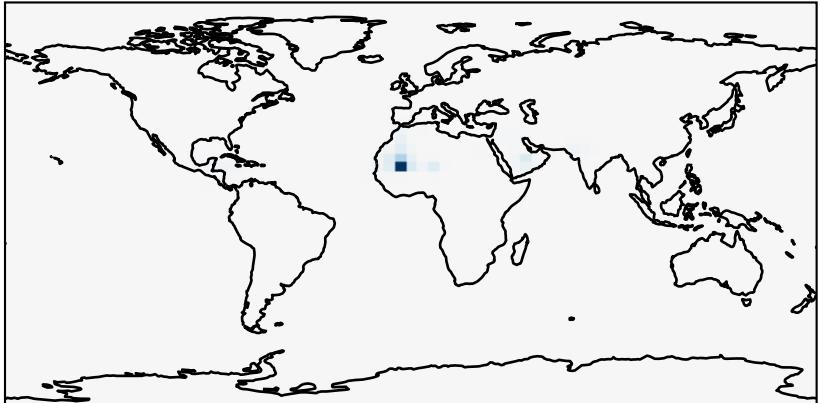


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

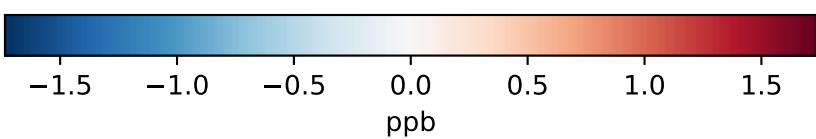
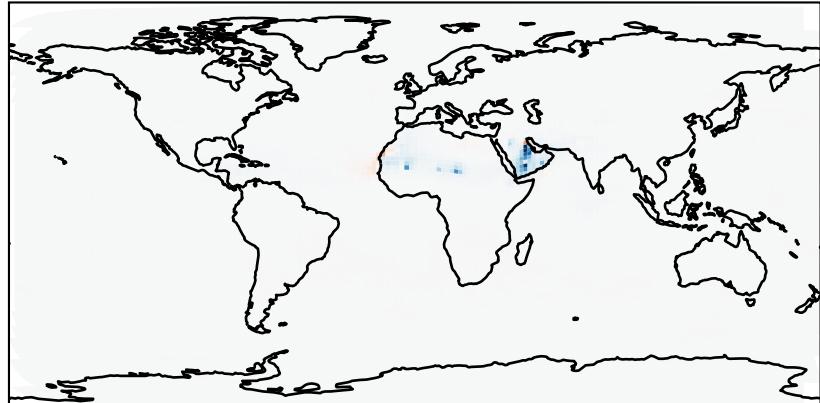


# SpeciesConc\_DST3

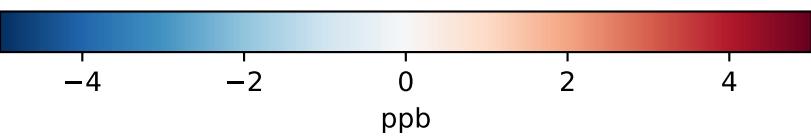
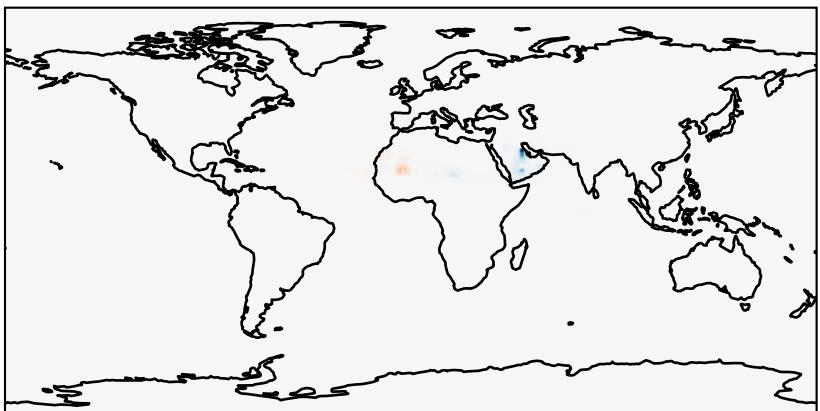
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



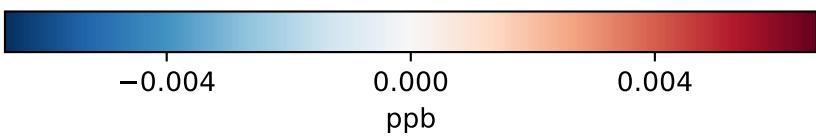
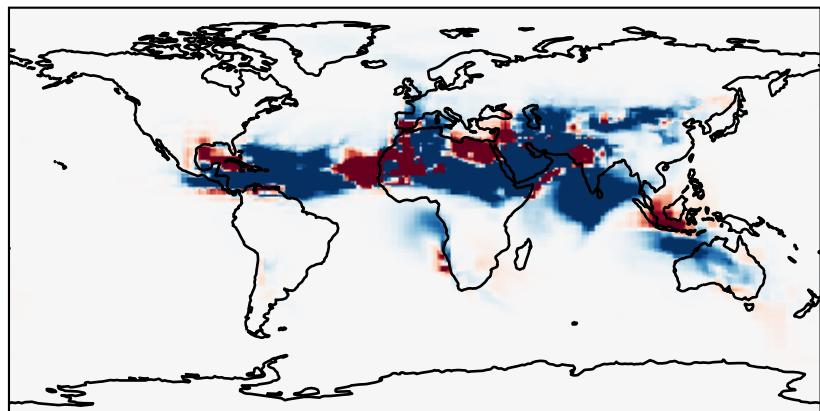
GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48



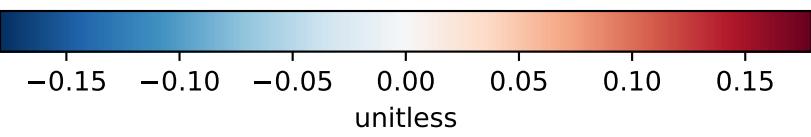
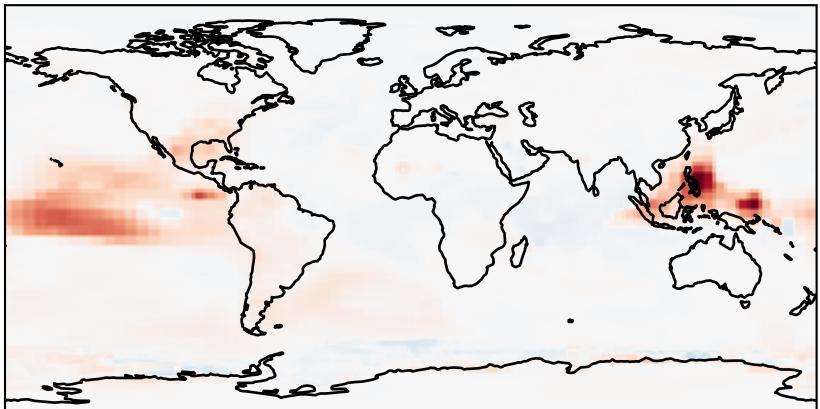
Difference (1x1.25)  
Dev - Ref, Dynamic Range



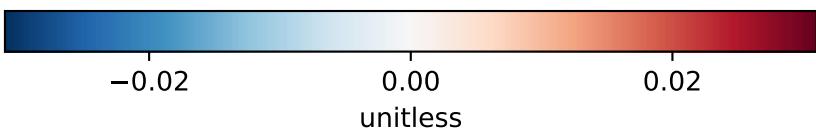
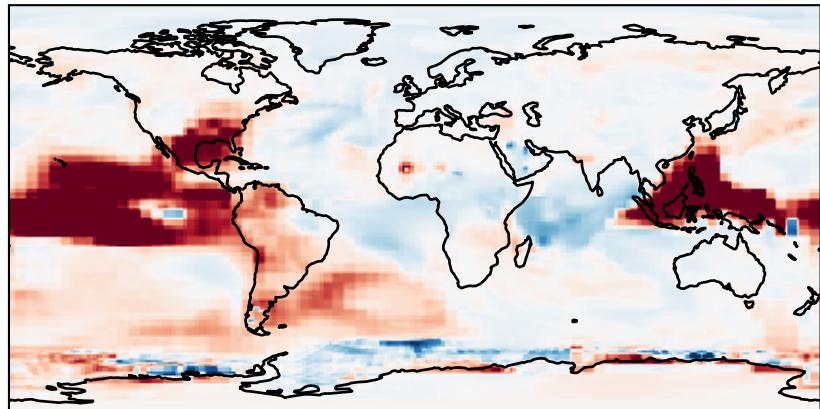
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

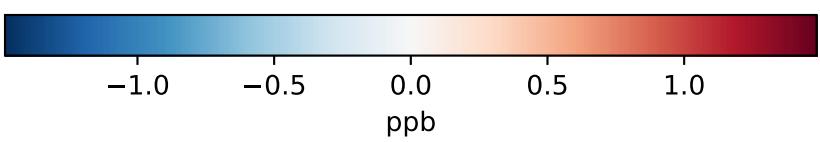
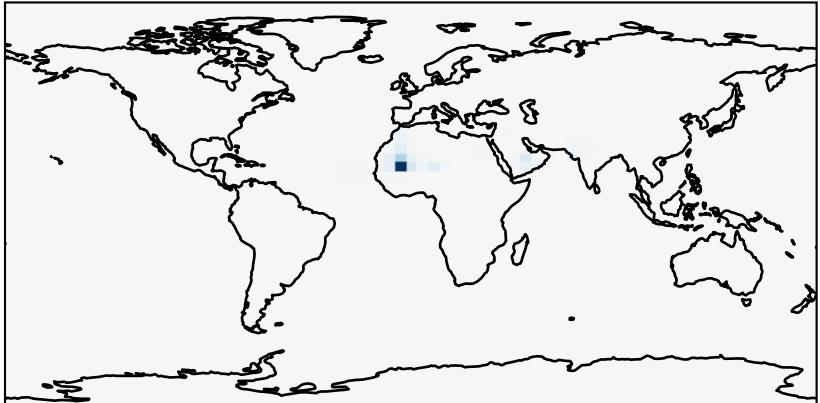


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

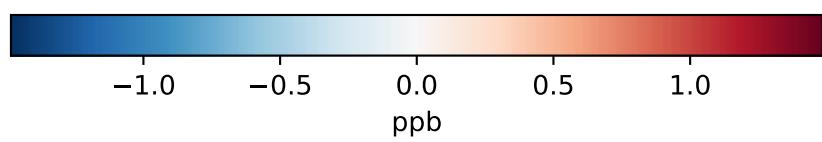
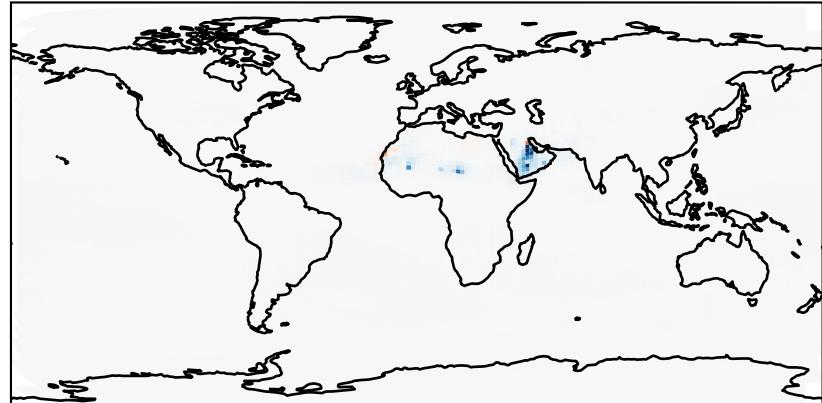


# SpeciesConc\_DST4

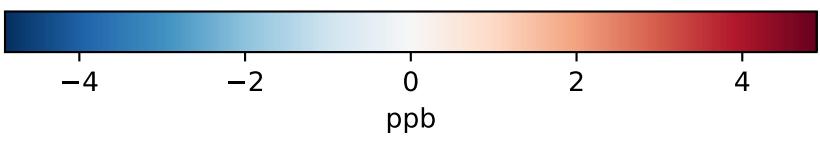
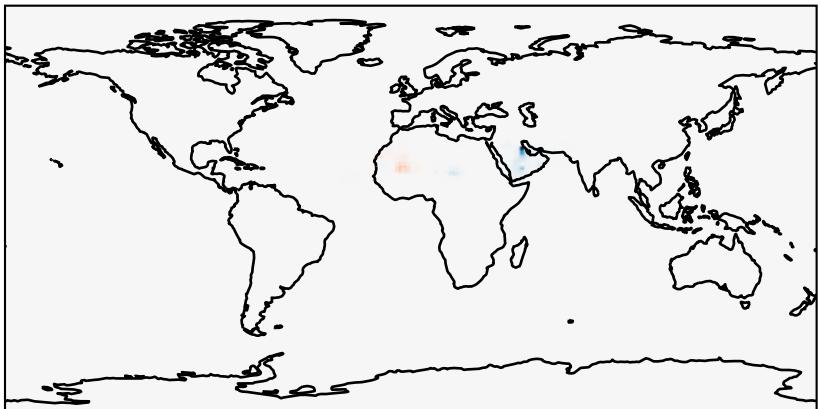
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



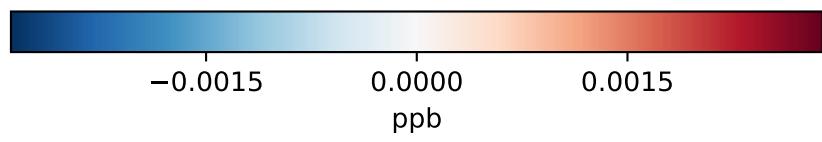
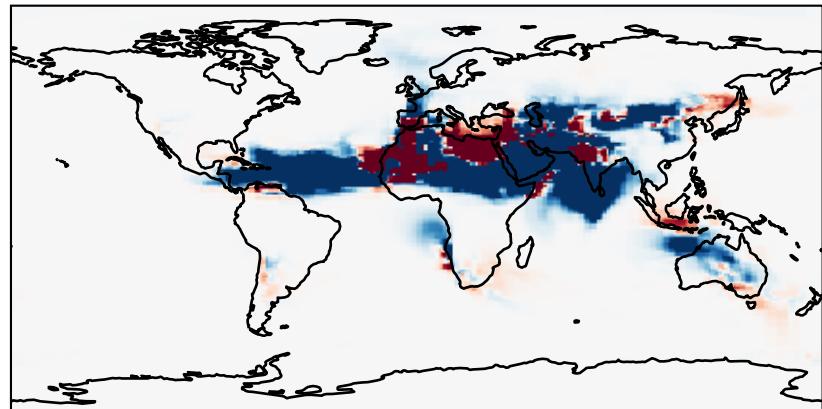
GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48



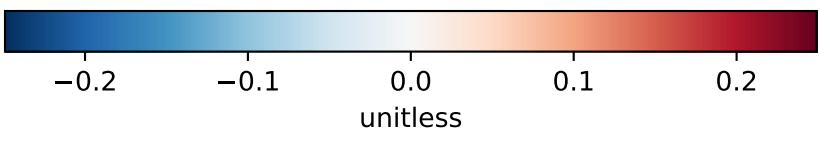
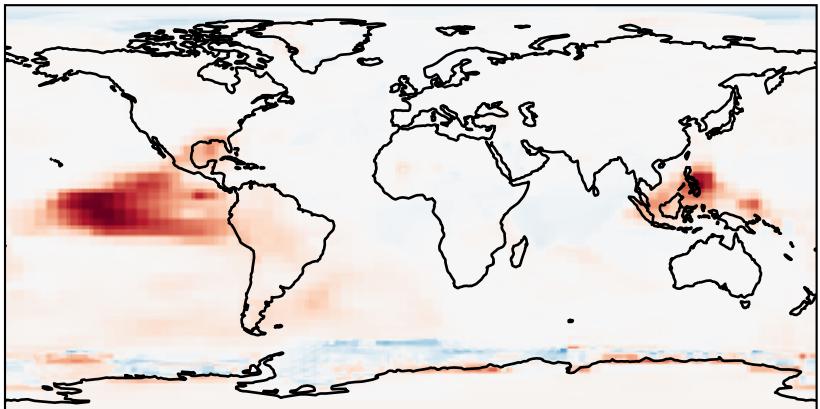
Difference (1x1.25)  
Dev - Ref, Dynamic Range



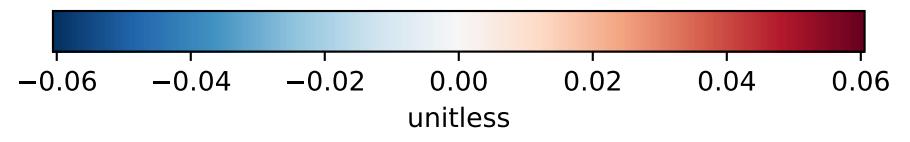
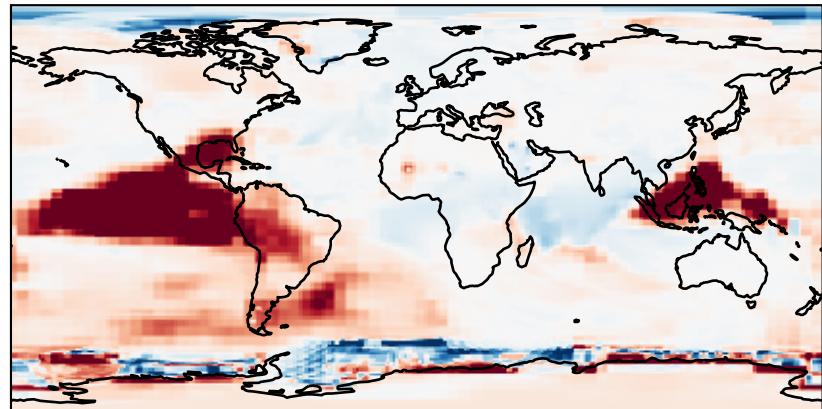
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

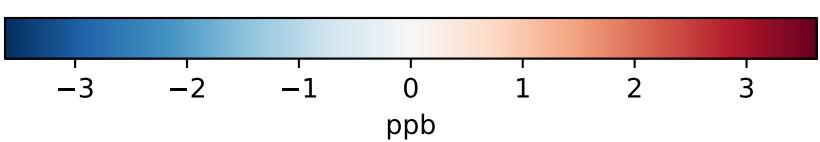
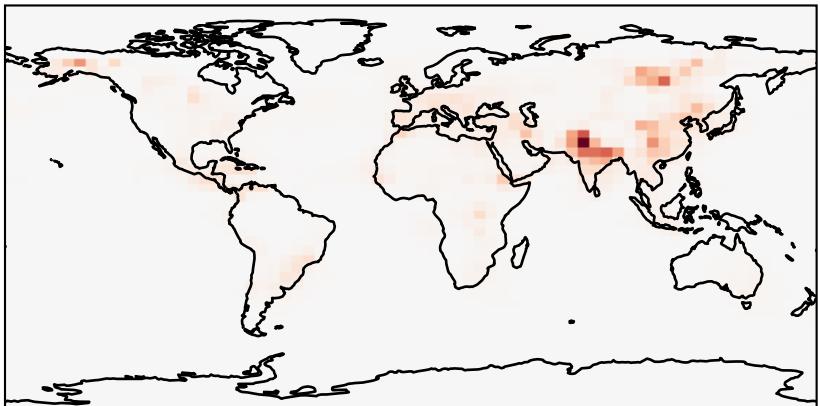


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

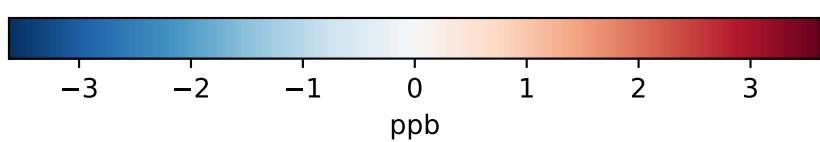
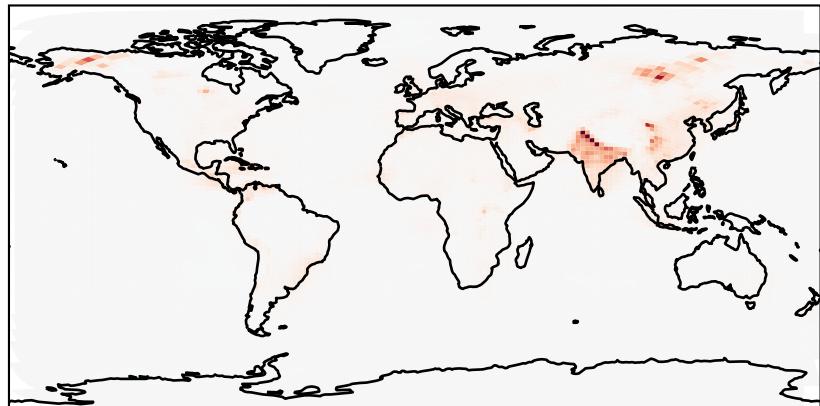


# SpeciesConc\_NH4

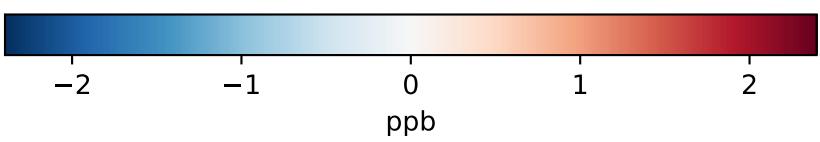
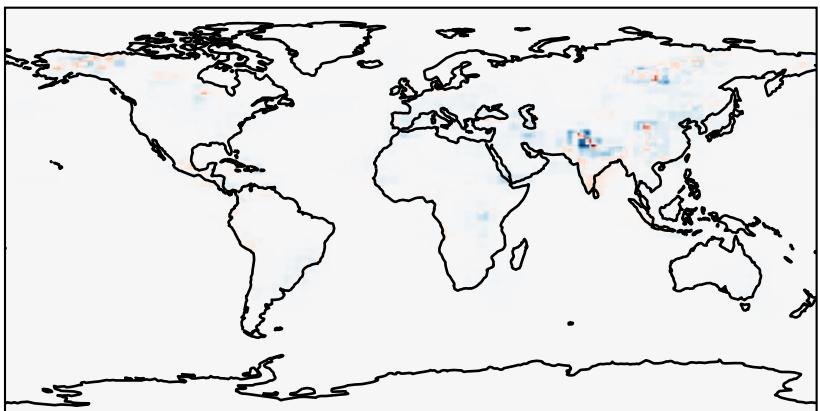
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



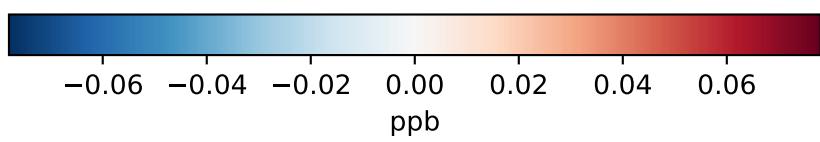
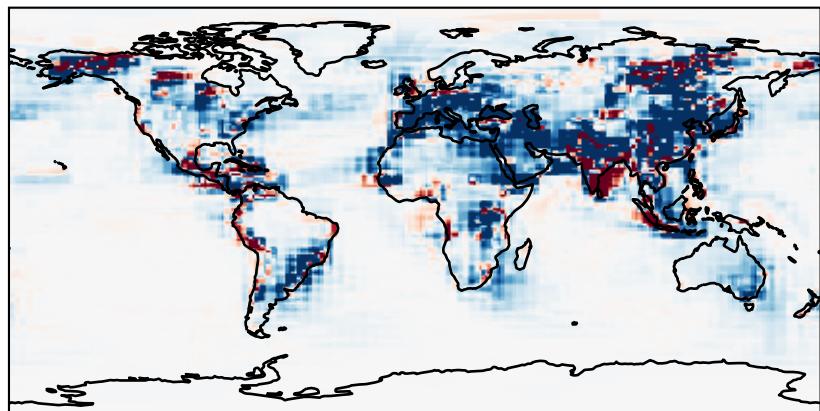
GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48



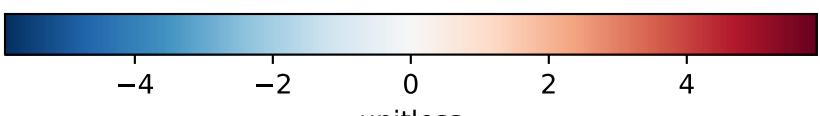
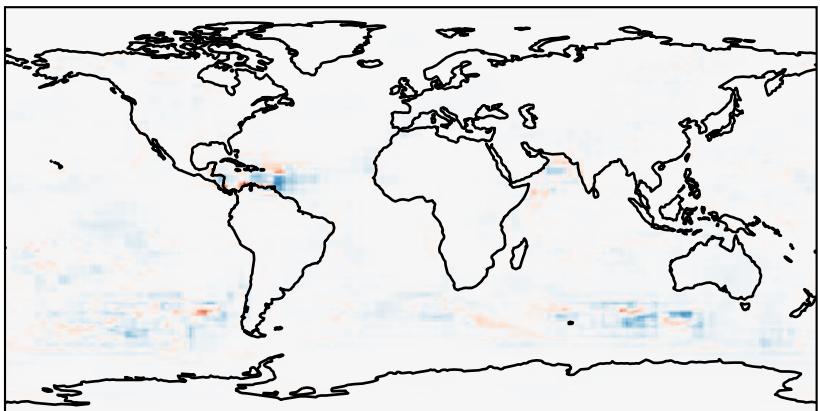
Difference (1x1.25)  
Dev - Ref, Dynamic Range



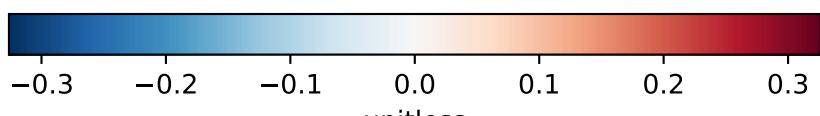
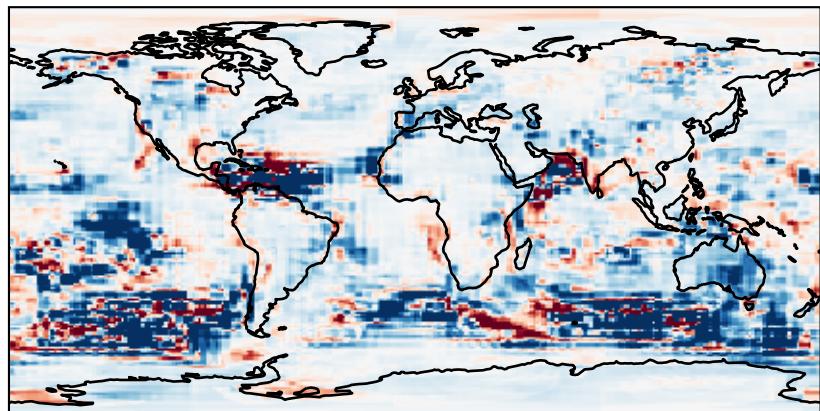
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



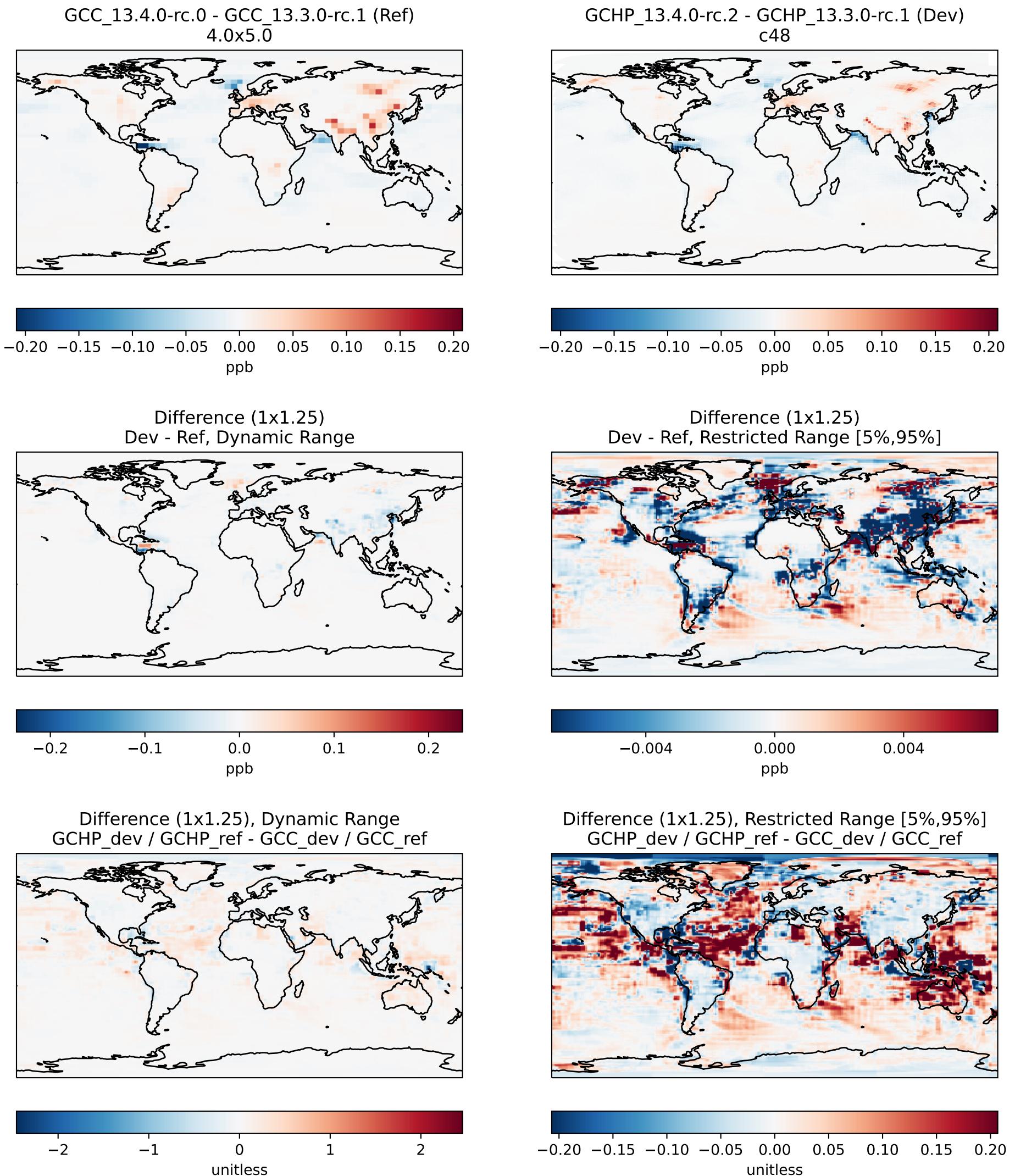
Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref



Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

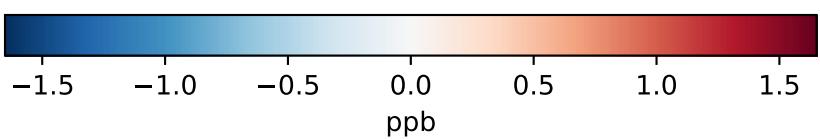
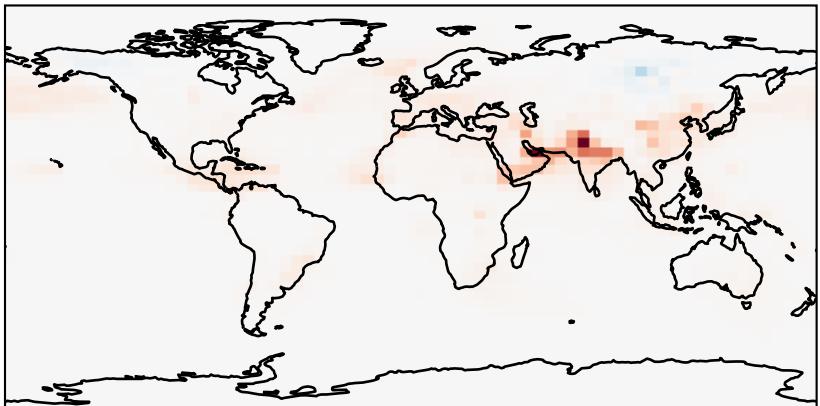


# SpeciesConc\_NIT

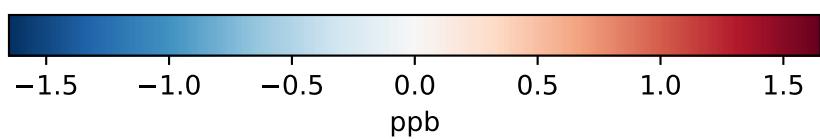
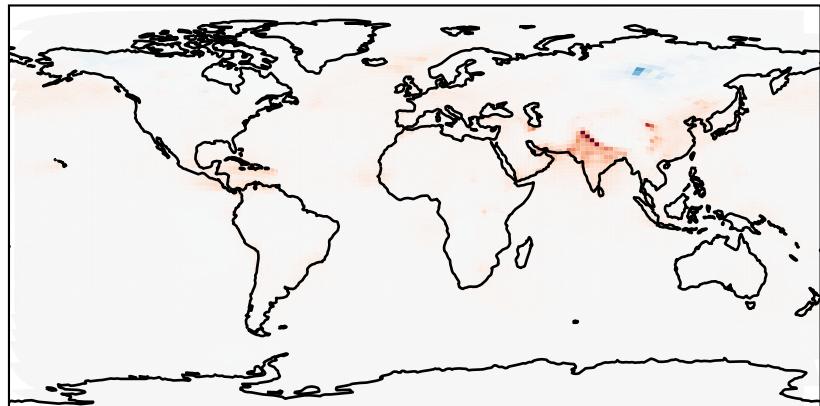


# SpeciesConc\_SO4

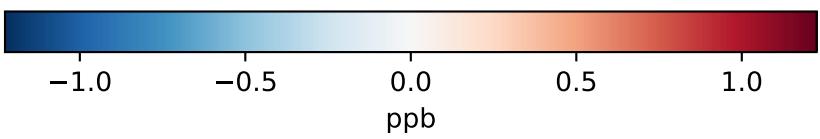
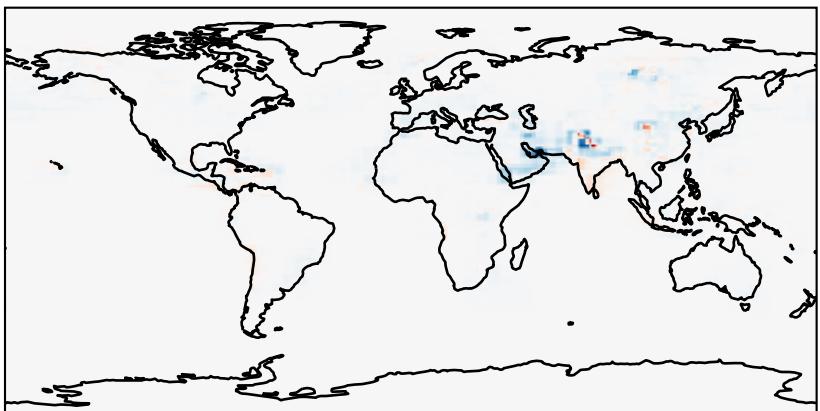
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



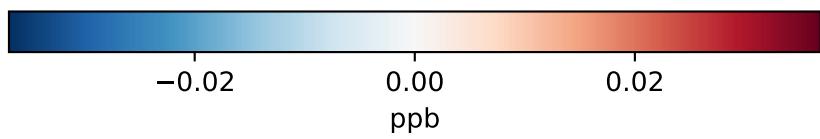
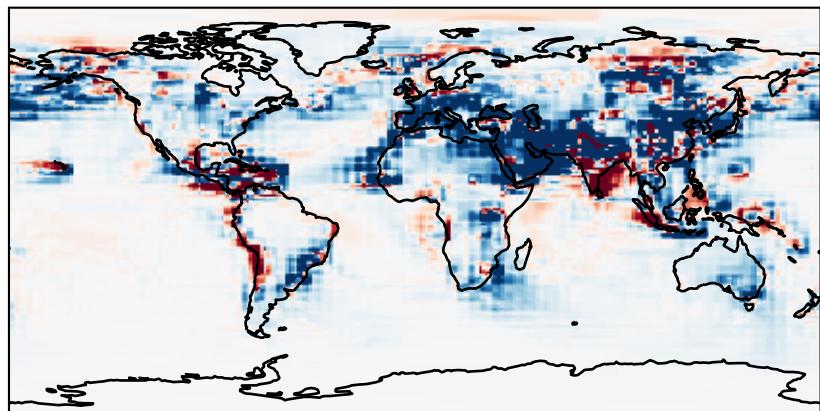
GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48



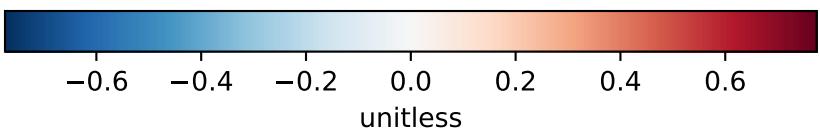
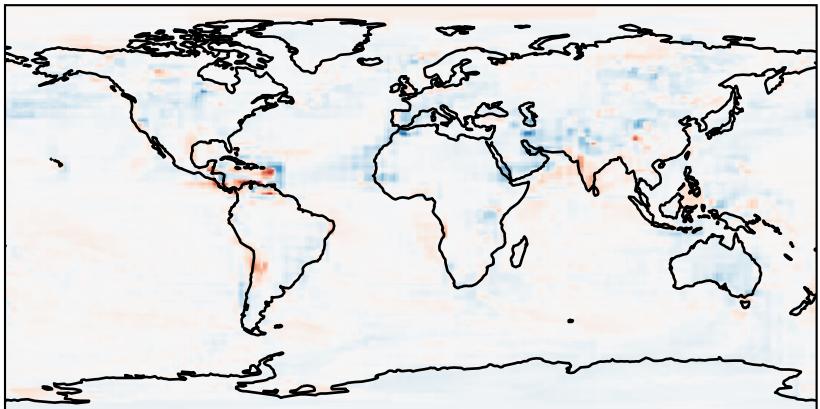
Difference (1x1.25)  
Dev - Ref, Dynamic Range



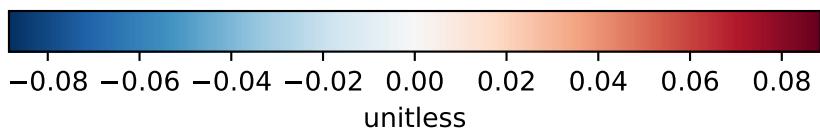
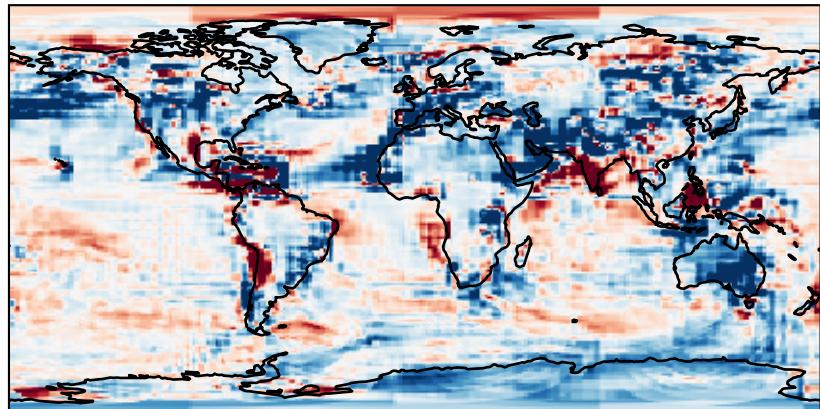
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

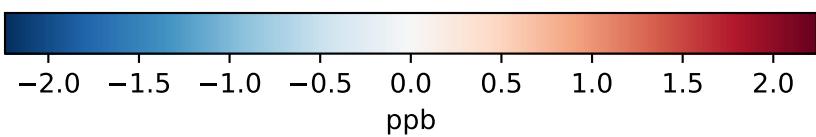
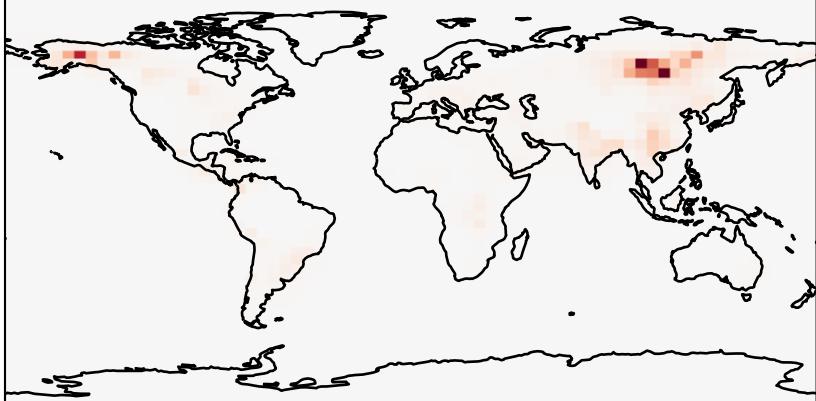


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

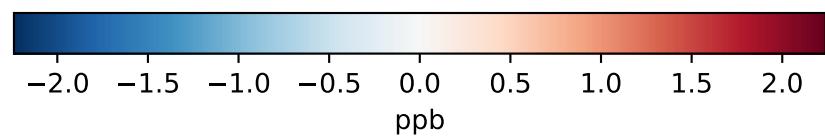
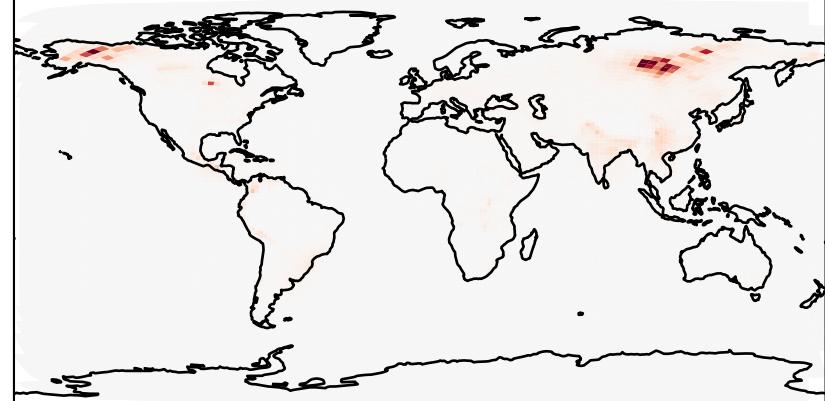


# SpeciesConc\_HMS

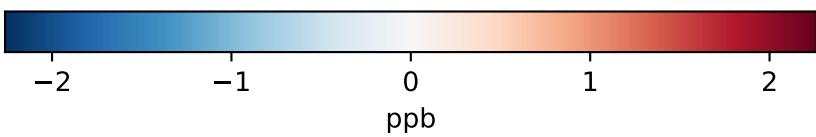
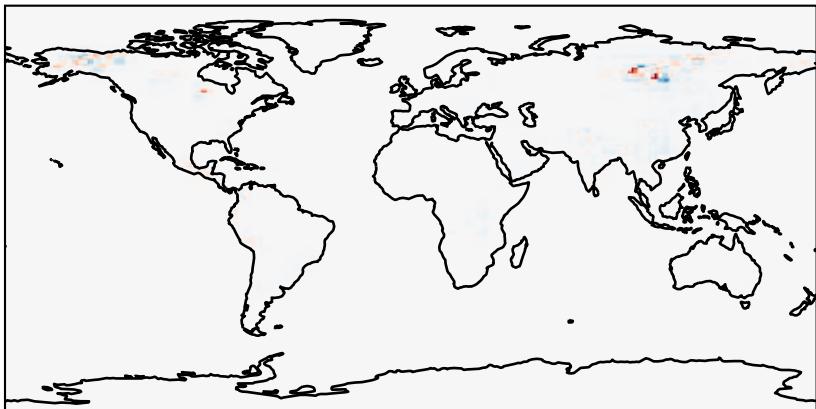
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



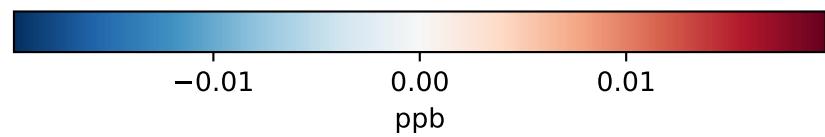
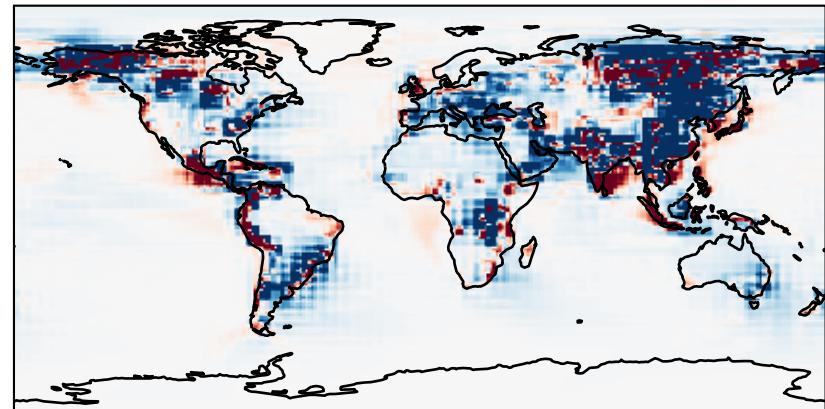
GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48



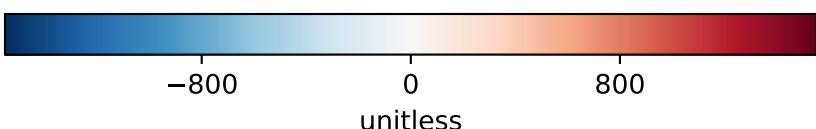
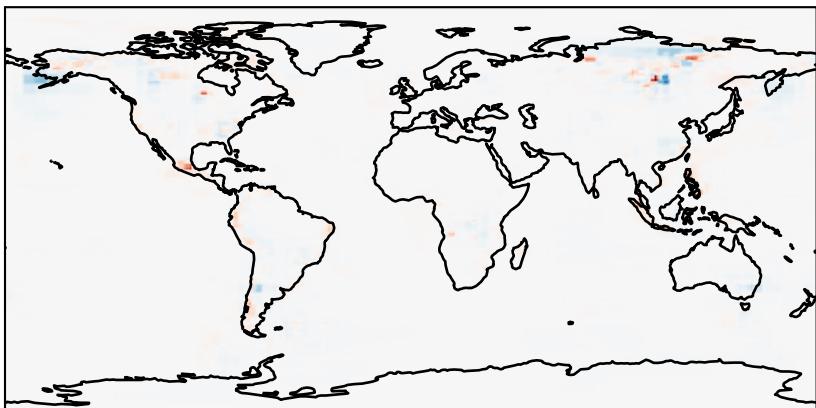
Difference (1x1.25)  
Dev - Ref, Dynamic Range



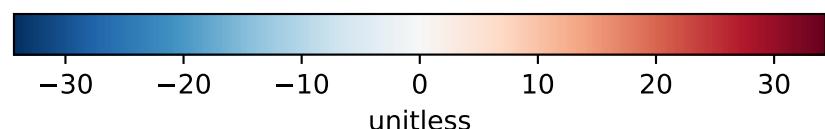
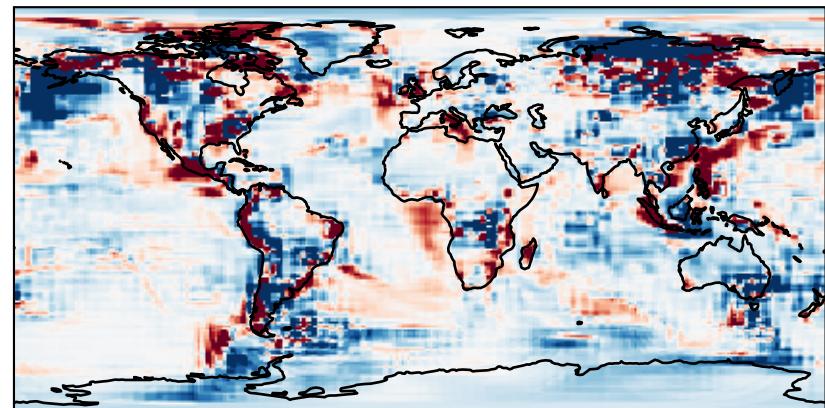
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

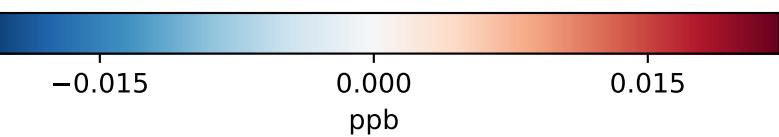
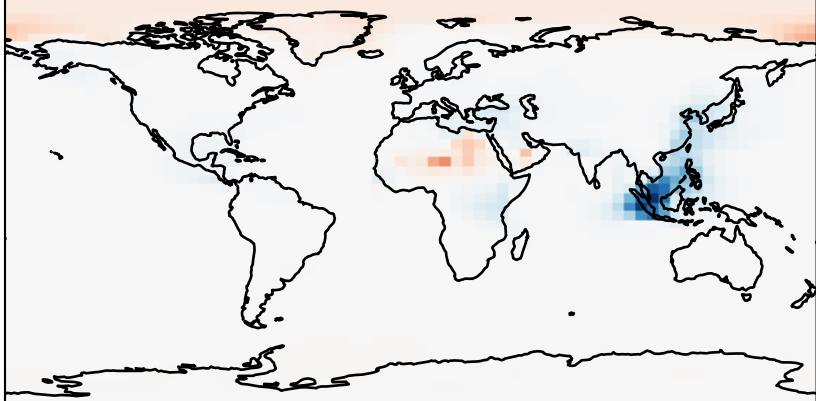


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

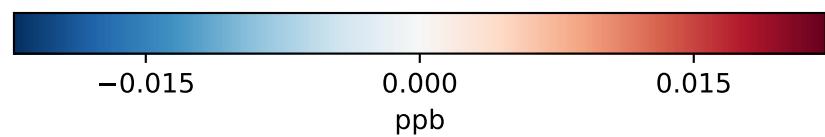
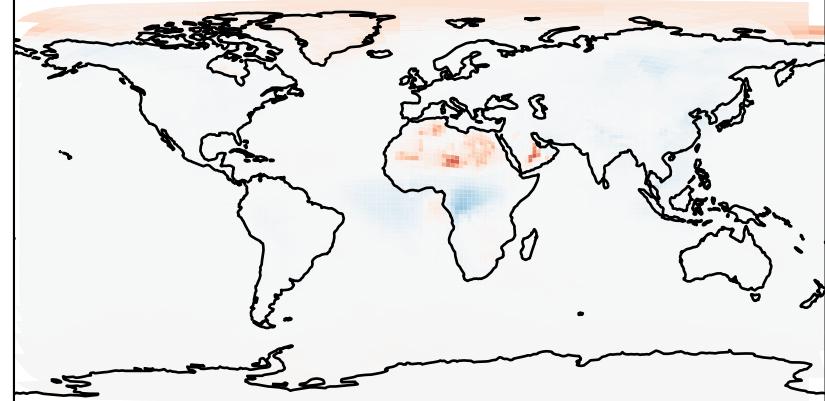


# SpeciesConc\_BCPI

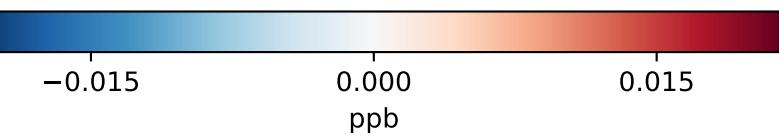
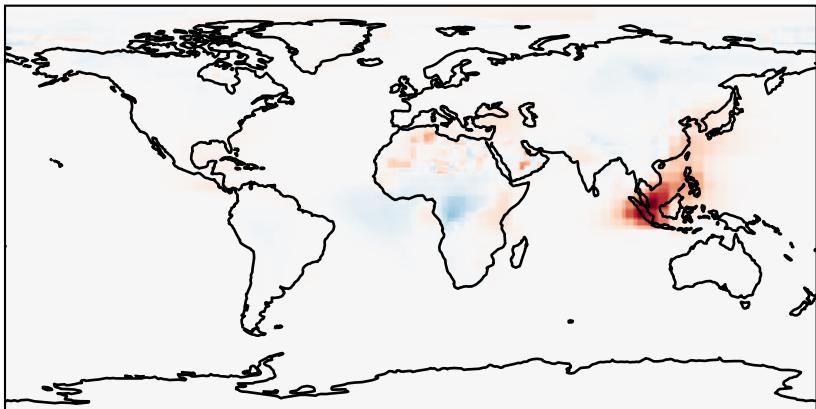
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



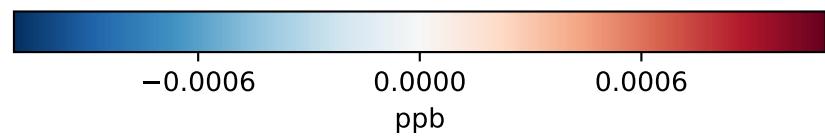
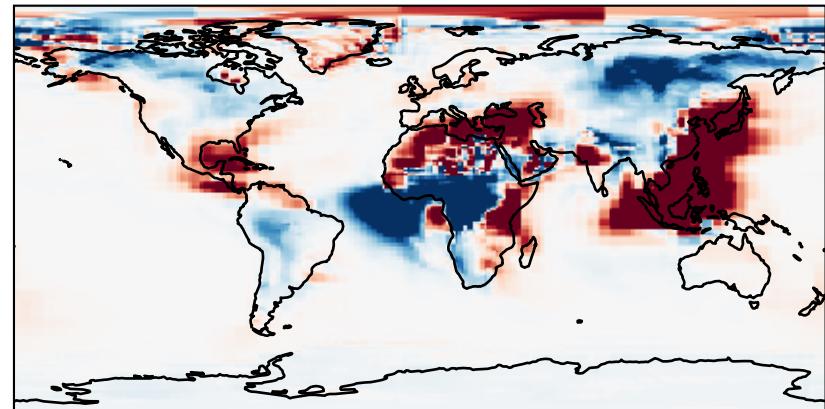
GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48



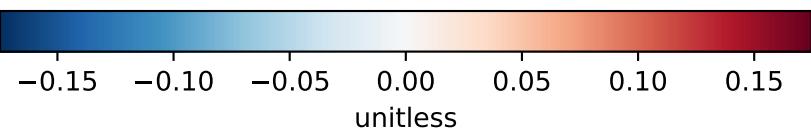
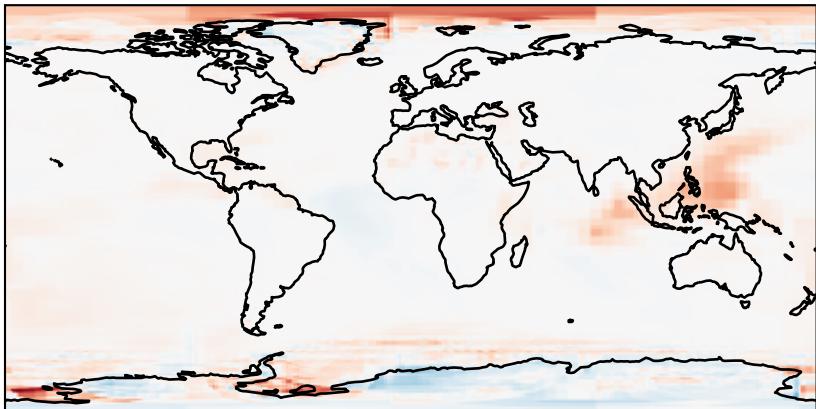
Difference (1x1.25)  
Dev - Ref, Dynamic Range



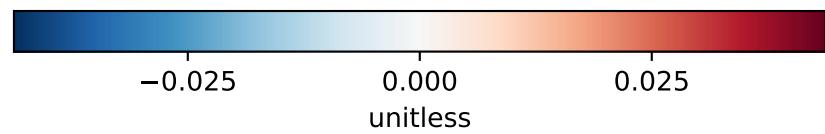
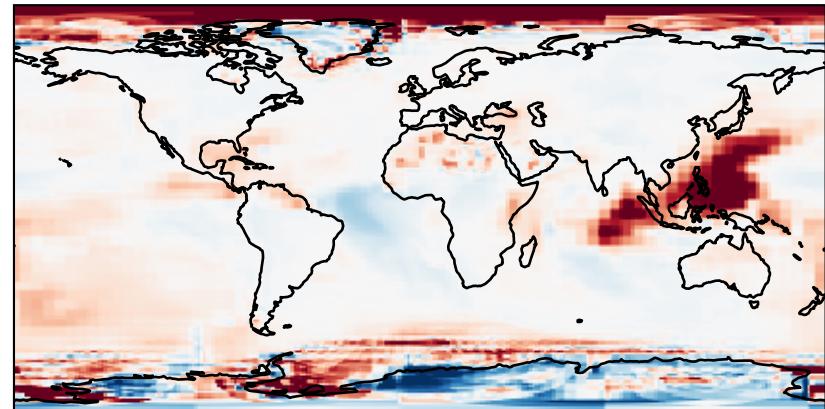
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

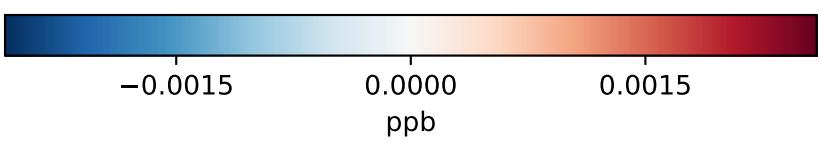
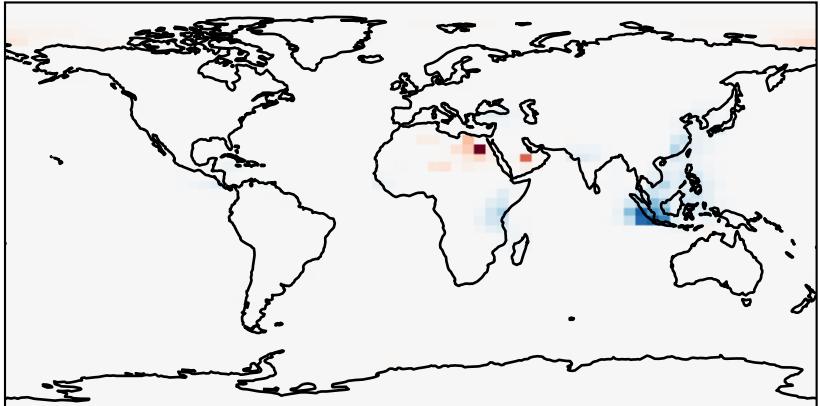


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

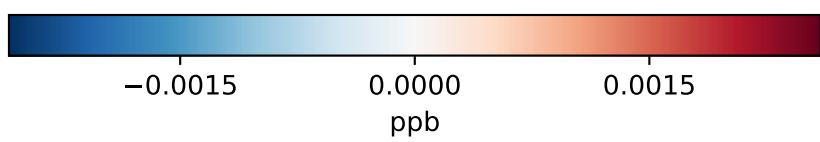
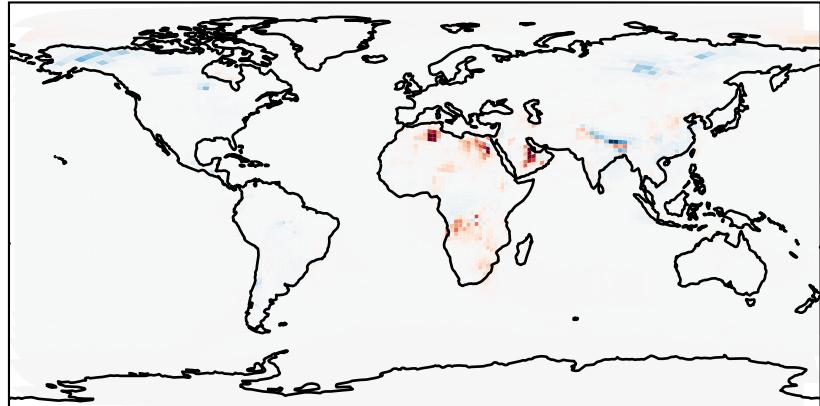


# SpeciesConc\_BCPO

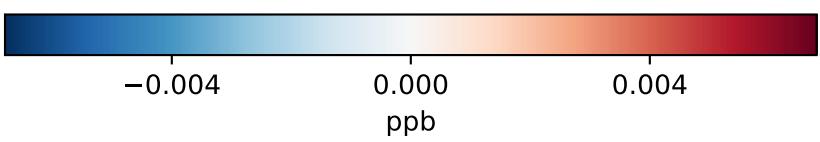
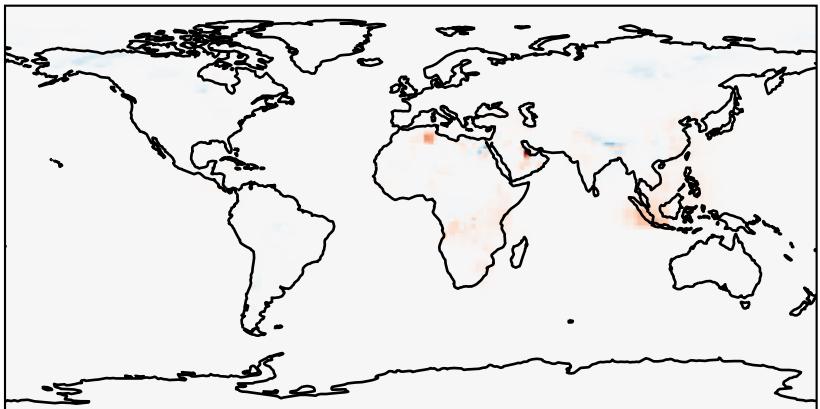
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



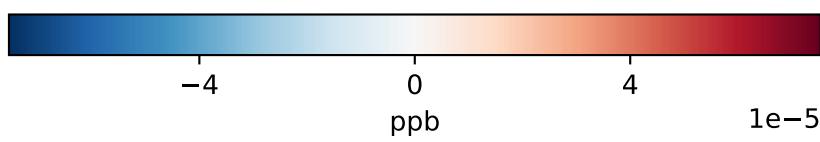
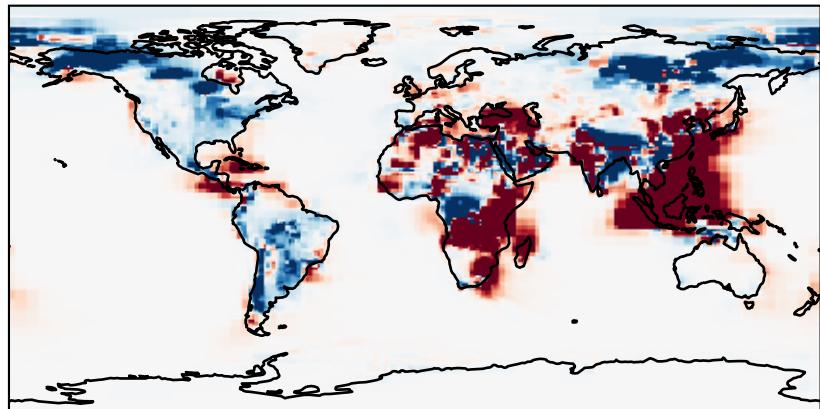
GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48



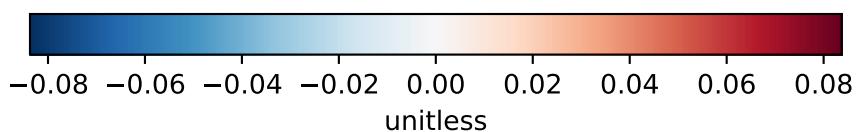
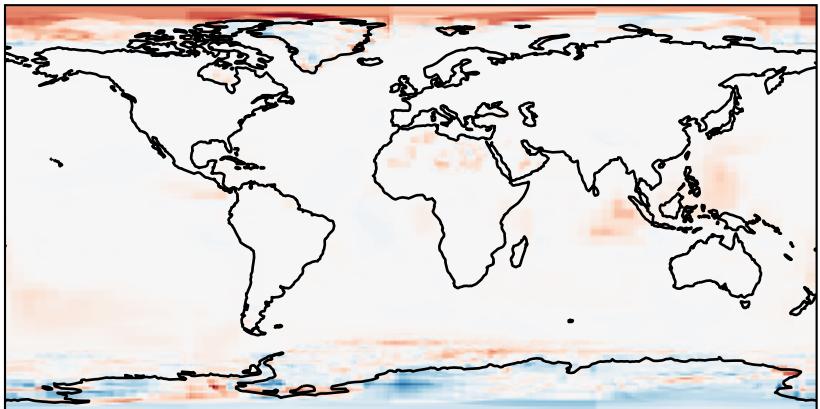
Difference (1x1.25)  
Dev - Ref, Dynamic Range



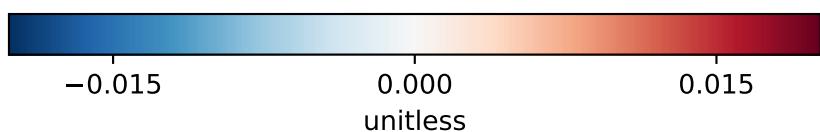
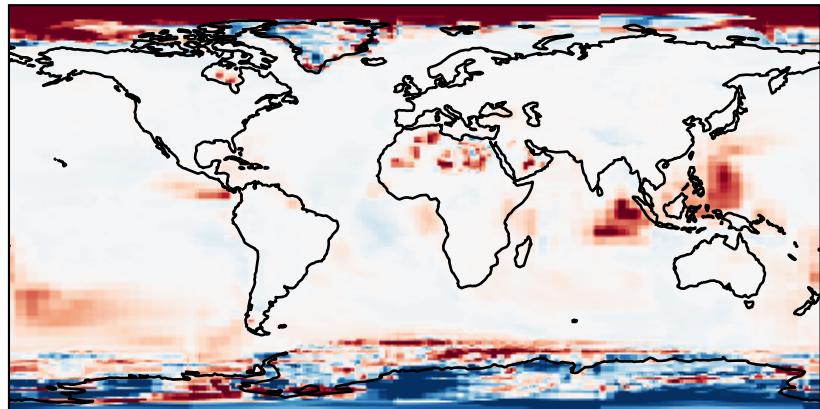
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

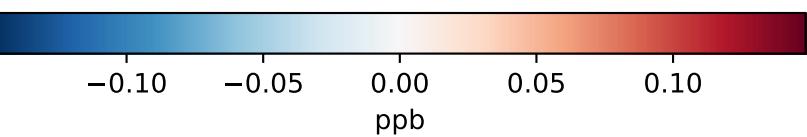
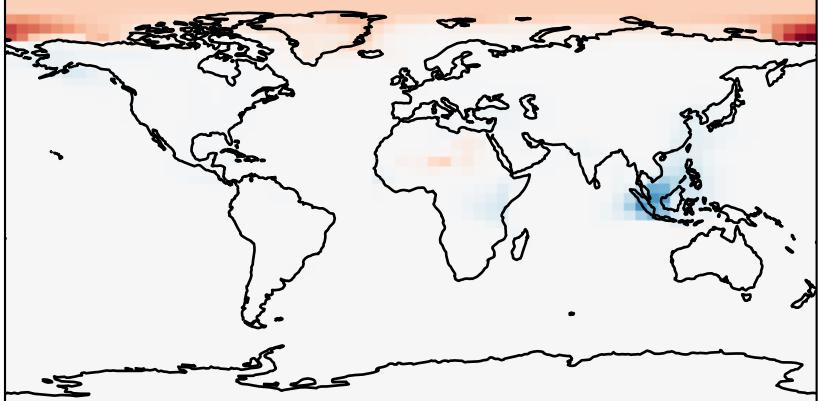


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

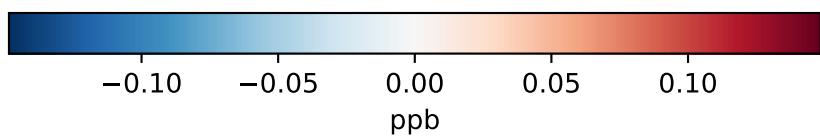
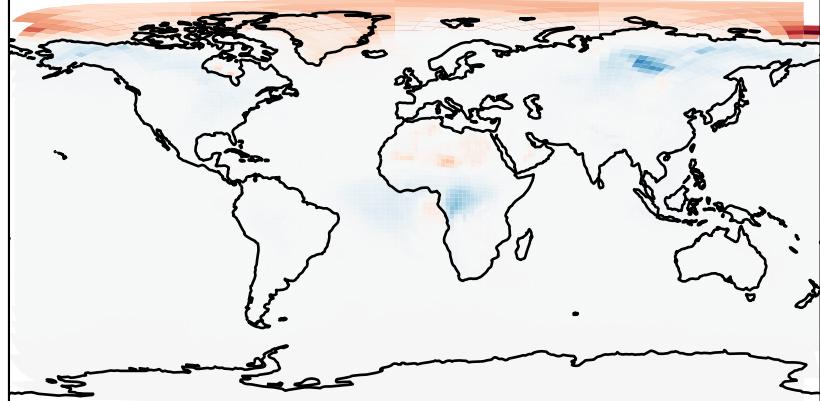


# SpeciesConc\_OCPI

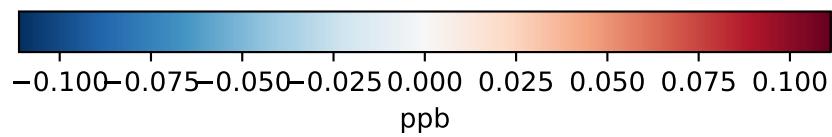
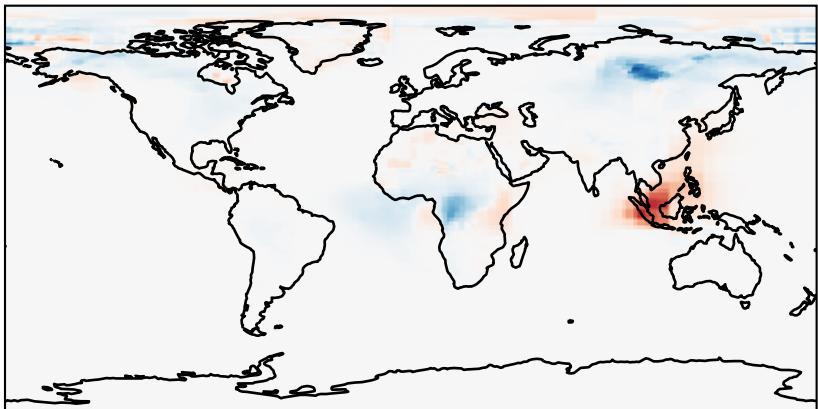
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



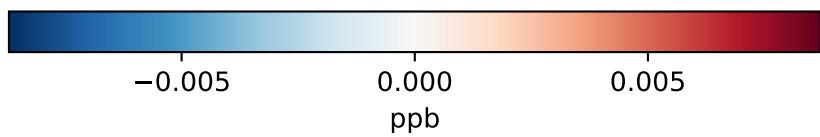
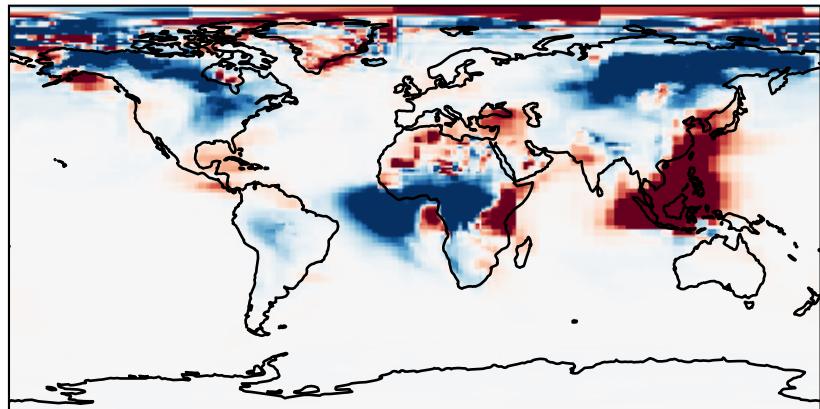
GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48



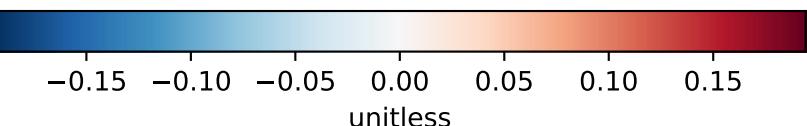
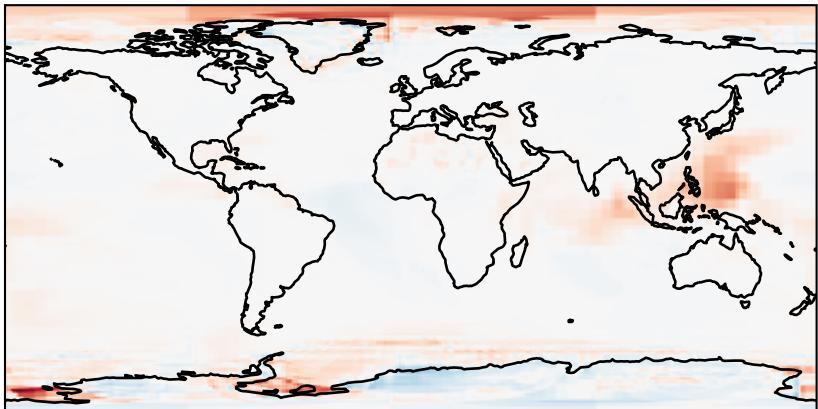
Difference (1x1.25)  
Dev - Ref, Dynamic Range



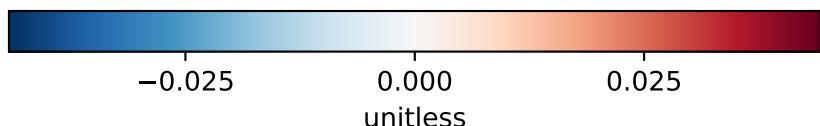
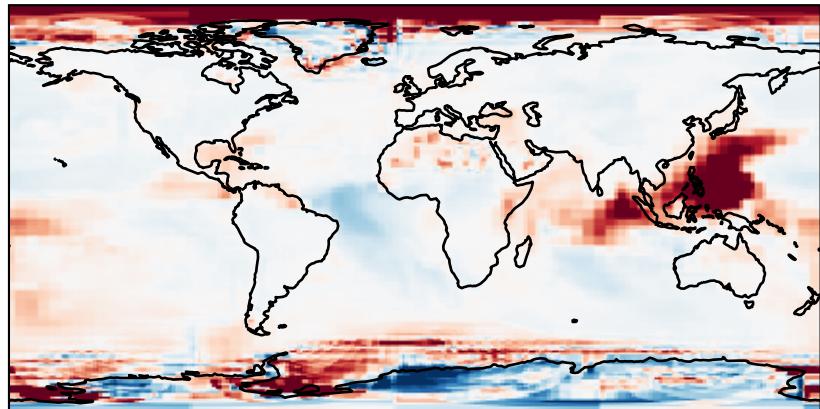
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

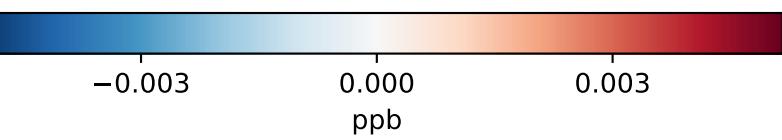
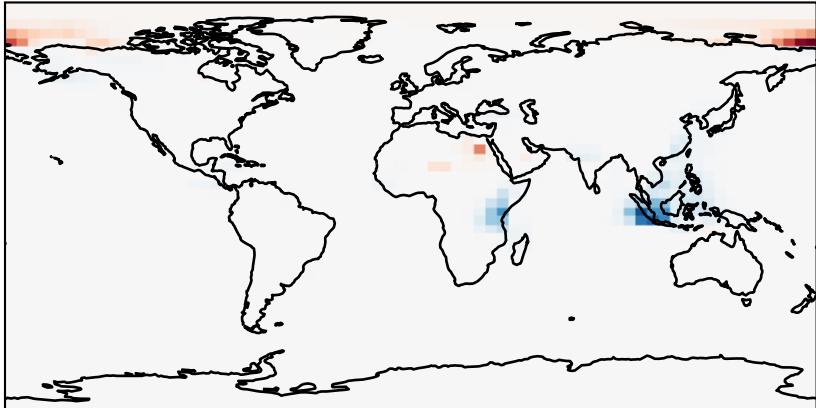


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

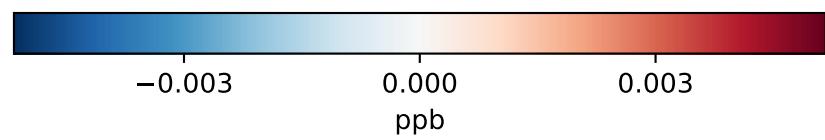
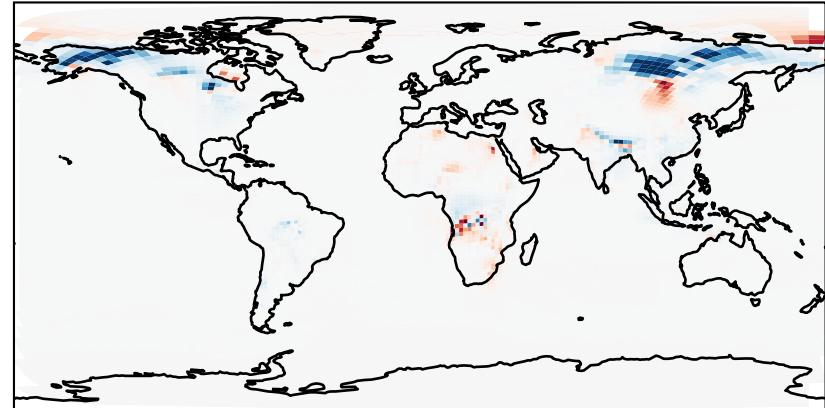


# SpeciesConc\_OCPO

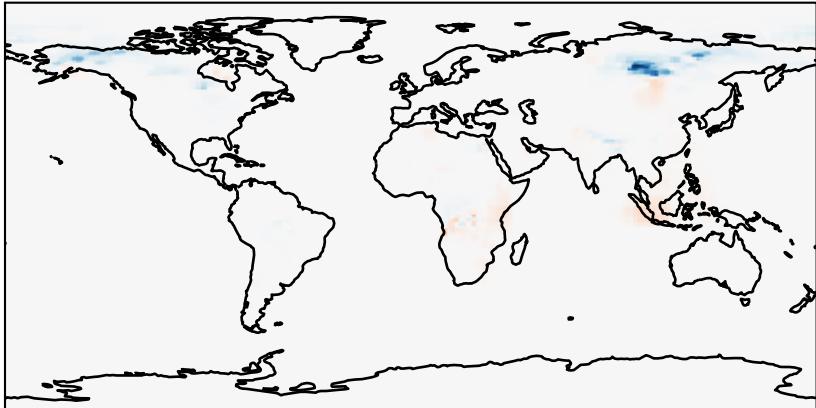
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48

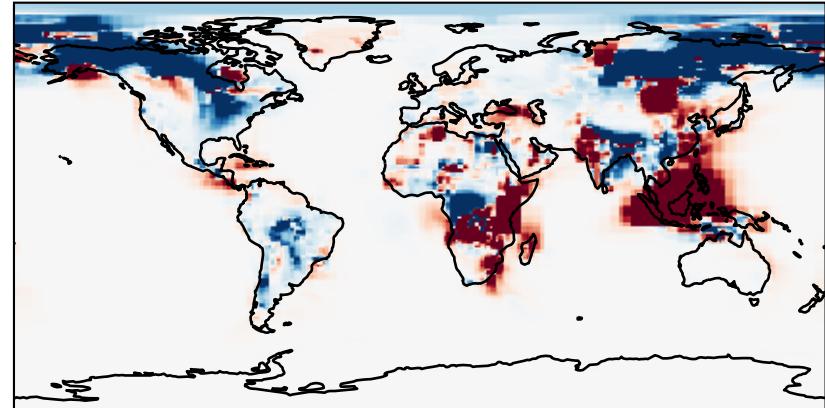


Difference (1x1.25)  
Dev - Ref, Dynamic Range



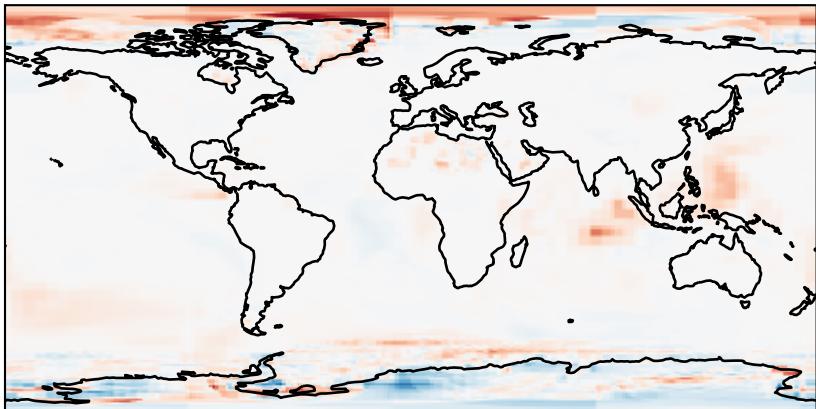
-0.015 0.000 0.015  
ppb

Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



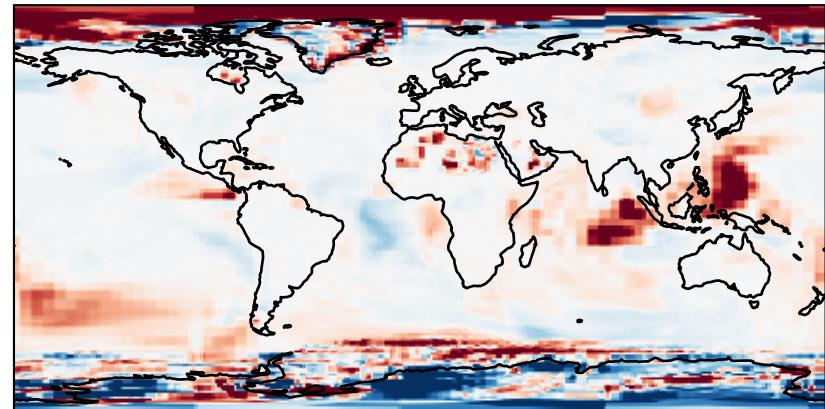
-0.00015 0.00000 0.00015  
ppb

Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref



-0.08 -0.06 -0.04 -0.02 0.00 0.02 0.04 0.06 0.08  
unitless

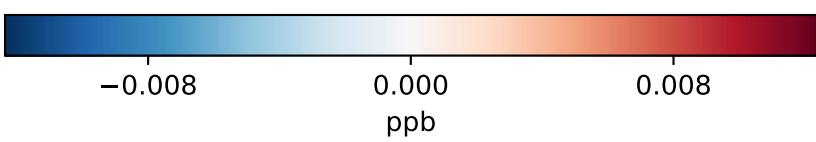
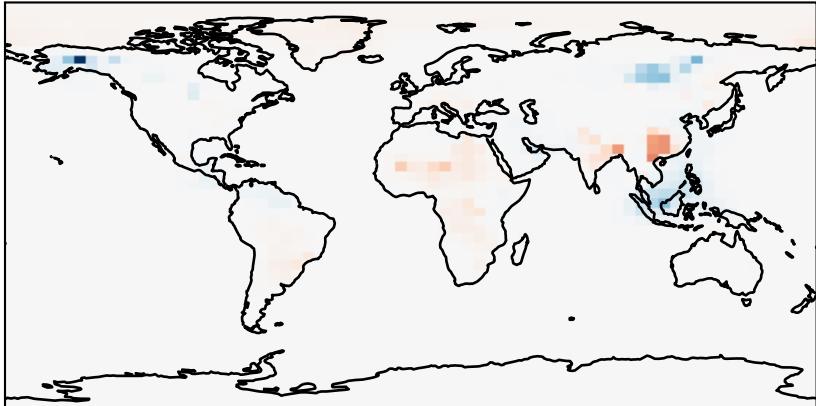
Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref



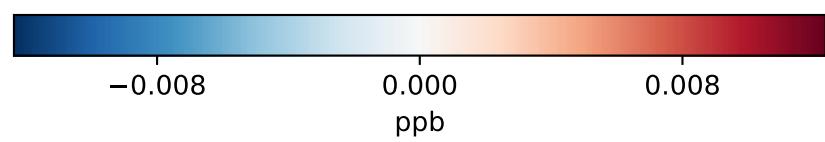
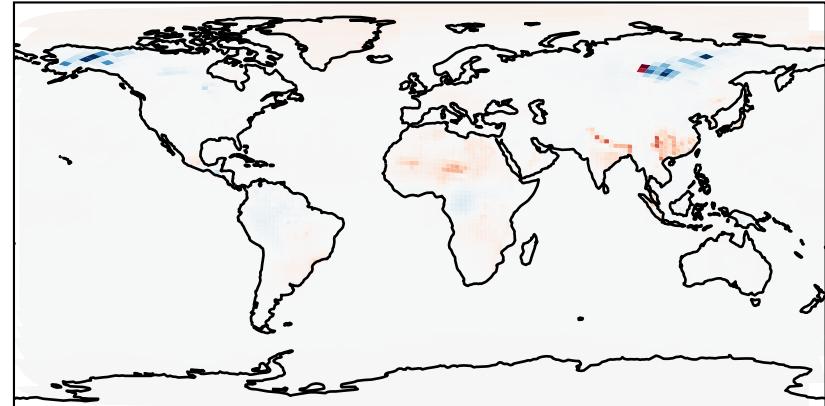
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unitless

# SpeciesConc\_Complex\_SOA

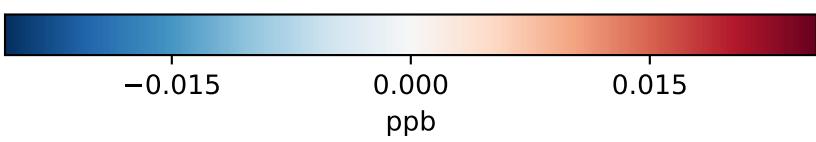
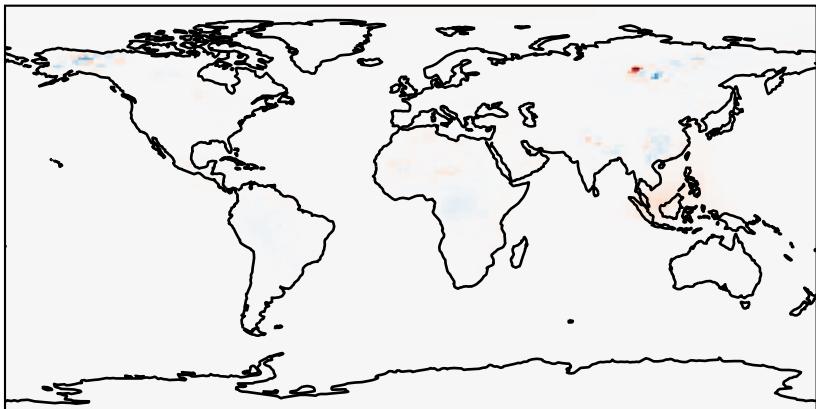
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



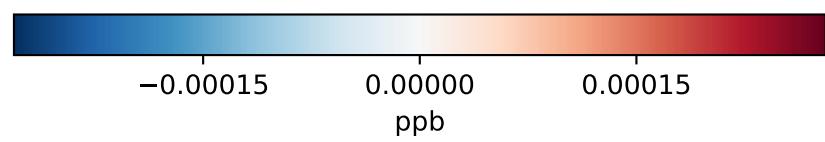
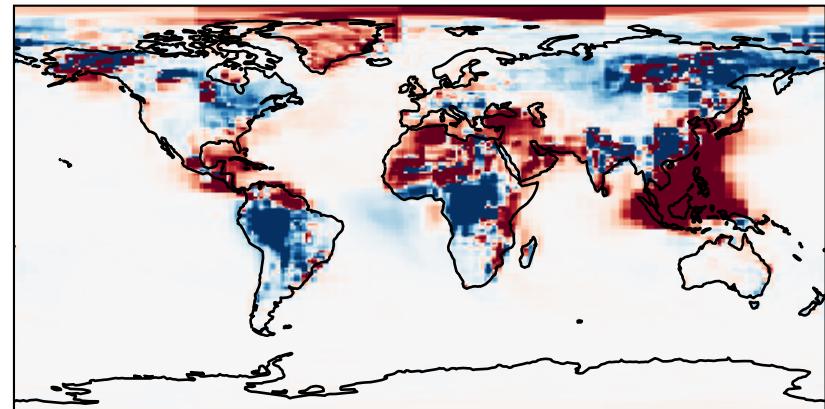
GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48



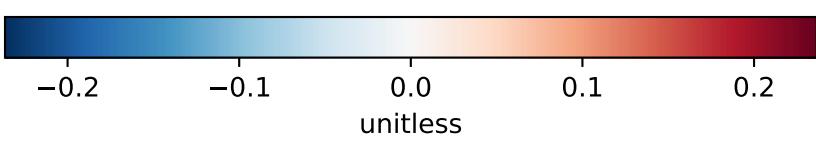
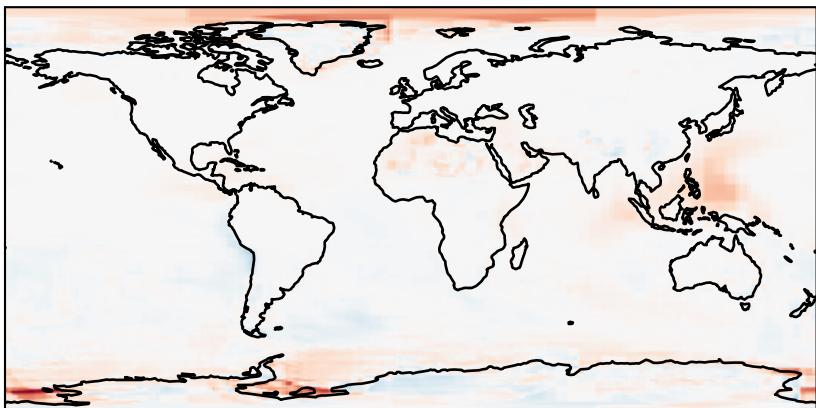
Difference (1x1.25)  
Dev - Ref, Dynamic Range



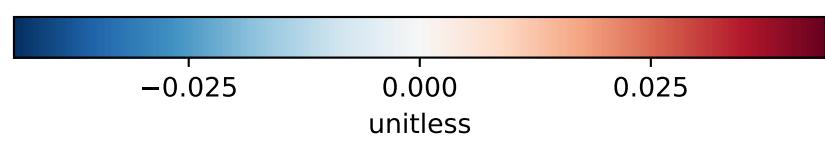
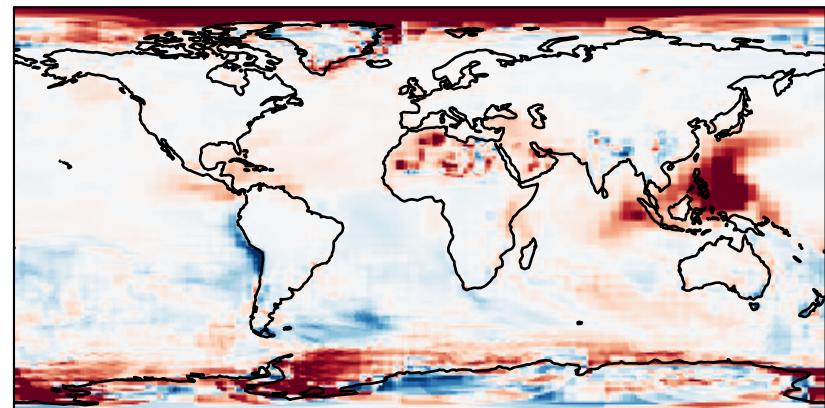
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

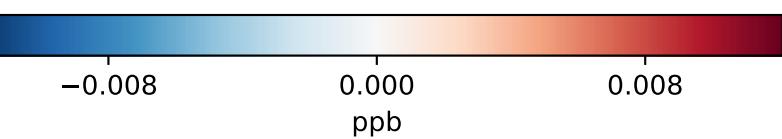
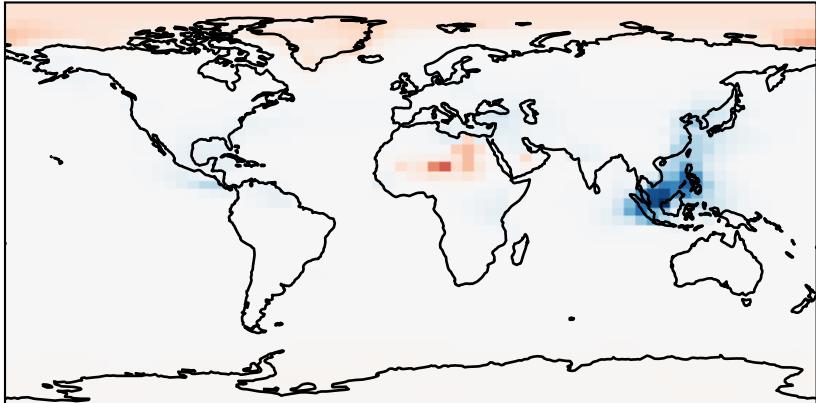


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

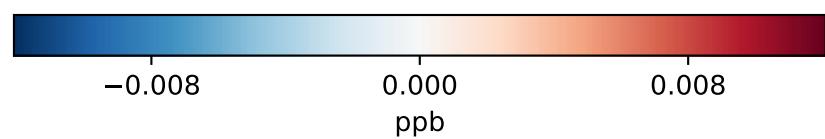
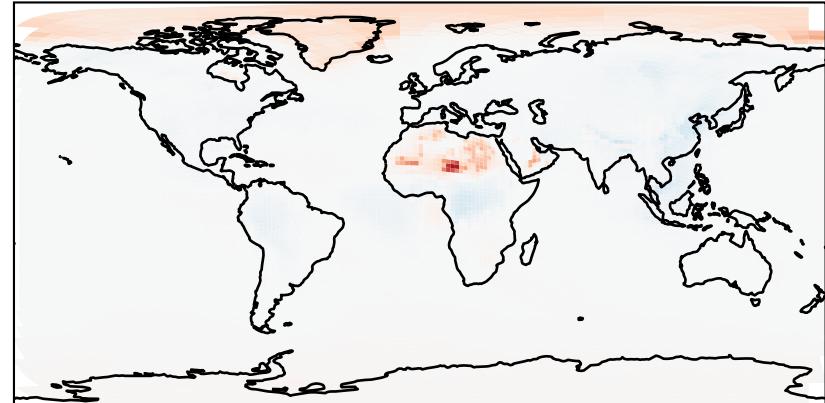


# SpeciesConc\_Simple\_SOA

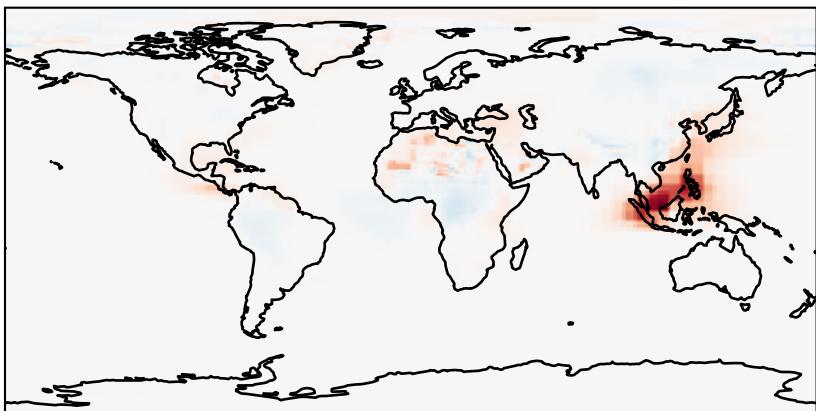
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48

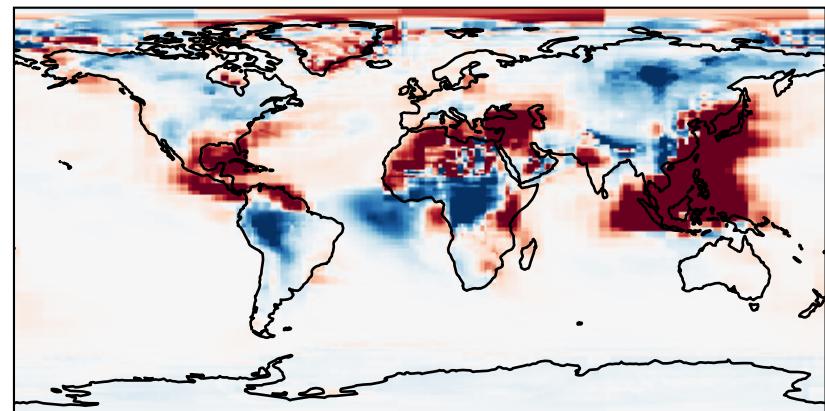


Difference (1x1.25)  
Dev - Ref, Dynamic Range



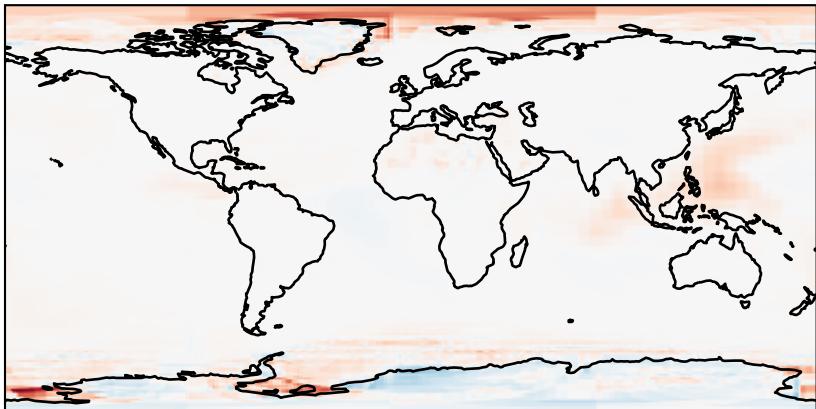
-0.006 0.000 0.006  
ppb

Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



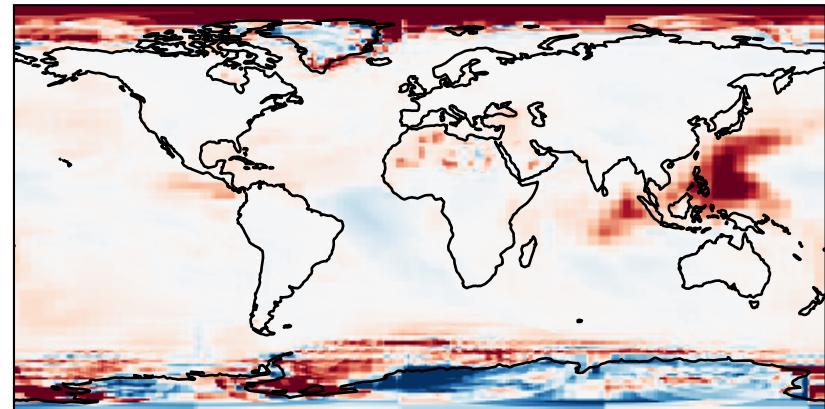
-0.0004 0.0000 0.0004  
ppb

Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref



-0.15 -0.10 -0.05 0.00 0.05 0.10 0.15  
unitless

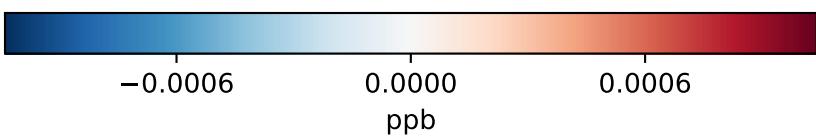
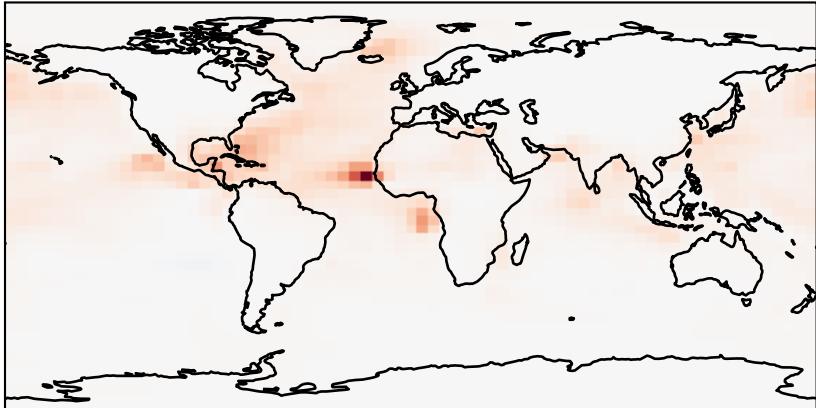
Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref



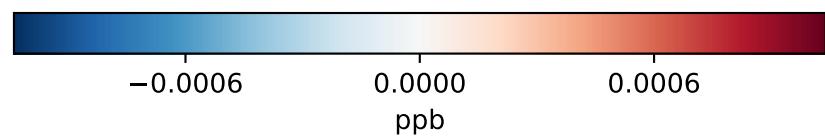
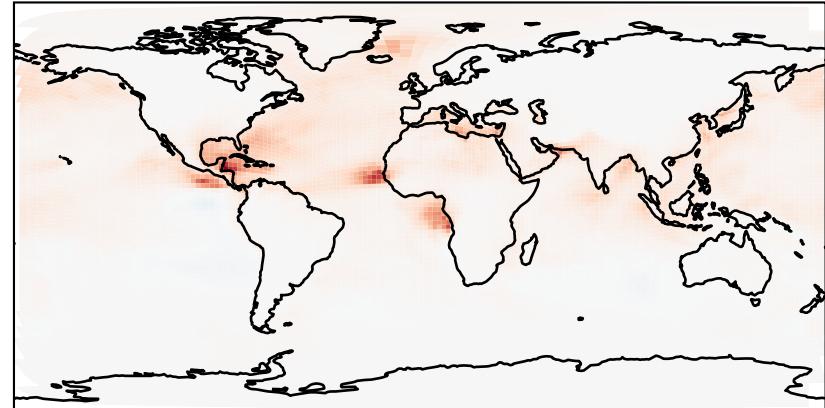
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unitless

# SpeciesConc\_AERI

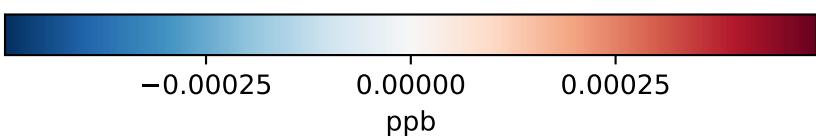
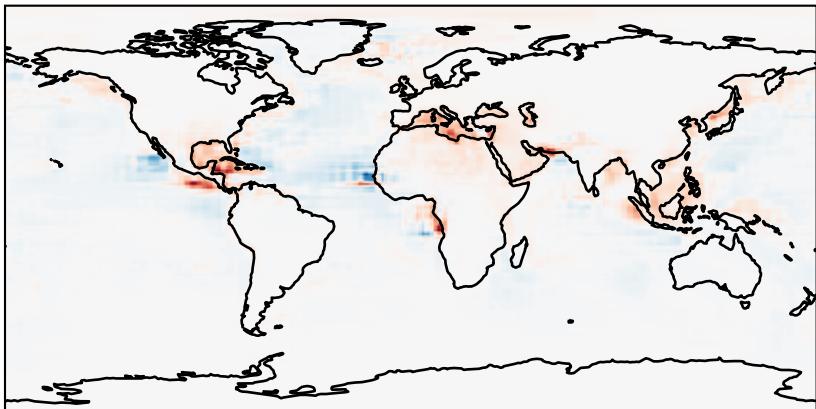
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



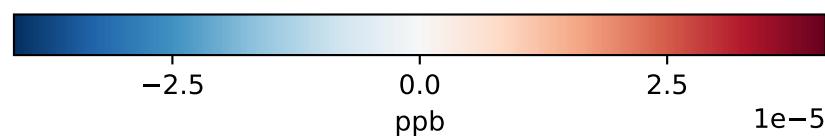
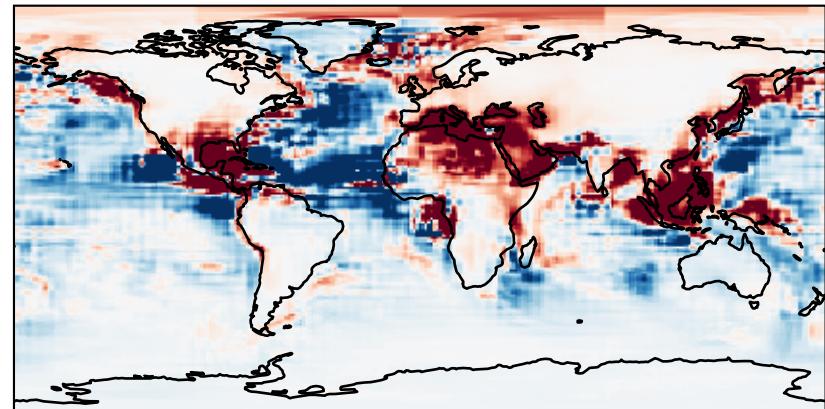
GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48



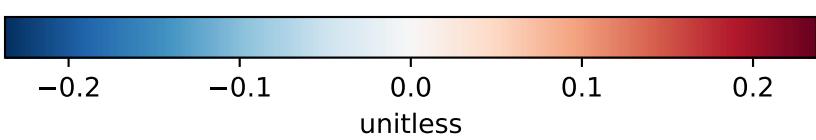
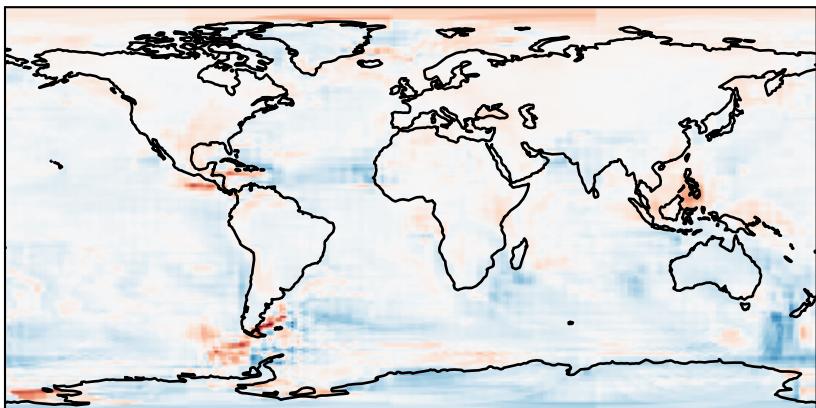
Difference (1x1.25)  
Dev - Ref, Dynamic Range



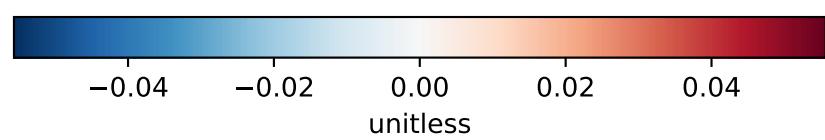
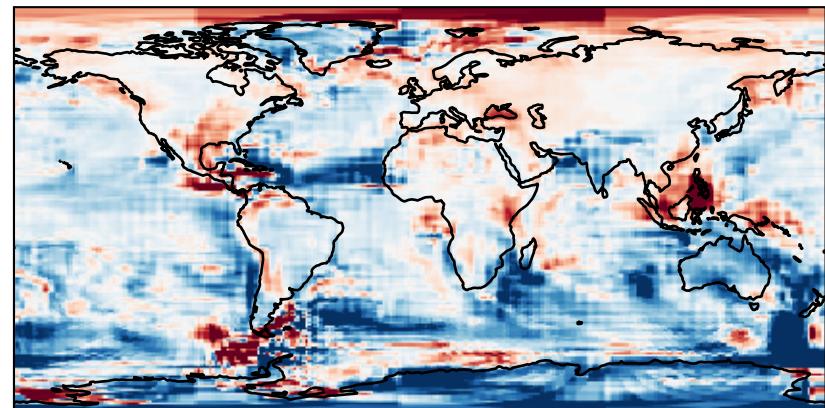
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

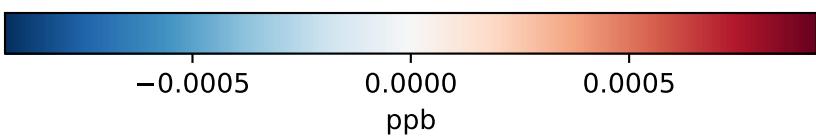
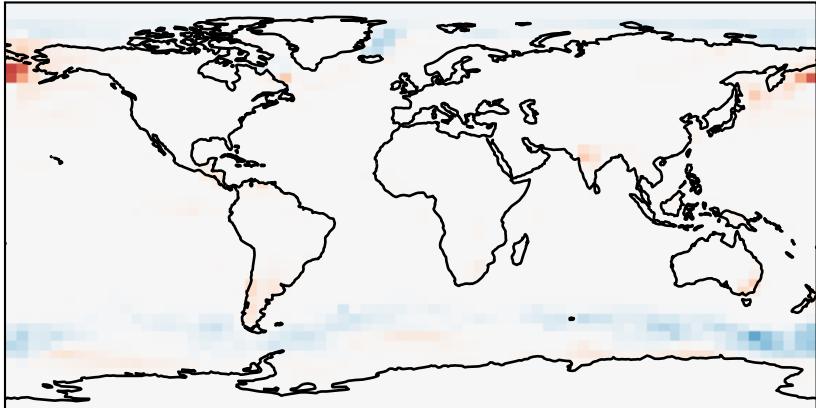


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

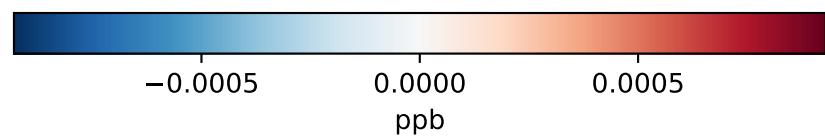
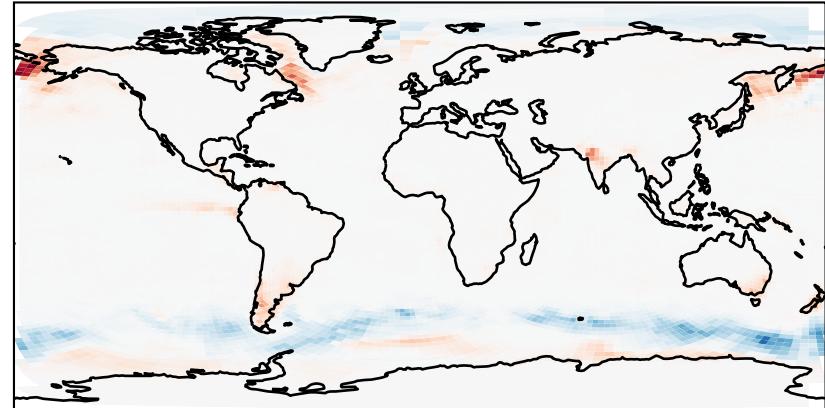


# SpeciesConc\_BrSALA

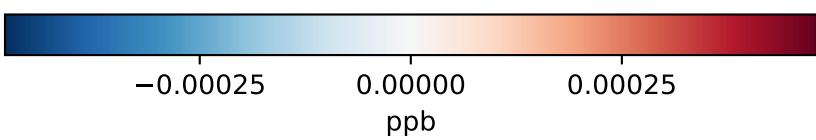
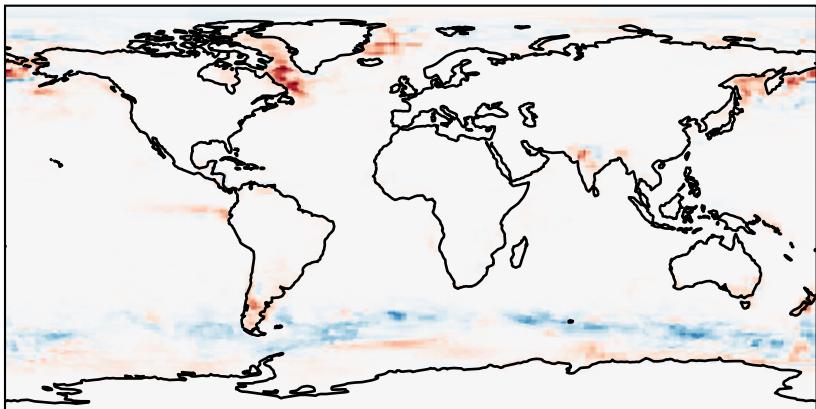
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



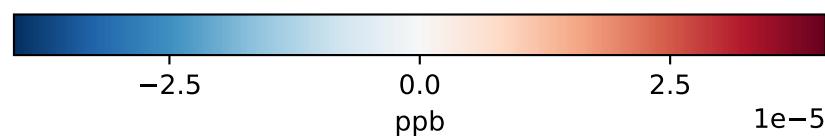
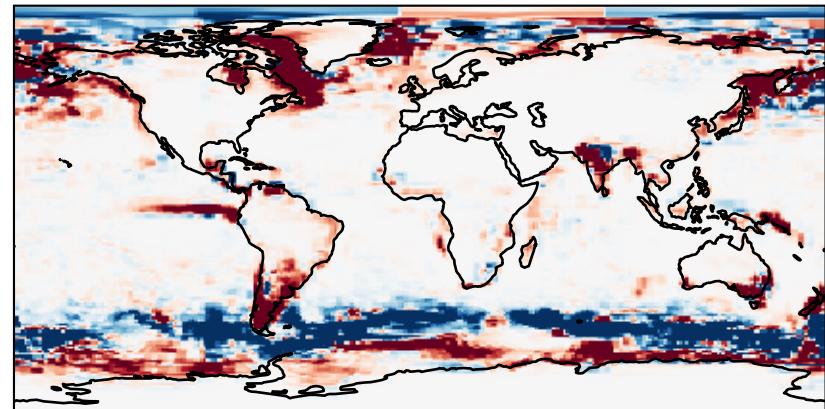
GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48



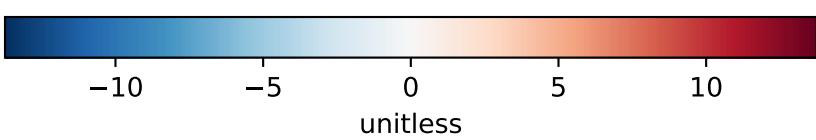
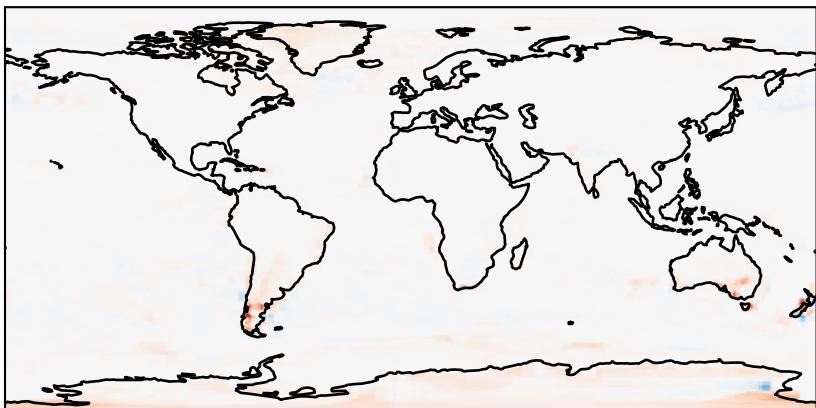
Difference (1x1.25)  
Dev - Ref, Dynamic Range



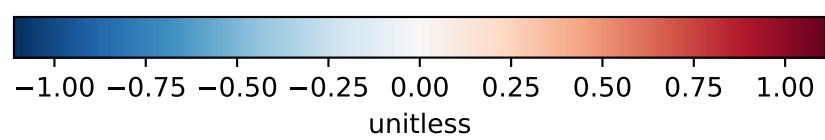
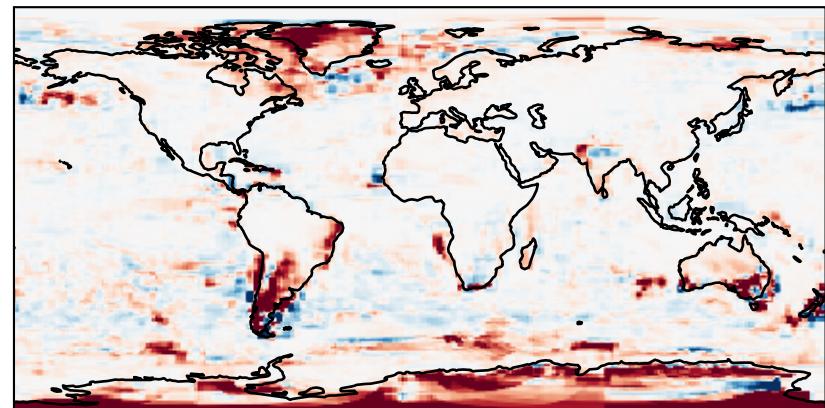
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

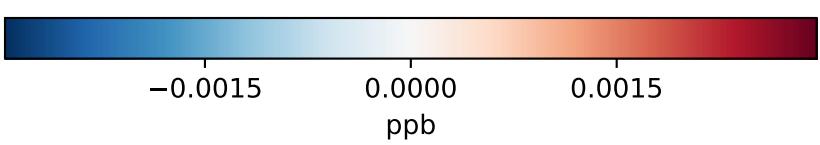
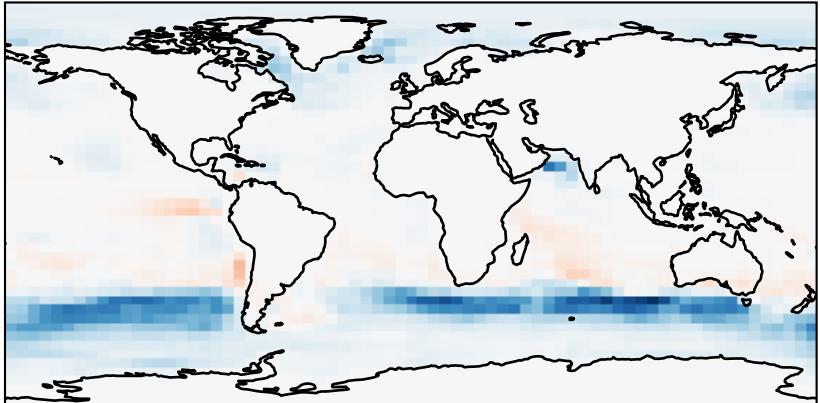


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

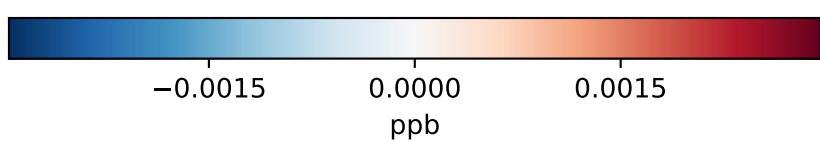
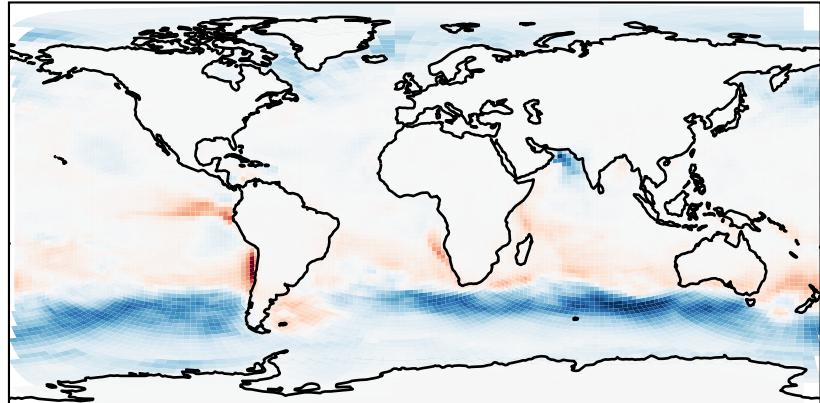


# SpeciesConc\_BrSALC

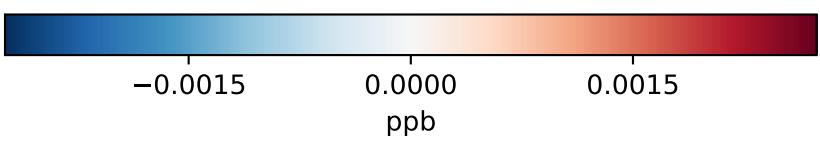
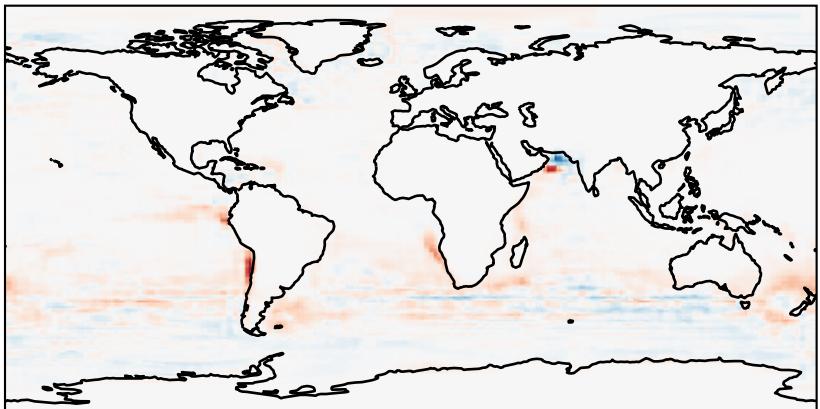
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



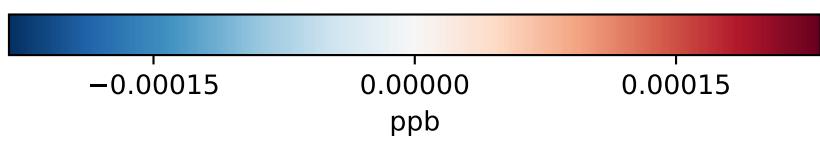
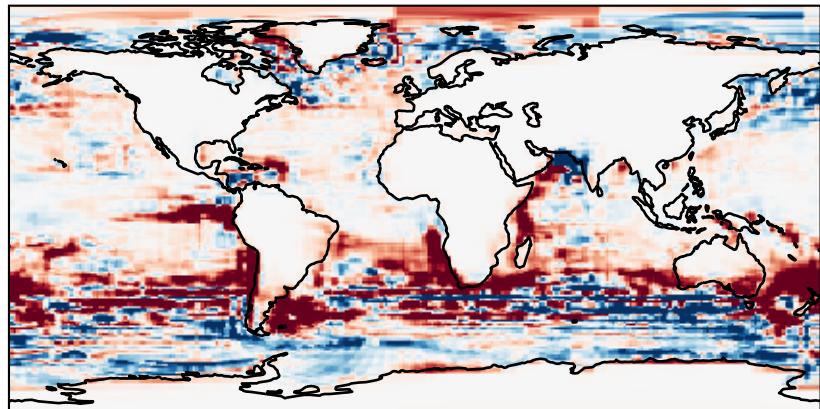
GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48



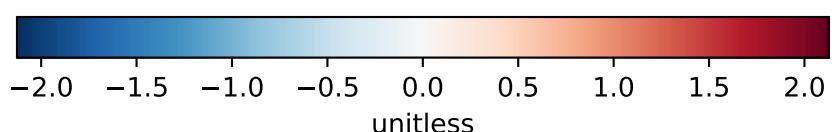
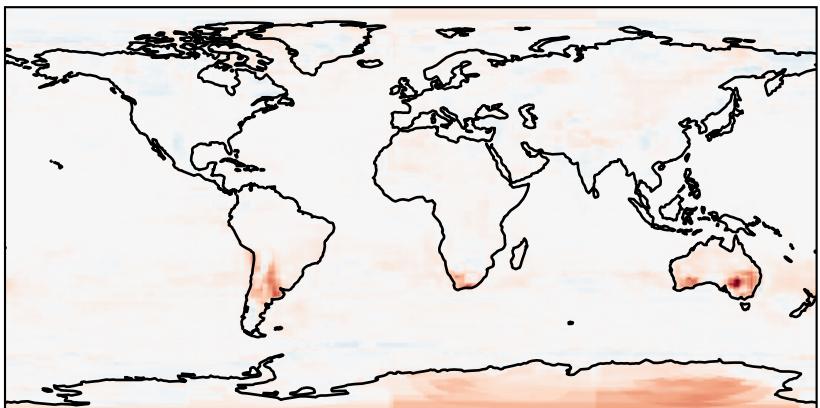
Difference (1x1.25)  
Dev - Ref, Dynamic Range



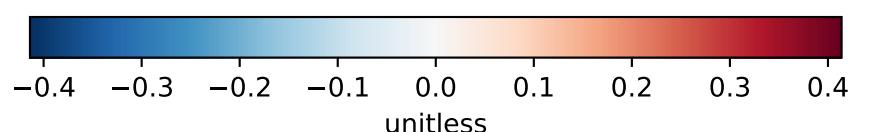
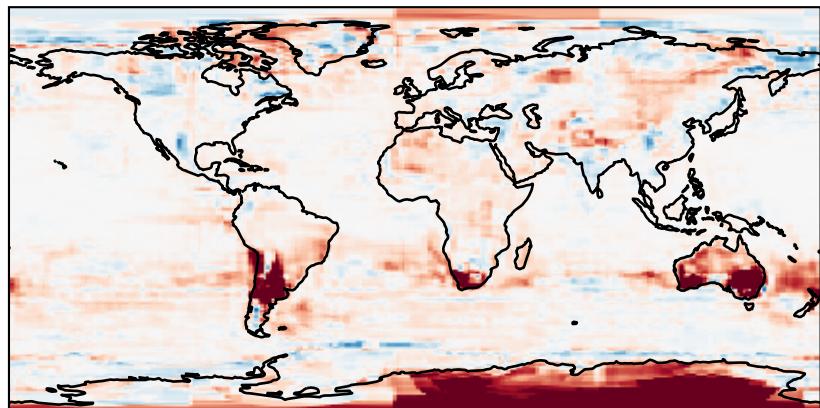
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

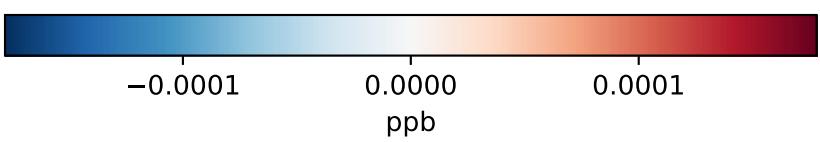
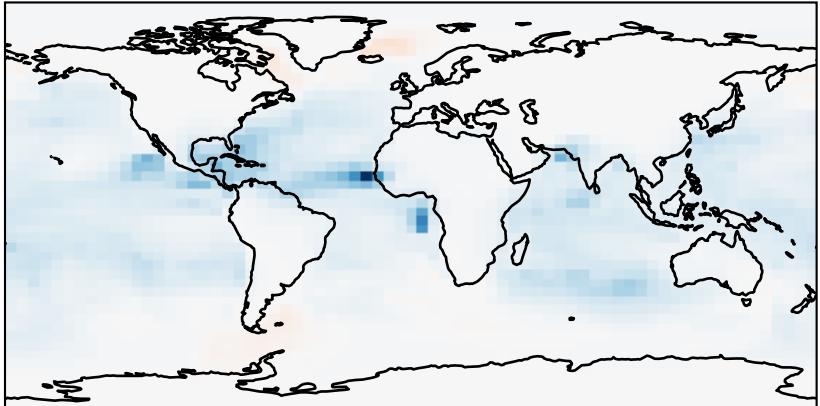


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

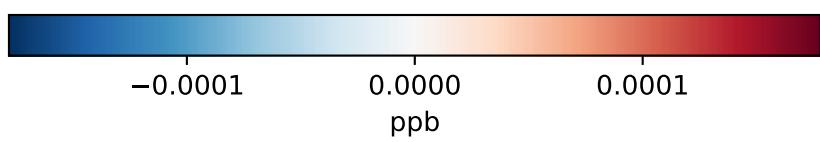
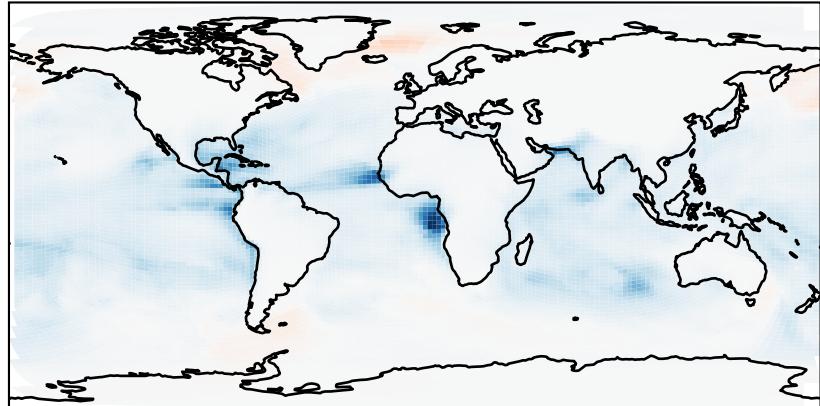


# SpeciesConc\_ISALA

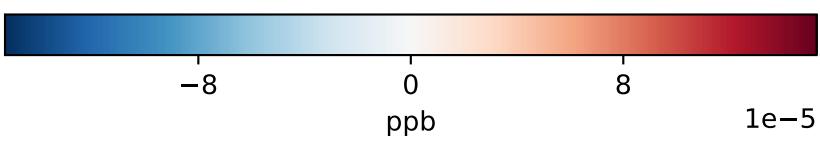
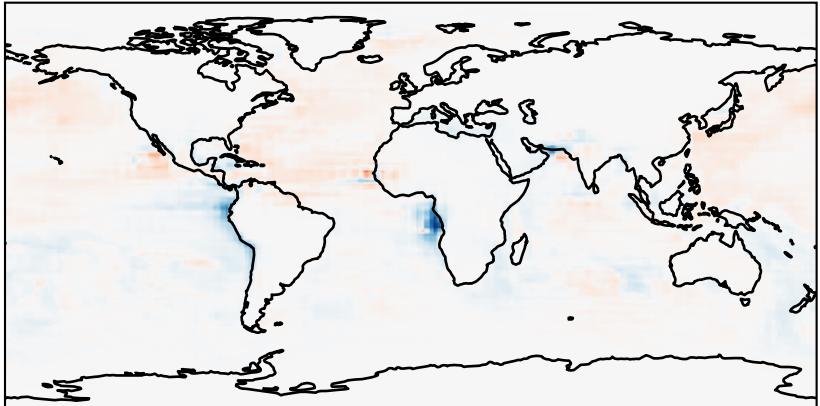
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



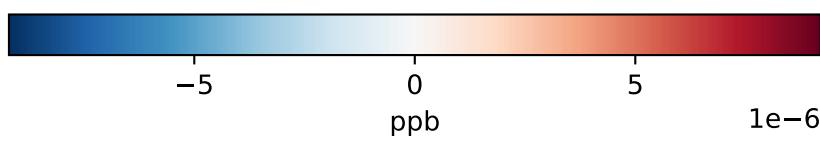
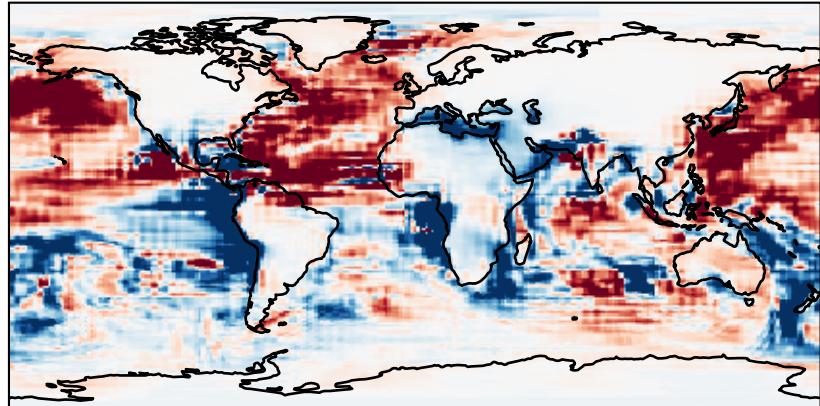
GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48



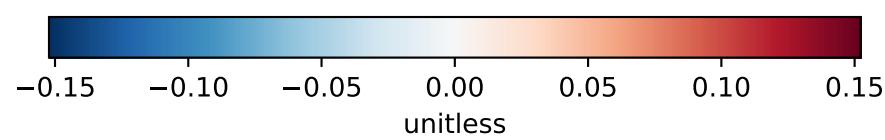
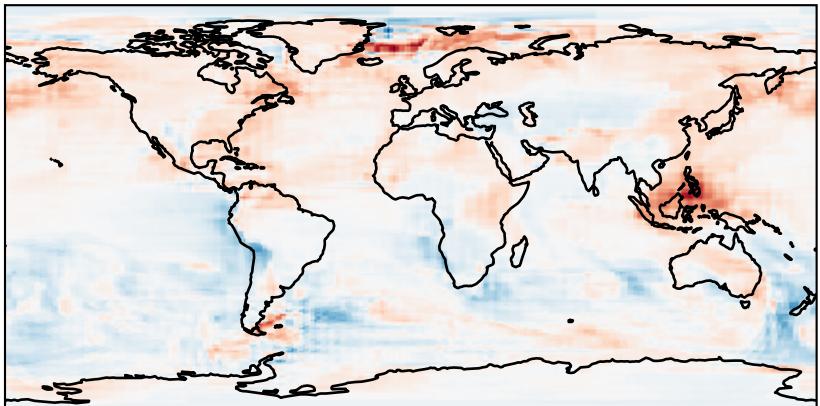
Difference (1x1.25)  
Dev - Ref, Dynamic Range



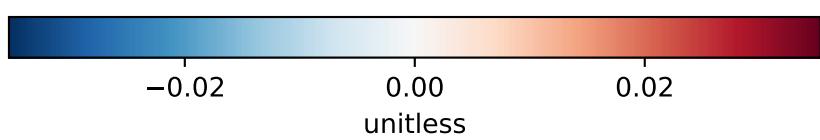
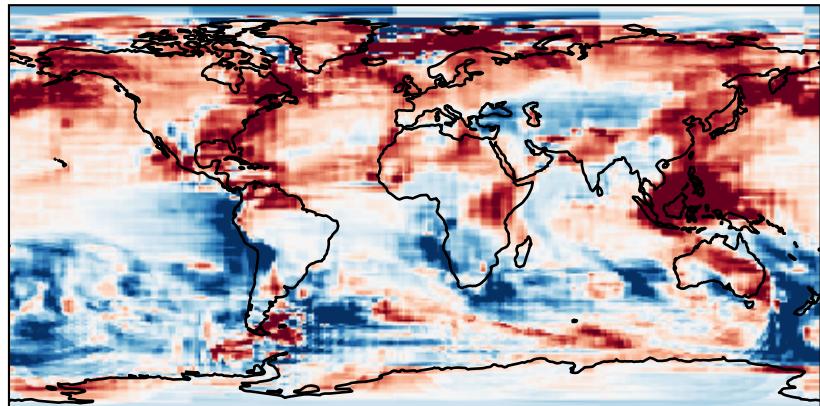
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

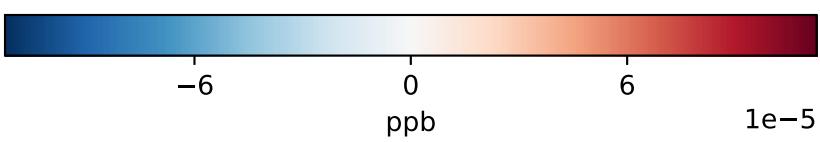
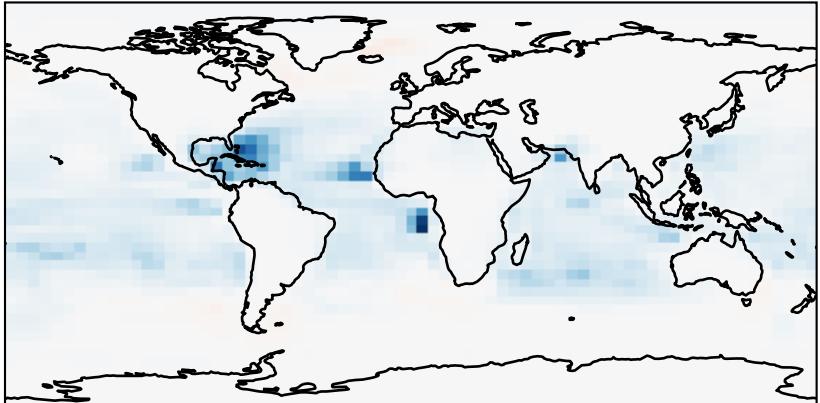


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

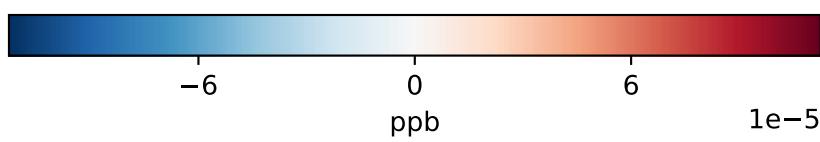
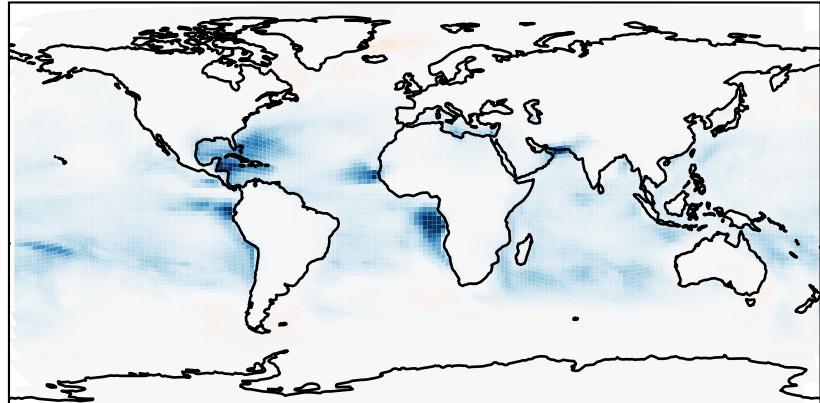


# SpeciesConc\_ISALC

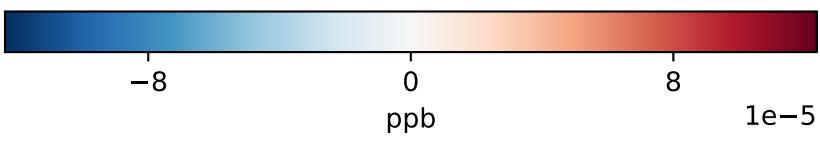
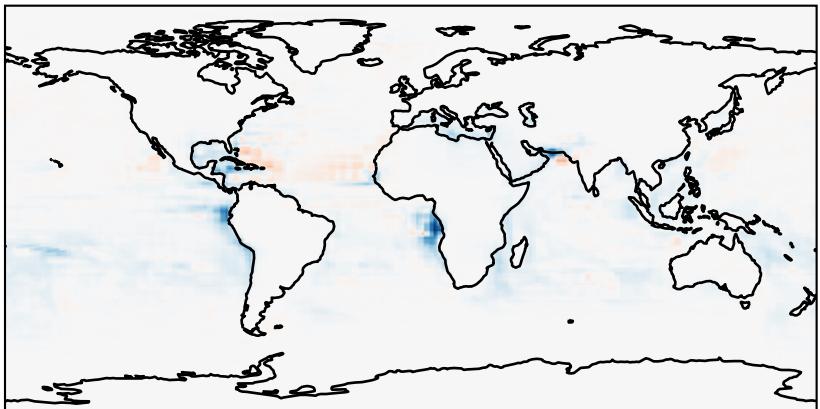
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



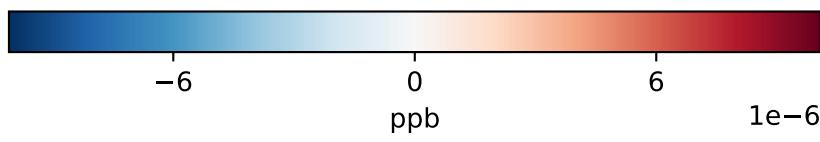
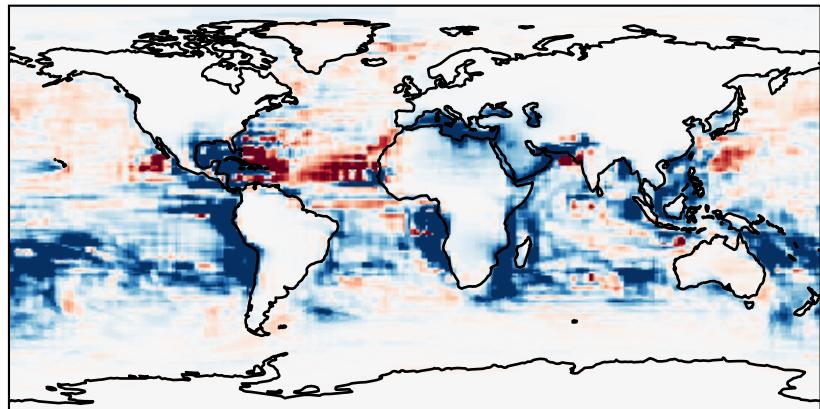
GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48



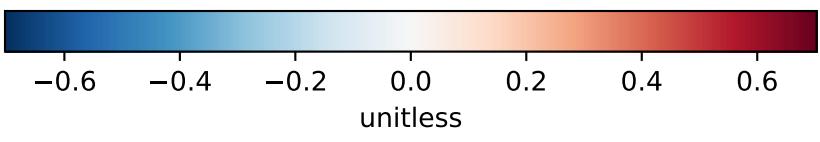
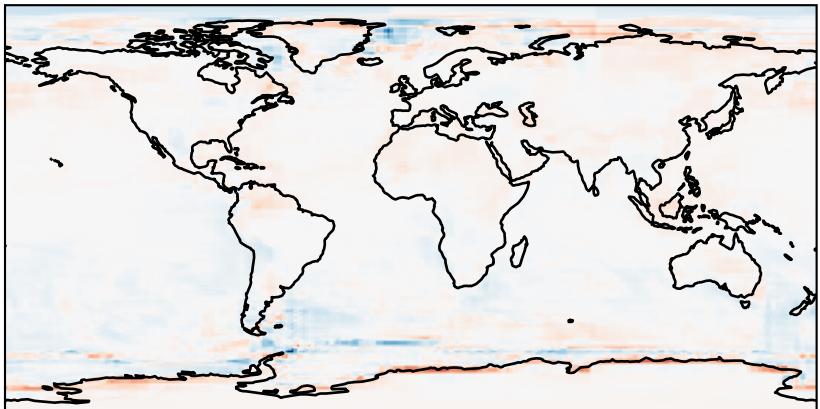
Difference (1x1.25)  
Dev - Ref, Dynamic Range



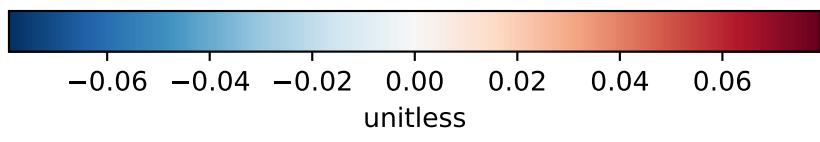
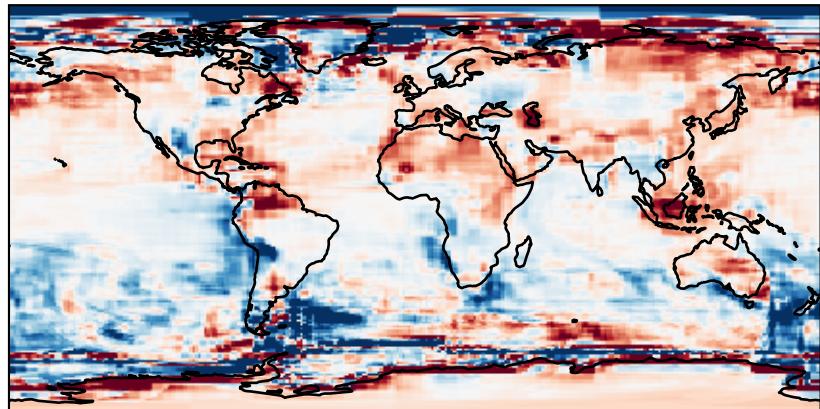
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



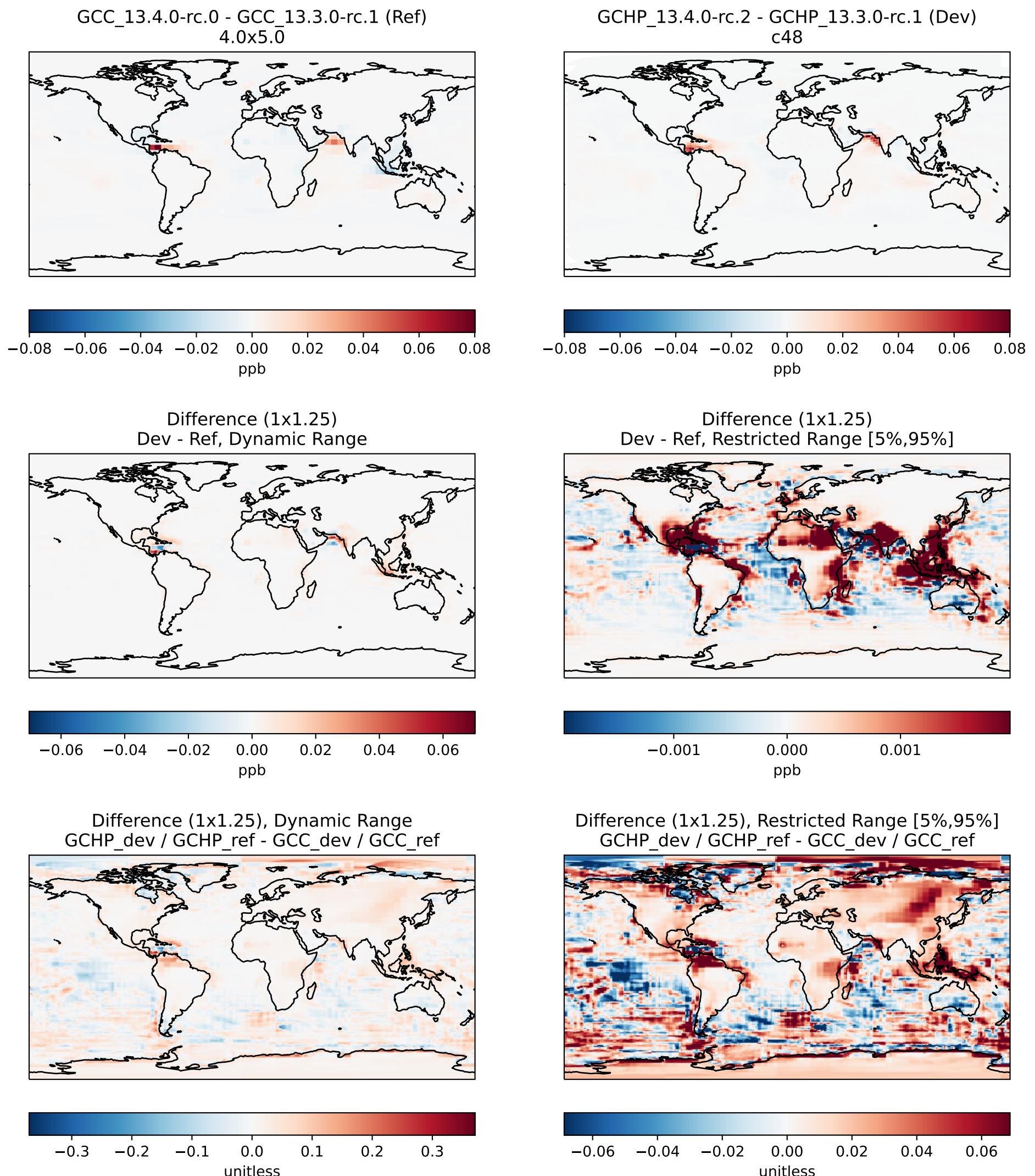
Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref



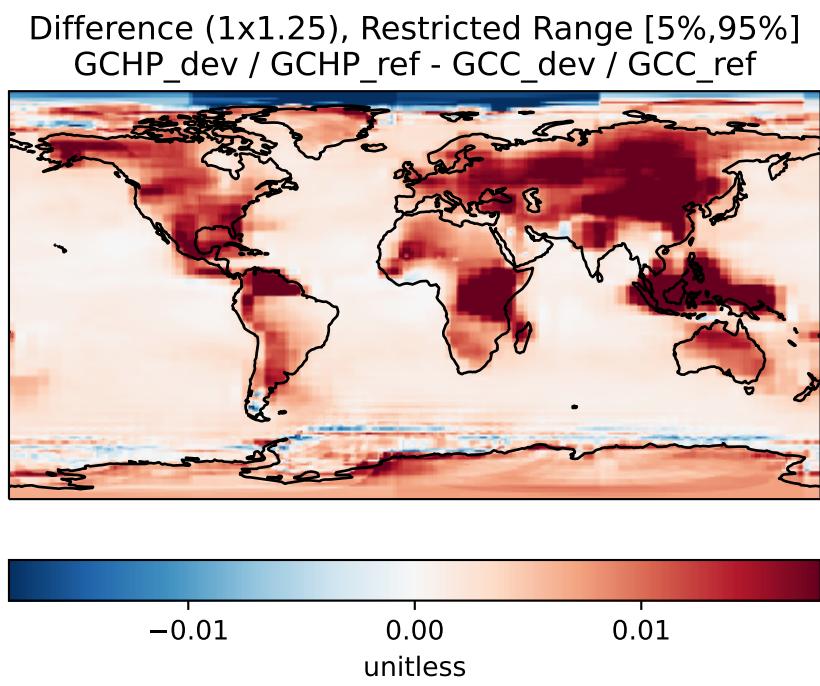
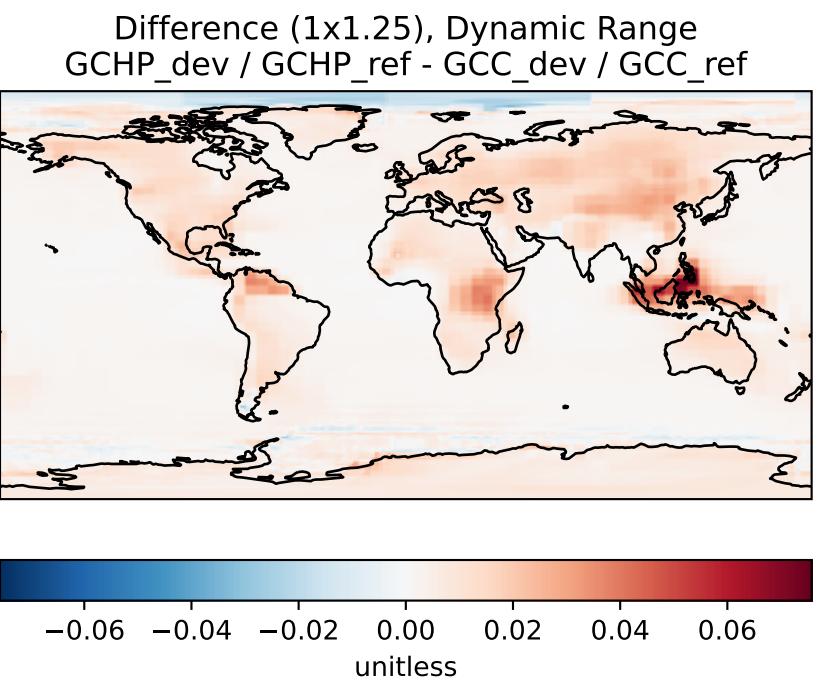
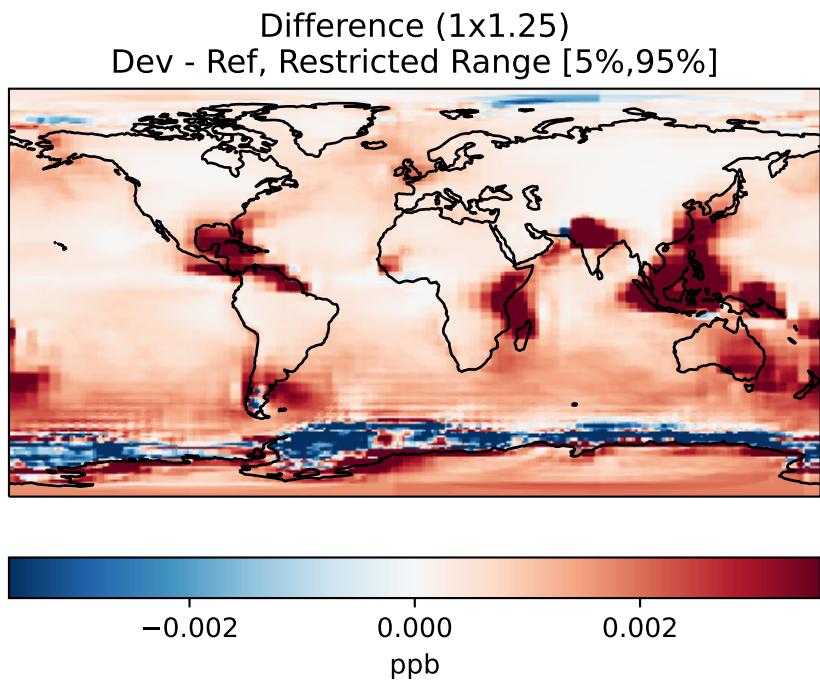
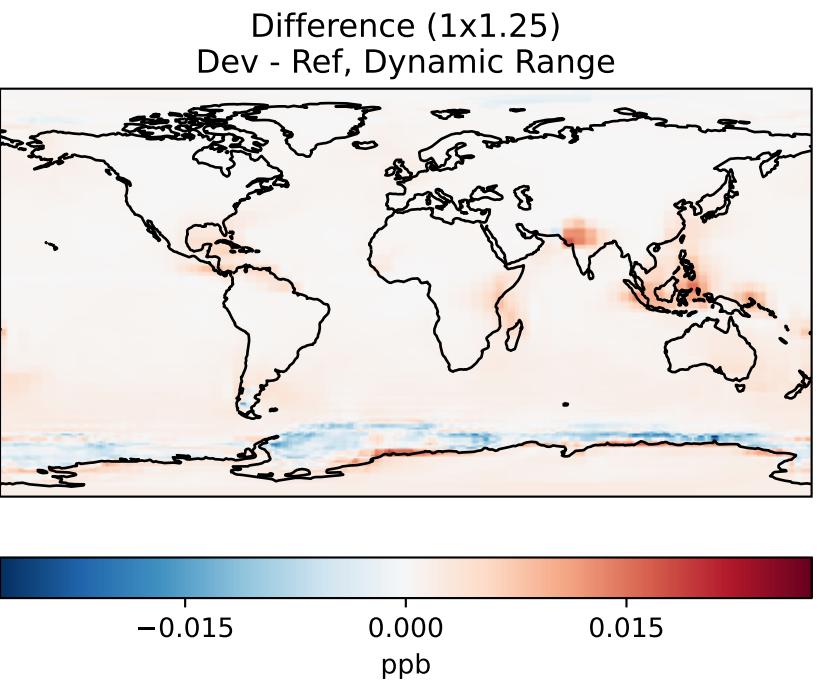
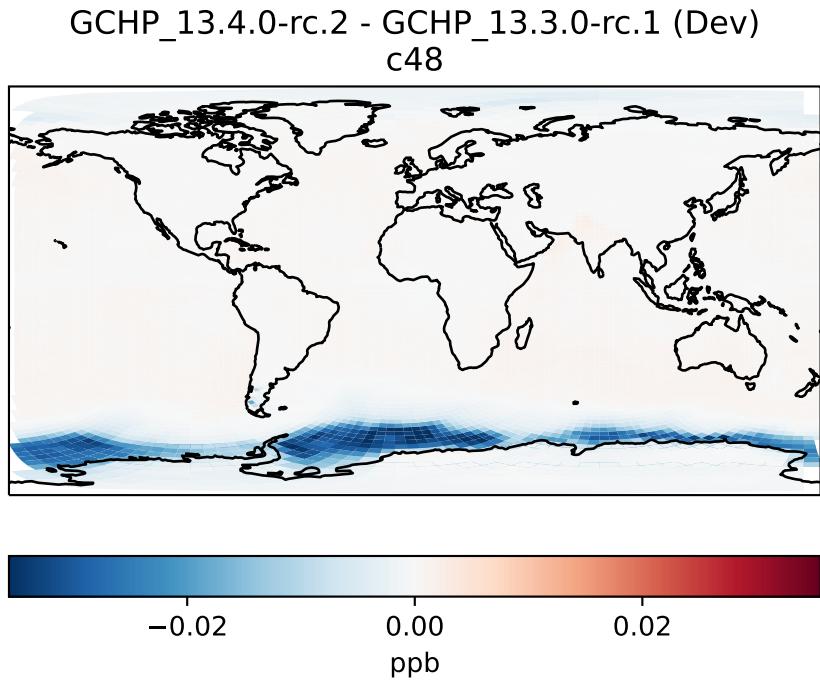
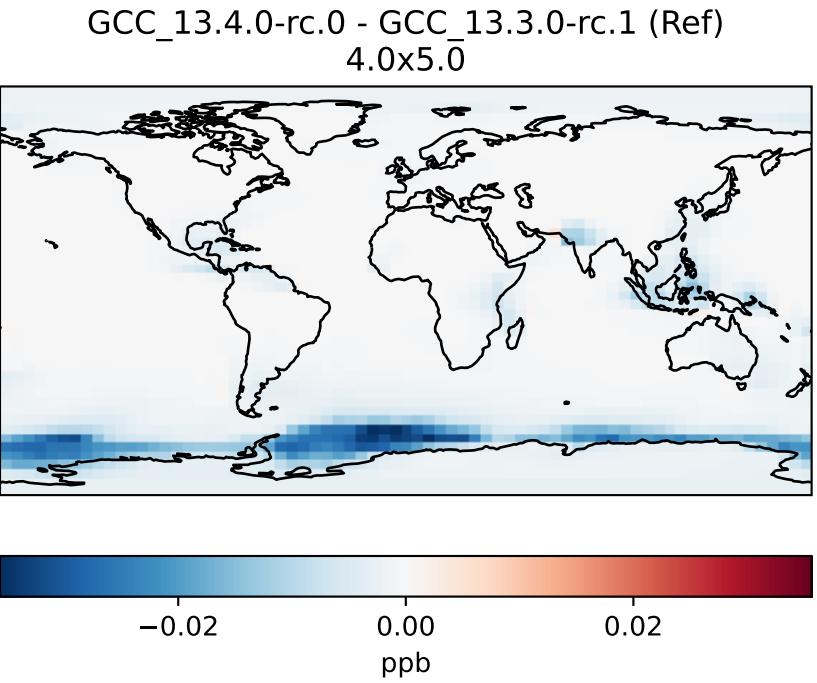
Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref



# SpeciesConc\_NITs

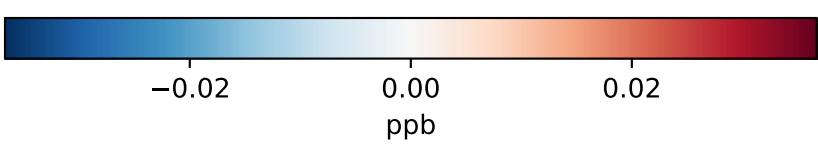
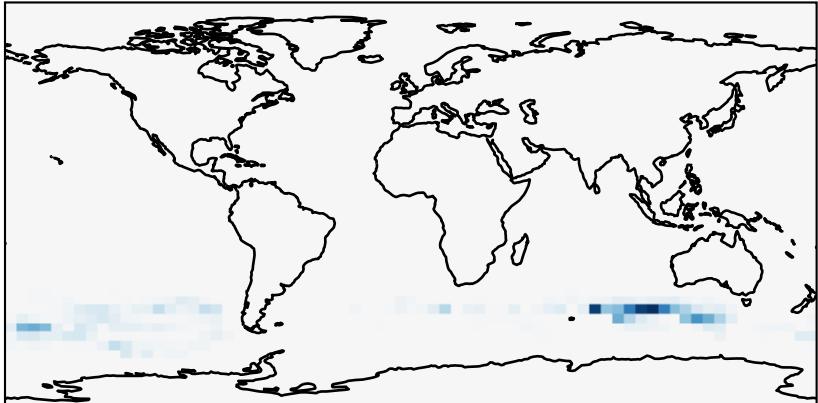


# SpeciesConc\_SALA

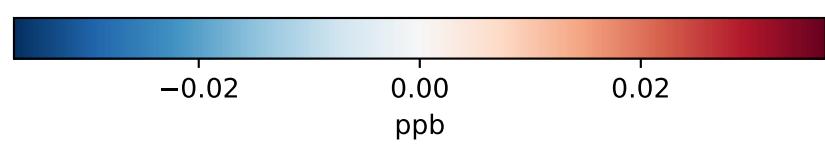
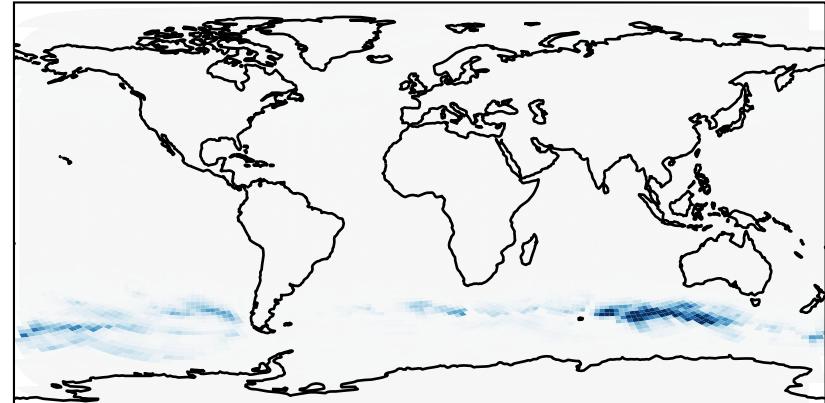


# SpeciesConc\_SALAAL

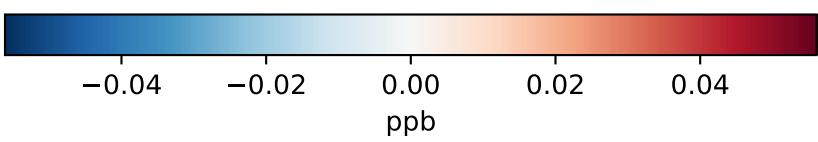
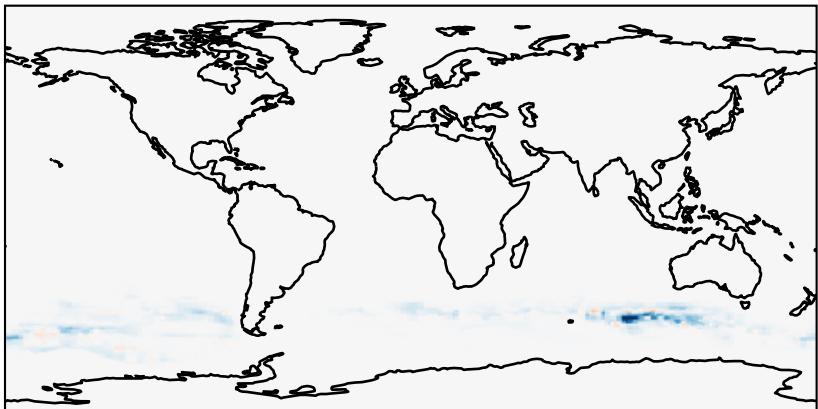
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



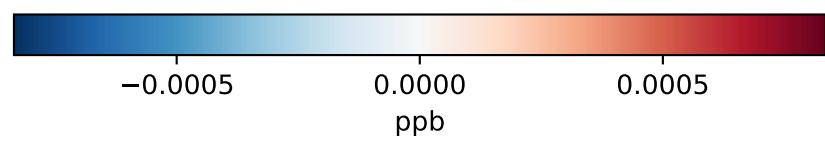
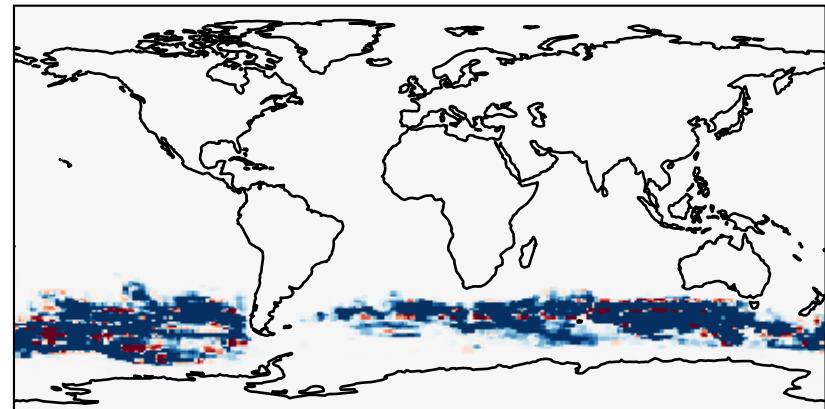
GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48



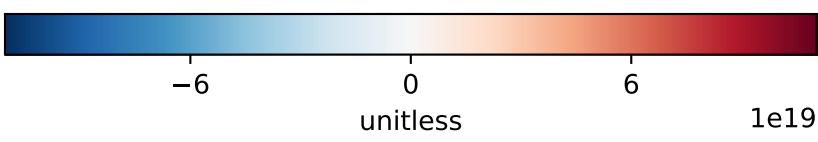
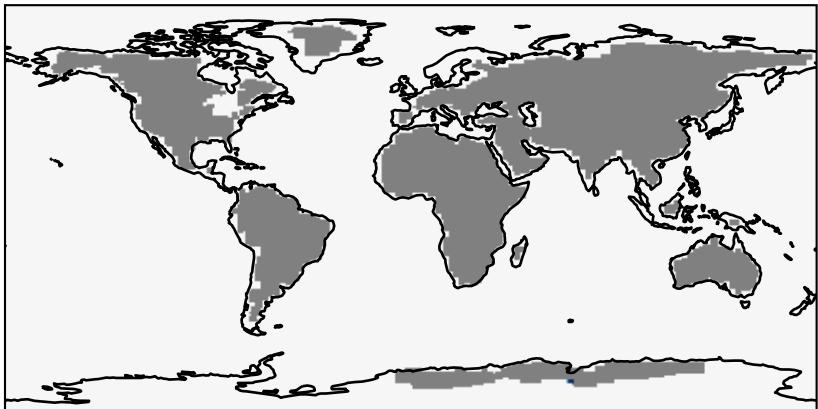
Difference (1x1.25)  
Dev - Ref, Dynamic Range



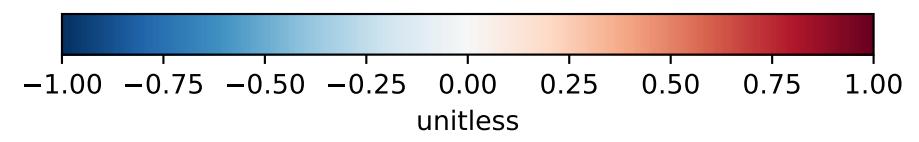
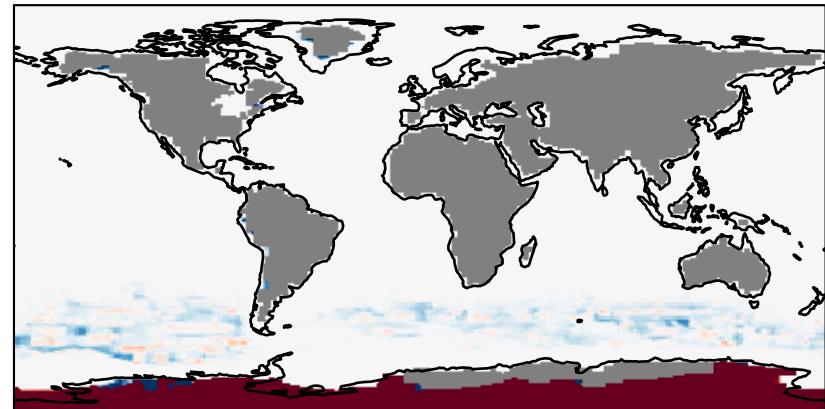
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

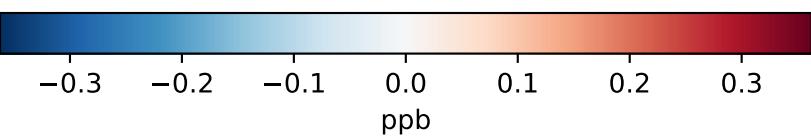
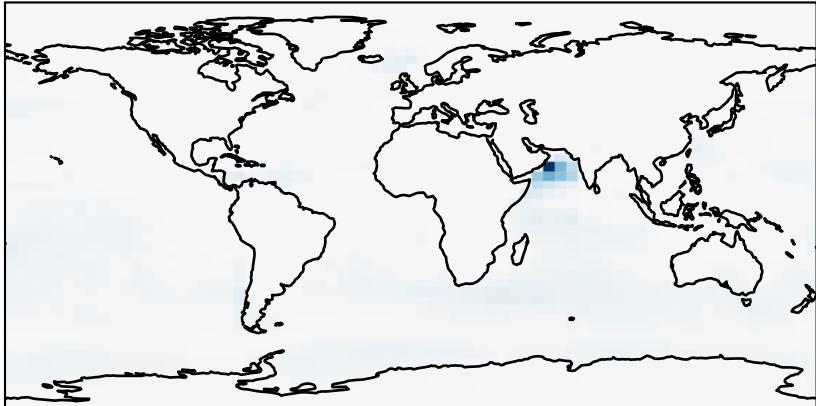


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

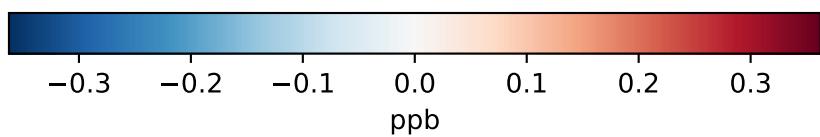
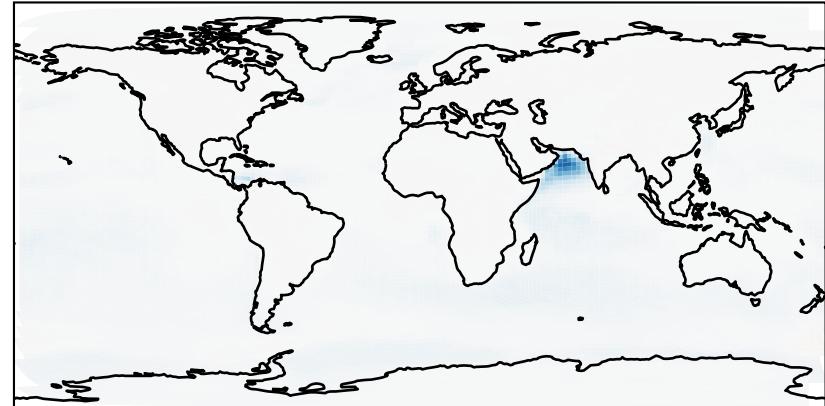


# SpeciesConc\_SALACL

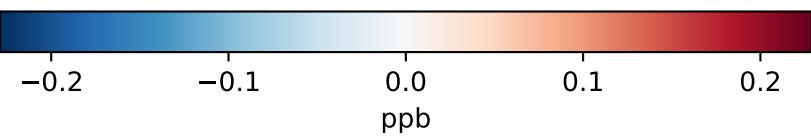
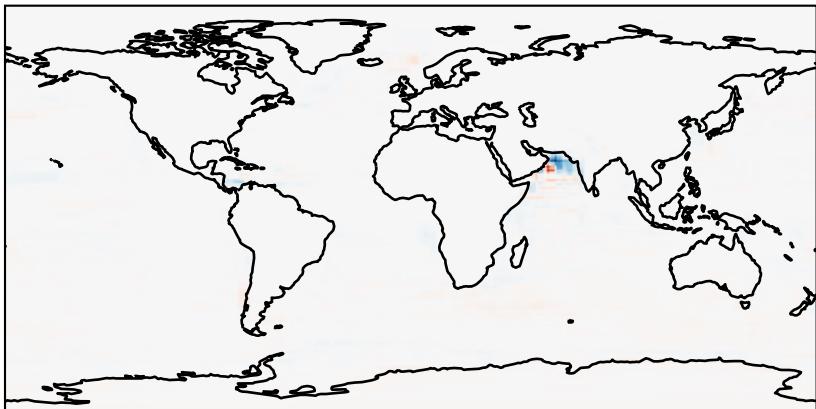
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



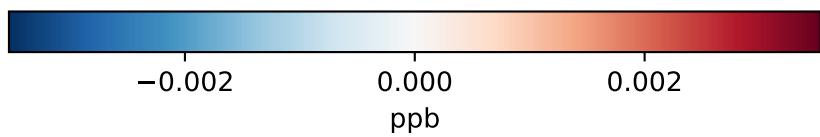
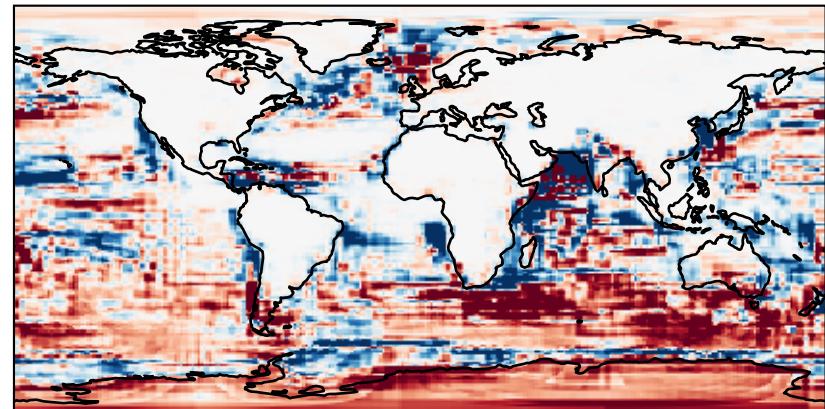
GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48



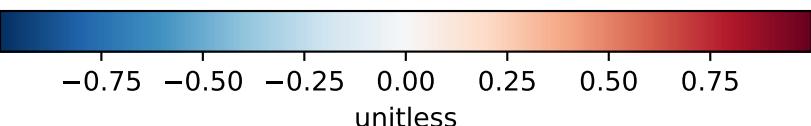
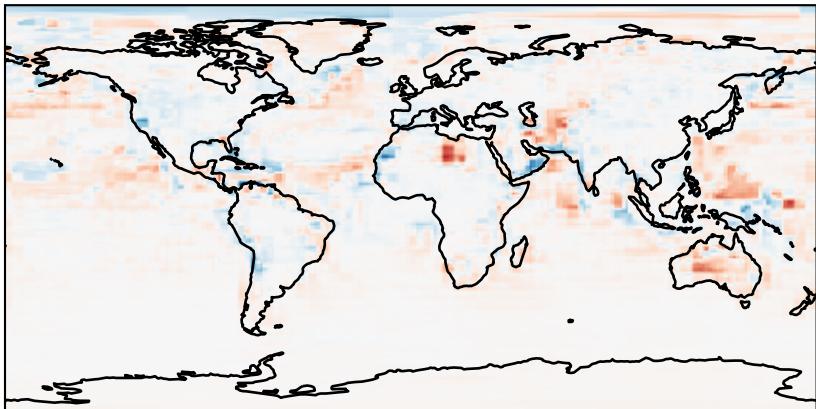
Difference (1x1.25)  
Dev - Ref, Dynamic Range



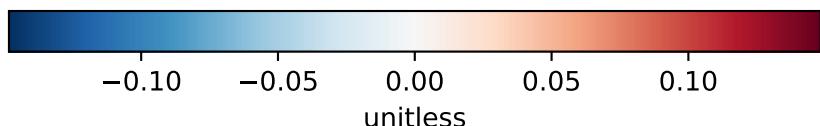
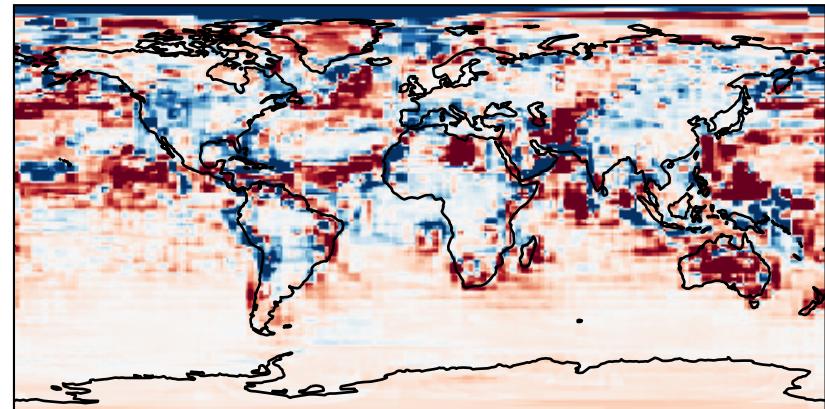
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

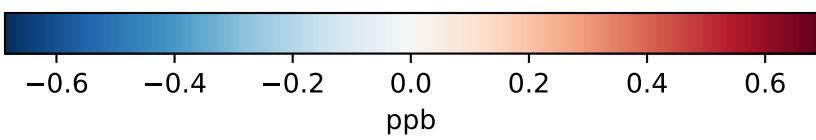
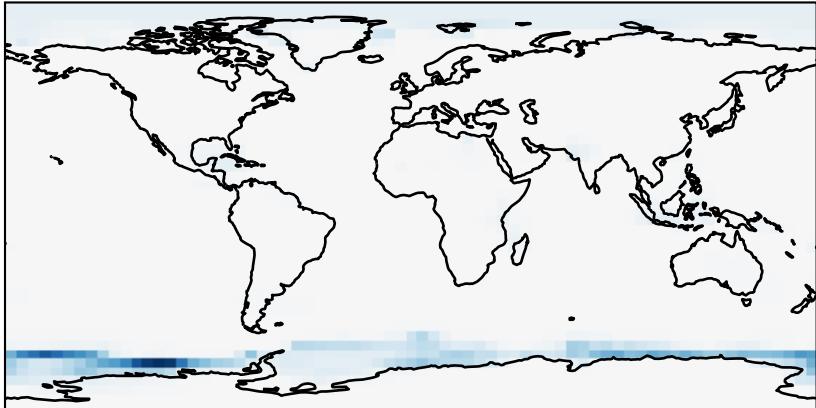


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

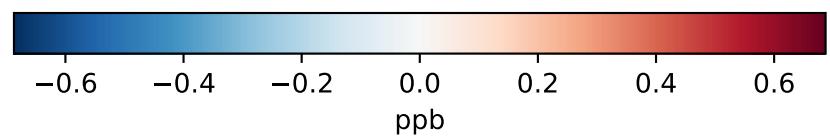
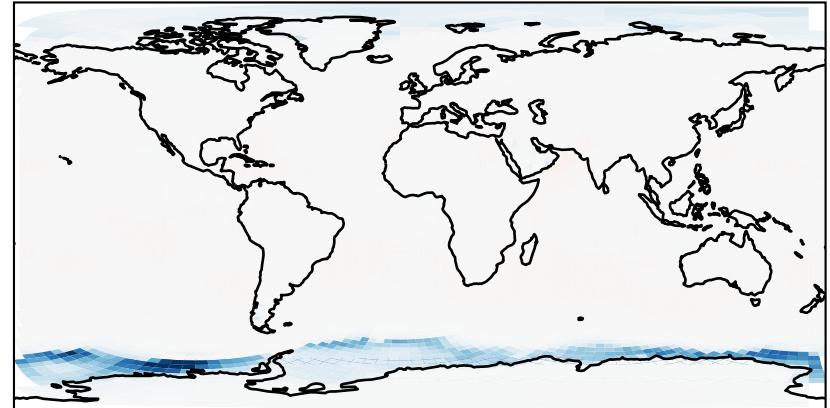


# SpeciesConc\_SALC

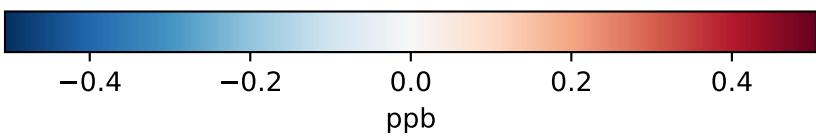
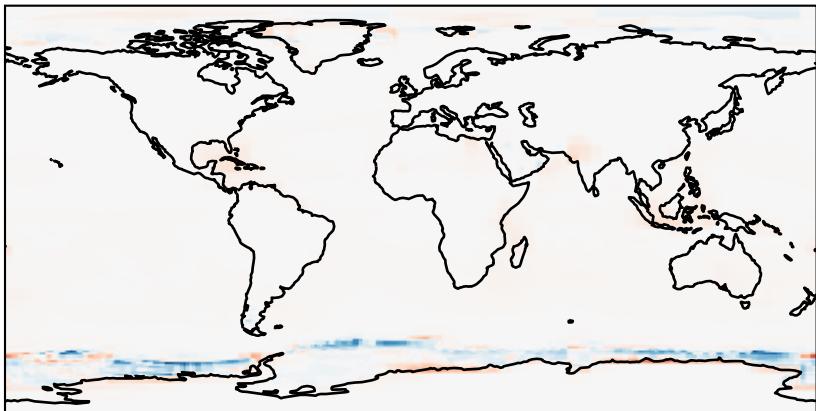
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



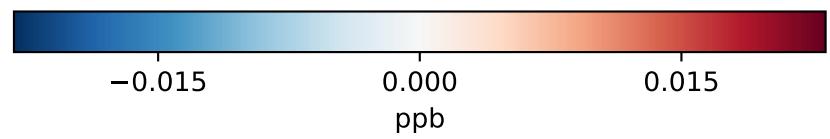
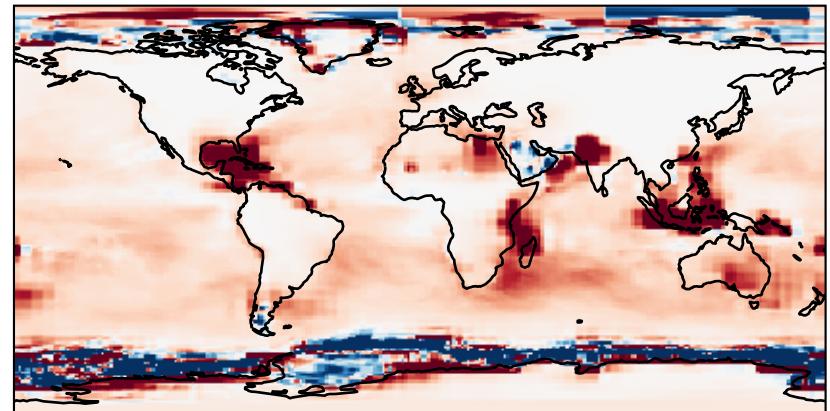
GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48



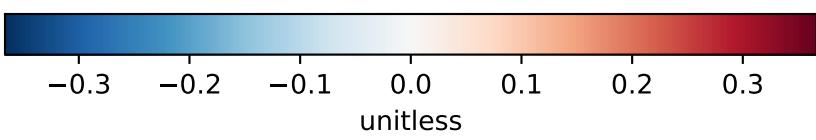
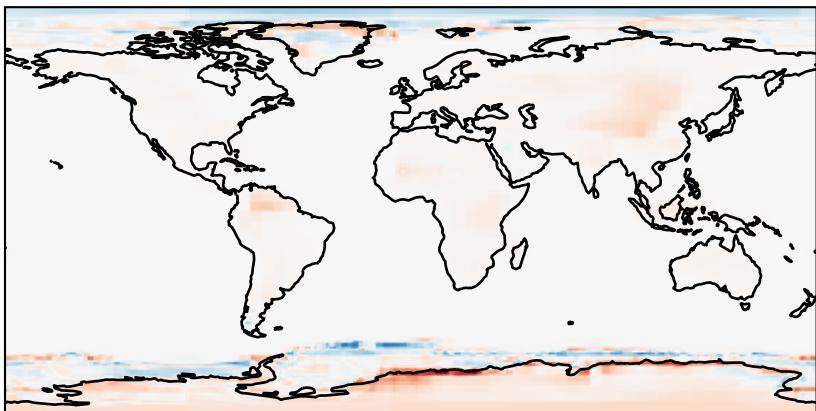
Difference (1x1.25)  
Dev - Ref, Dynamic Range



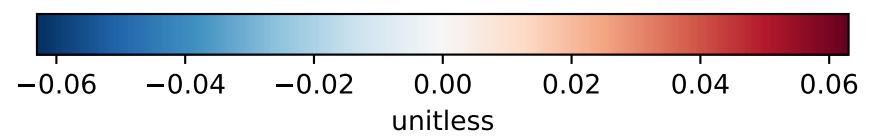
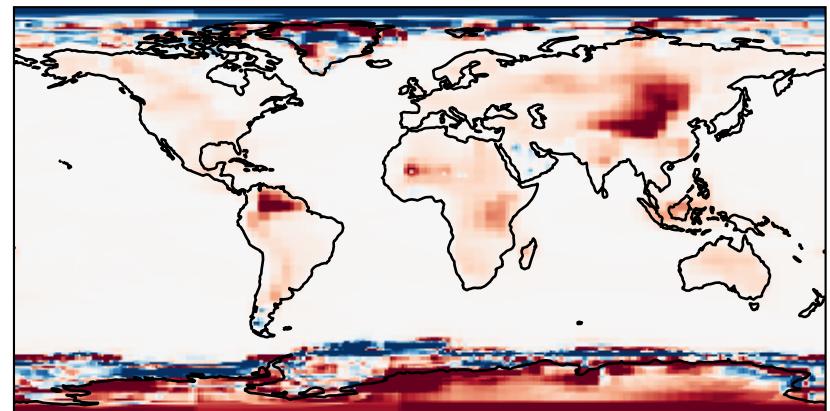
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

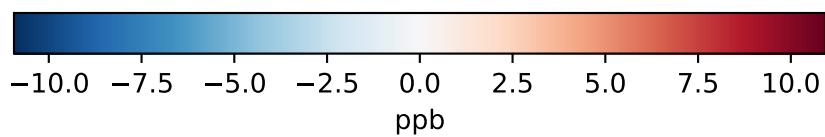
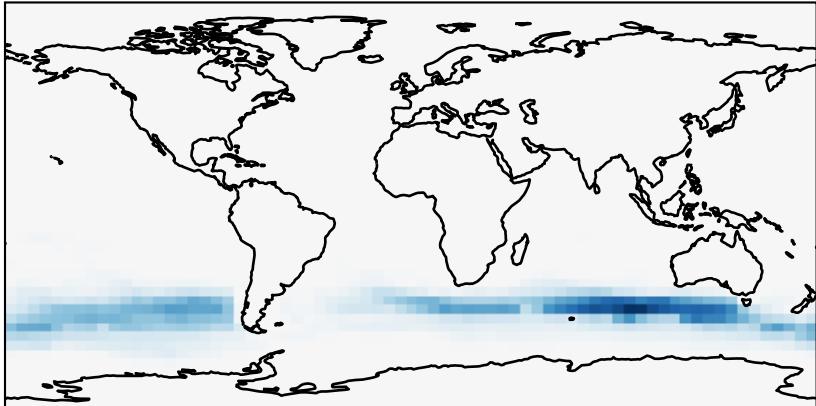


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

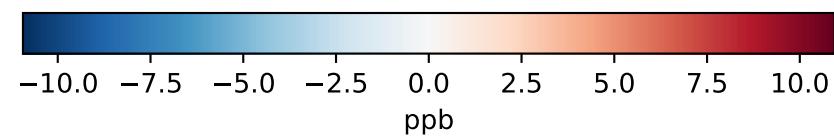
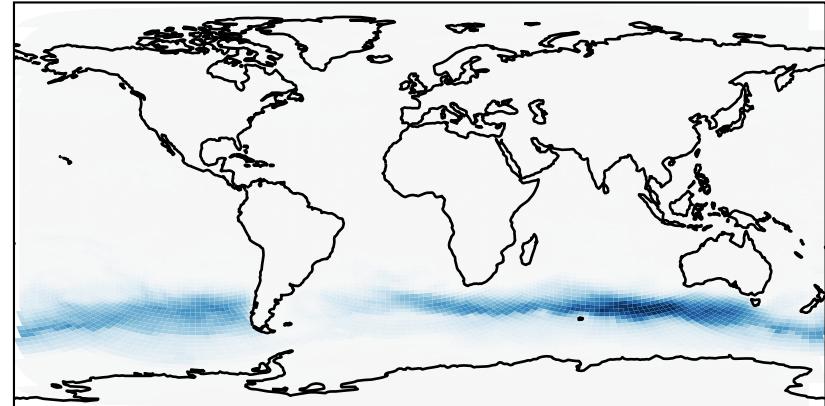


# SpeciesConc\_SALCAL

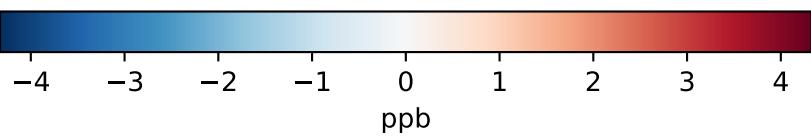
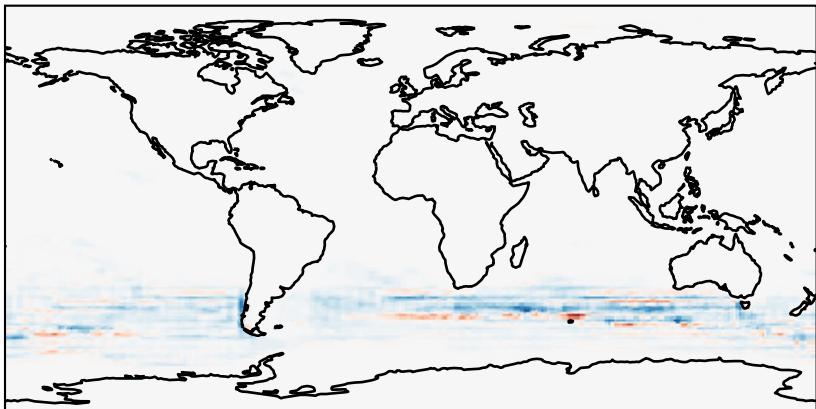
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



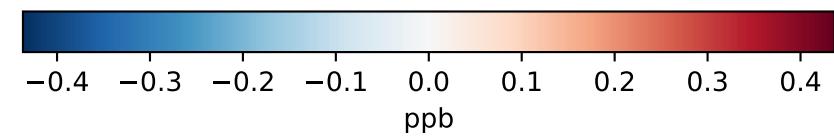
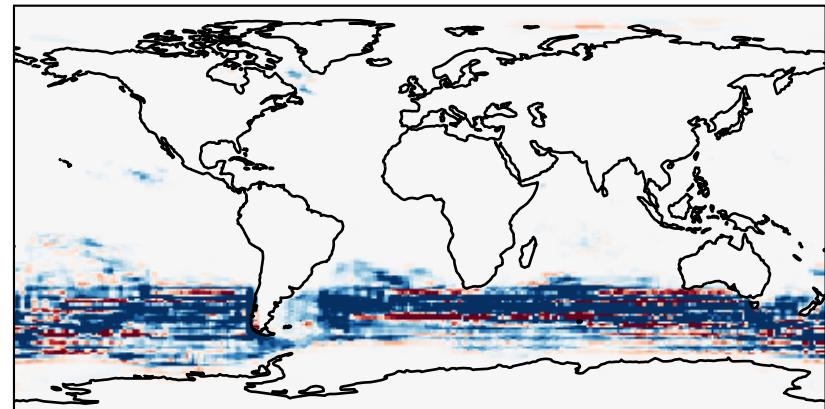
GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48



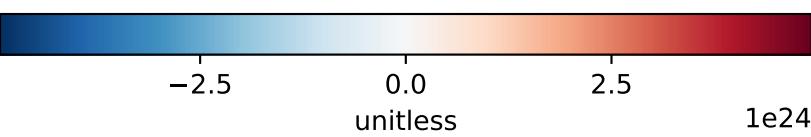
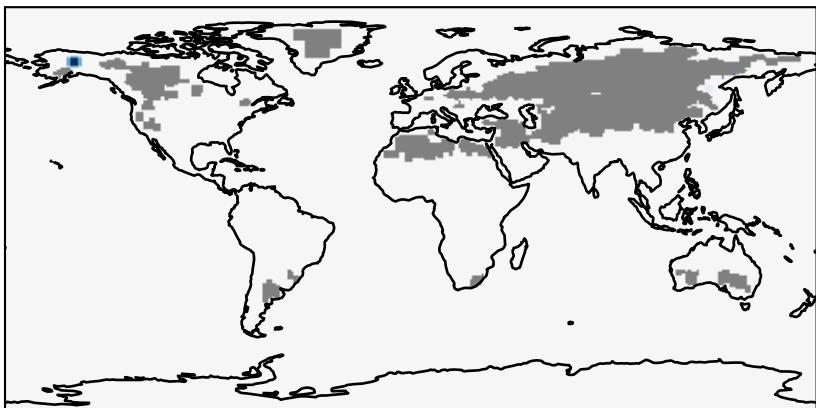
Difference (1x1.25)  
Dev - Ref, Dynamic Range



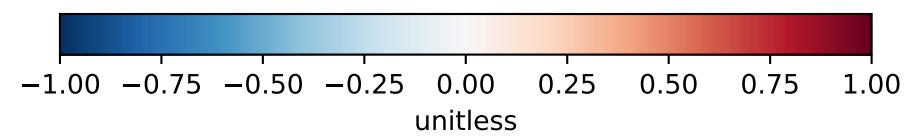
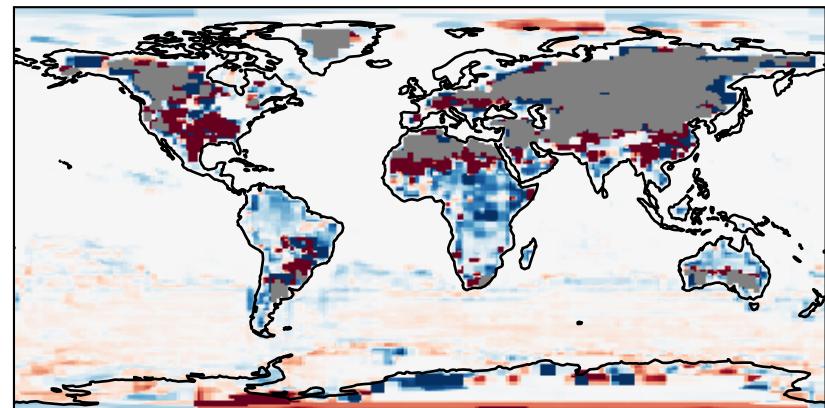
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

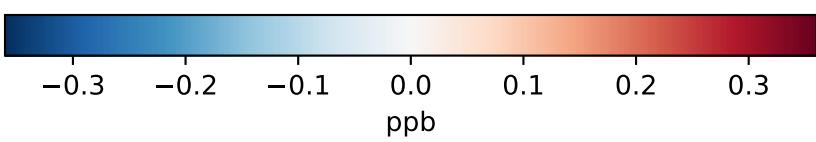
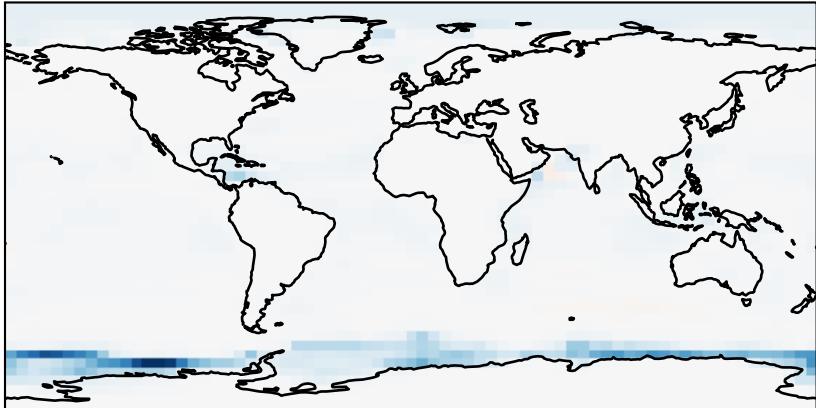


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

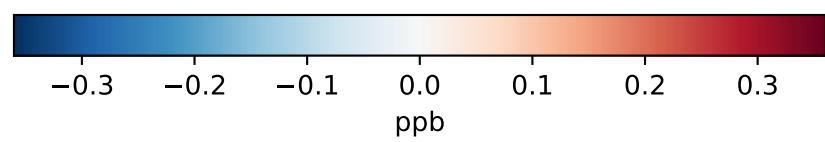


# SpeciesConc\_SALCCL

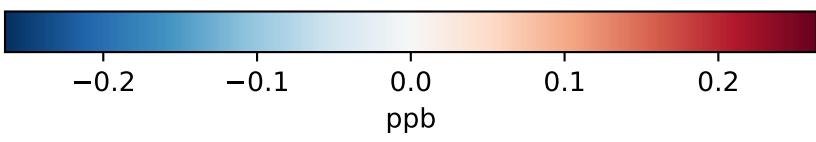
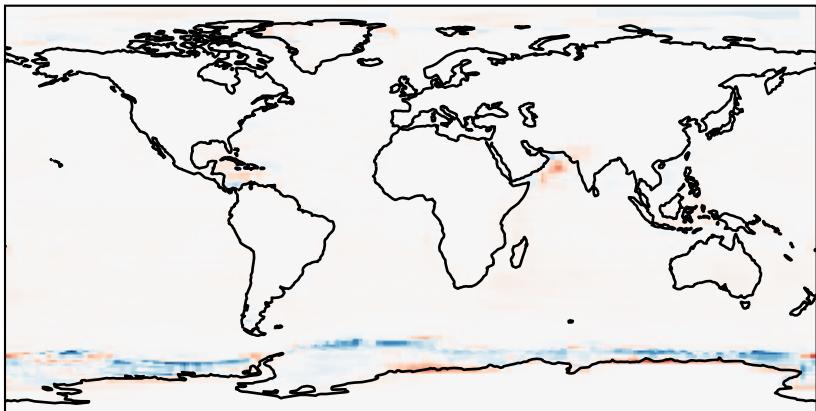
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



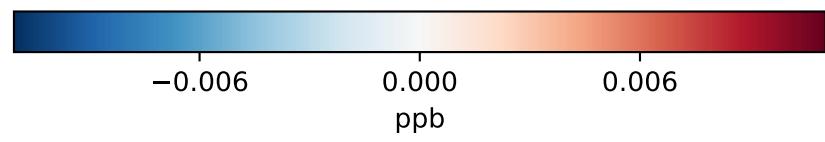
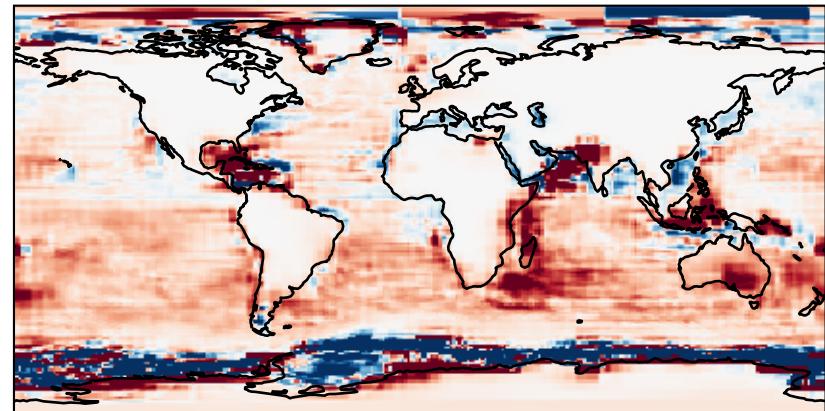
GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48



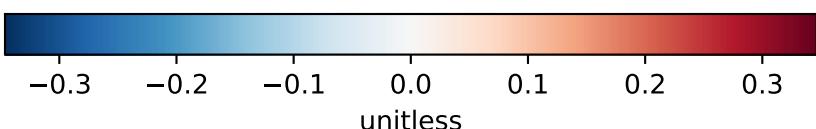
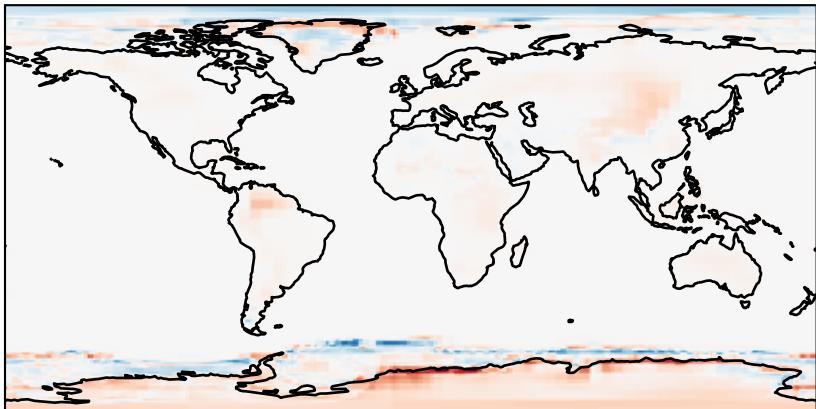
Difference (1x1.25)  
Dev - Ref, Dynamic Range



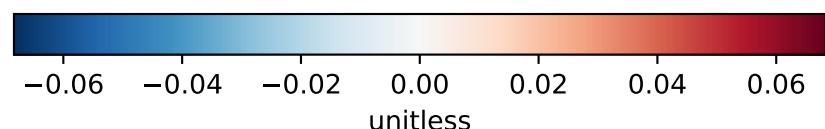
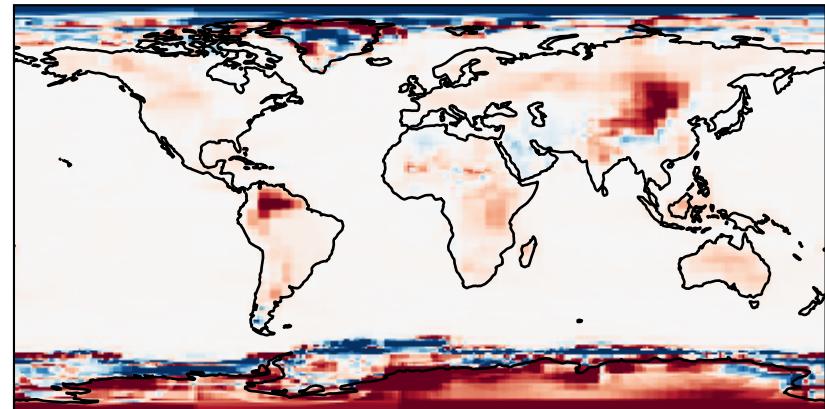
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

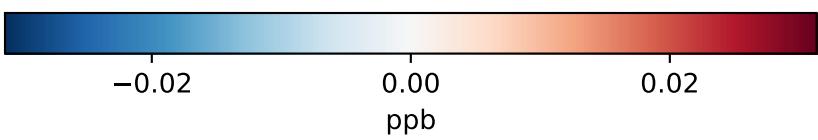
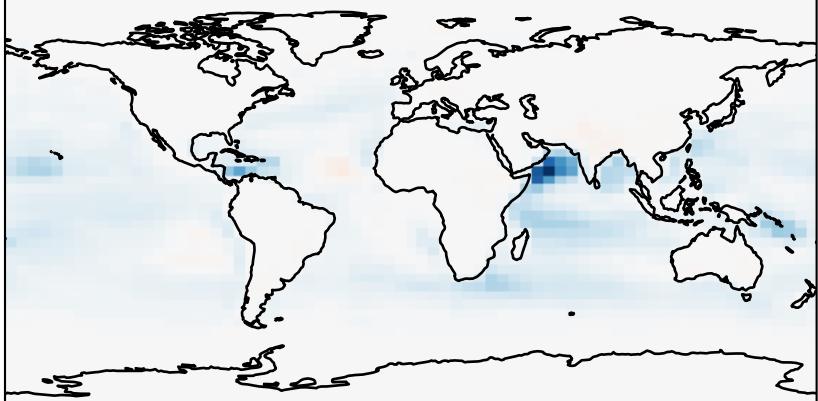


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

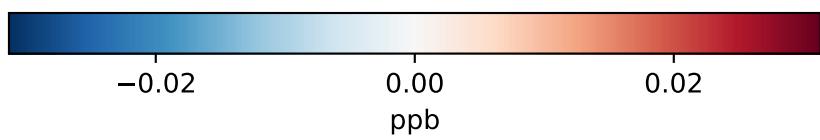
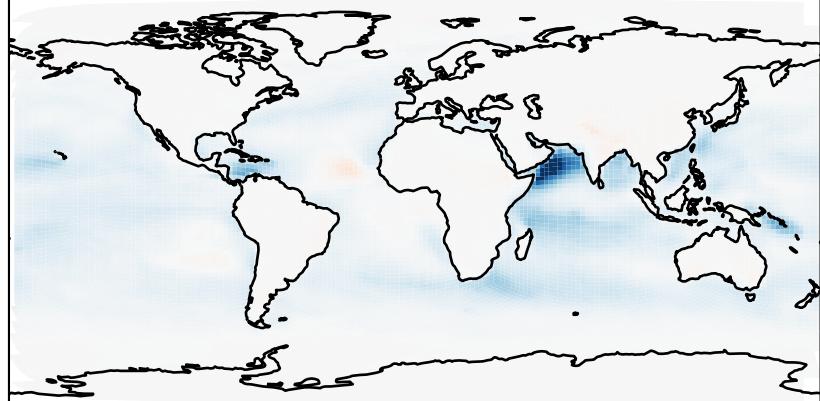


# SpeciesConc\_SO4s

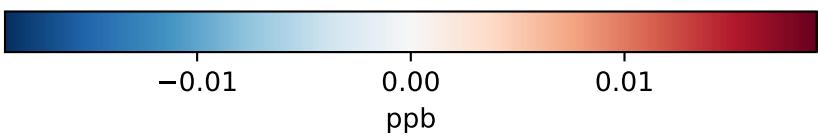
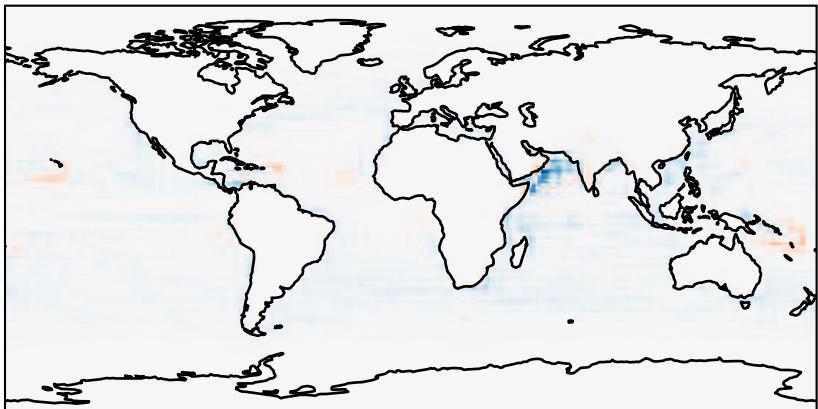
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



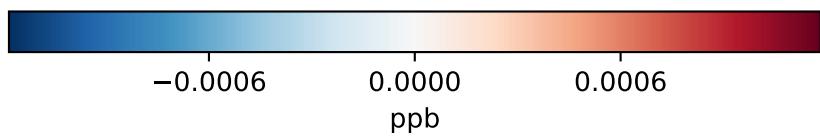
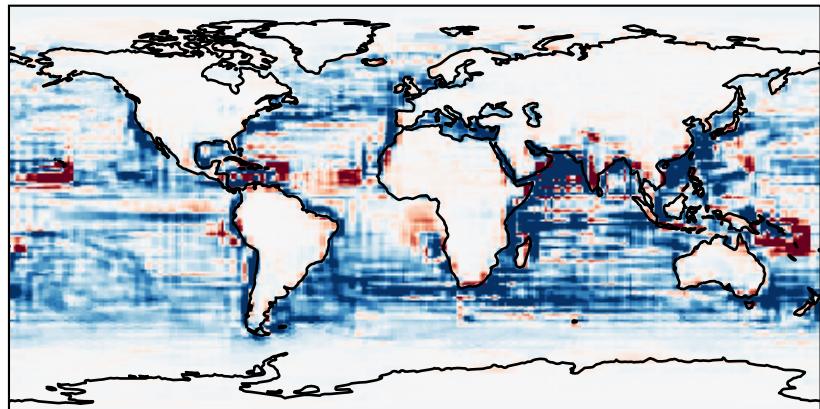
GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48



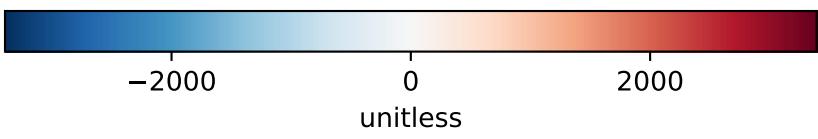
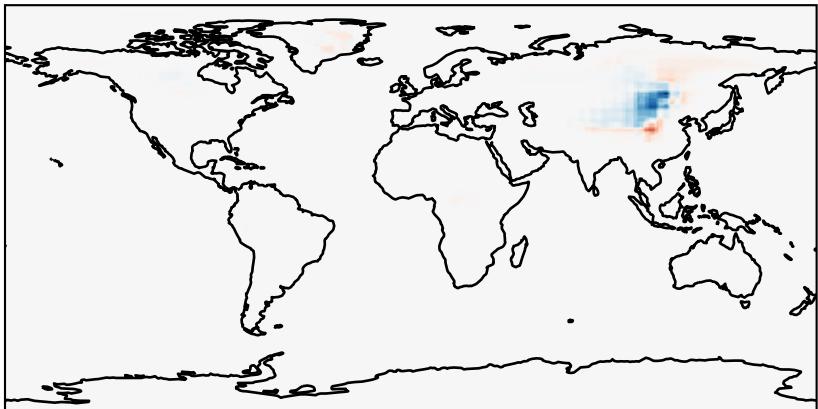
Difference (1x1.25)  
Dev - Ref, Dynamic Range



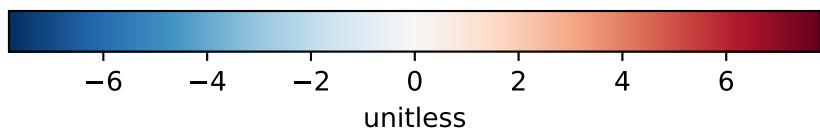
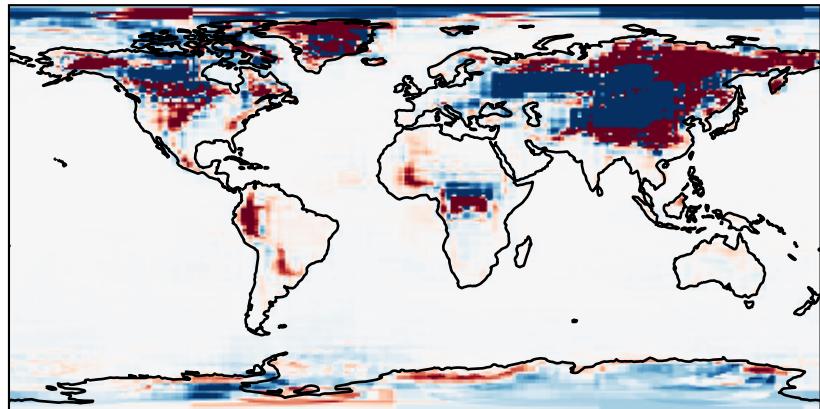
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

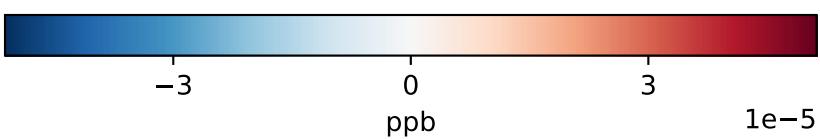
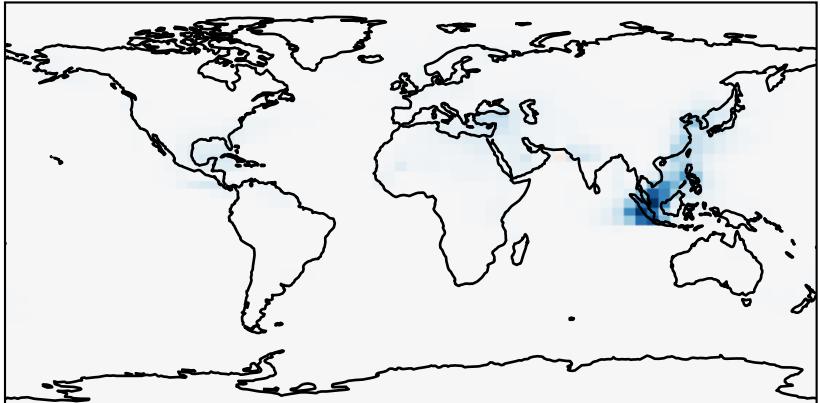


Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

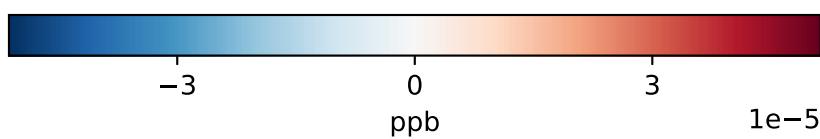
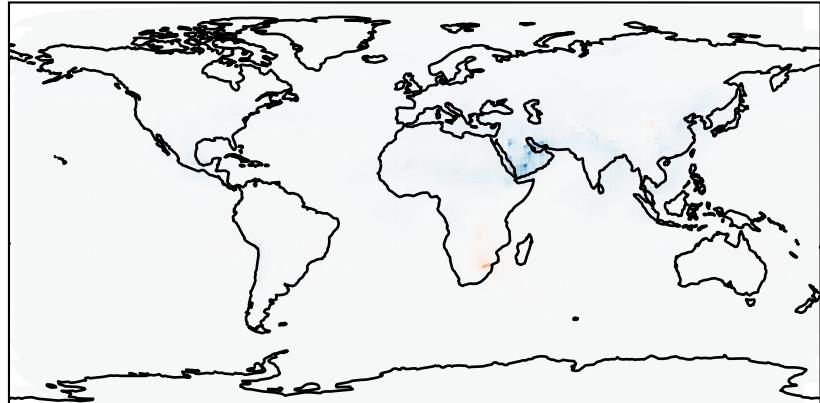


# SpeciesConc\_pFe

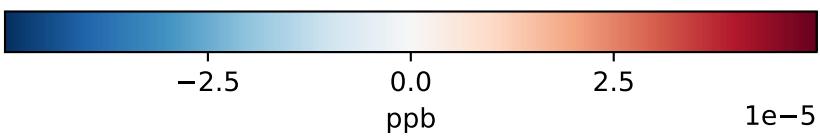
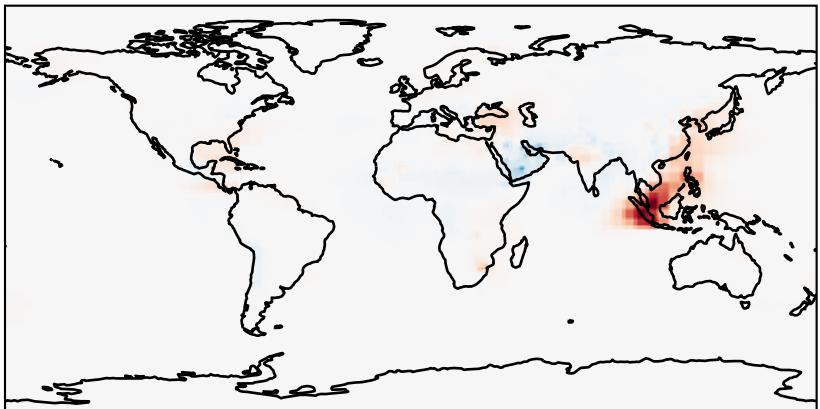
GCC\_13.4.0-rc.0 - GCC\_13.3.0-rc.1 (Ref)  
4.0x5.0



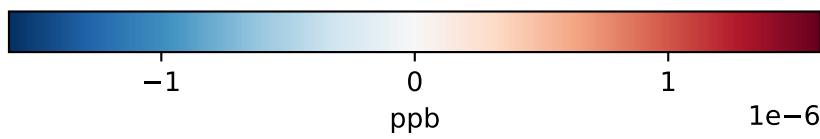
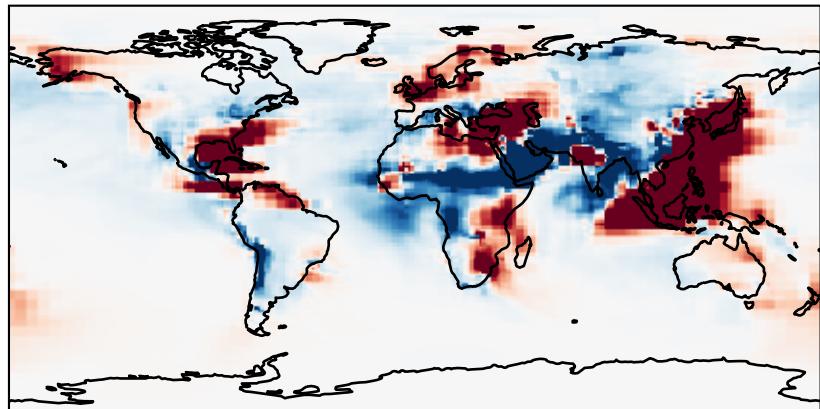
GCHP\_13.4.0-rc.2 - GCHP\_13.3.0-rc.1 (Dev)  
c48



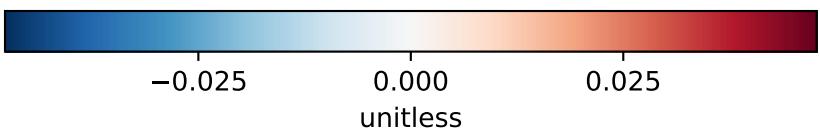
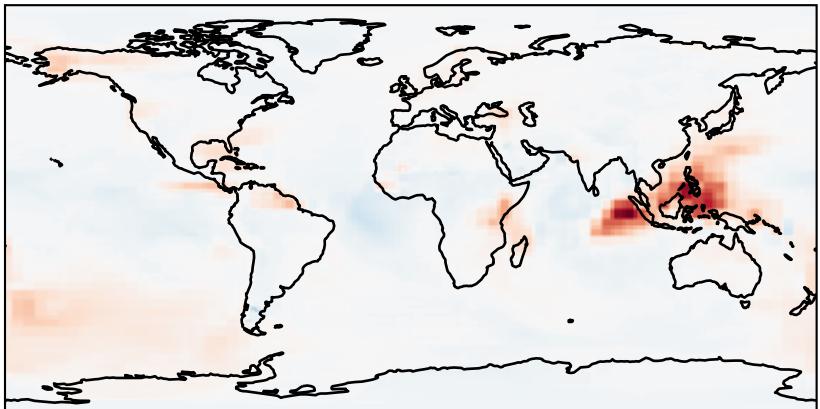
Difference (1x1.25)  
Dev - Ref, Dynamic Range



Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Difference (1x1.25), Dynamic Range  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref



Difference (1x1.25), Restricted Range [5%,95%]  
GCHP\_dev / GCHP\_ref - GCC\_dev / GCC\_ref

