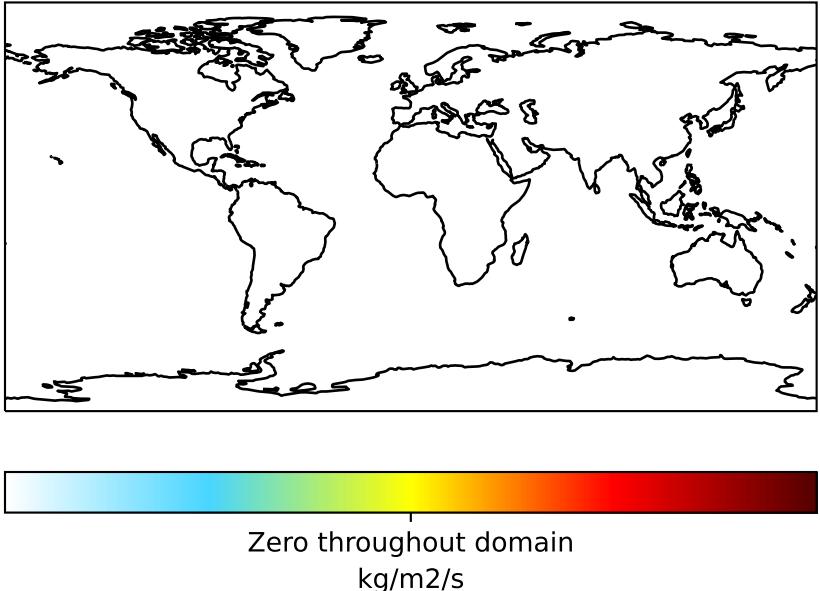
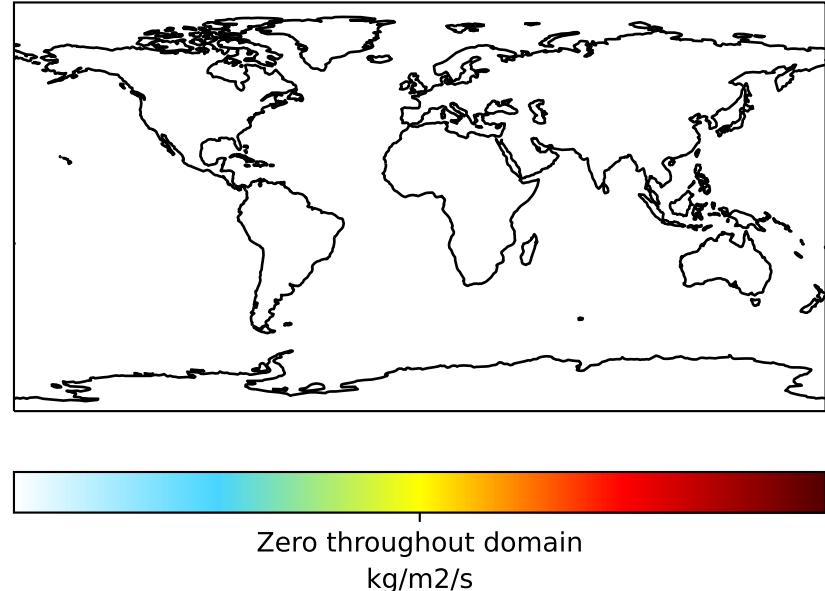


EmisALD2_Ship

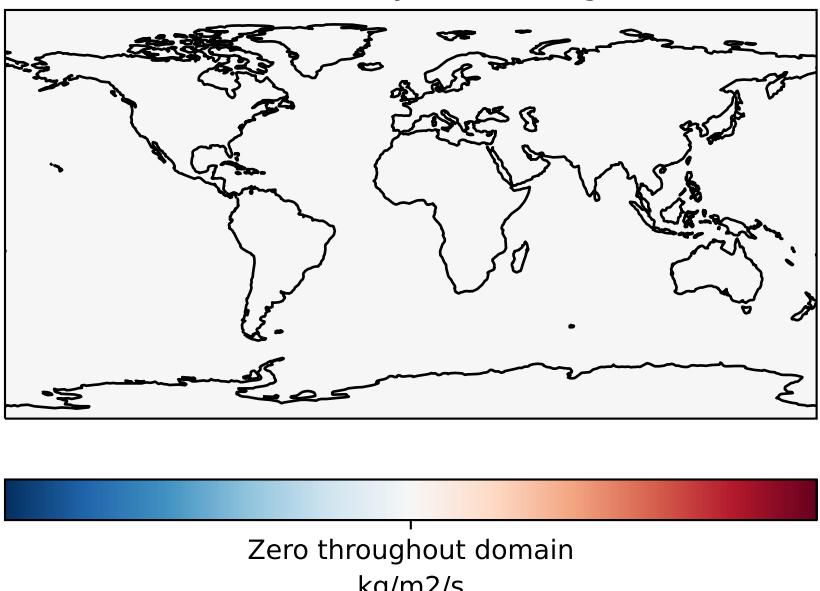
GCC_13.3.0-rc.1 (Ref)
4.0x5.0



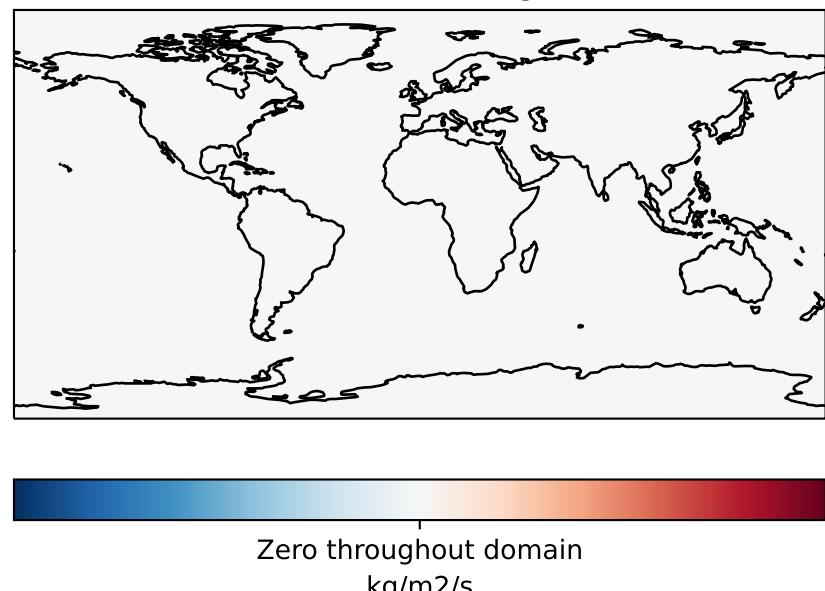
GCC_13.4.0-rc.4 (Dev)
4.0x5.0



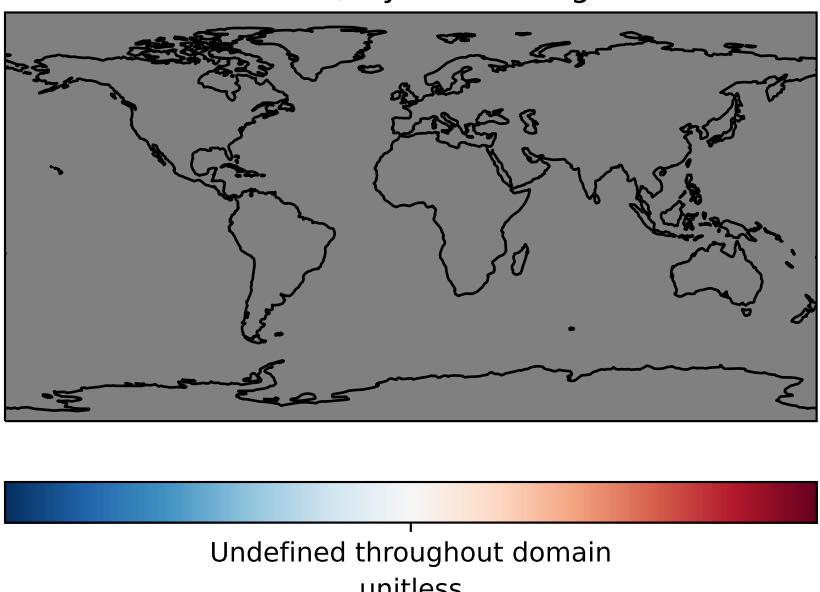
Difference
Dev - Ref, Dynamic Range



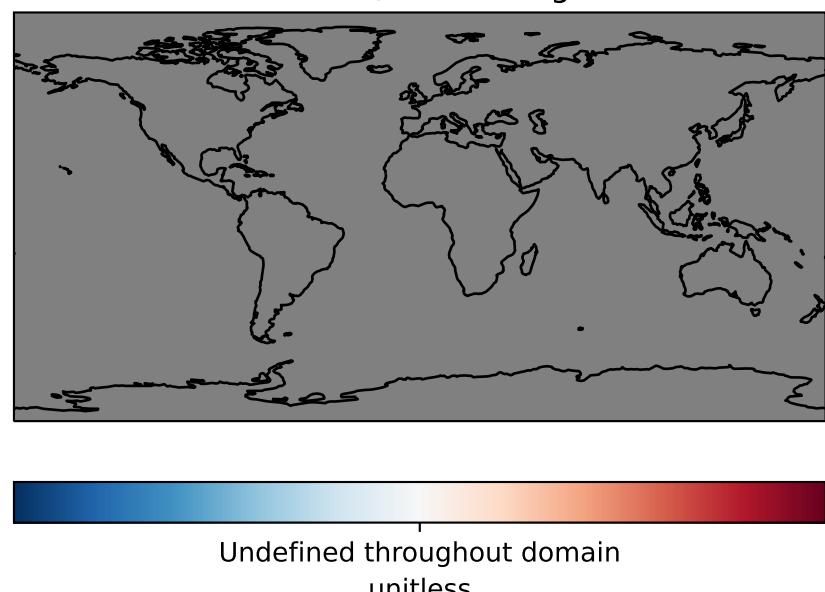
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

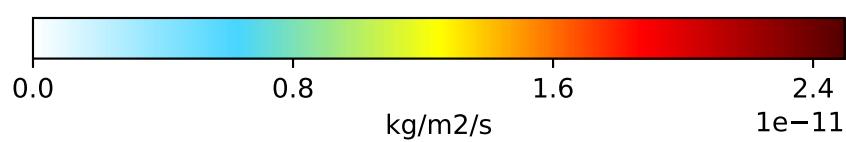
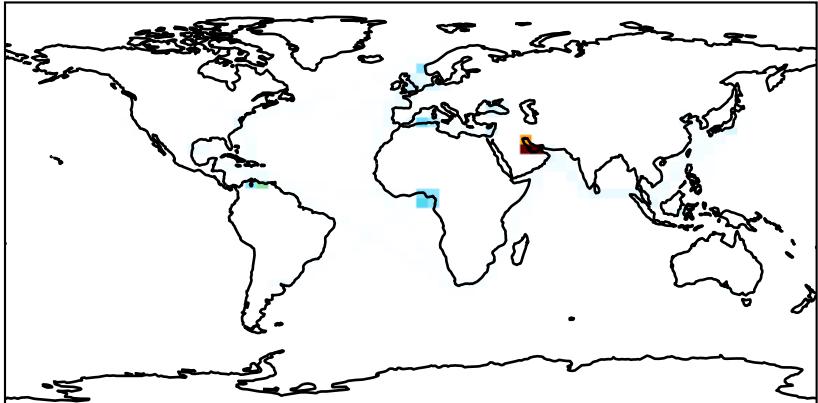


Ratio
Dev/Ref, Fixed Range

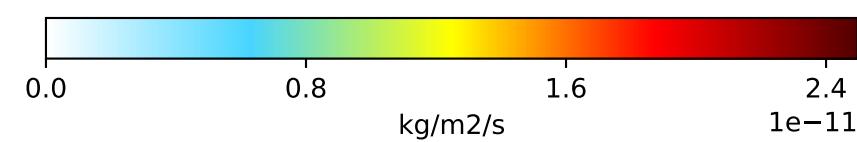
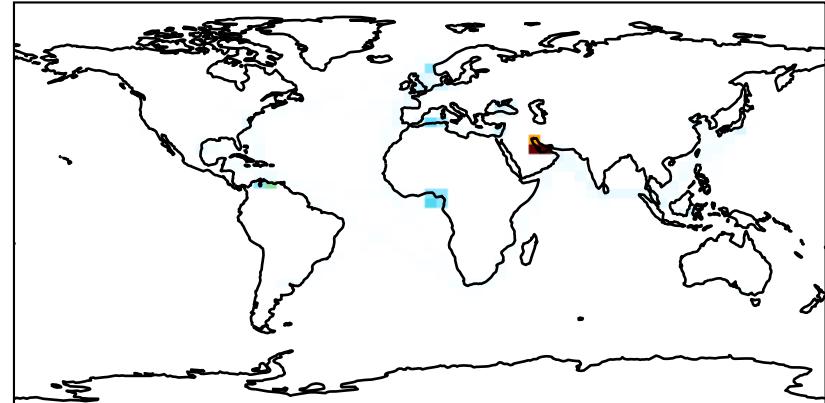


EmisALK4_Ship

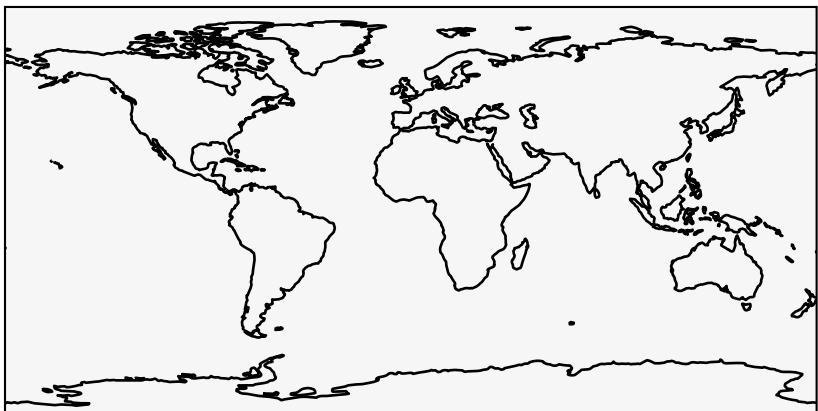
GCC_13.3.0-rc.1 (Ref)
4.0x5.0



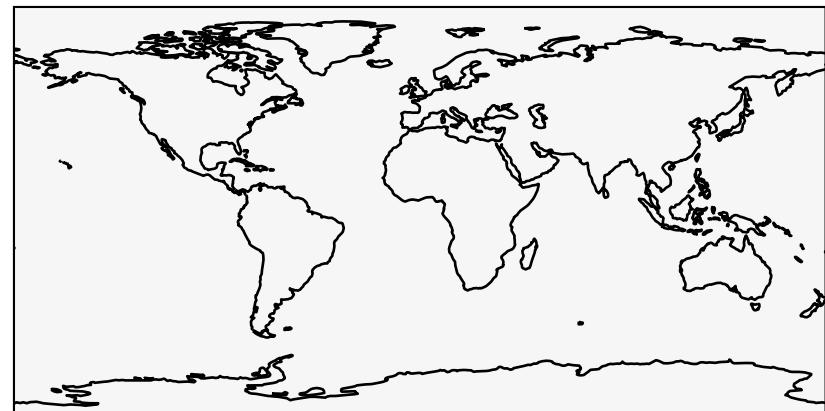
GCC_13.4.0-rc.4 (Dev)
4.0x5.0



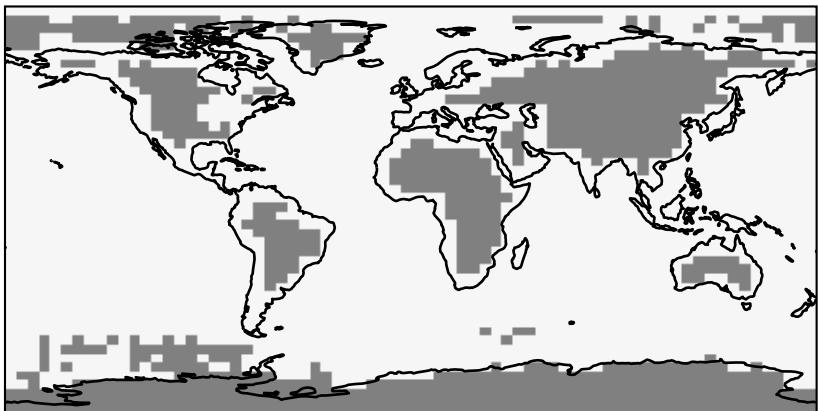
Difference
Dev - Ref, Dynamic Range



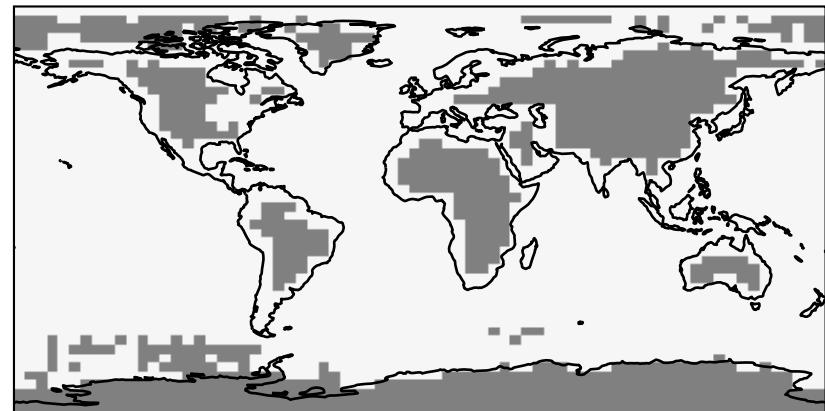
Difference
Dev - Ref, Restricted Range [5%, 95%]



Ratio
Dev/Ref, Dynamic Range

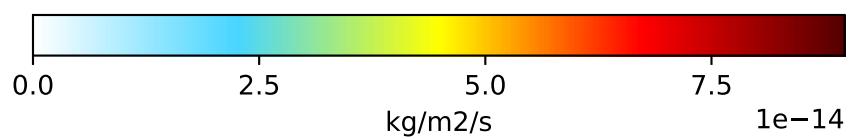
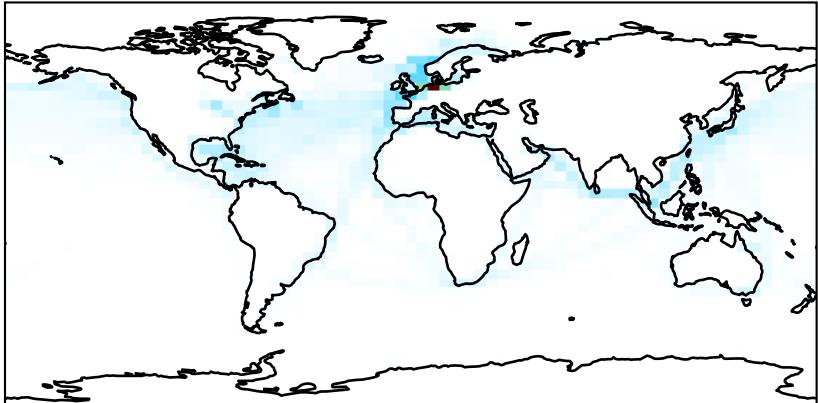


Ratio
Dev/Ref, Fixed Range

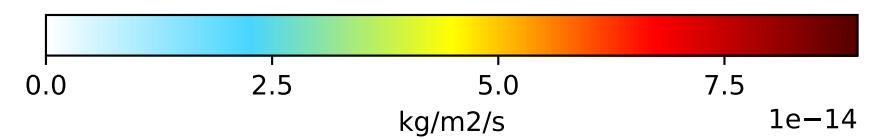
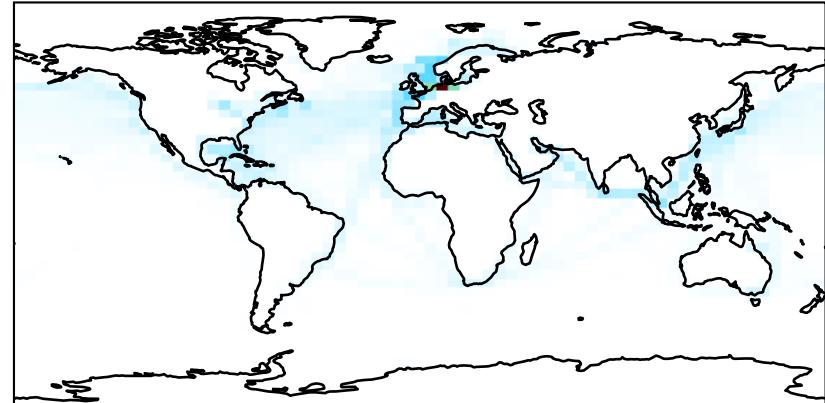


EmisBCPI_Ship

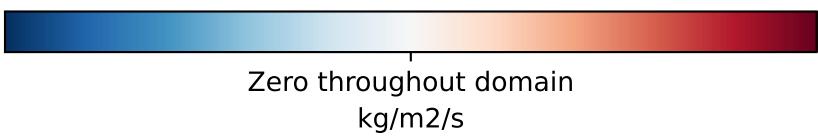
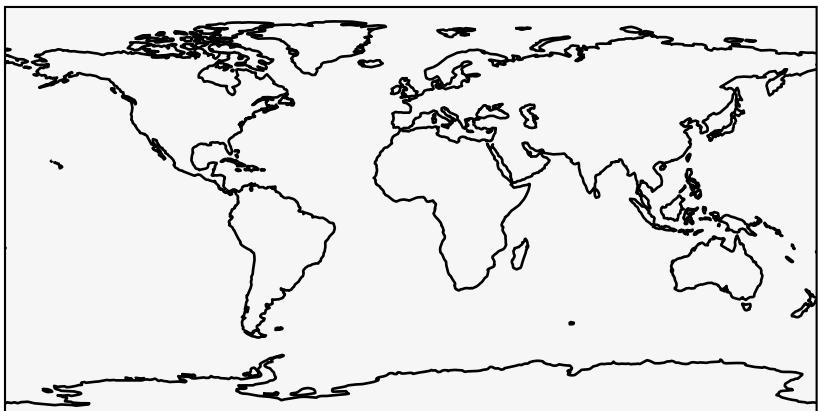
GCC_13.3.0-rc.1 (Ref)
4.0x5.0



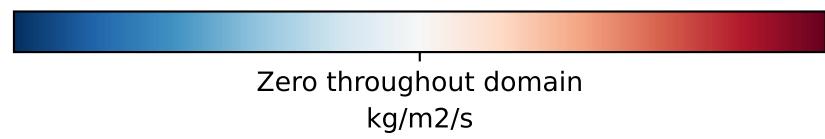
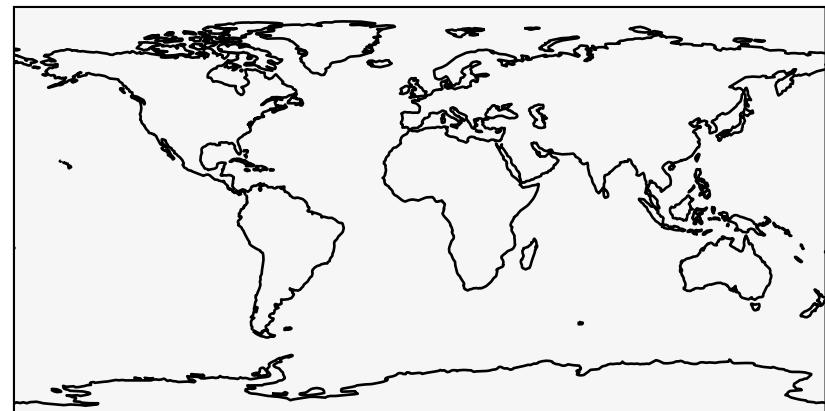
GCC_13.4.0-rc.4 (Dev)
4.0x5.0



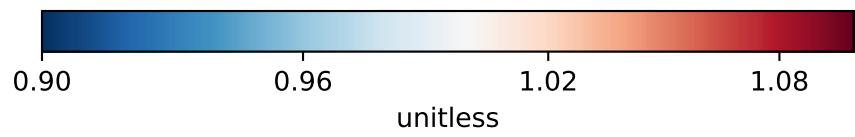
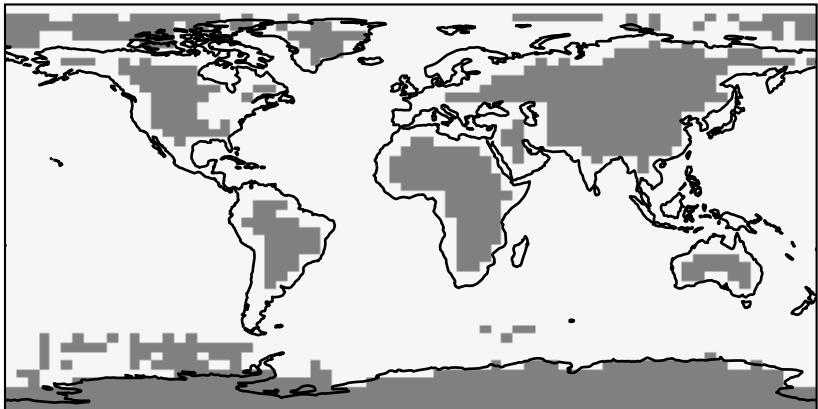
Difference
Dev - Ref, Dynamic Range



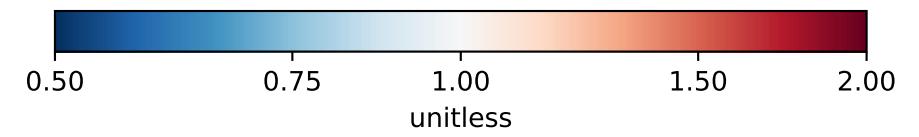
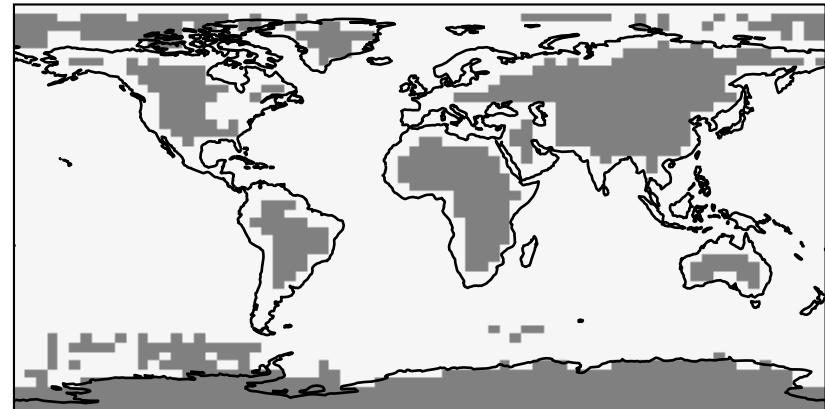
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

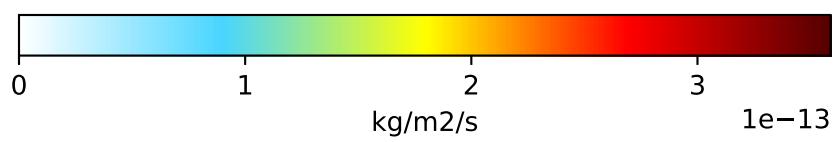
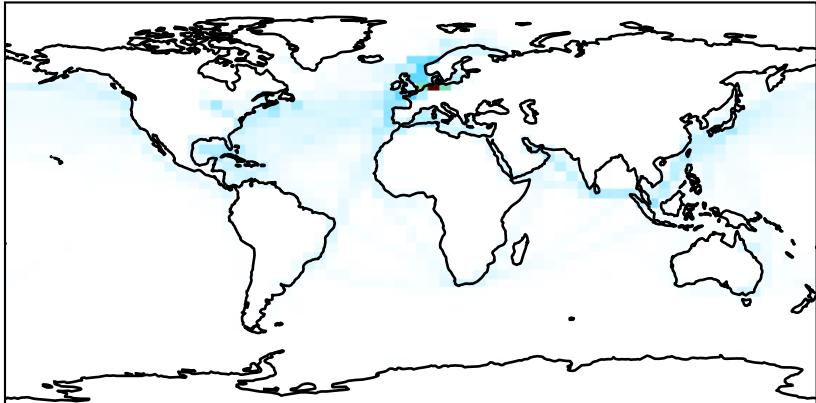


Ratio
Dev/Ref, Fixed Range

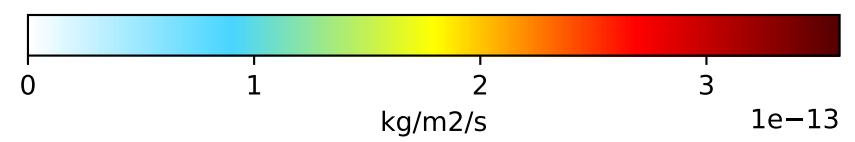
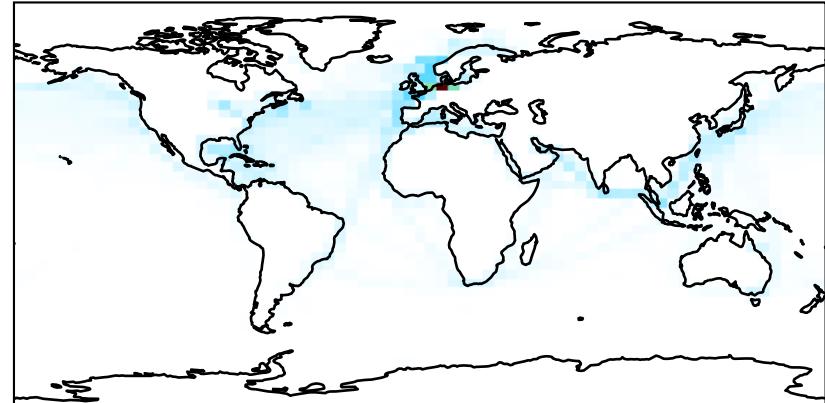


EmisBCPO_Ship

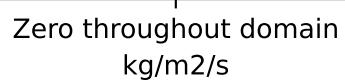
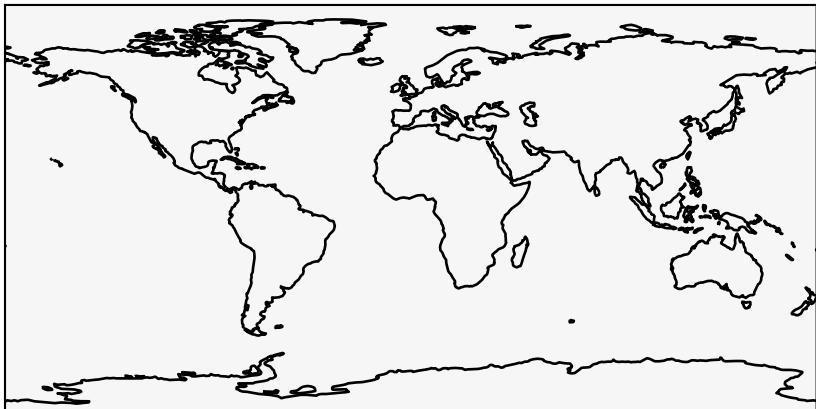
GCC_13.3.0-rc.1 (Ref)
4.0x5.0



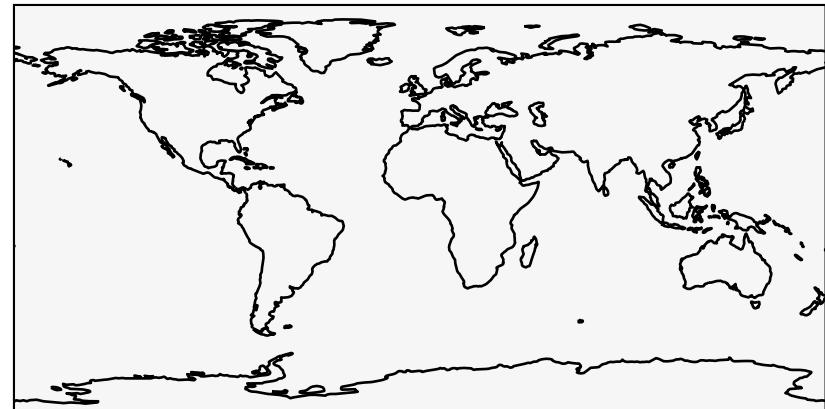
GCC_13.4.0-rc.4 (Dev)
4.0x5.0



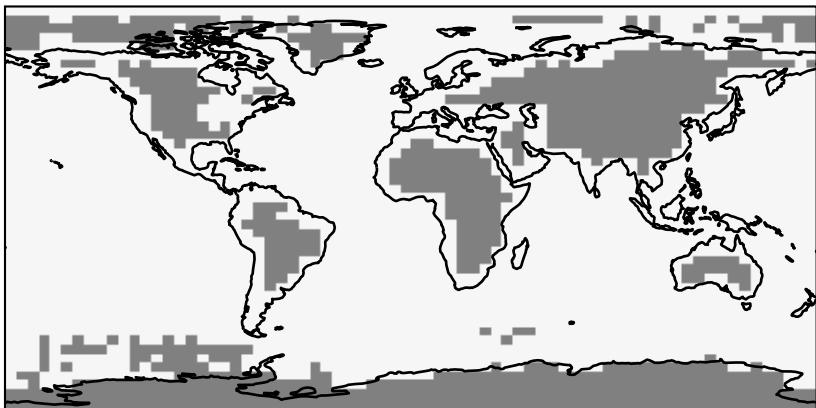
Difference
Dev - Ref, Dynamic Range



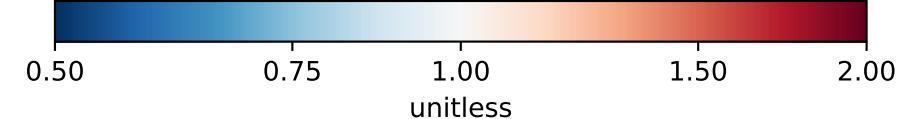
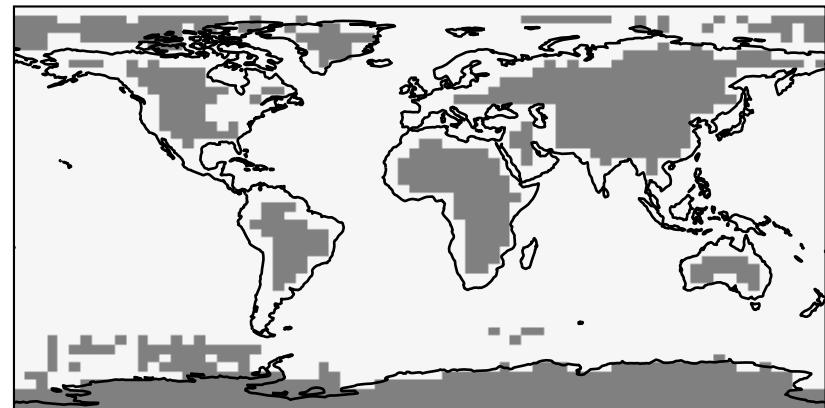
Difference
Dev - Ref, Restricted Range [5%, 95%]



Ratio
Dev/Ref, Dynamic Range

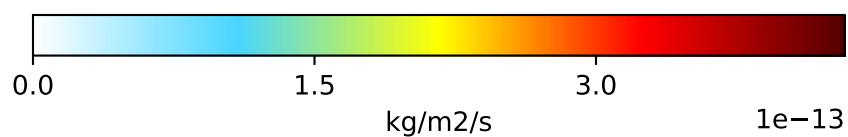
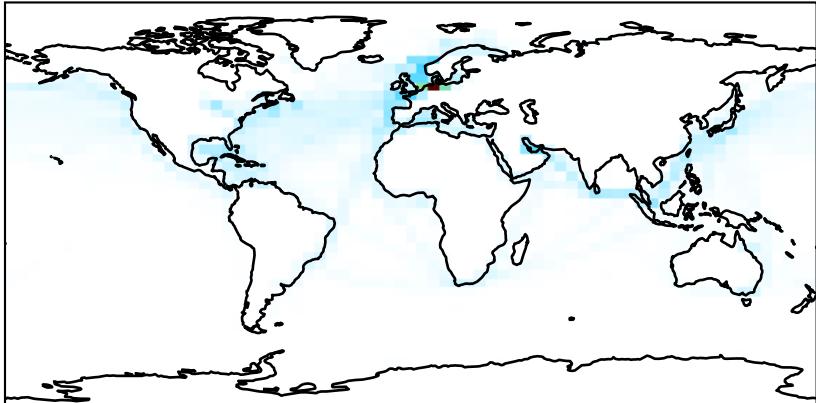


Ratio
Dev/Ref, Fixed Range

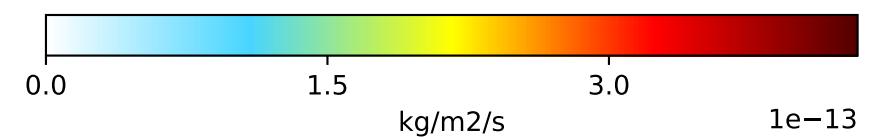
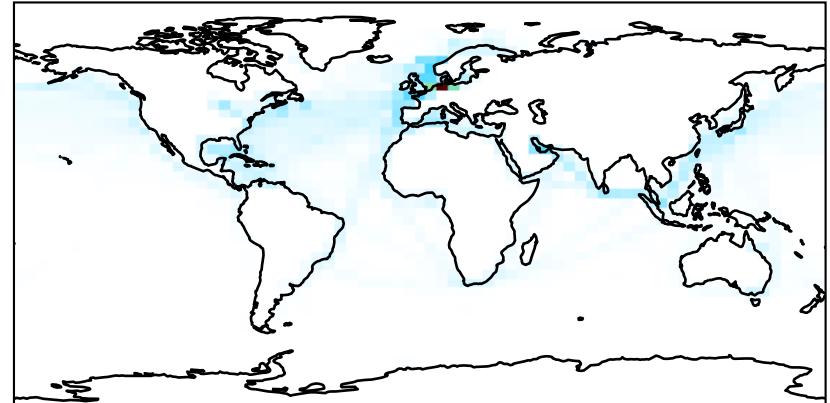


EmisBENZ_Ship

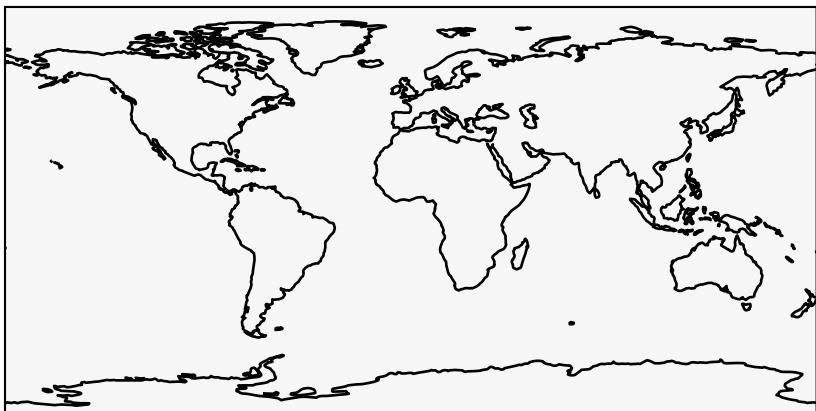
GCC_13.3.0-rc.1 (Ref)
4.0x5.0



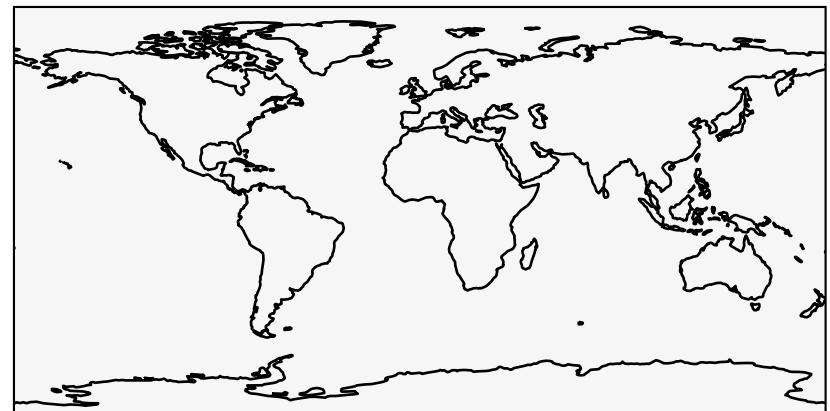
GCC_13.4.0-rc.4 (Dev)
4.0x5.0



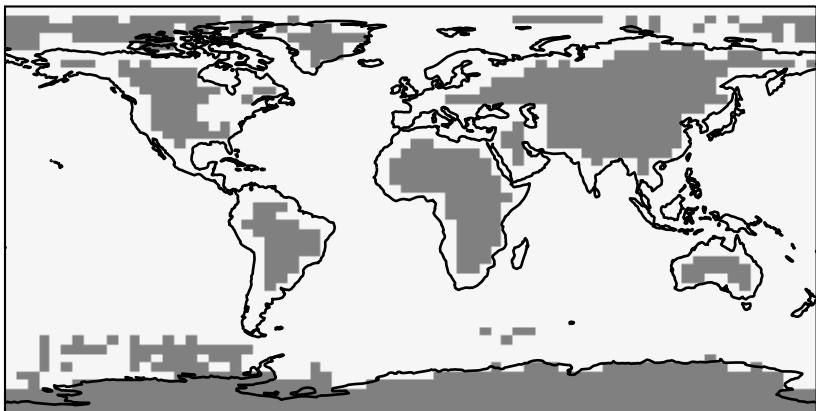
Difference
Dev - Ref, Dynamic Range



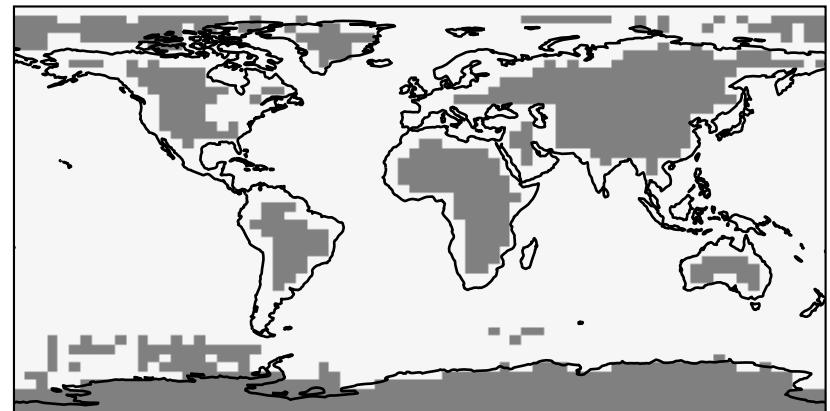
Difference
Dev - Ref, Restricted Range [5%, 95%]



Ratio
Dev/Ref, Dynamic Range

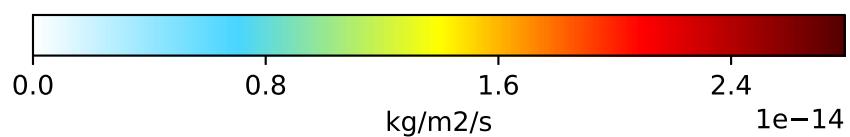
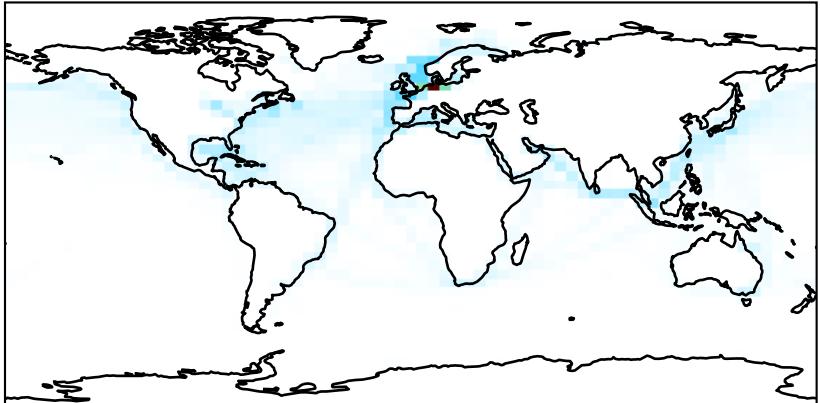


Ratio
Dev/Ref, Fixed Range

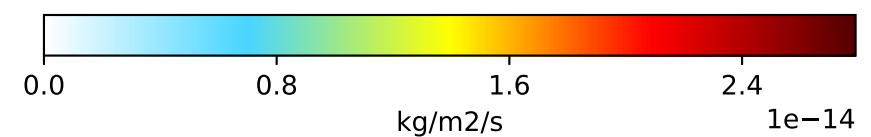
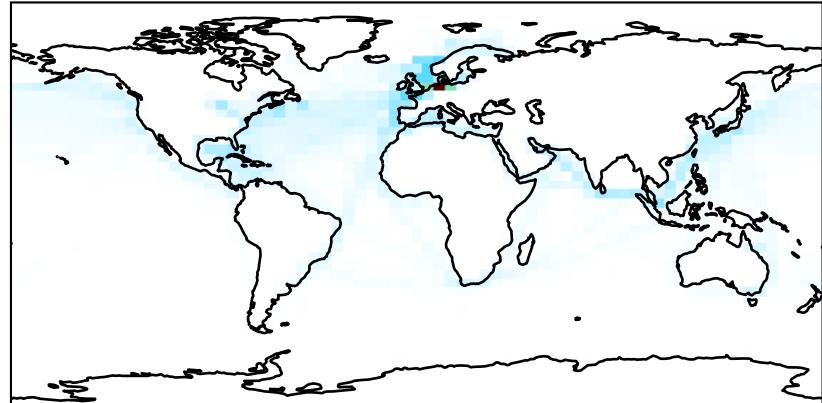


EmisC2H2_Ship

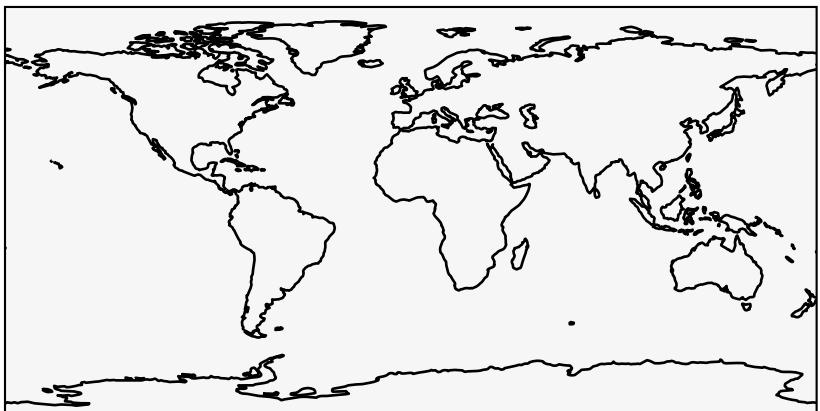
GCC_13.3.0-rc.1 (Ref)
4.0x5.0



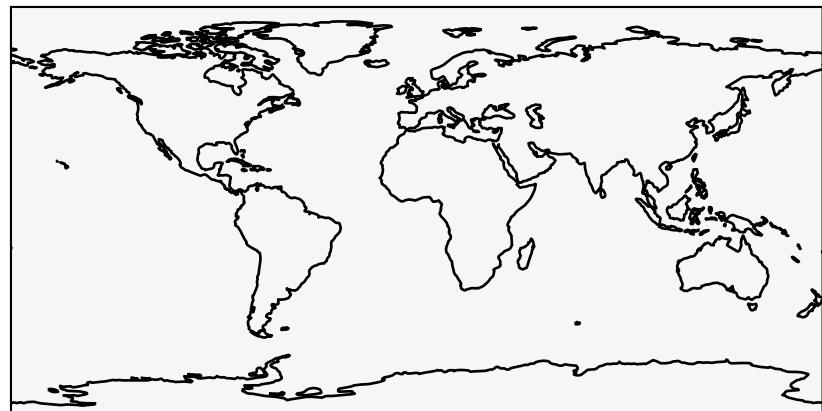
GCC_13.4.0-rc.4 (Dev)
4.0x5.0



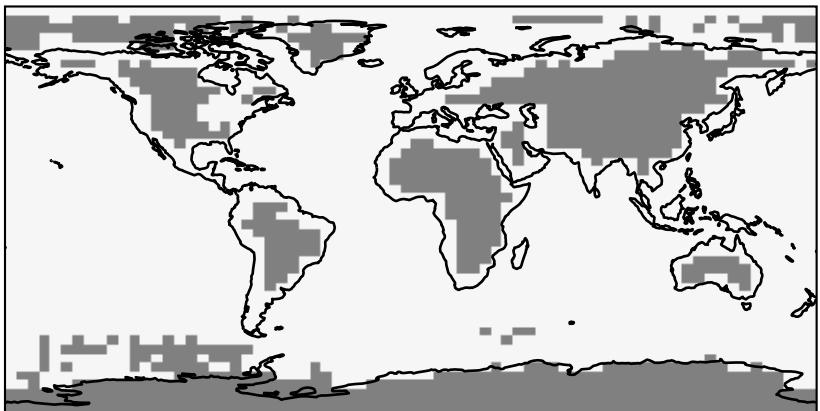
Difference
Dev - Ref, Dynamic Range



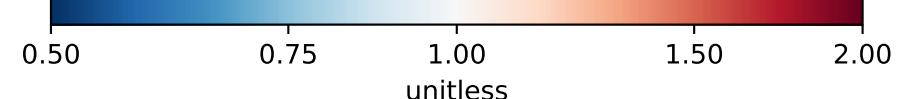
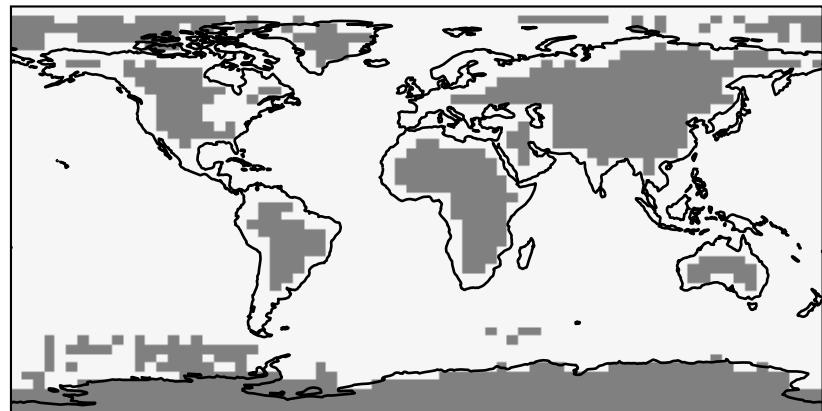
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

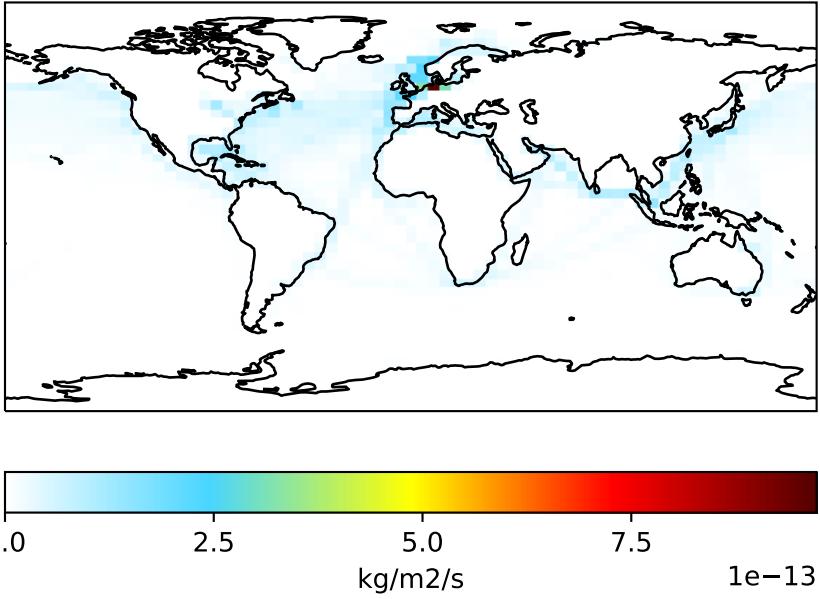


Ratio
Dev/Ref, Fixed Range

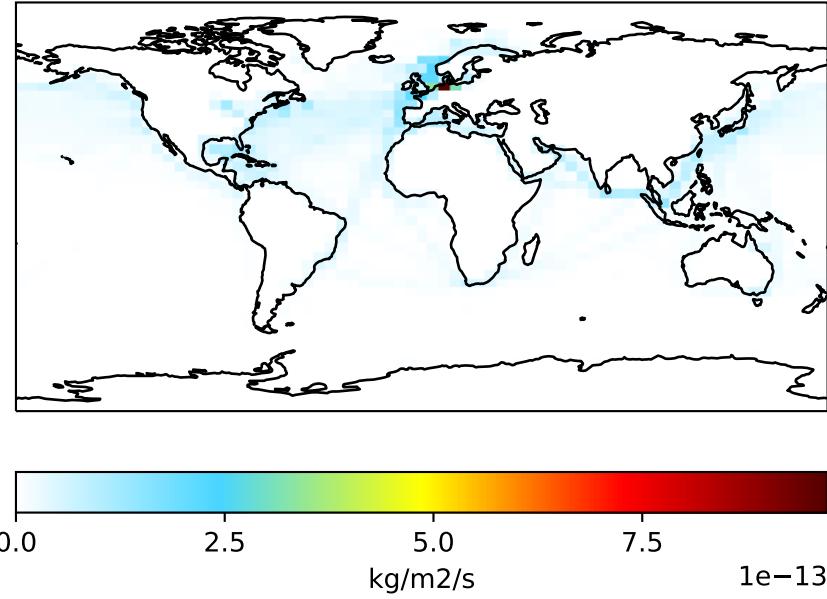


EmisC2H4_Ship

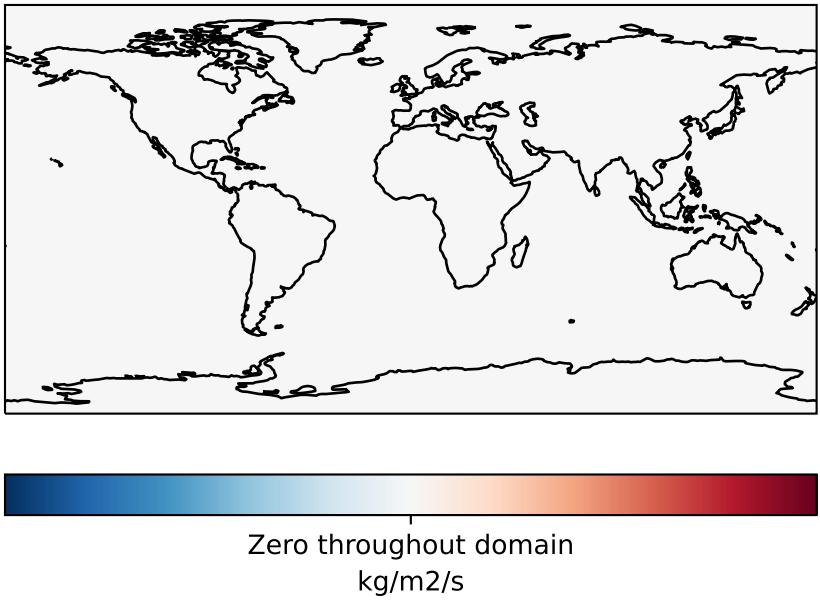
GCC_13.3.0-rc.1 (Ref)
4.0x5.0



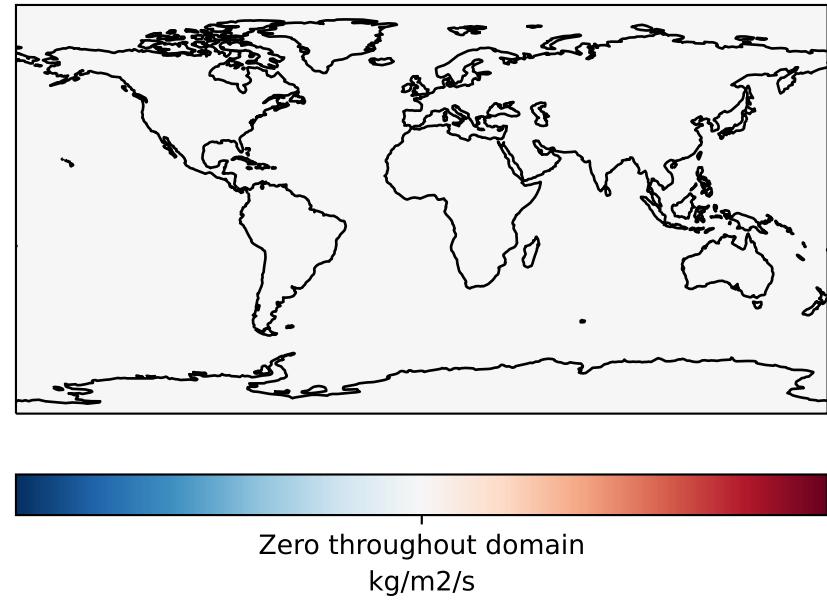
GCC_13.4.0-rc.4 (Dev)
4.0x5.0



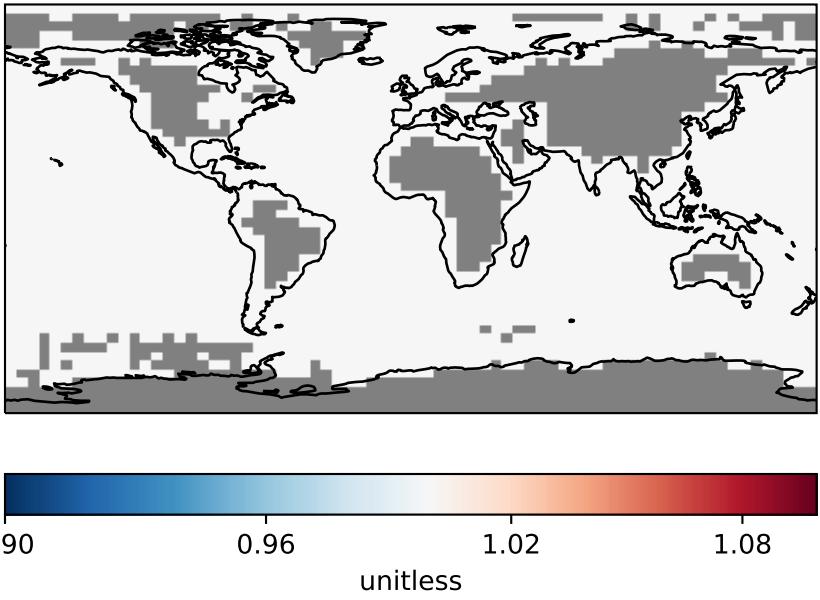
Difference
Dev - Ref, Dynamic Range



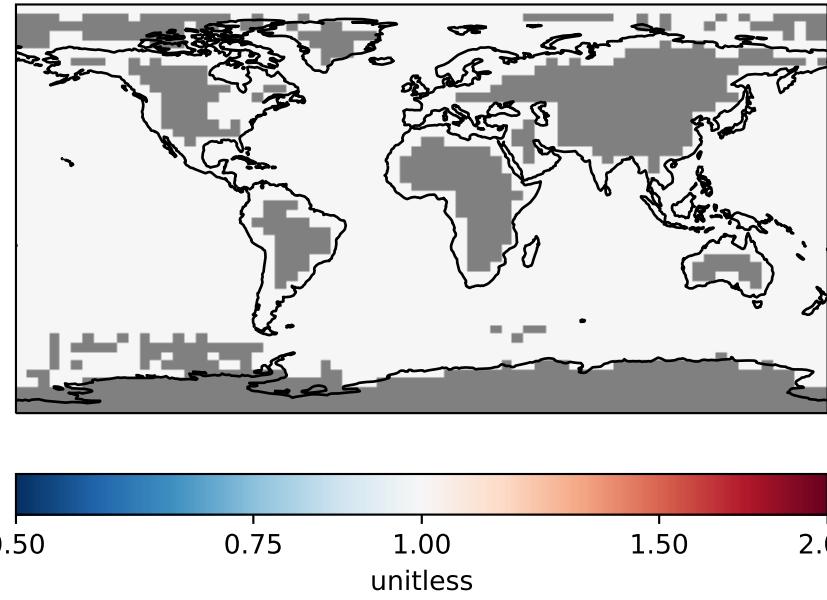
Difference
Dev - Ref, Restricted Range [5%, 95%]



Ratio
Dev/Ref, Dynamic Range

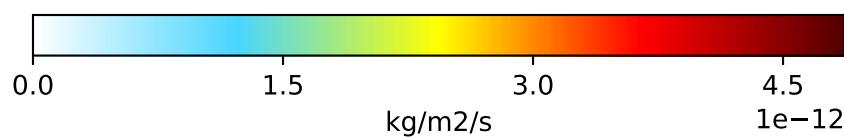
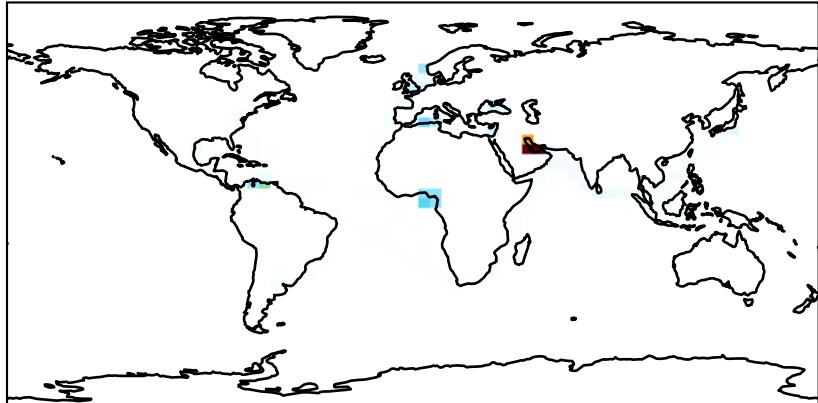


Ratio
Dev/Ref, Fixed Range

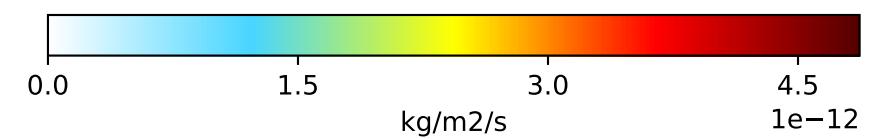
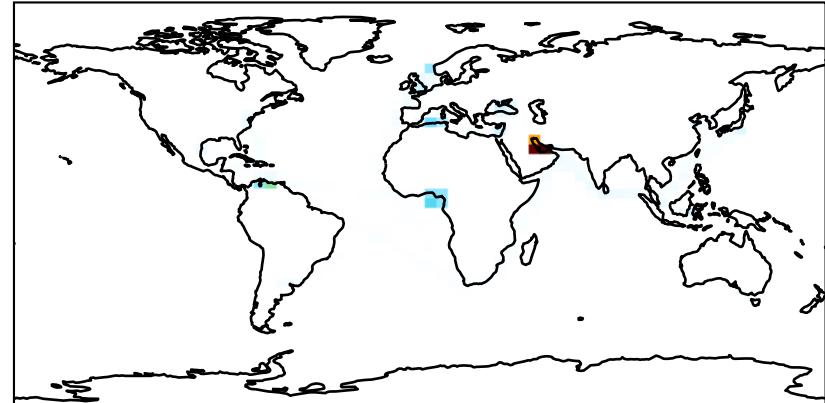


EmisC2H6_Ship

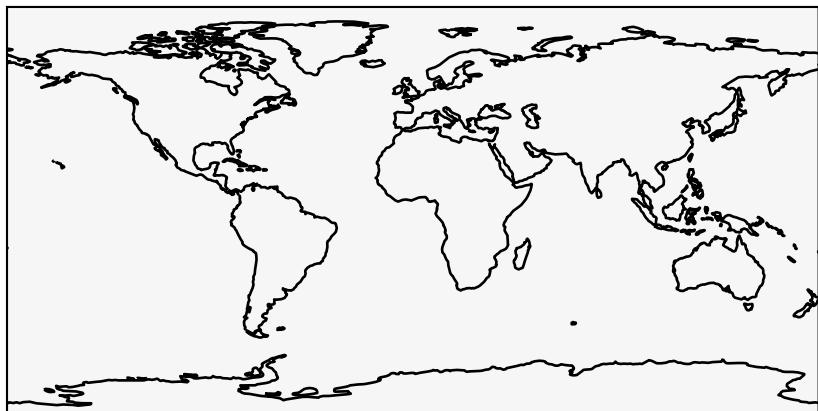
GCC_13.3.0-rc.1 (Ref)
4.0x5.0



GCC_13.4.0-rc.4 (Dev)
4.0x5.0

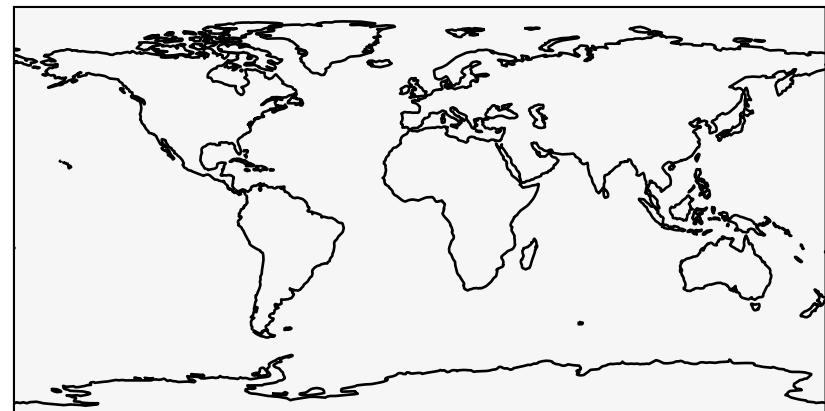


Difference
Dev - Ref, Dynamic Range



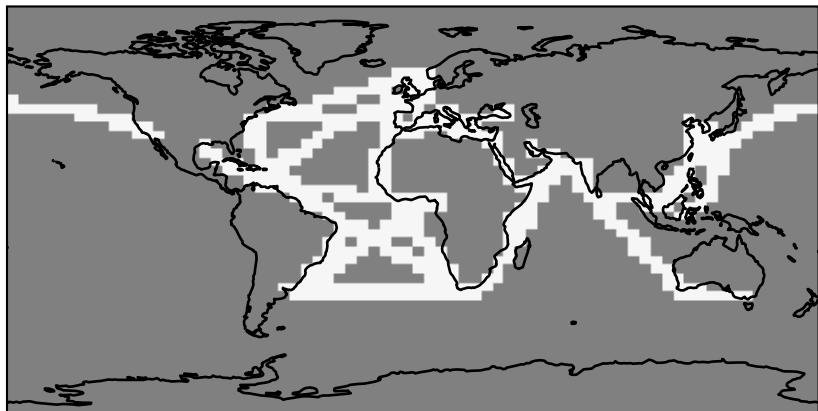
Zero throughout domain
kg/m²/s

Difference
Dev - Ref, Restricted Range [5%, 95%]



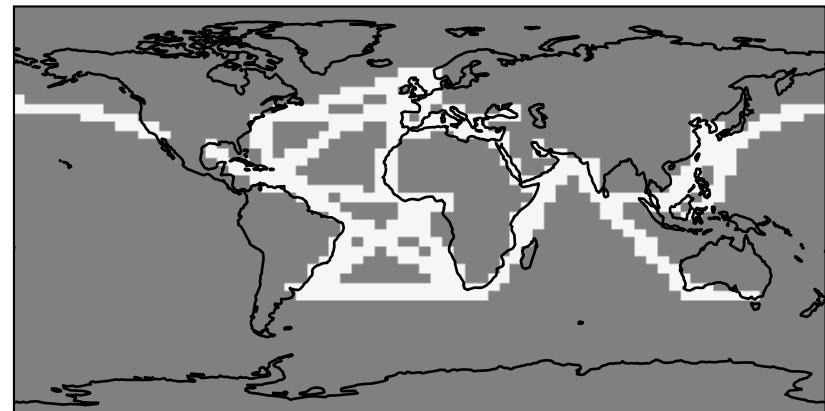
Zero throughout domain
kg/m²/s

Ratio
Dev/Ref, Dynamic Range



0.90 0.96 1.02 1.08
unitless

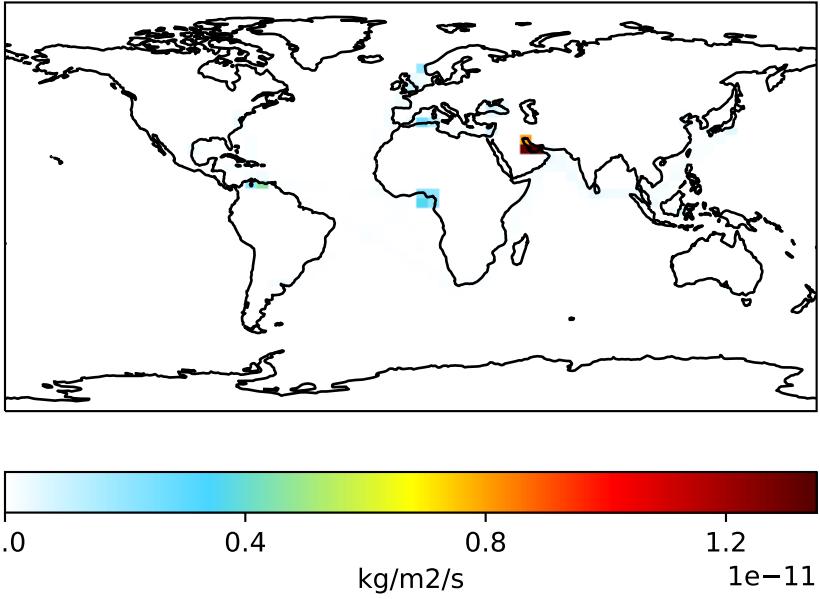
Ratio
Dev/Ref, Fixed Range



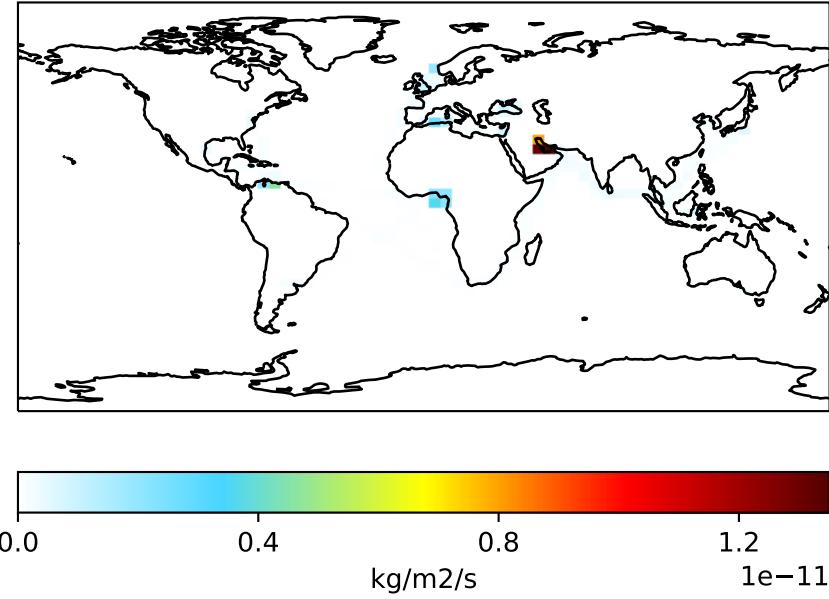
0.50 0.75 1.00 1.50 2.00
unitless

EmisC3H8_Ship

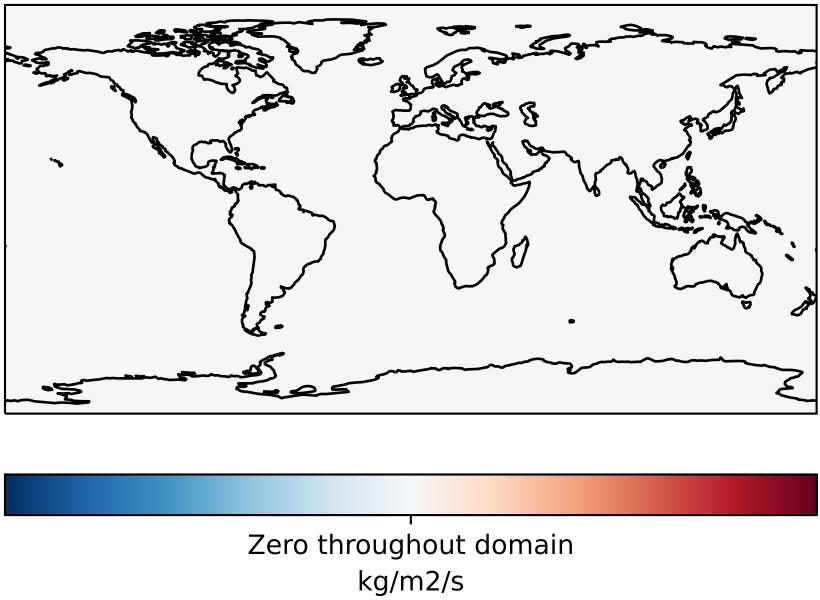
GCC_13.3.0-rc.1 (Ref)
4.0x5.0



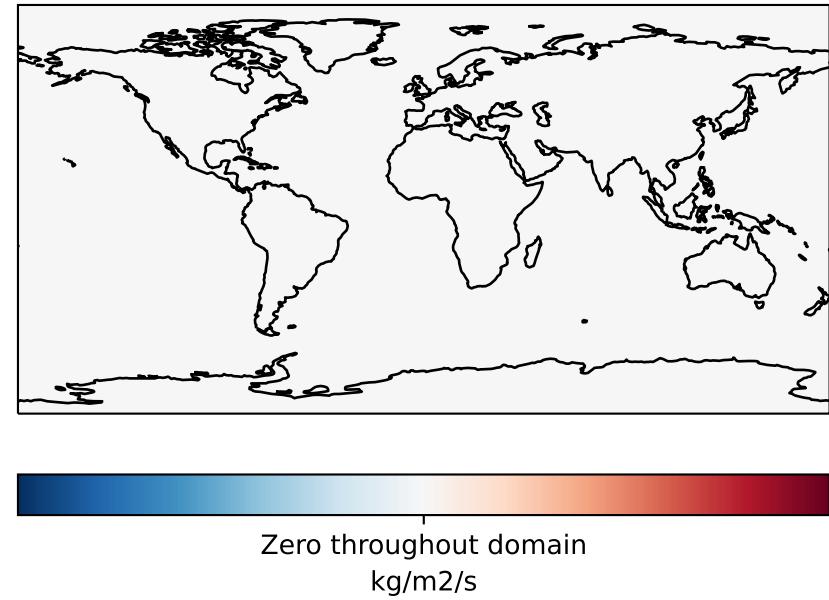
GCC_13.4.0-rc.4 (Dev)
4.0x5.0



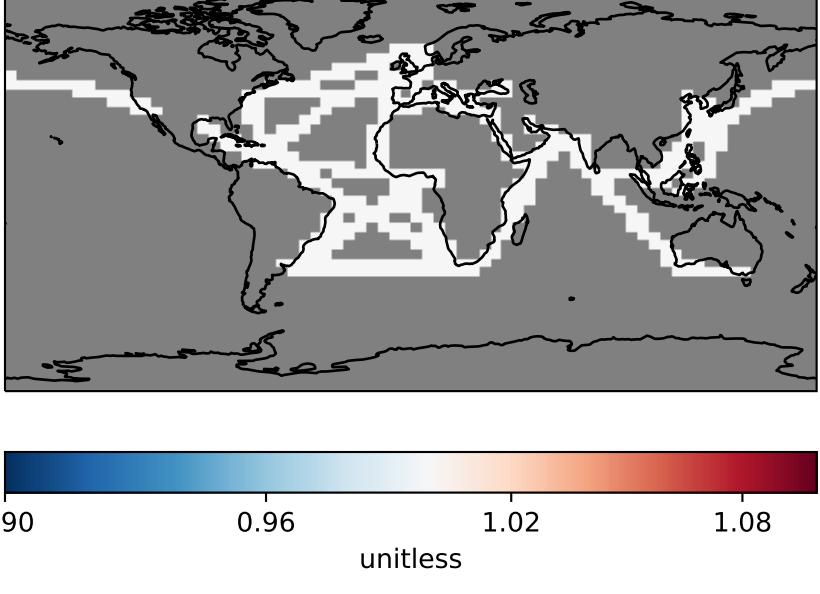
Difference
Dev - Ref, Dynamic Range



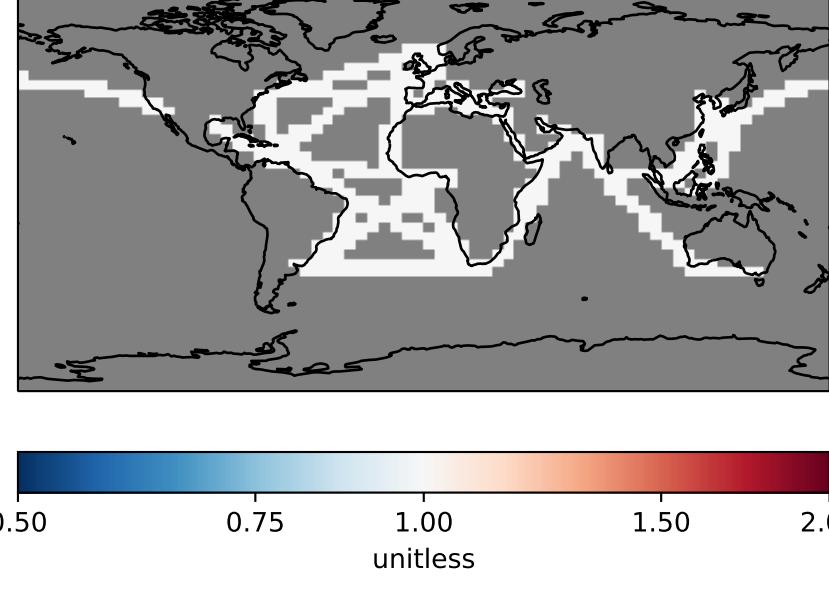
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

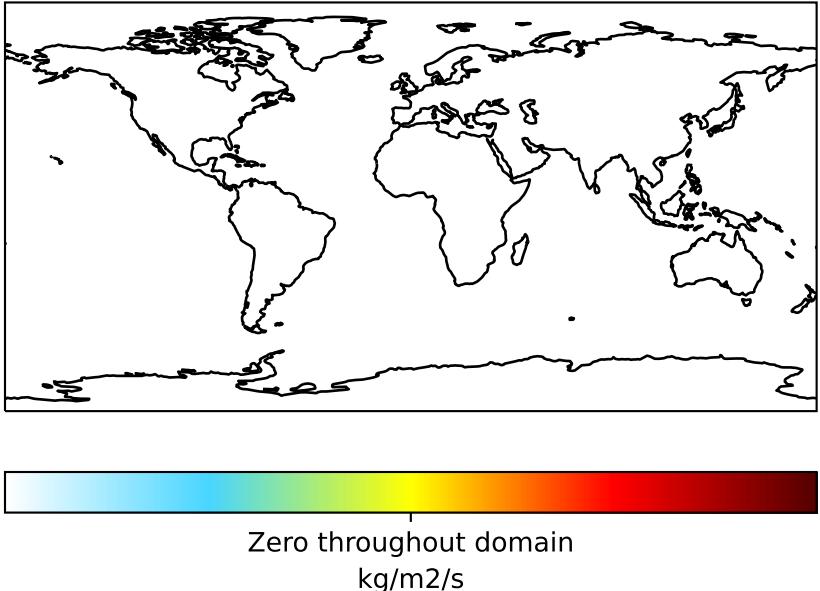


Ratio
Dev/Ref, Fixed Range

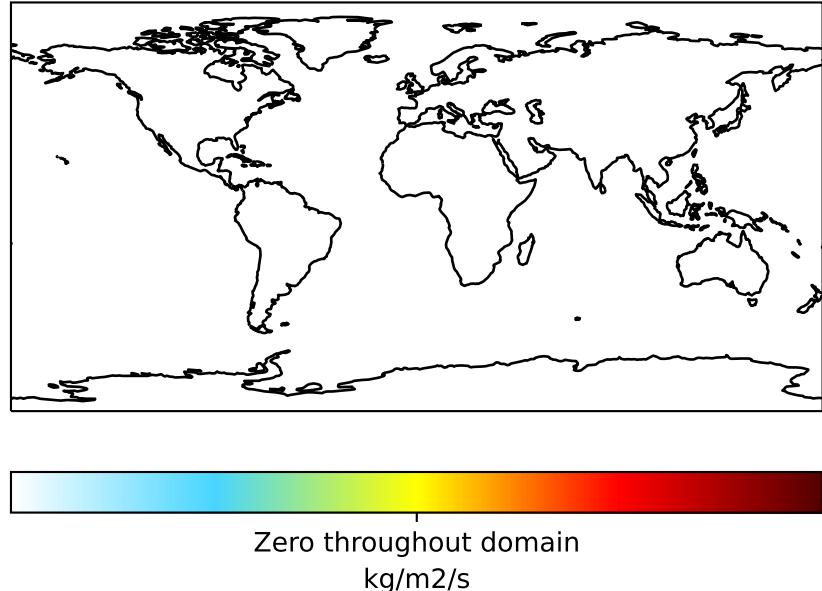


EmisCH2O_Ship

GCC_13.3.0-rc.1 (Ref)
4.0x5.0



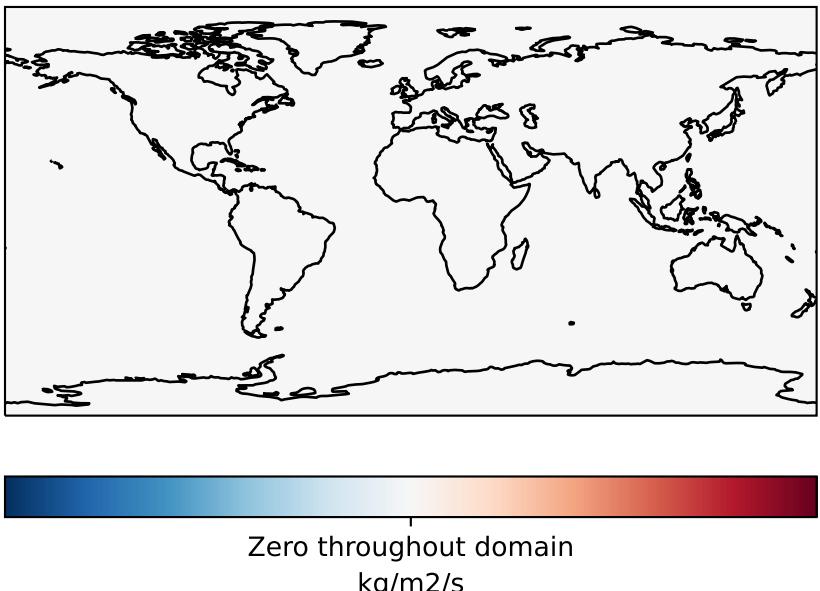
GCC_13.4.0-rc.4 (Dev)
4.0x5.0



Zero throughout domain
kg/m²/s

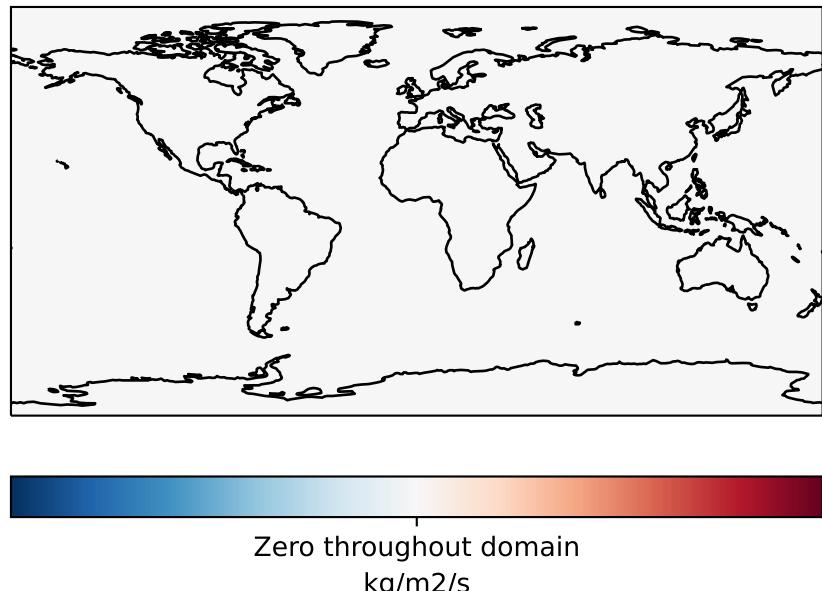
Zero throughout domain
kg/m²/s

Difference
Dev - Ref, Dynamic Range



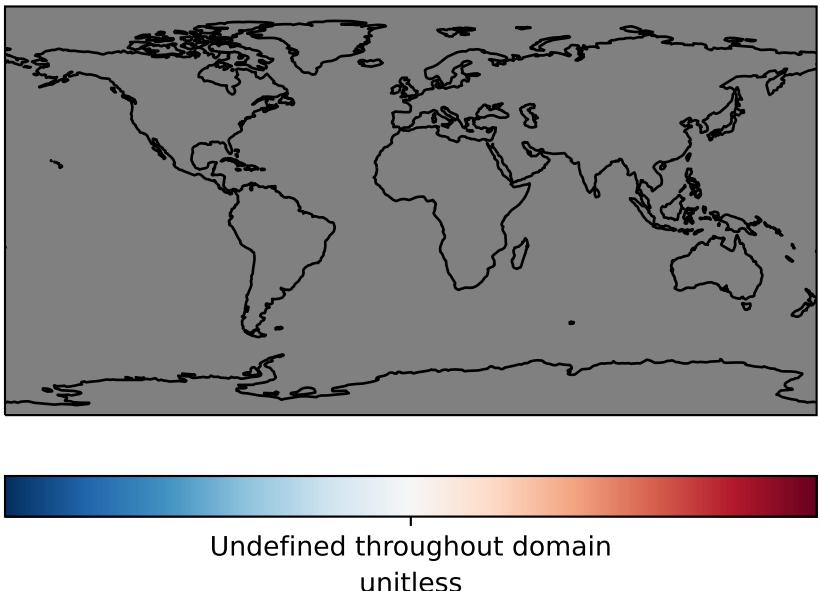
Zero throughout domain
kg/m²/s

Difference
Dev - Ref, Restricted Range [5%,95%]



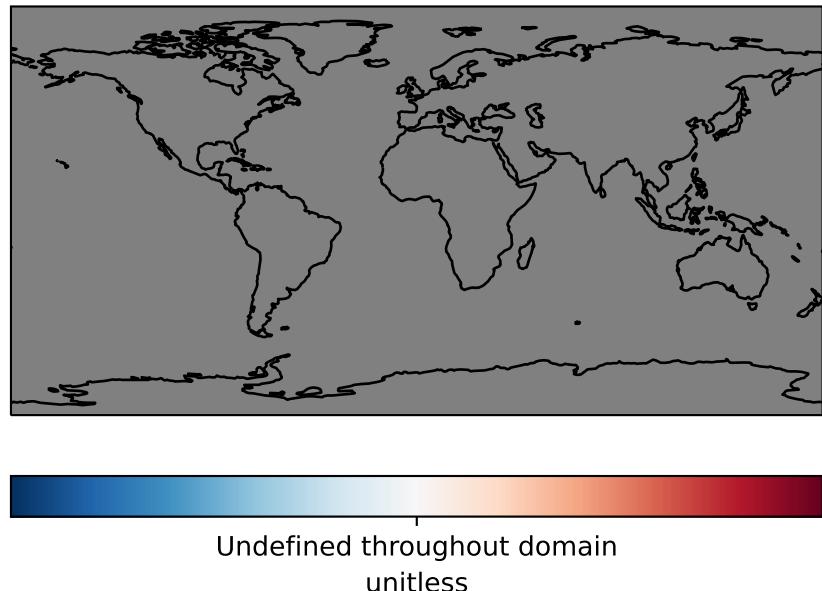
Zero throughout domain
kg/m²/s

Ratio
Dev/Ref, Dynamic Range



Undefined throughout domain
unitless

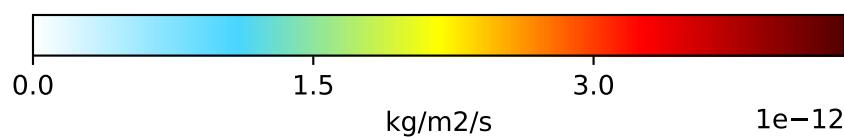
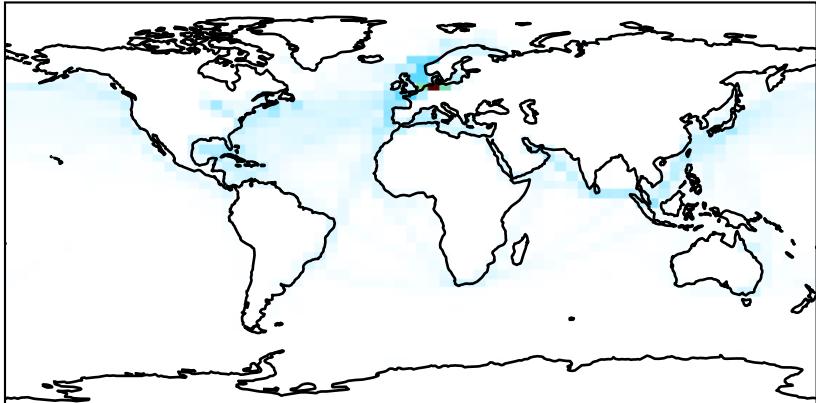
Ratio
Dev/Ref, Fixed Range



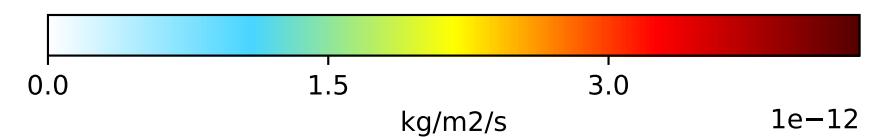
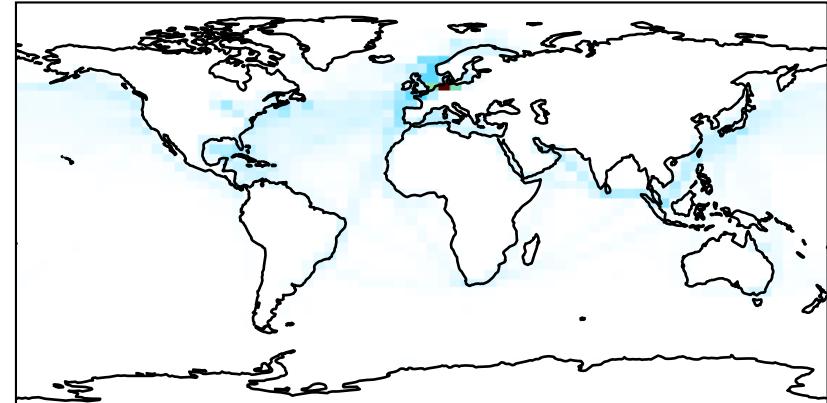
Undefined throughout domain
unitless

EmisCO_Ship

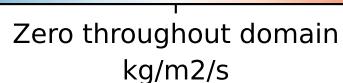
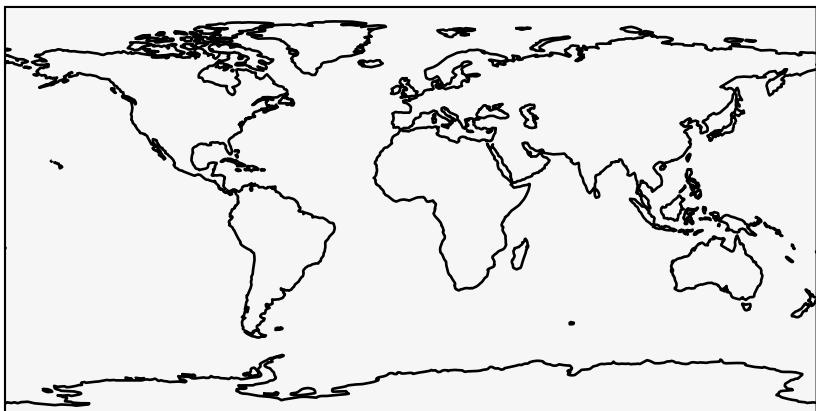
GCC_13.3.0-rc.1 (Ref)
4.0x5.0



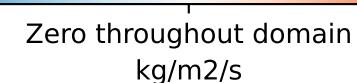
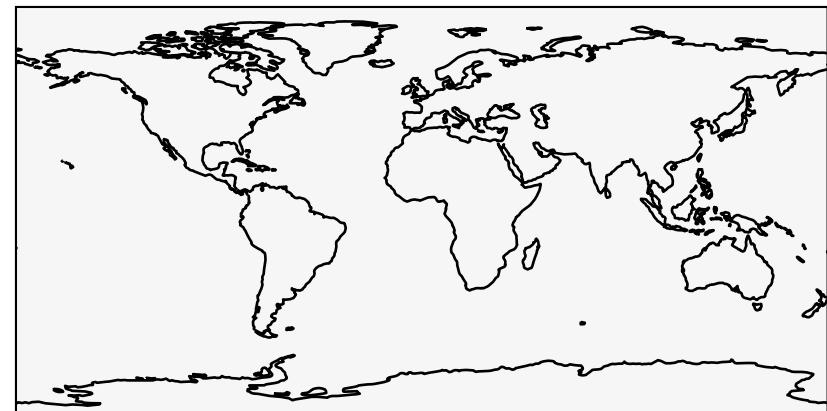
GCC_13.4.0-rc.4 (Dev)
4.0x5.0



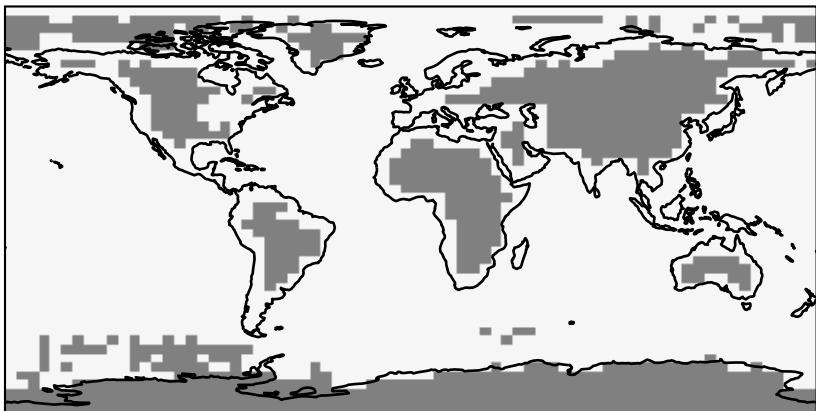
Difference
Dev - Ref, Dynamic Range



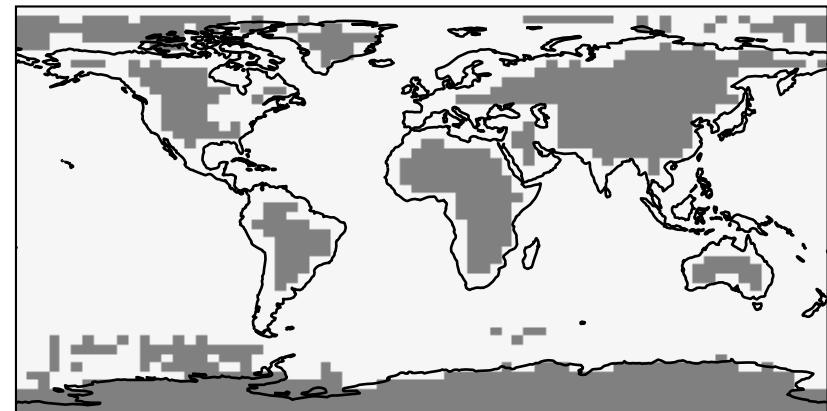
Difference
Dev - Ref, Restricted Range [5%, 95%]



Ratio
Dev/Ref, Dynamic Range

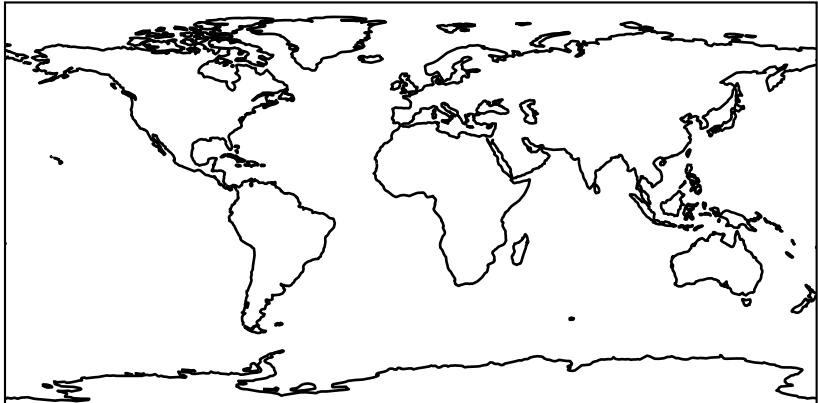


Ratio
Dev/Ref, Fixed Range



EmisEOH_Ship

GCC_13.3.0-rc.1 (Ref)
4.0x5.0



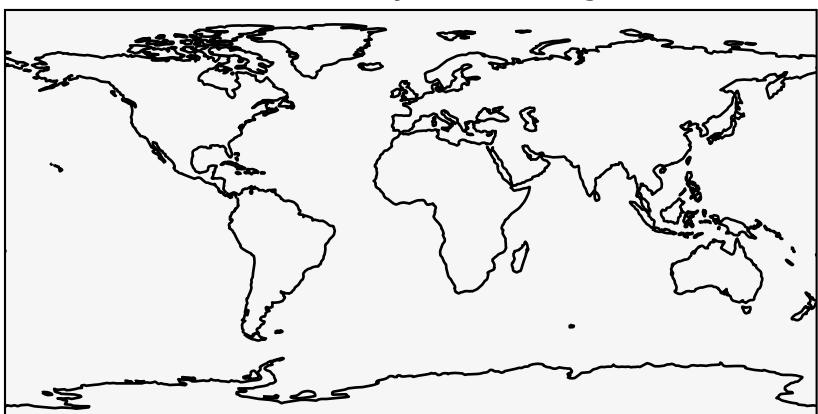
Zero throughout domain
kg/m²/s

GCC_13.4.0-rc.4 (Dev)
4.0x5.0



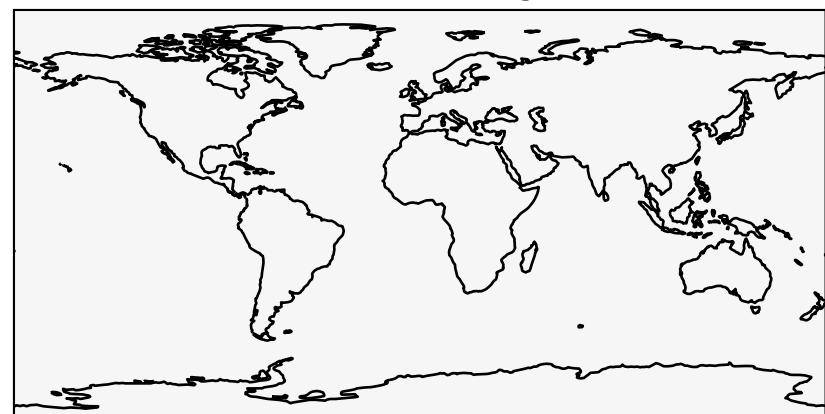
Zero throughout domain
kg/m²/s

Difference
Dev - Ref, Dynamic Range



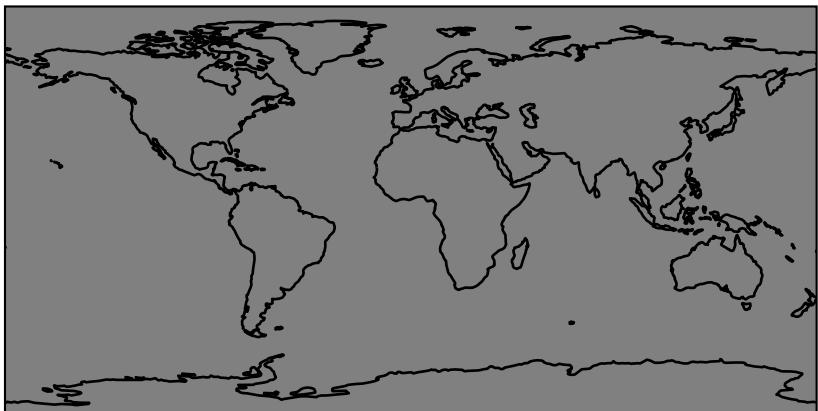
Zero throughout domain
kg/m²/s

Difference
Dev - Ref, Restricted Range [5%,95%]



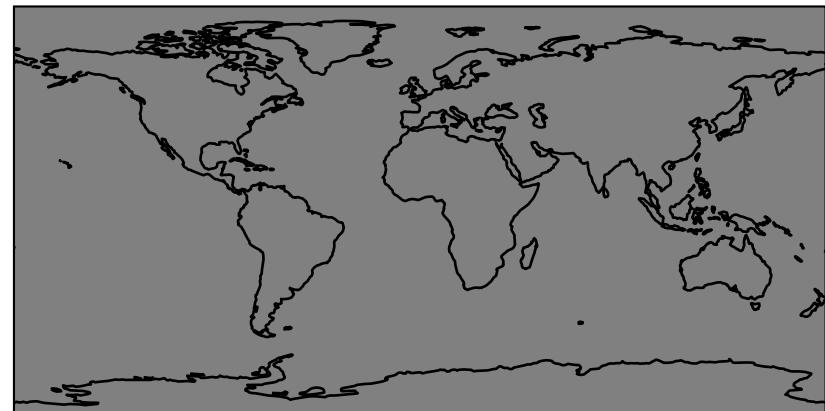
Zero throughout domain
kg/m²/s

Ratio
Dev/Ref, Dynamic Range



Undefined throughout domain
unitless

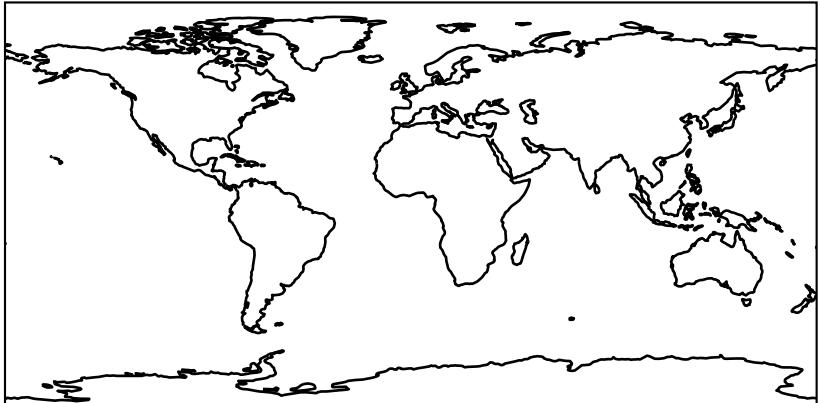
Ratio
Dev/Ref, Fixed Range



Undefined throughout domain
unitless

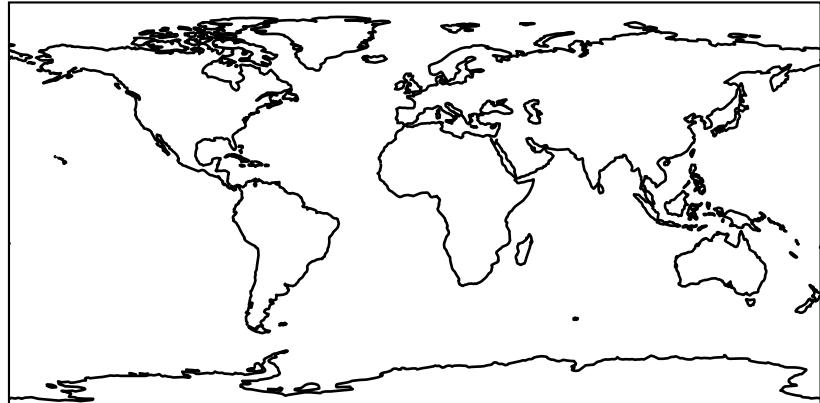
EmisHCOOH_Ship

GCC_13.3.0-rc.1 (Ref)
4.0x5.0



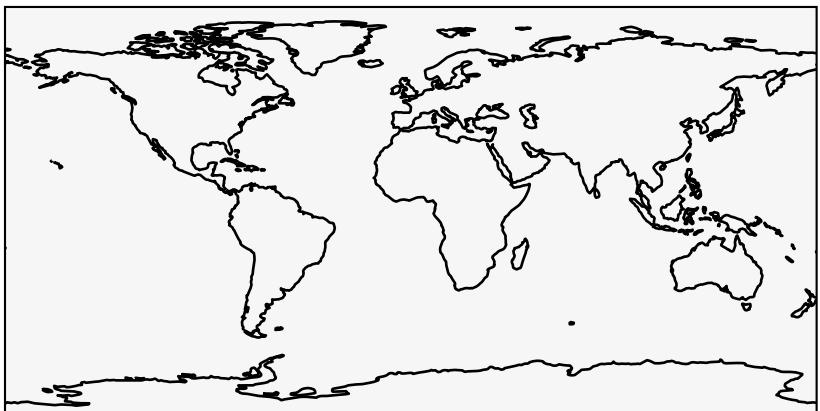
Zero throughout domain
kg/m²/s

GCC_13.4.0-rc.4 (Dev)
4.0x5.0



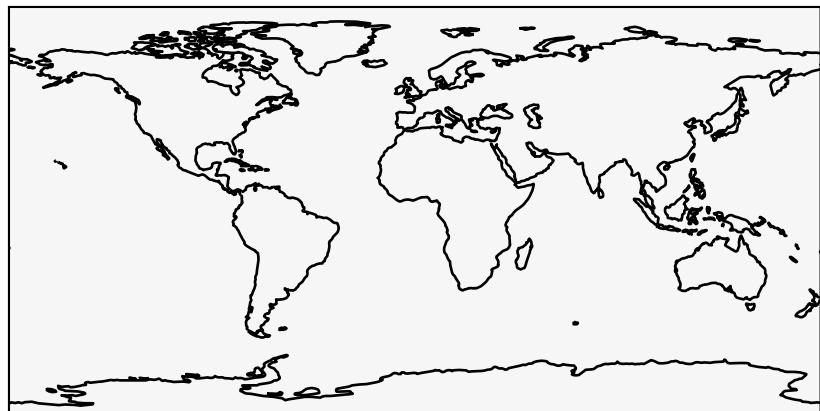
Zero throughout domain
kg/m²/s

Difference
Dev - Ref, Dynamic Range



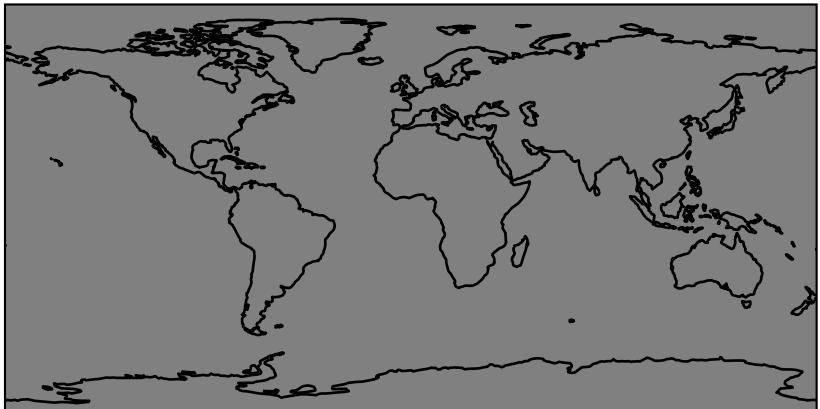
Zero throughout domain
kg/m²/s

Difference
Dev - Ref, Restricted Range [5%,95%]



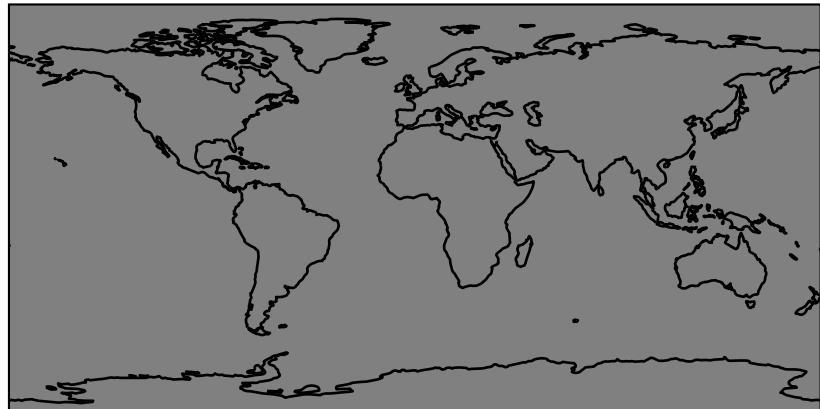
Zero throughout domain
kg/m²/s

Ratio
Dev/Ref, Dynamic Range



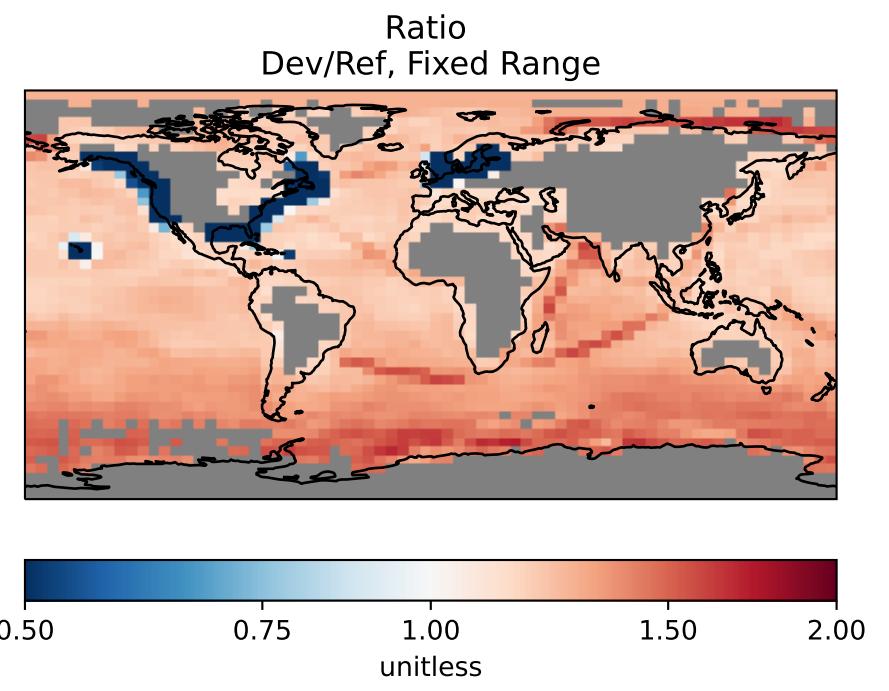
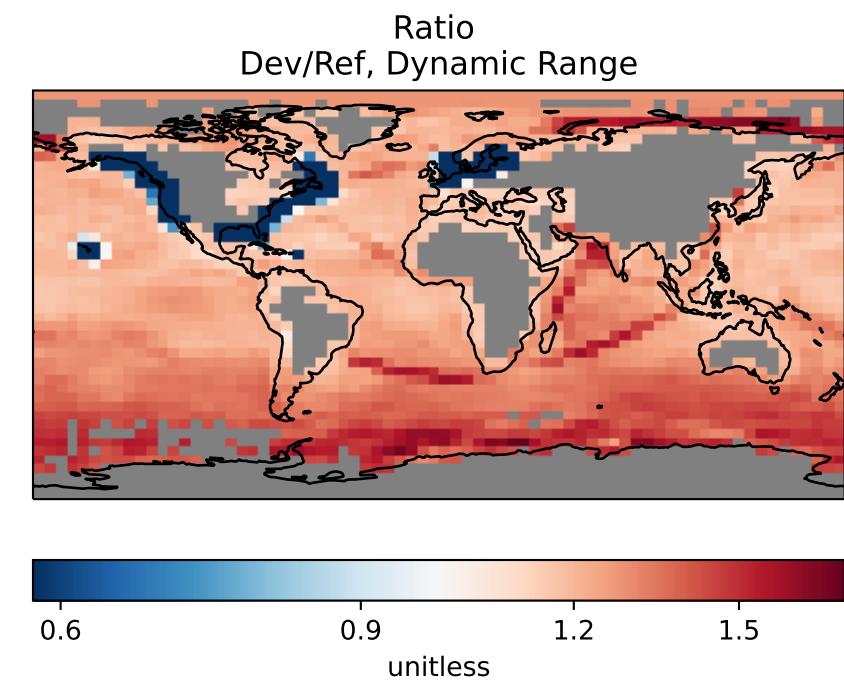
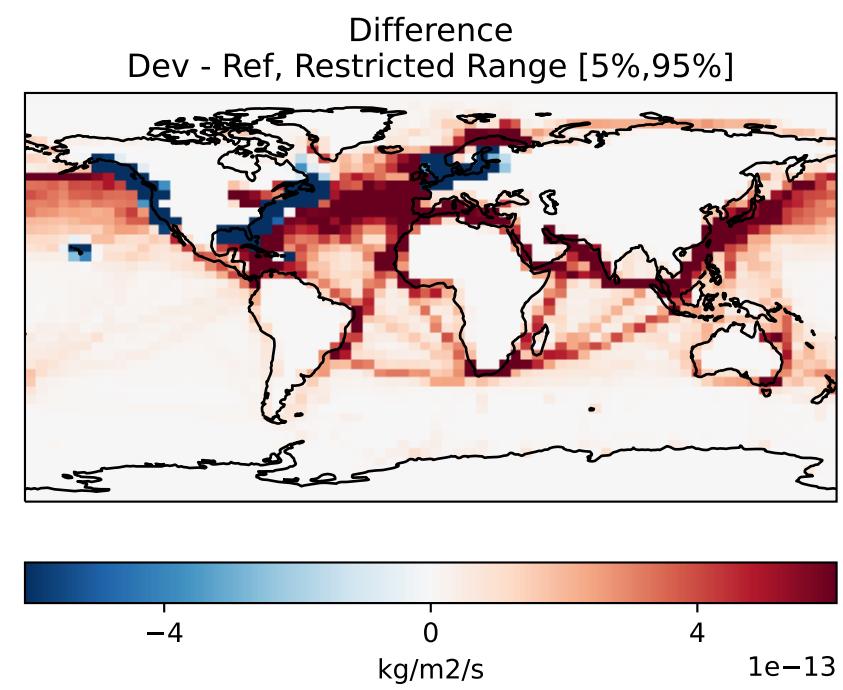
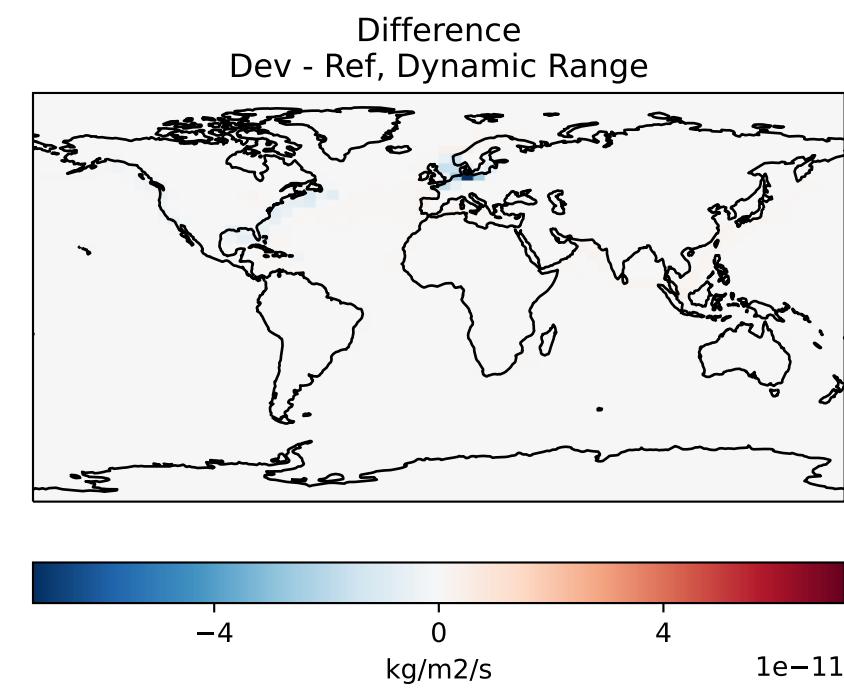
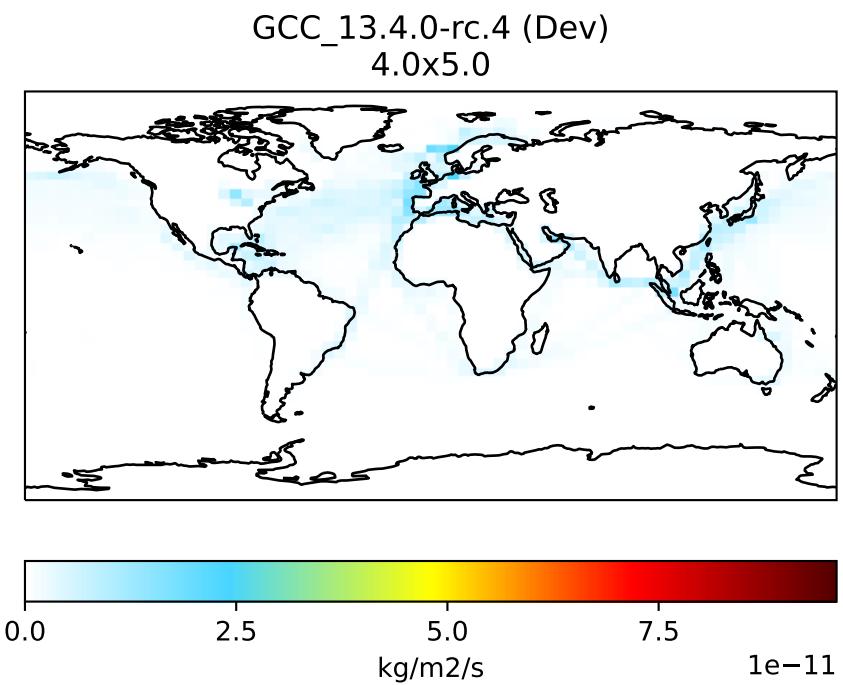
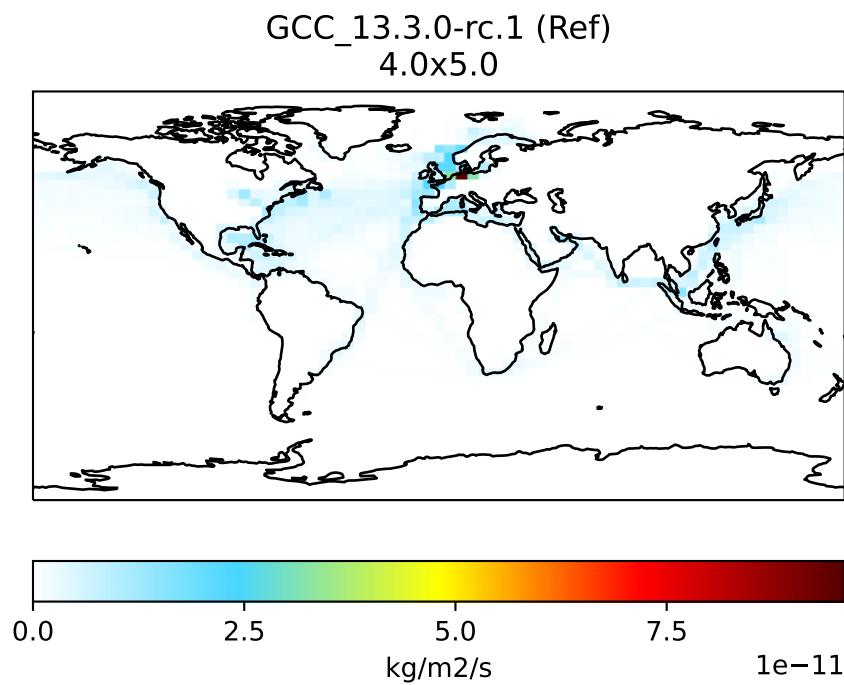
Undefined throughout domain
unitless

Ratio
Dev/Ref, Fixed Range



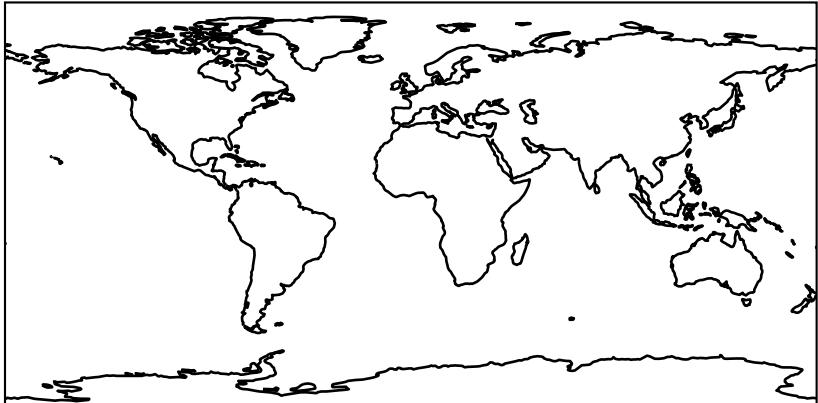
Undefined throughout domain
unitless

EmisHNO3_Ship



EmisMEK_Ship

GCC_13.3.0-rc.1 (Ref)
4.0x5.0



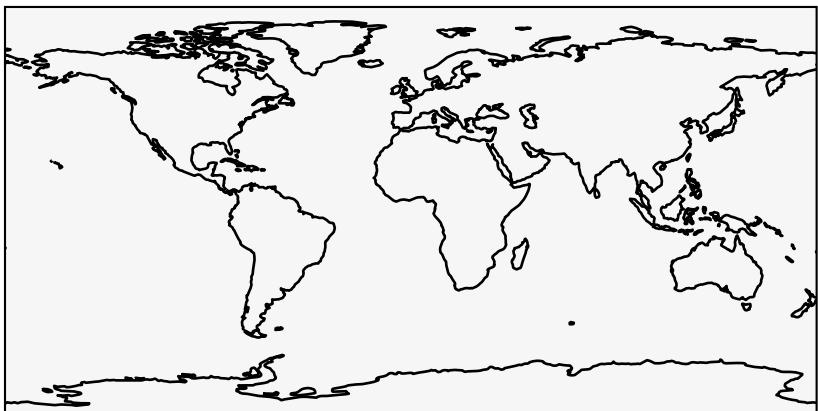
Zero throughout domain
kg/m²/s

GCC_13.4.0-rc.4 (Dev)
4.0x5.0



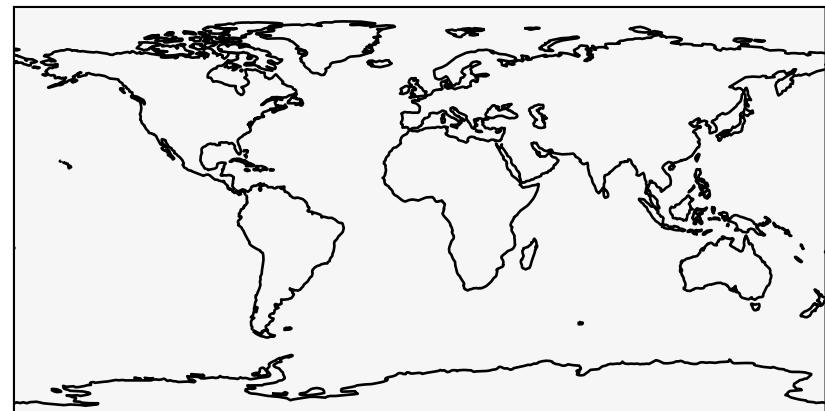
Zero throughout domain
kg/m²/s

Difference
Dev - Ref, Dynamic Range



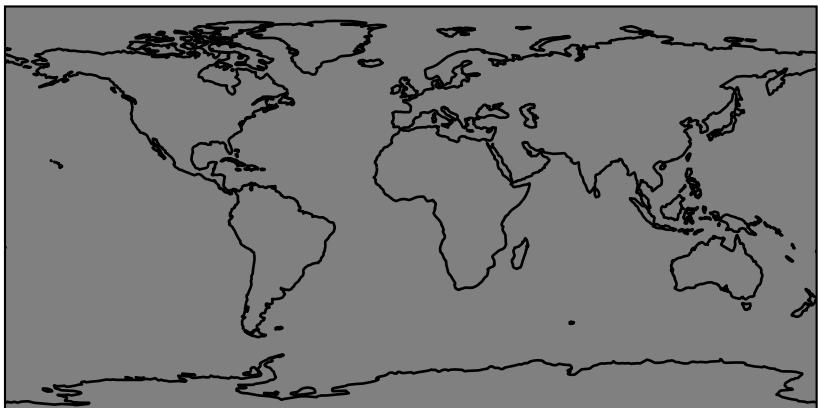
Zero throughout domain
kg/m²/s

Difference
Dev - Ref, Restricted Range [5%,95%]



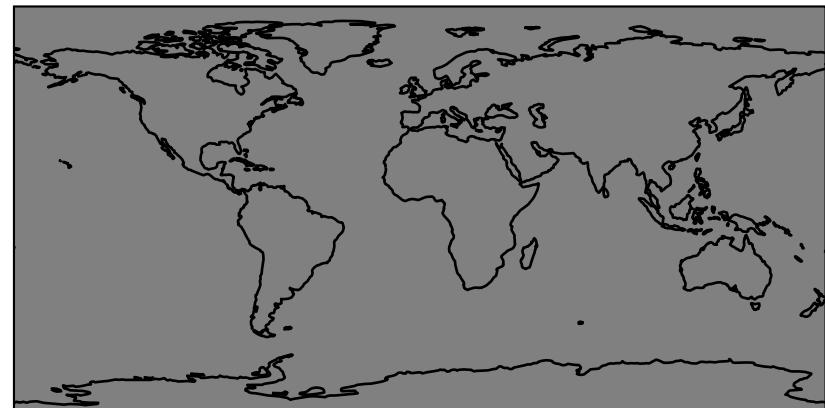
Zero throughout domain
kg/m²/s

Ratio
Dev/Ref, Dynamic Range



Undefined throughout domain
unitless

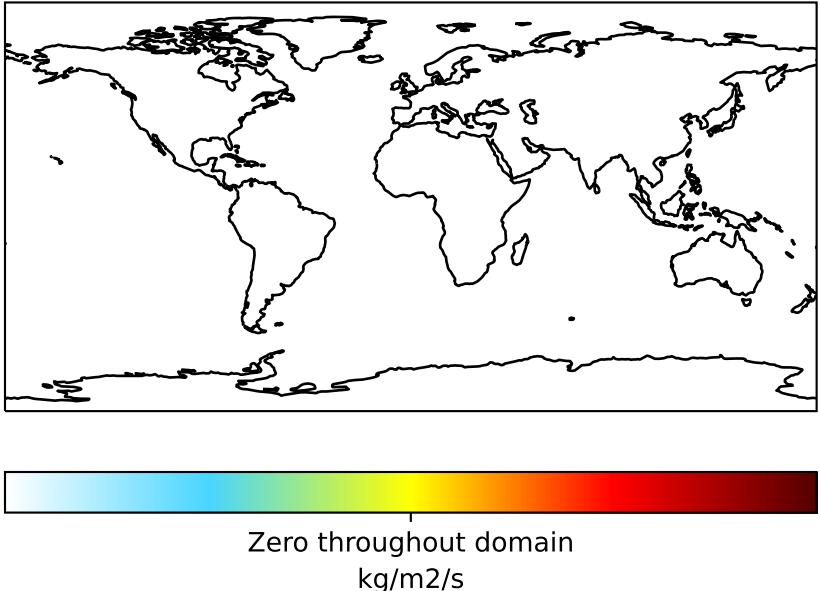
Ratio
Dev/Ref, Fixed Range



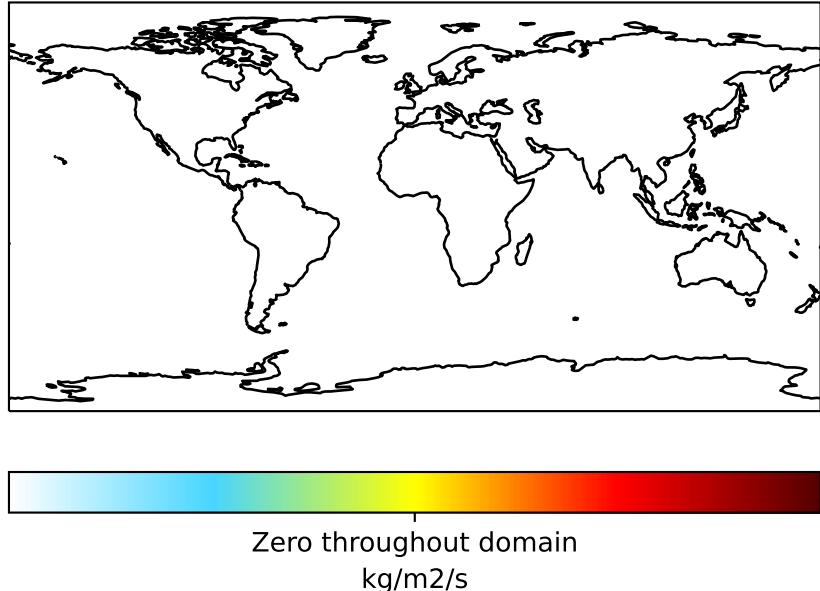
Undefined throughout domain
unitless

EmisMOH_Ship

GCC_13.3.0-rc.1 (Ref)
4.0x5.0



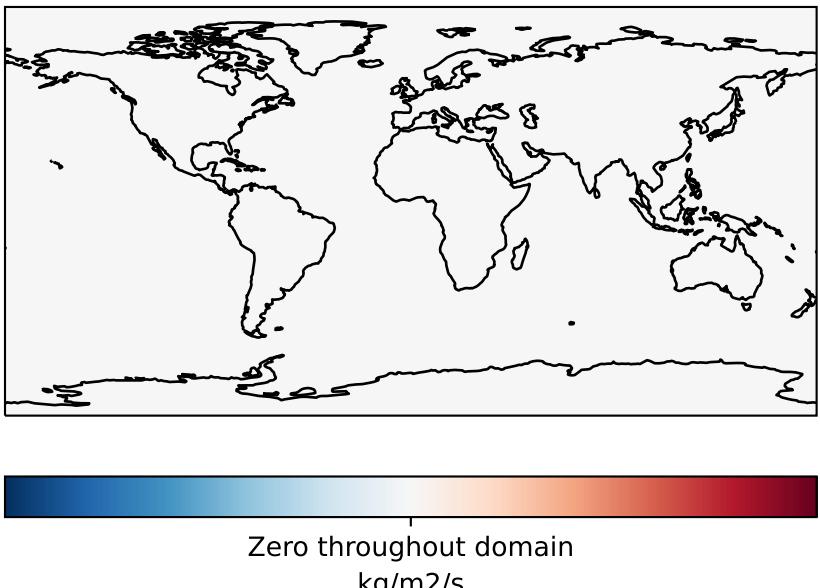
GCC_13.4.0-rc.4 (Dev)
4.0x5.0



Zero throughout domain
kg/m²/s

Zero throughout domain
kg/m²/s

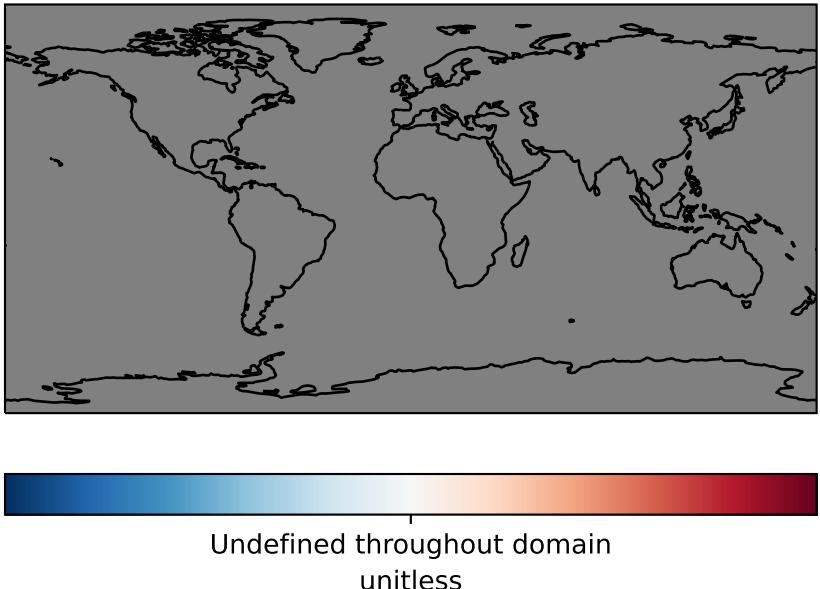
Difference
Dev - Ref, Dynamic Range



Zero throughout domain
kg/m²/s

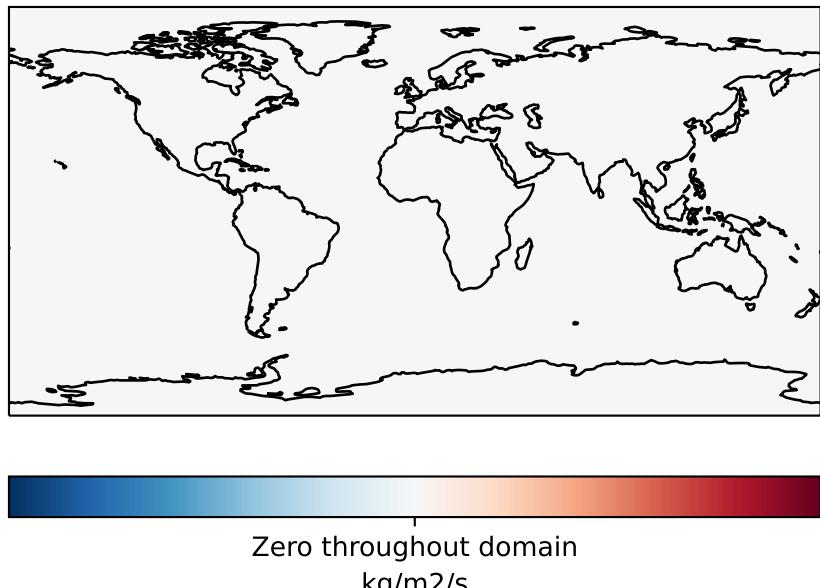
Zero throughout domain
kg/m²/s

Ratio
Dev/Ref, Dynamic Range



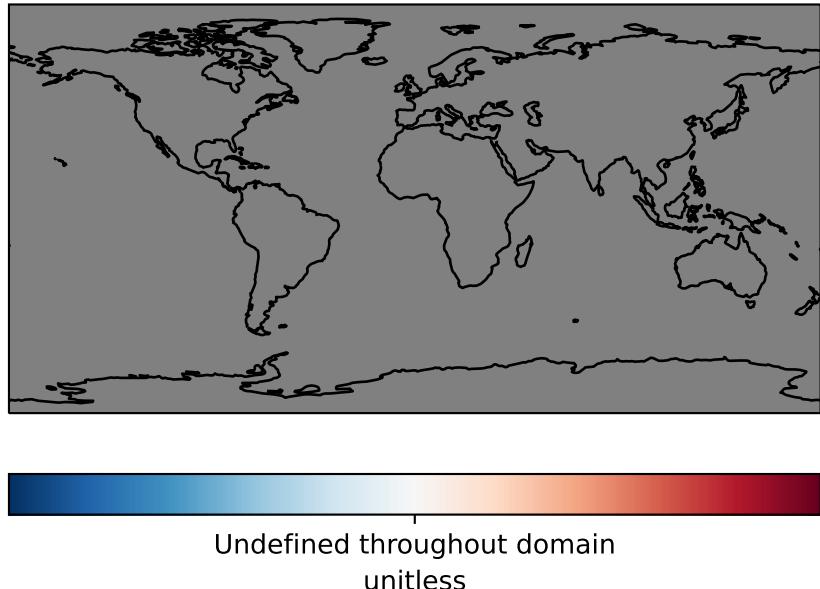
Undefined throughout domain
unitless

Difference
Dev - Ref, Restricted Range [5%,95%]



Zero throughout domain
kg/m²/s

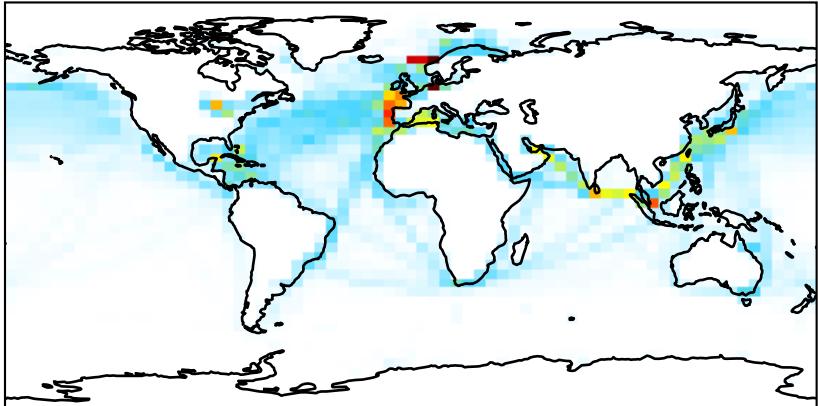
Ratio
Dev/Ref, Fixed Range



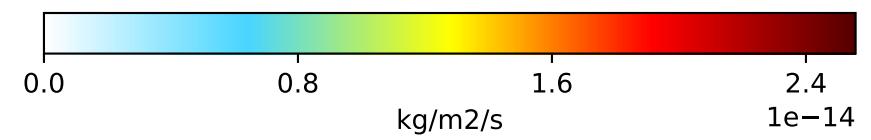
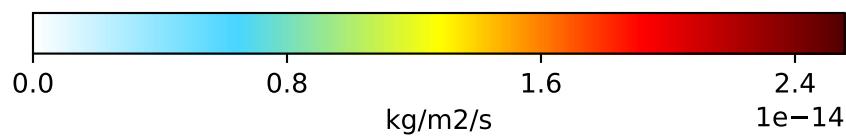
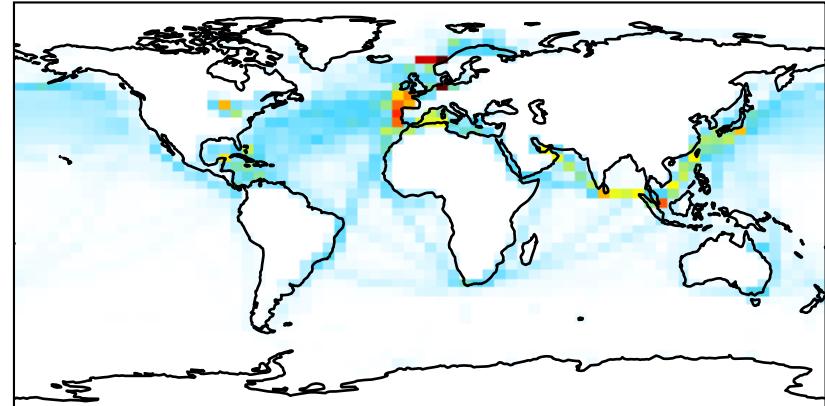
Undefined throughout domain
unitless

EmisNH3_Ship

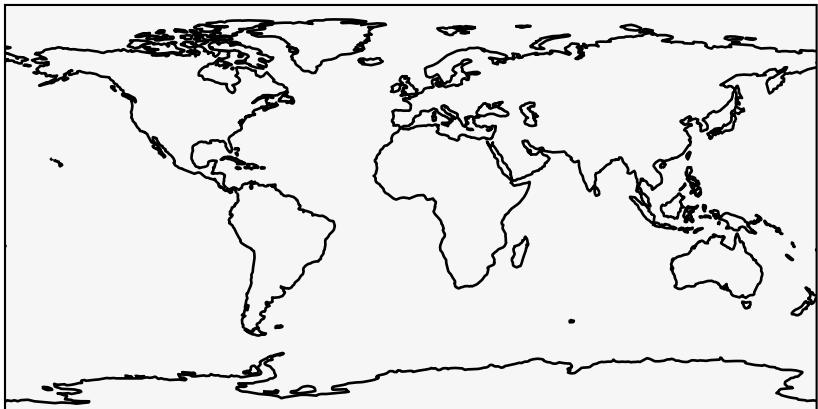
GCC_13.3.0-rc.1 (Ref)
4.0x5.0



GCC_13.4.0-rc.4 (Dev)
4.0x5.0

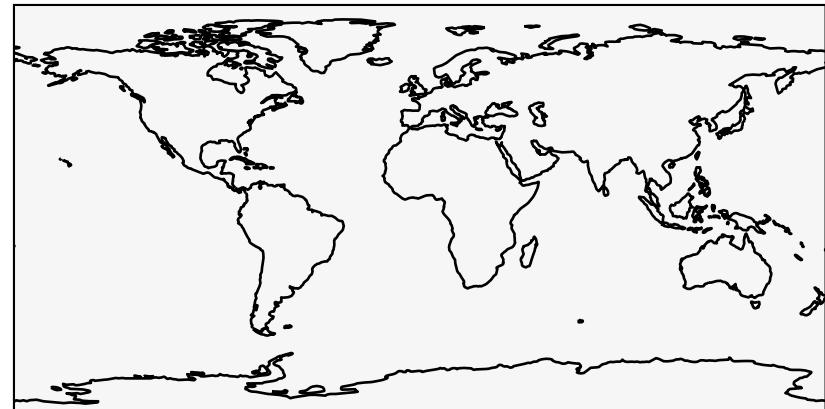


Difference
Dev - Ref, Dynamic Range



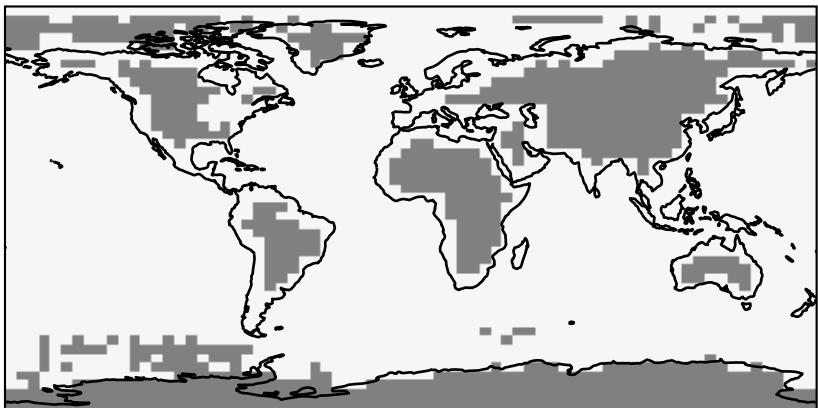
Zero throughout domain
kg/m²/s

Difference
Dev - Ref, Restricted Range [5%, 95%]



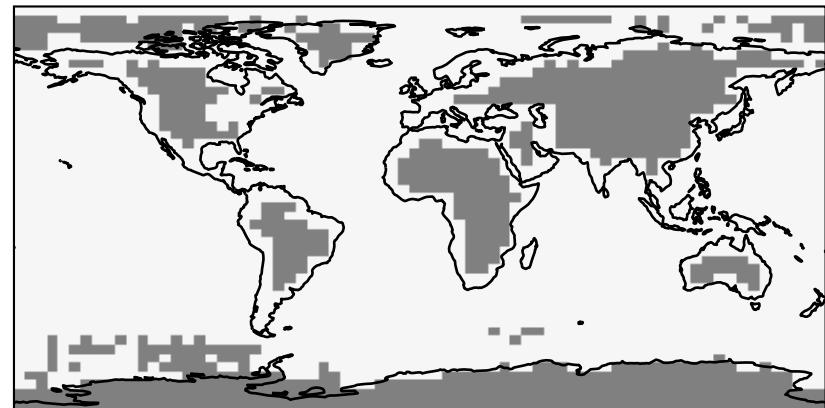
Zero throughout domain
kg/m²/s

Ratio
Dev/Ref, Dynamic Range



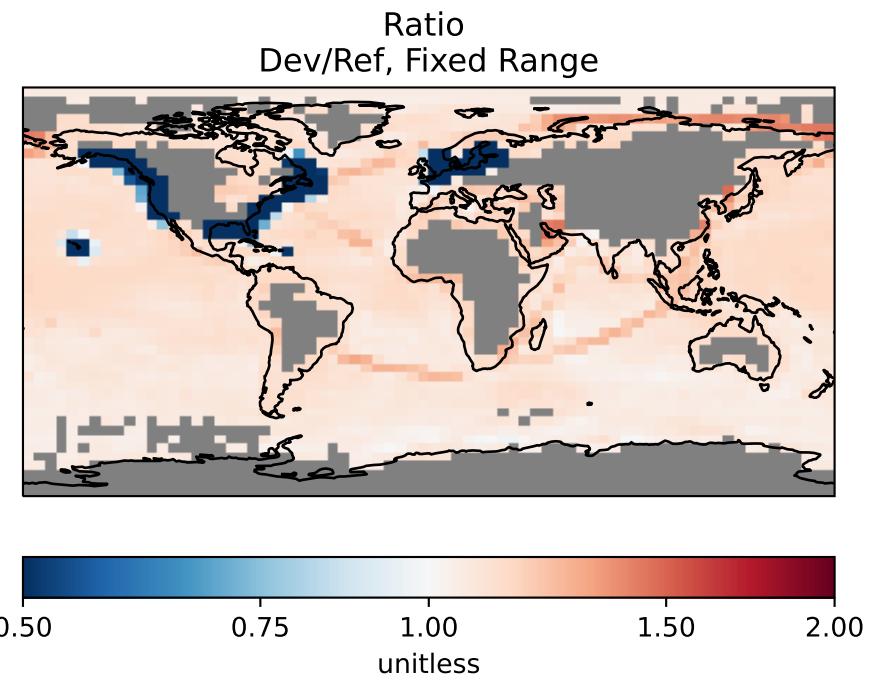
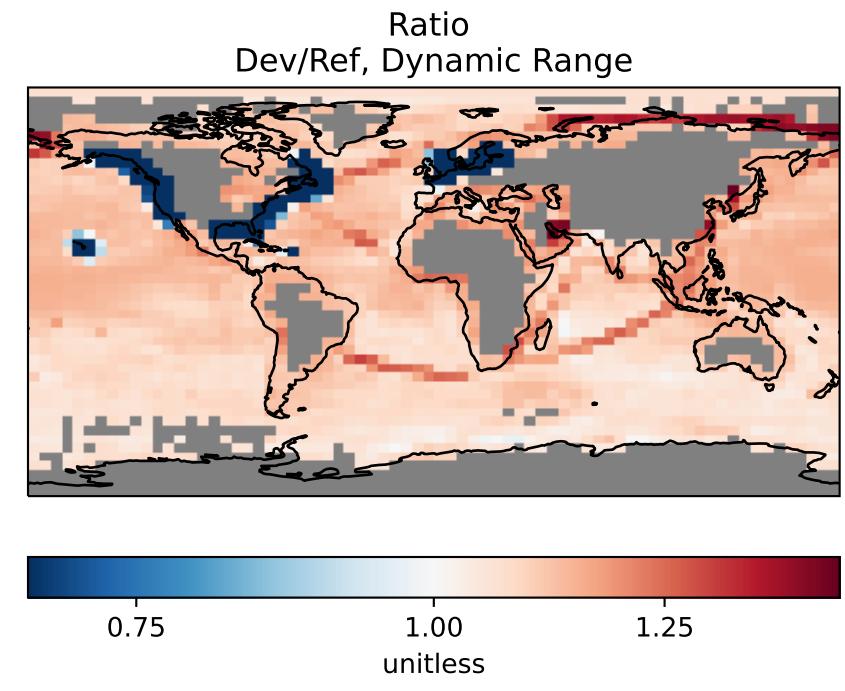
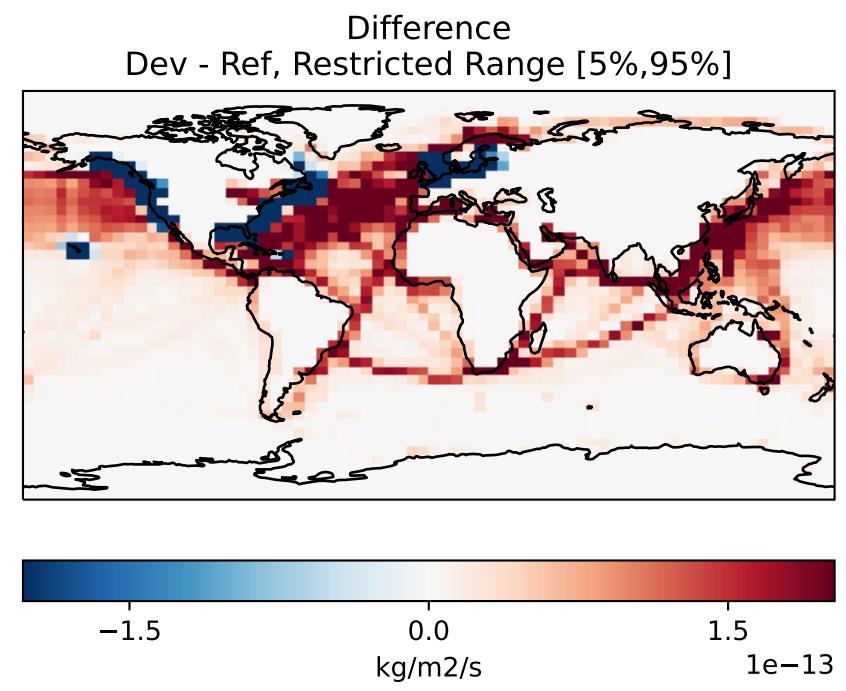
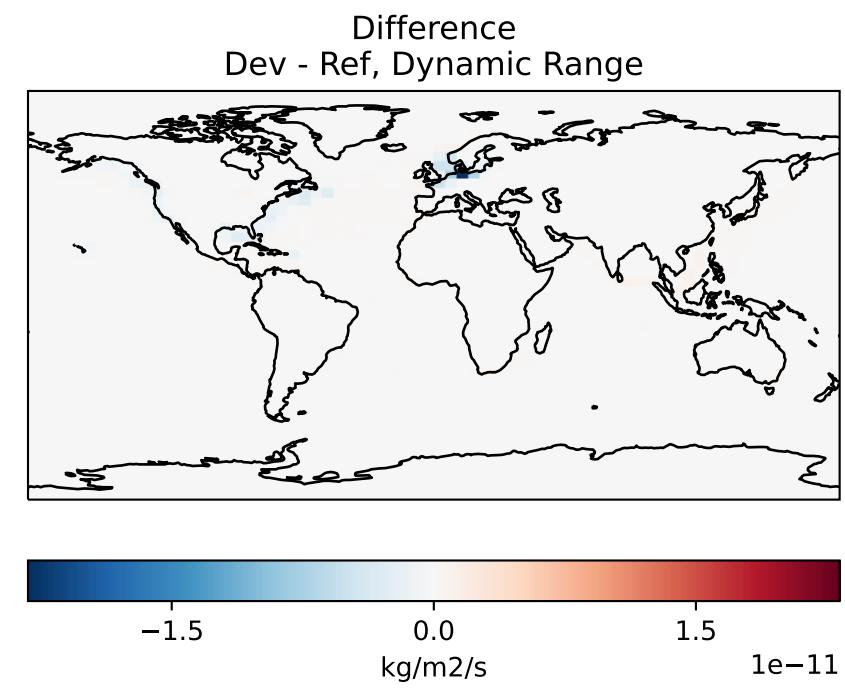
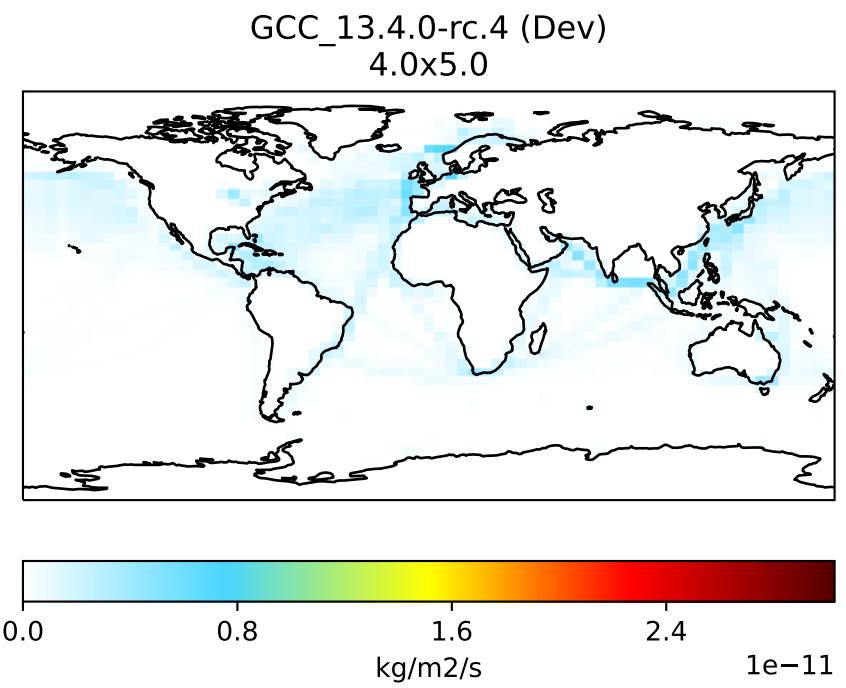
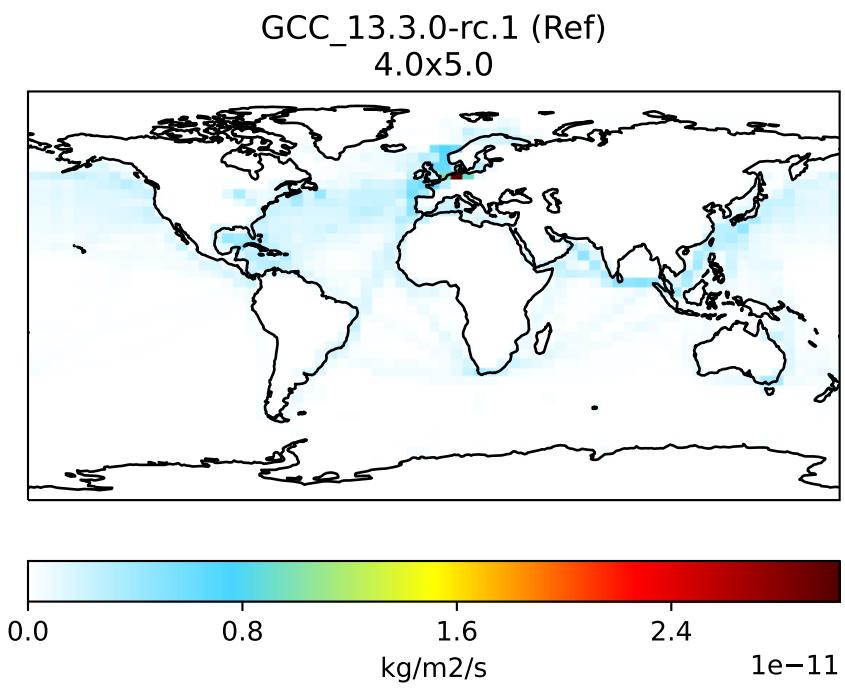
0.90 0.96 1.02 1.08
unitless

Ratio
Dev/Ref, Fixed Range



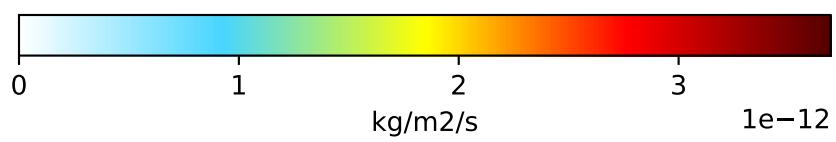
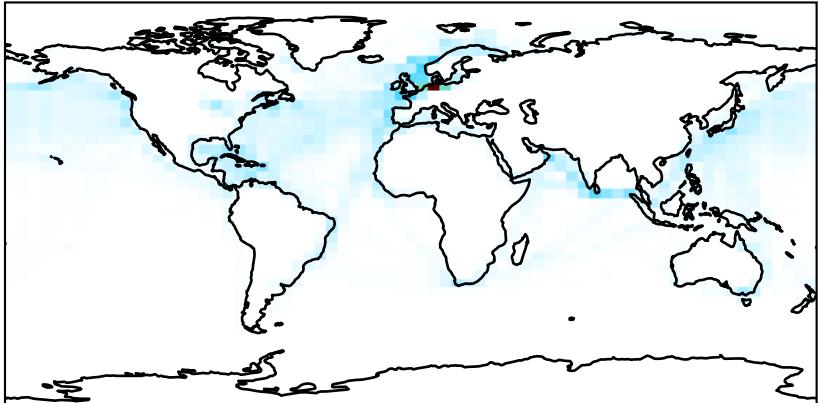
0.50 0.75 1.00 1.50 2.00
unitless

EmisNO2_Ship

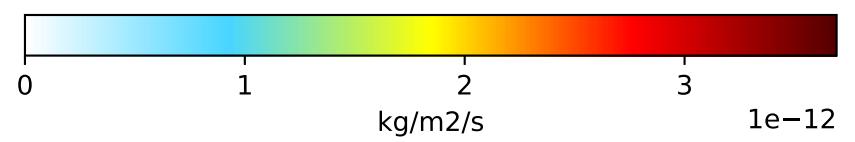
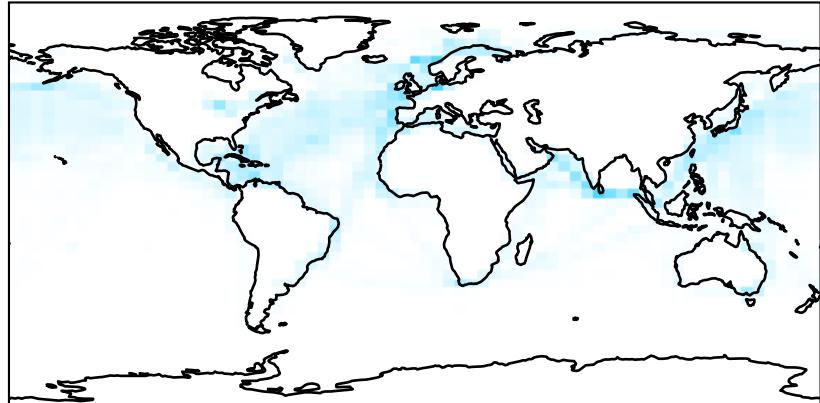


EmisNO_Ship

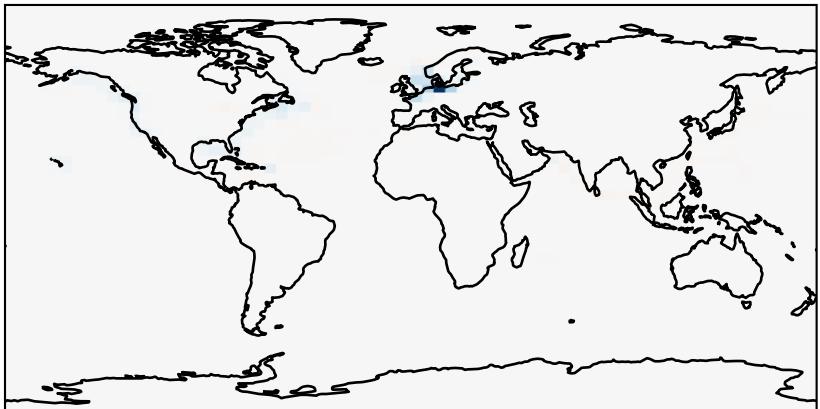
GCC_13.3.0-rc.1 (Ref)
4.0x5.0



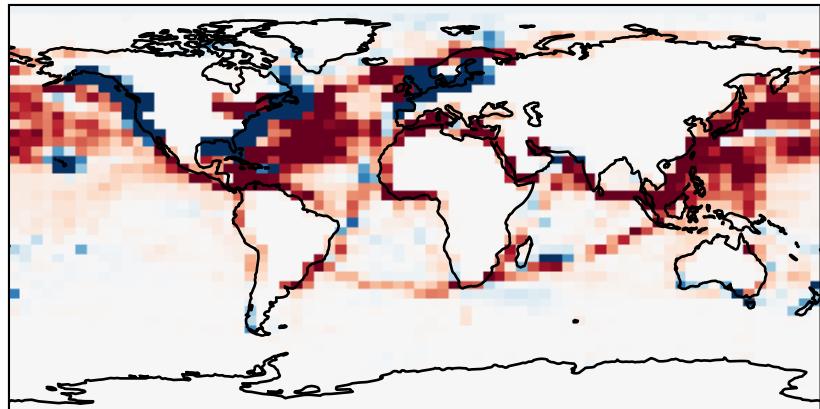
GCC_13.4.0-rc.4 (Dev)
4.0x5.0



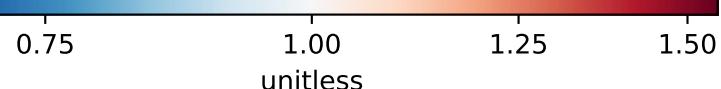
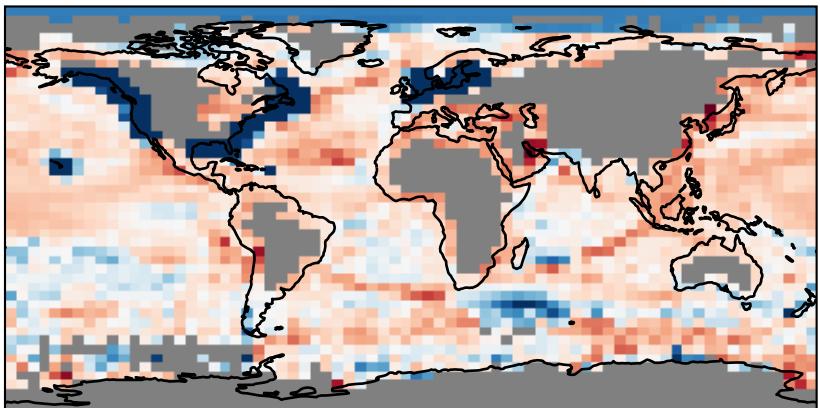
Difference
Dev - Ref, Dynamic Range



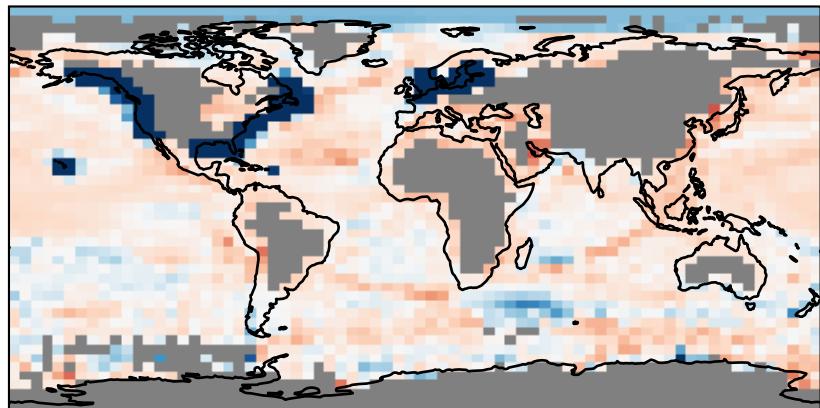
Difference
Dev - Ref, Restricted Range [5%, 95%]



Ratio
Dev/Ref, Dynamic Range

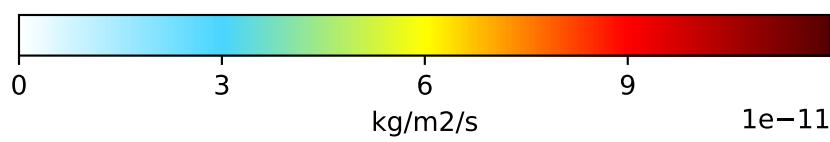
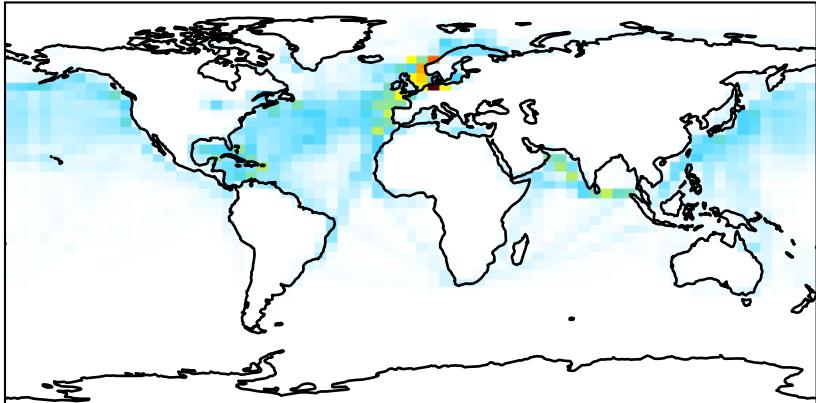


Ratio
Dev/Ref, Fixed Range

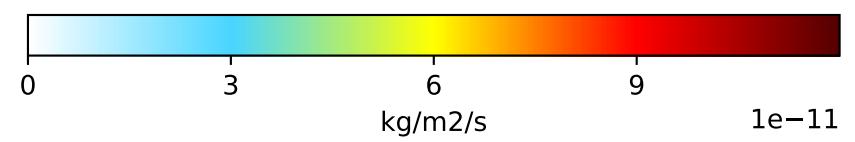
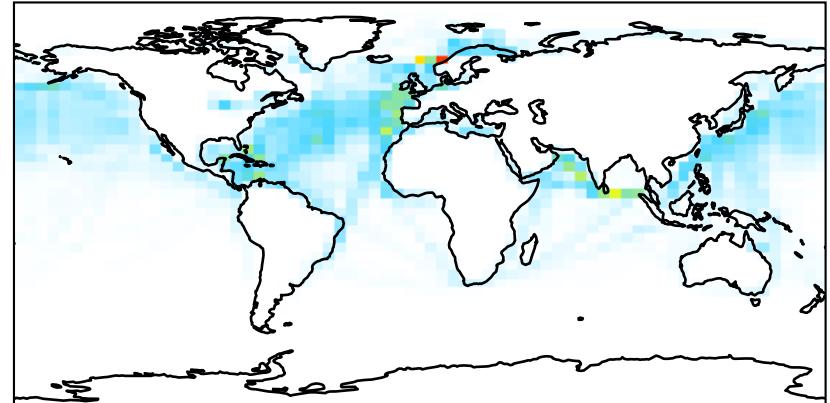


EmisO3_Ship

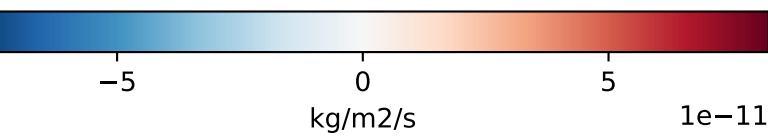
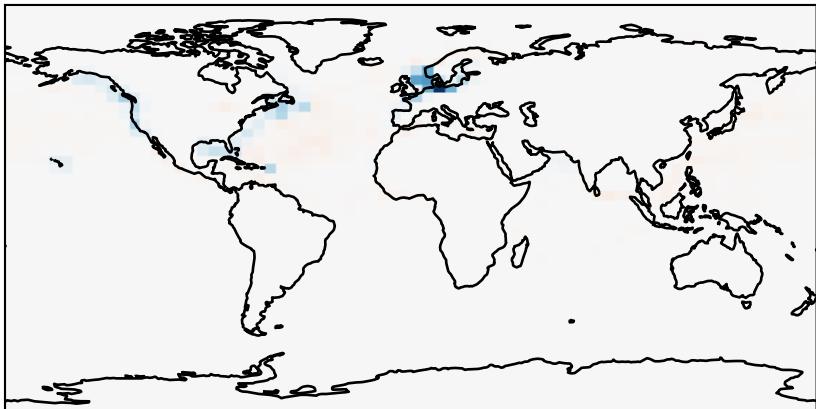
GCC_13.3.0-rc.1 (Ref)
4.0x5.0



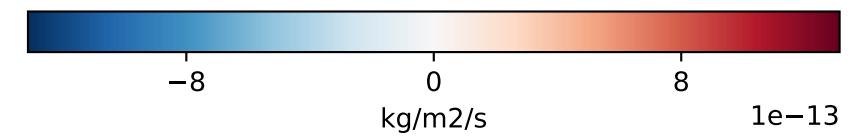
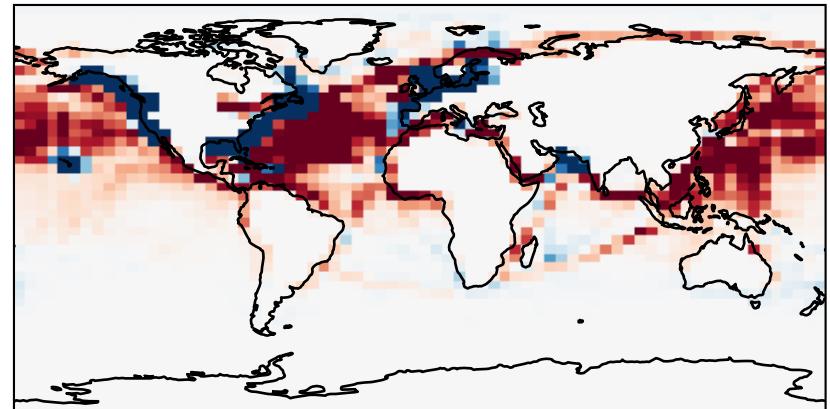
GCC_13.4.0-rc.4 (Dev)
4.0x5.0



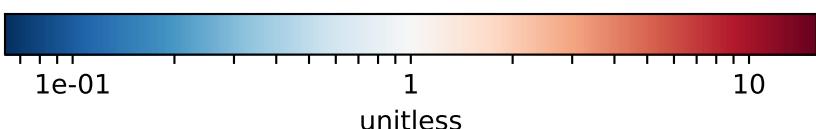
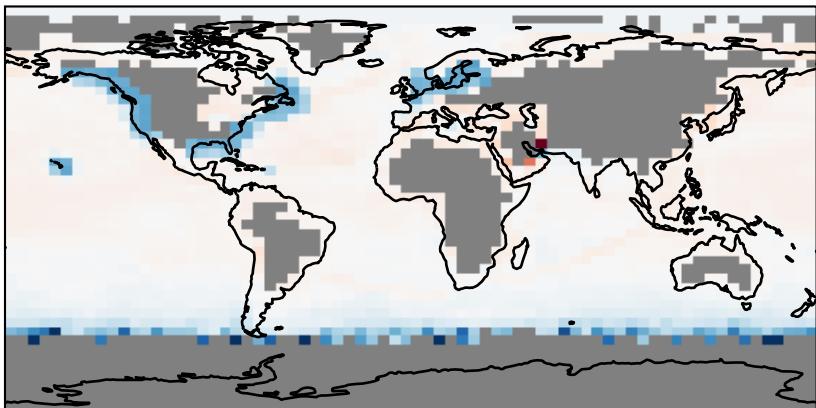
Difference
Dev - Ref, Dynamic Range



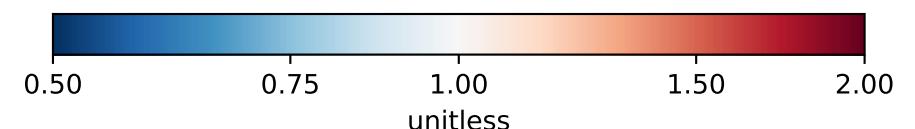
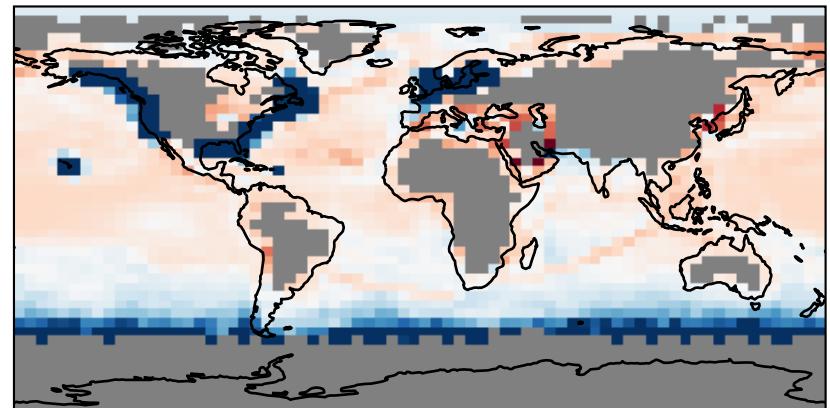
Difference
Dev - Ref, Restricted Range [5%, 95%]



Ratio
Dev/Ref, Dynamic Range

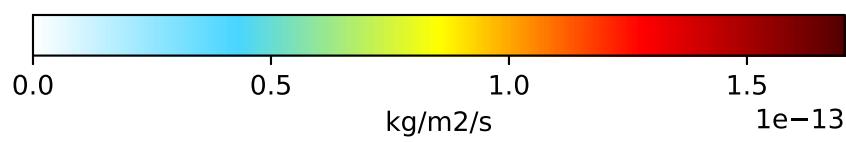
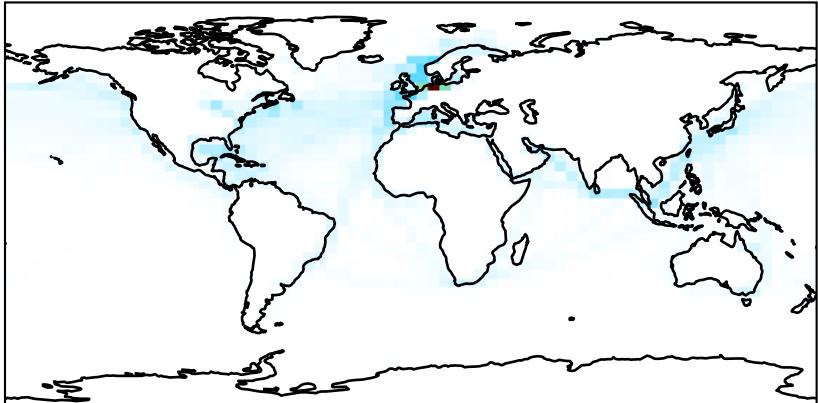


Ratio
Dev/Ref, Fixed Range

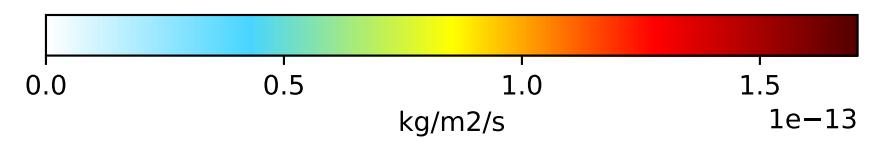
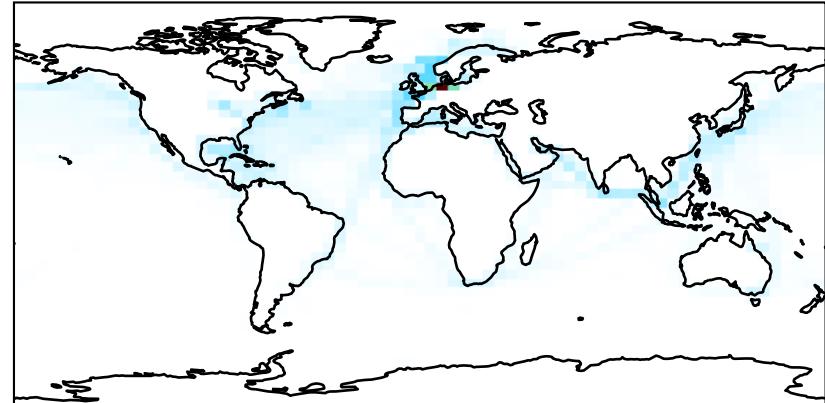


EmisOCPI_Ship

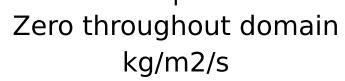
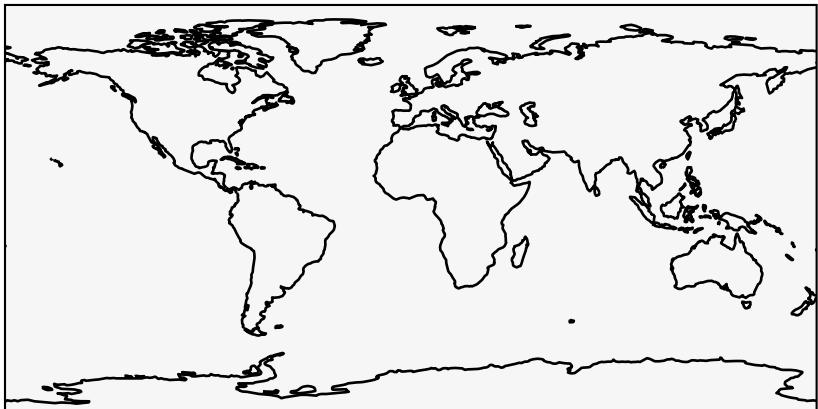
GCC_13.3.0-rc.1 (Ref)
4.0x5.0



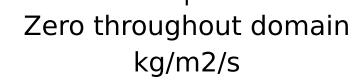
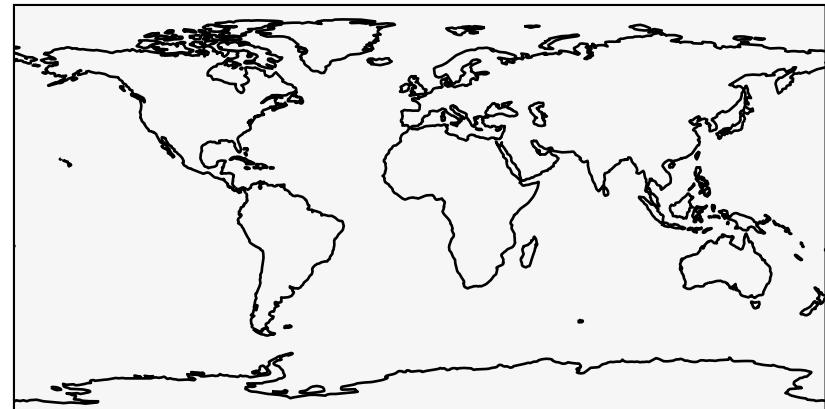
GCC_13.4.0-rc.4 (Dev)
4.0x5.0



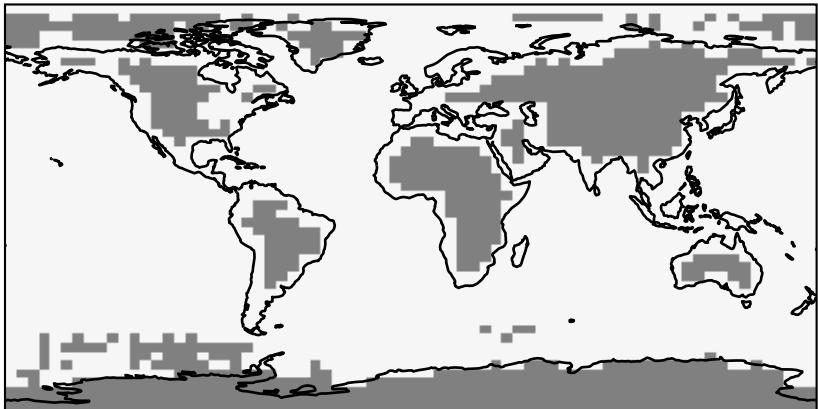
Difference
Dev - Ref, Dynamic Range



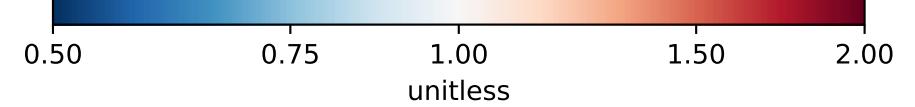
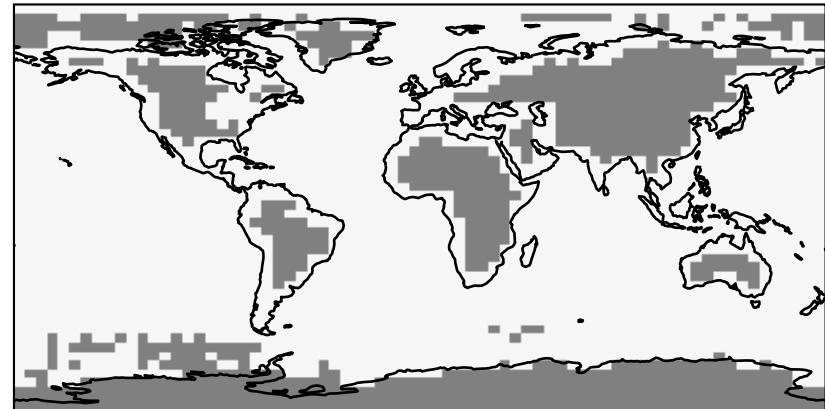
Difference
Dev - Ref, Restricted Range [5%, 95%]



Ratio
Dev/Ref, Dynamic Range

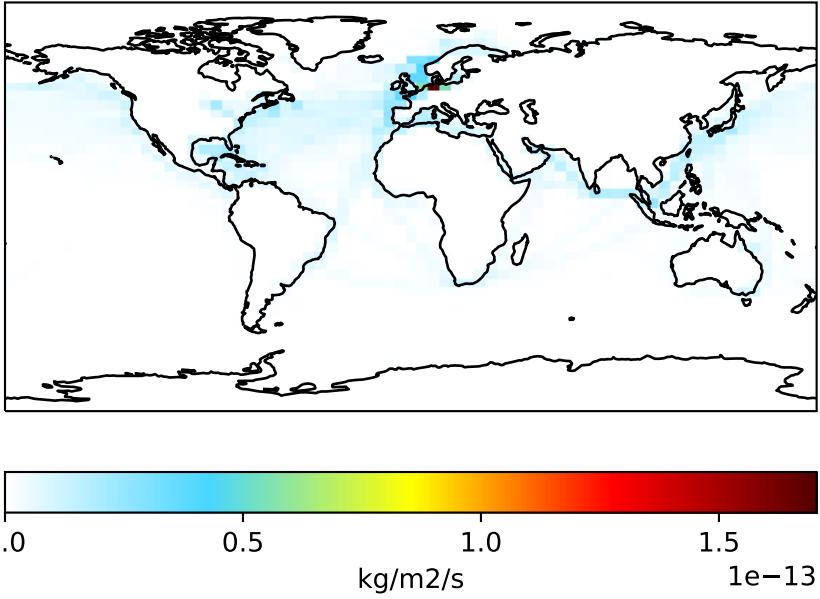


Ratio
Dev/Ref, Fixed Range

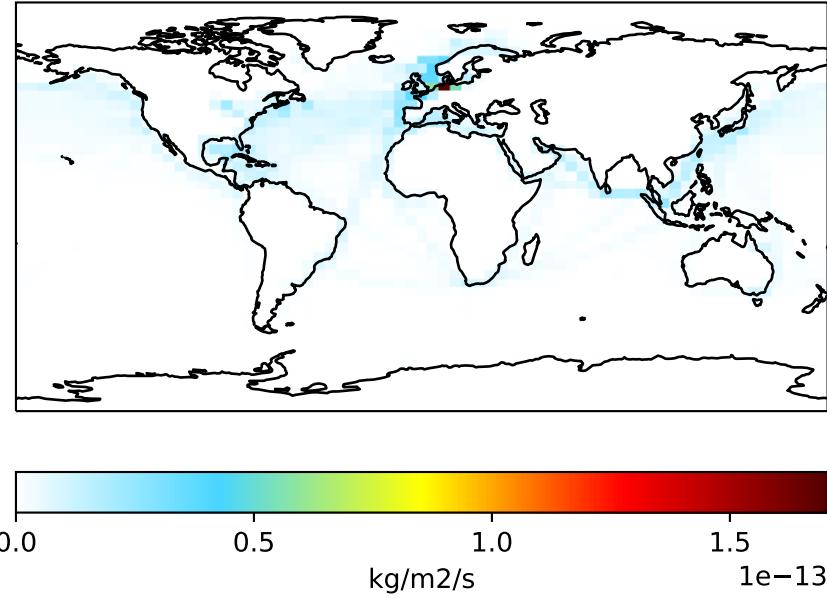


EmisOCPO_Ship

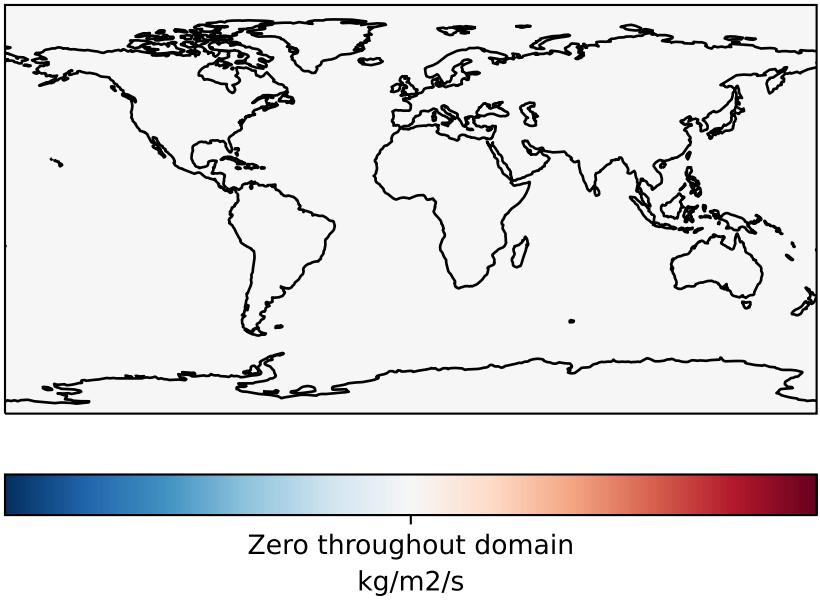
GCC_13.3.0-rc.1 (Ref)
4.0x5.0



GCC_13.4.0-rc.4 (Dev)
4.0x5.0

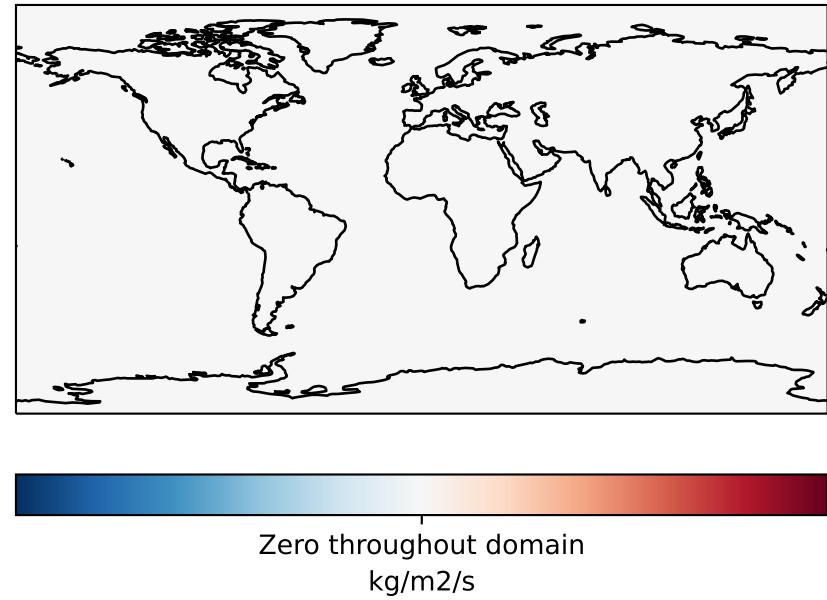


Difference
Dev - Ref, Dynamic Range

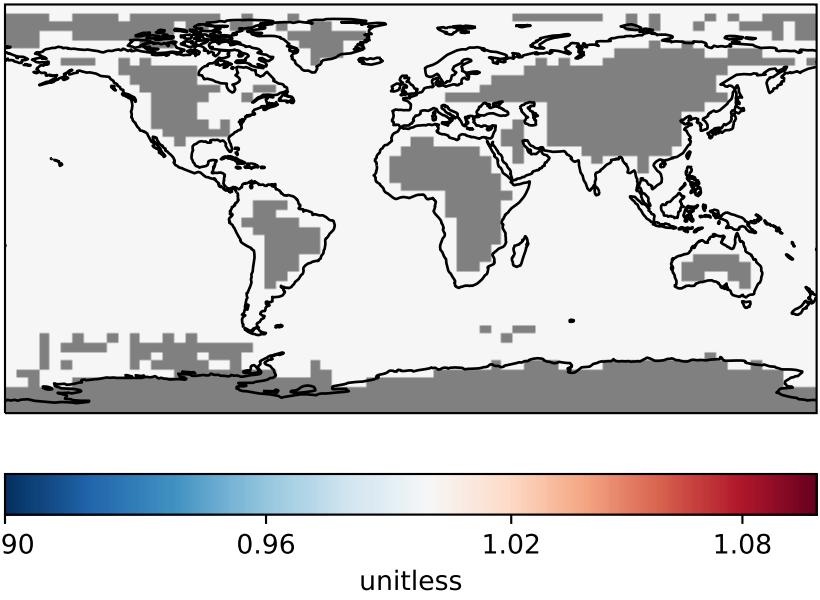


Zero throughout domain
kg/m²/s

Difference
Dev - Ref, Restricted Range [5%,95%]

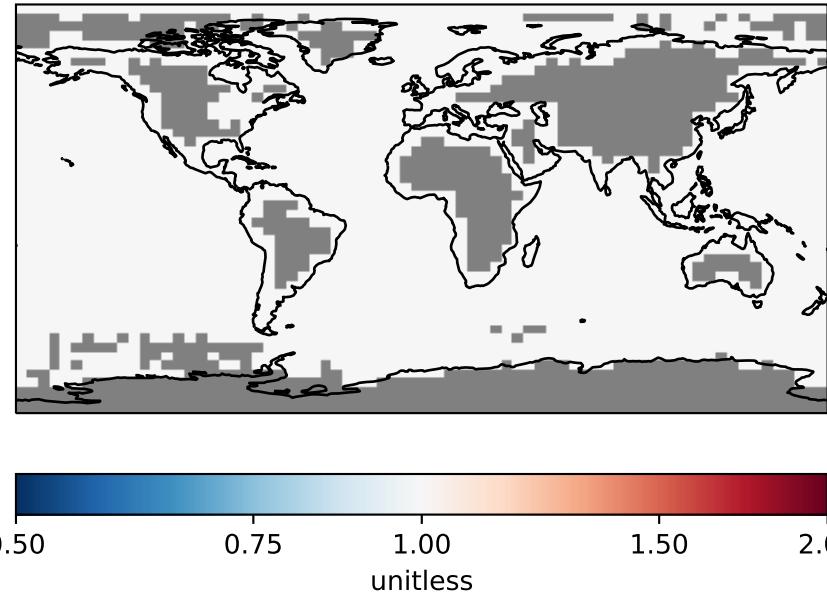


Ratio
Dev/Ref, Dynamic Range



0.90 0.96 1.02 1.08
unitless

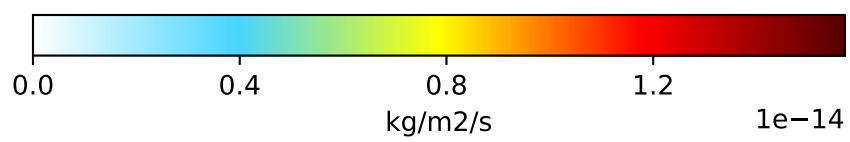
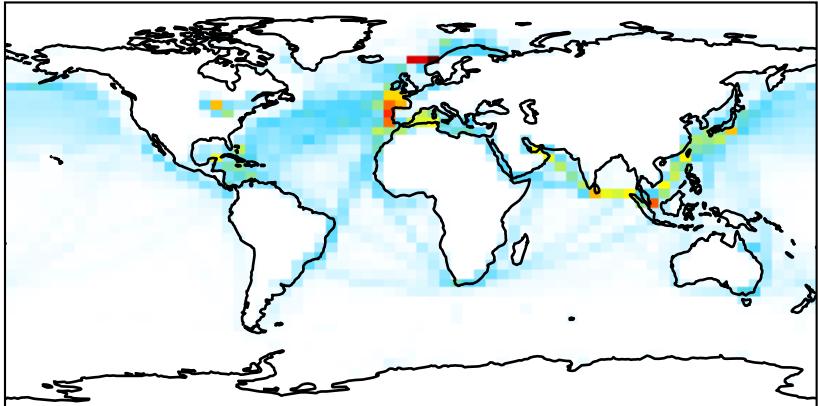
Ratio
Dev/Ref, Fixed Range



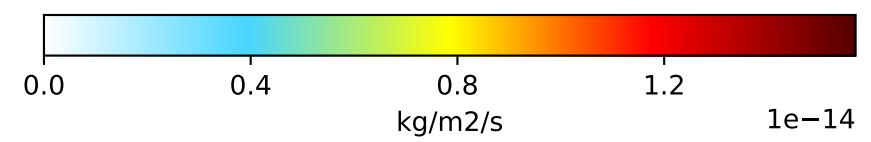
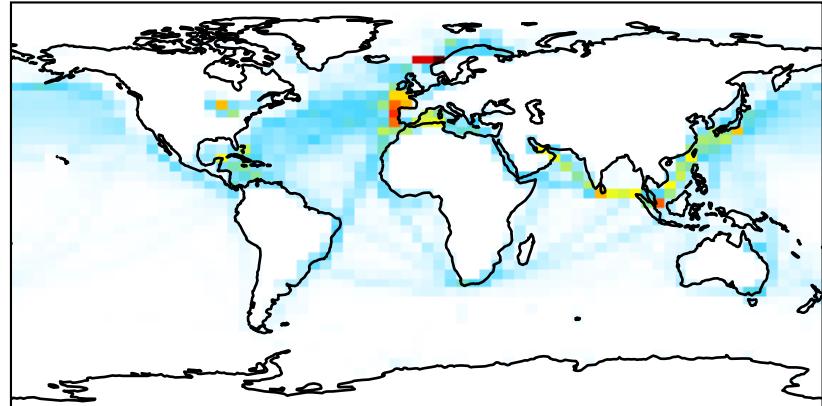
0.50 0.75 1.00 1.50 2.00
unitless

EmispFe_Ship

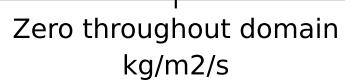
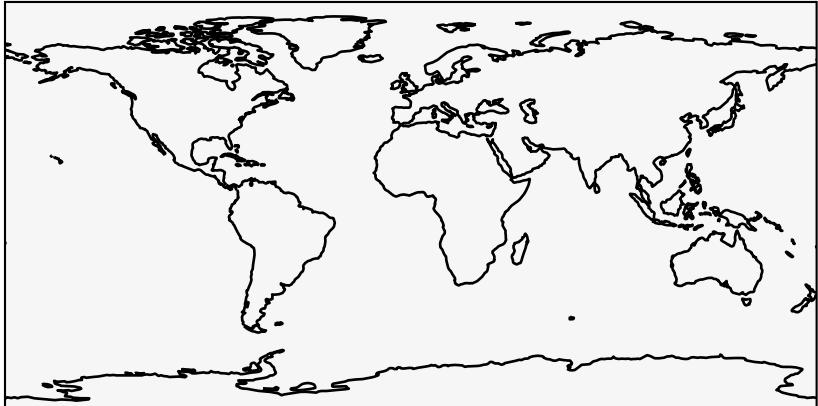
GCC_13.3.0-rc.1 (Ref)
4.0x5.0



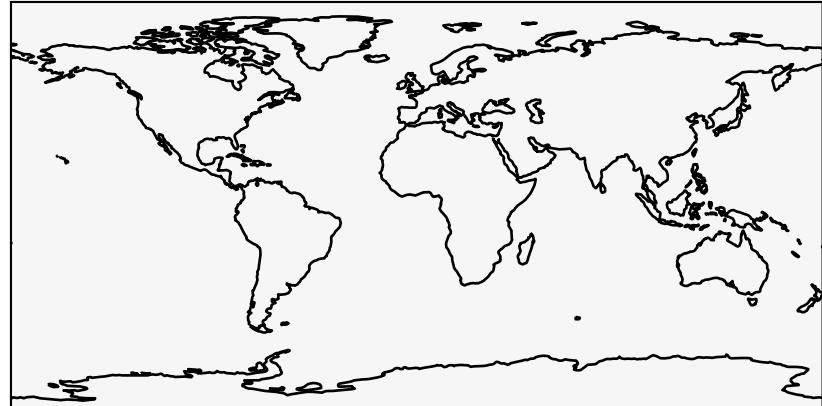
GCC_13.4.0-rc.4 (Dev)
4.0x5.0



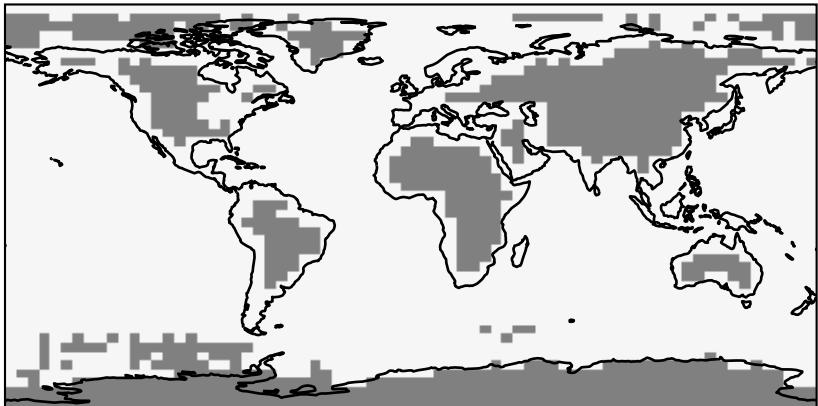
Difference
Dev - Ref, Dynamic Range



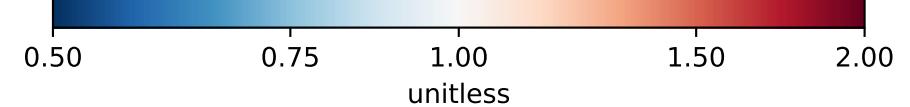
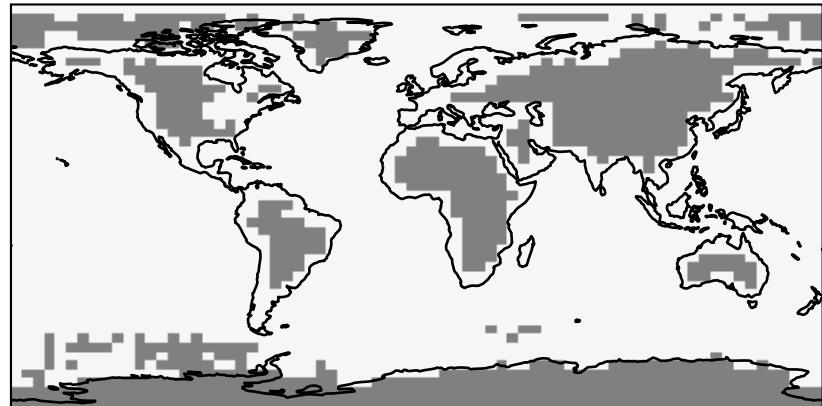
Difference
Dev - Ref, Restricted Range [5%, 95%]



Ratio
Dev/Ref, Dynamic Range

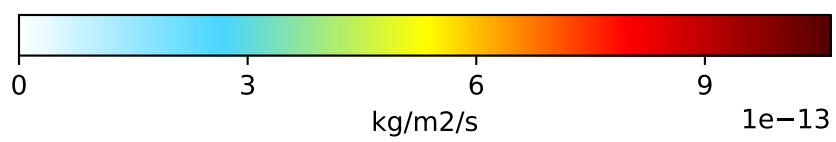
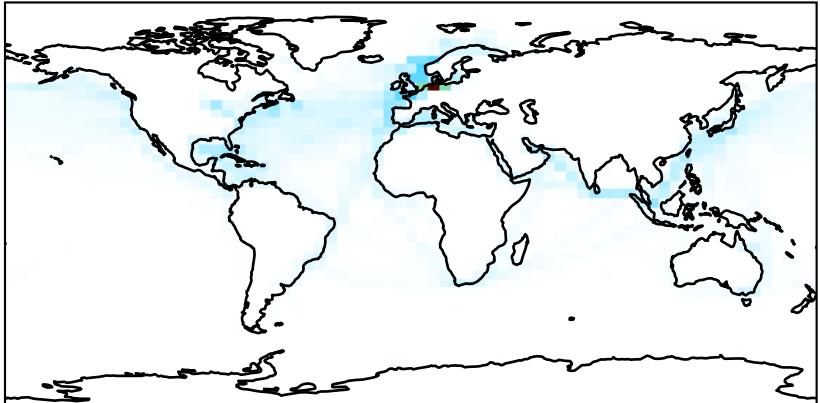


Ratio
Dev/Ref, Fixed Range

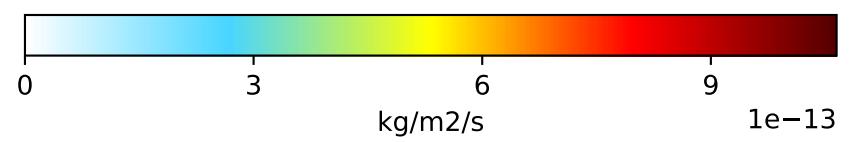
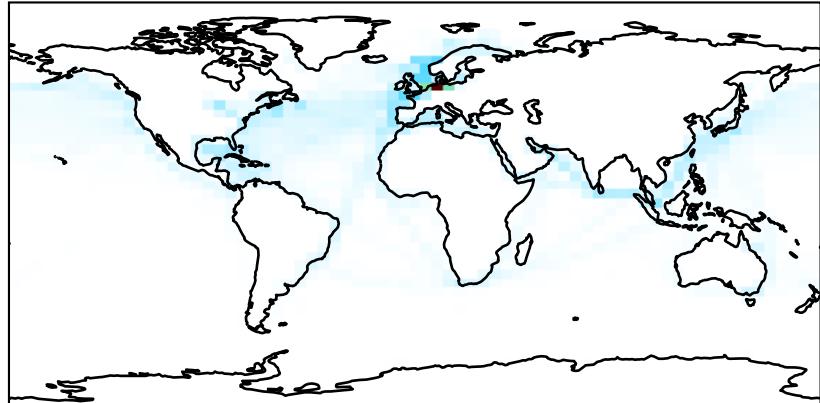


EmisPRPE_Ship

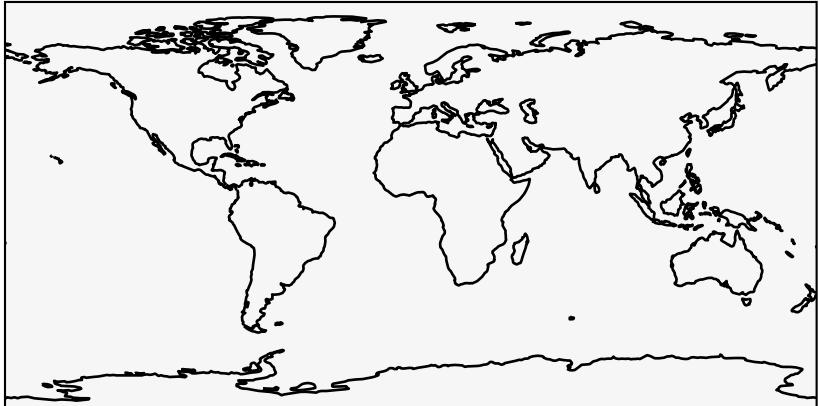
GCC_13.3.0-rc.1 (Ref)
4.0x5.0



GCC_13.4.0-rc.4 (Dev)
4.0x5.0

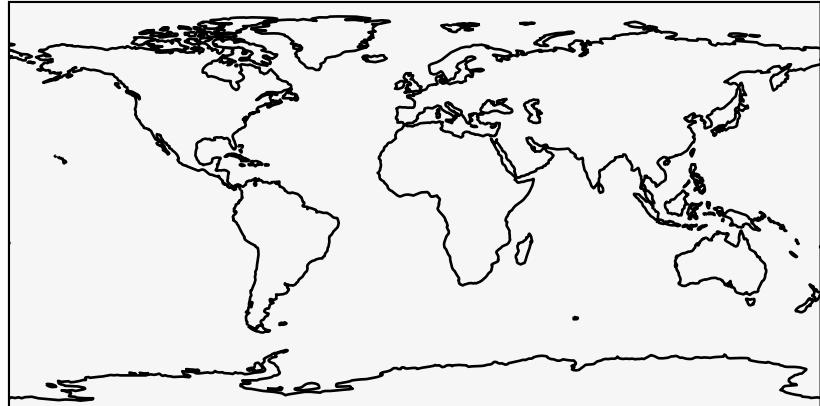


Difference
Dev - Ref, Dynamic Range



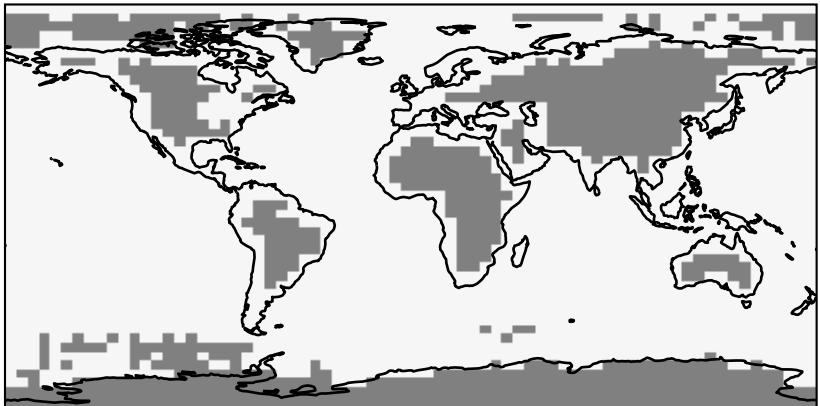
Zero throughout domain
kg/m²/s

Difference
Dev - Ref, Restricted Range [5%, 95%]



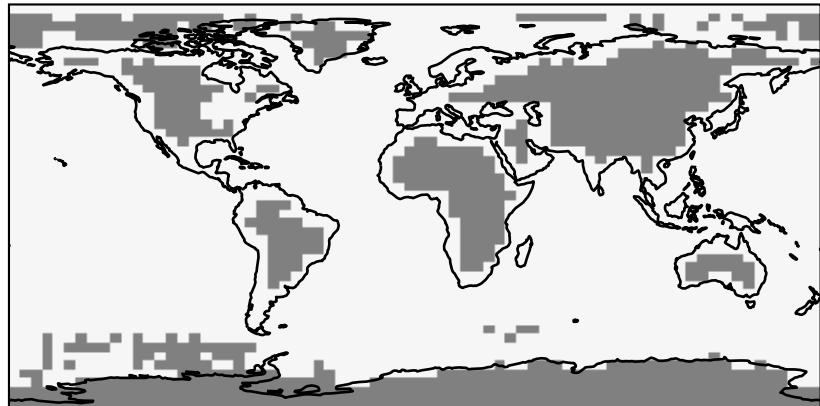
Zero throughout domain
kg/m²/s

Ratio
Dev/Ref, Dynamic Range



0.90 0.96 1.02 1.08
unitless

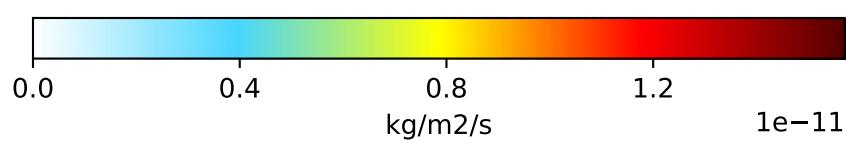
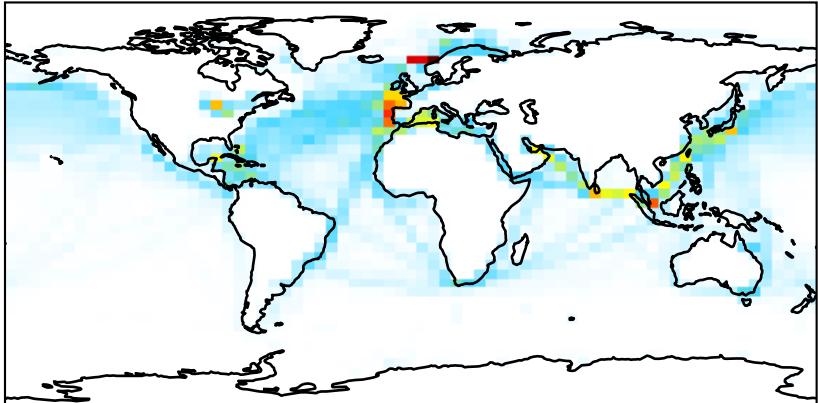
Ratio
Dev/Ref, Fixed Range



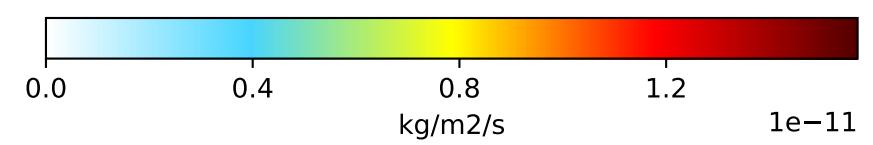
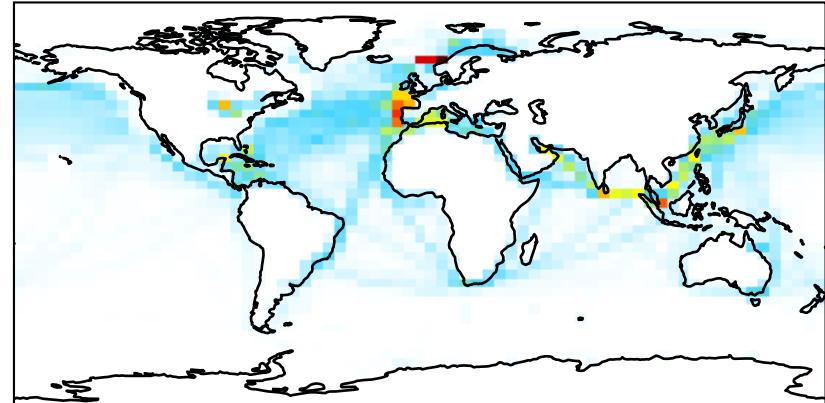
0.50 0.75 1.00 1.50 2.00
unitless

EmisSO2_Ship

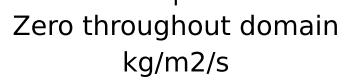
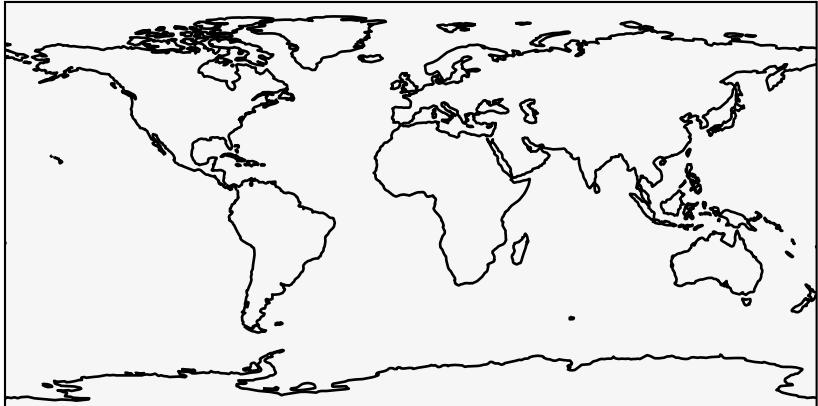
GCC_13.3.0-rc.1 (Ref)
4.0x5.0



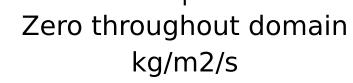
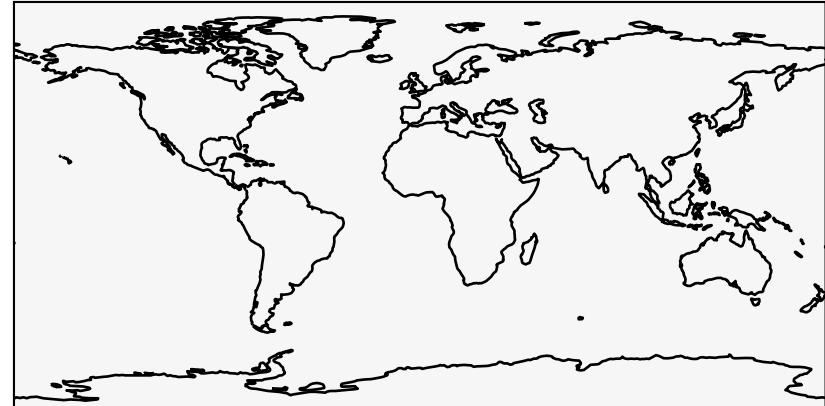
GCC_13.4.0-rc.4 (Dev)
4.0x5.0



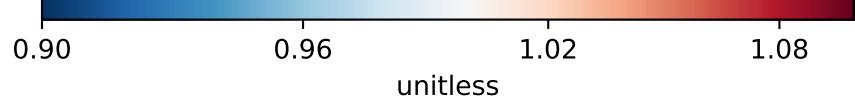
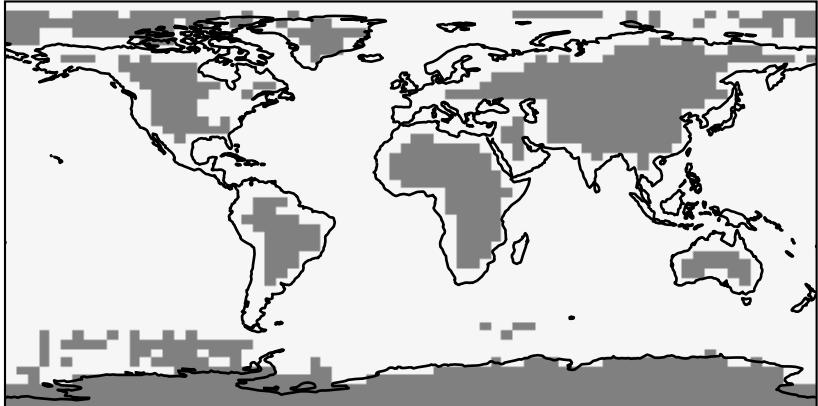
Difference
Dev - Ref, Dynamic Range



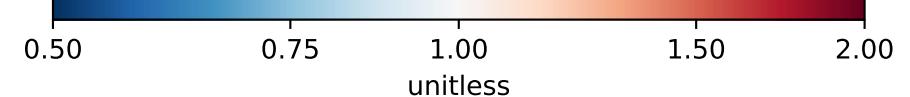
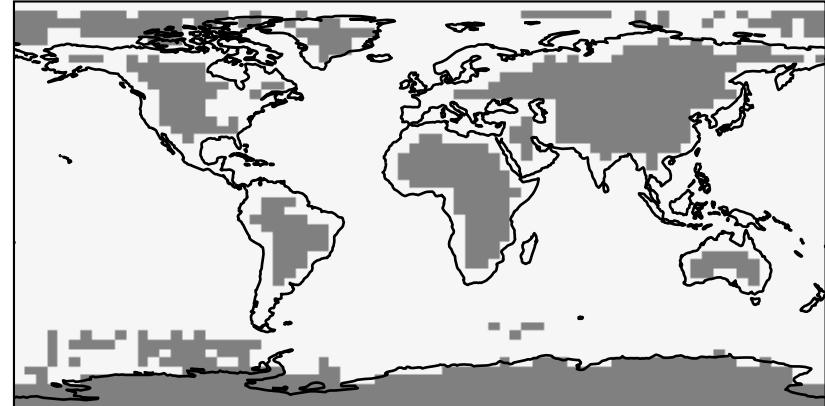
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

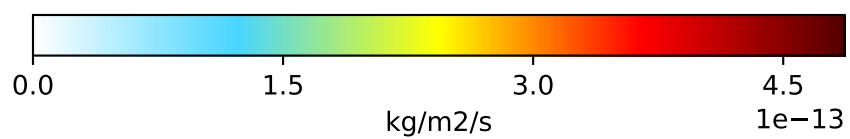
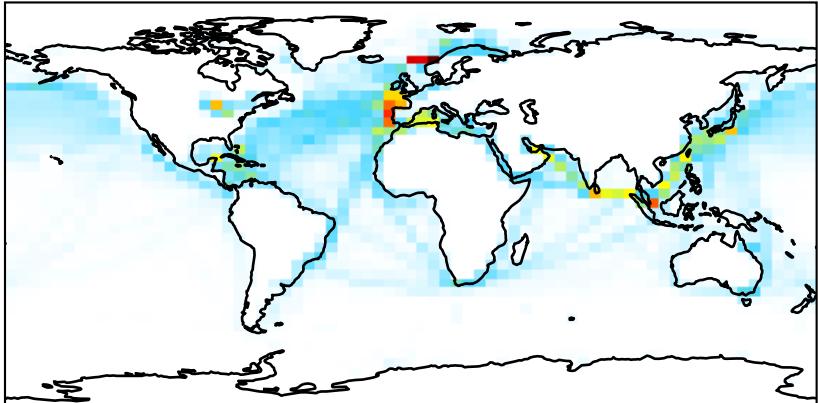


Ratio
Dev/Ref, Fixed Range

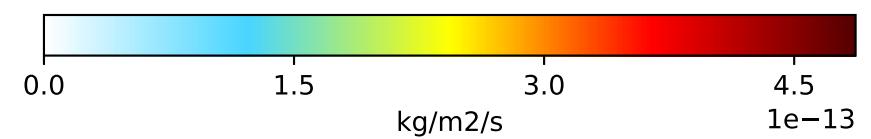
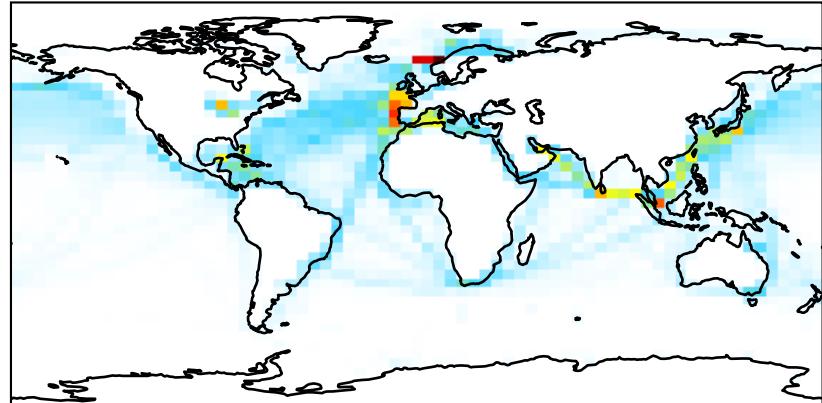


EmisSO4_Ship

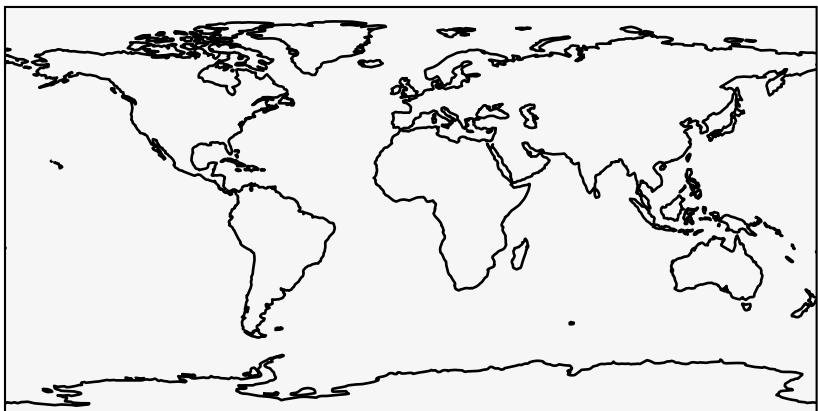
GCC_13.3.0-rc.1 (Ref)
4.0x5.0



GCC_13.4.0-rc.4 (Dev)
4.0x5.0

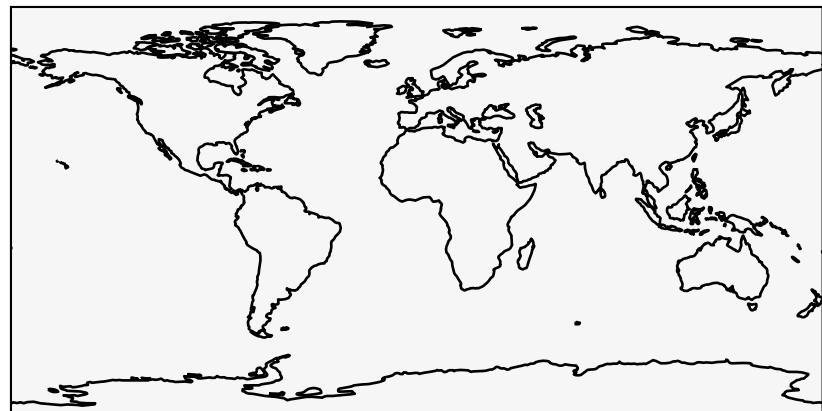


Difference
Dev - Ref, Dynamic Range



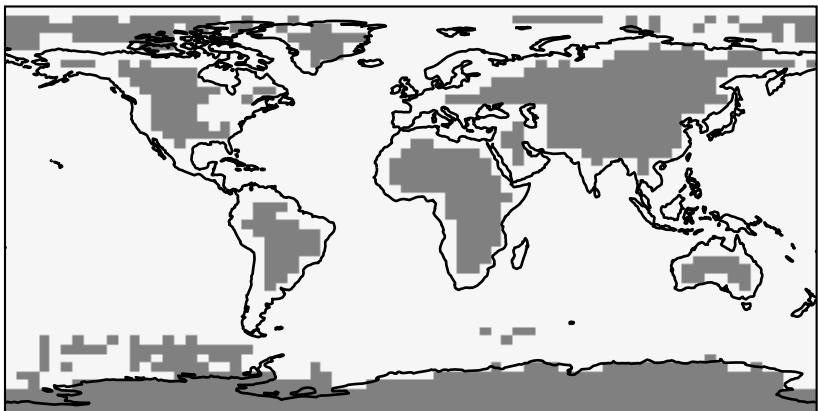
Zero throughout domain
kg/m²/s

Difference
Dev - Ref, Restricted Range [5%, 95%]



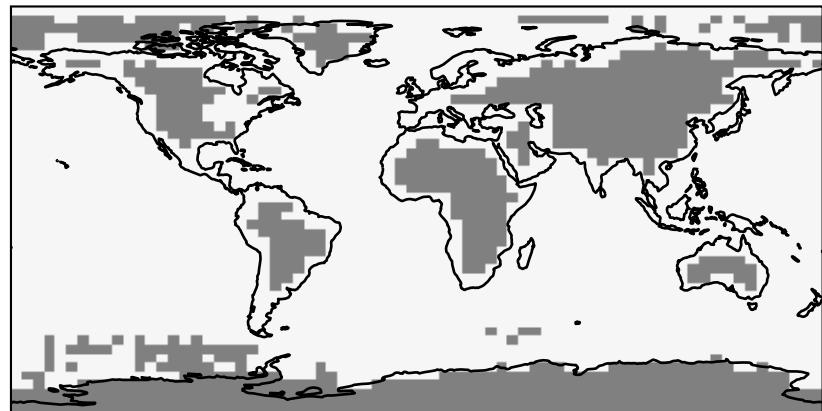
Zero throughout domain
kg/m²/s

Ratio
Dev/Ref, Dynamic Range



0.90 0.96 1.02 1.08
unitless

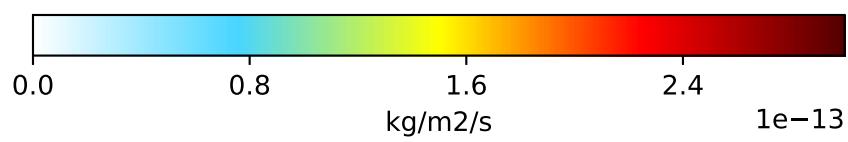
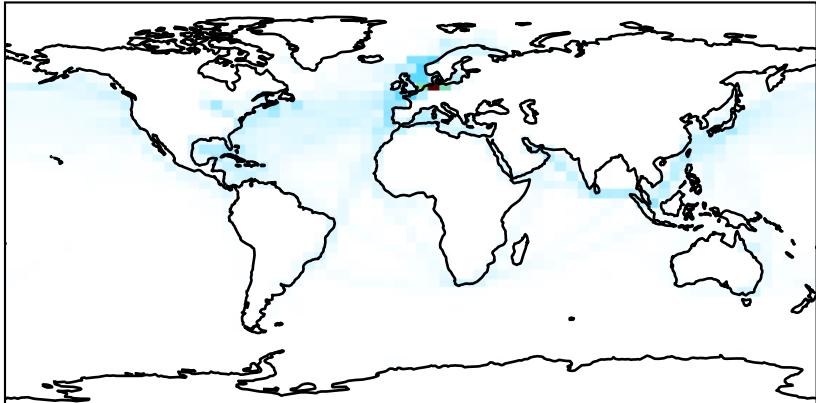
Ratio
Dev/Ref, Fixed Range



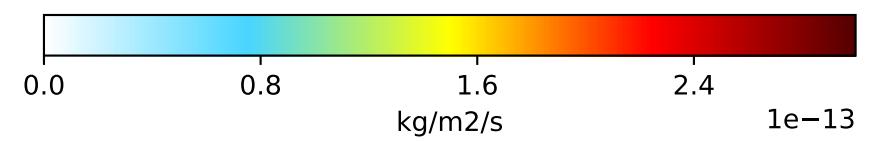
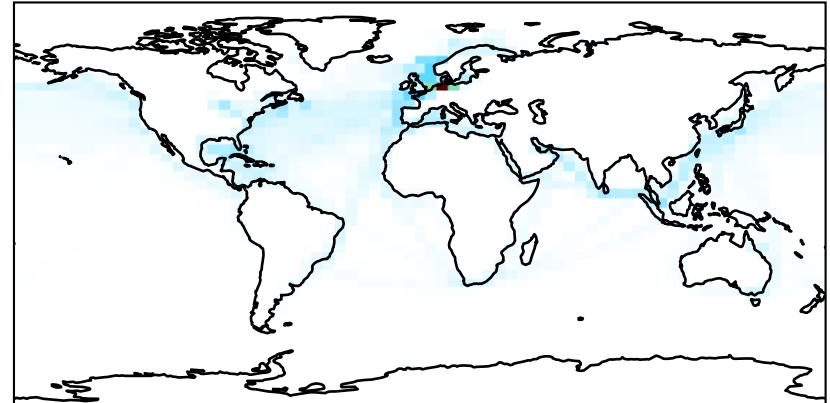
0.50 0.75 1.00 1.50 2.00
unitless

EmisSOAP_Ship

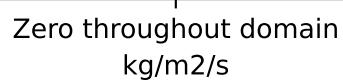
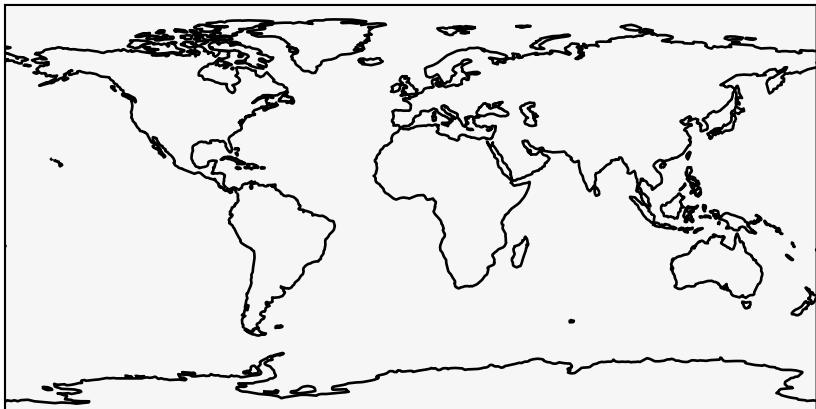
GCC_13.3.0-rc.1 (Ref)
4.0x5.0



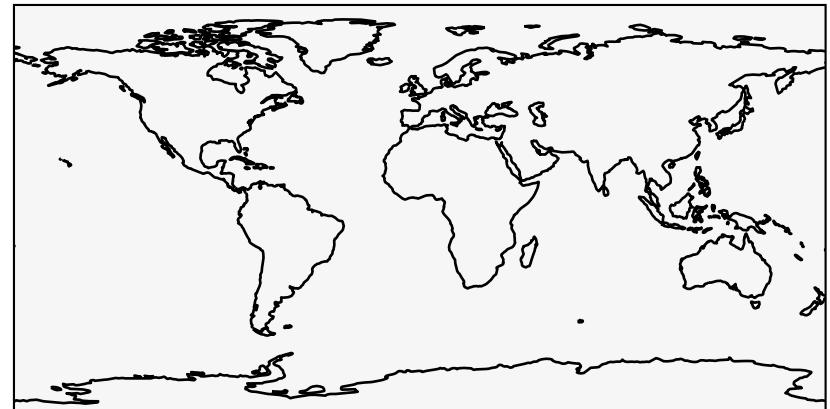
GCC_13.4.0-rc.4 (Dev)
4.0x5.0



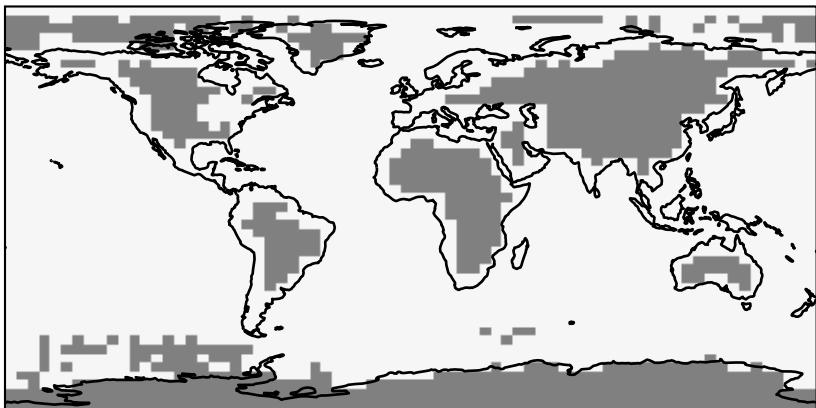
Difference
Dev - Ref, Dynamic Range



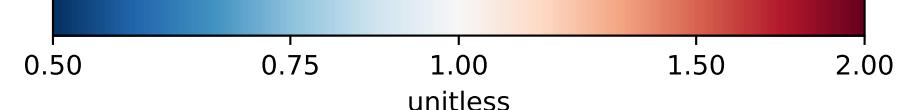
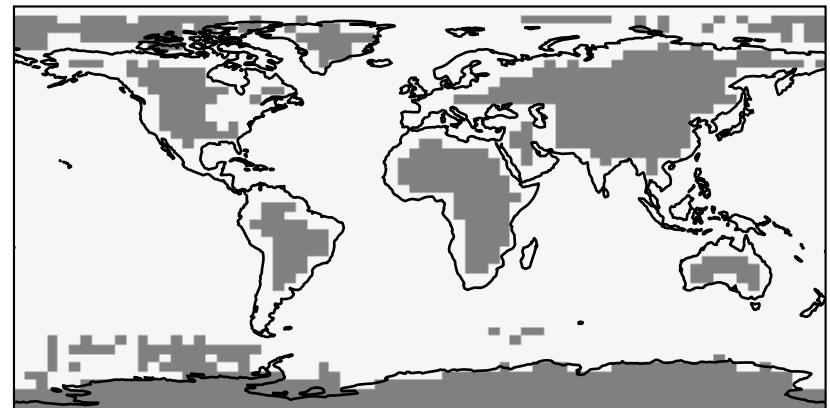
Difference
Dev - Ref, Restricted Range [5%, 95%]



Ratio
Dev/Ref, Dynamic Range

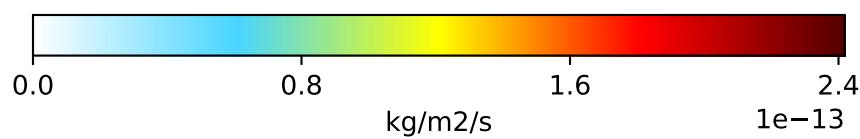
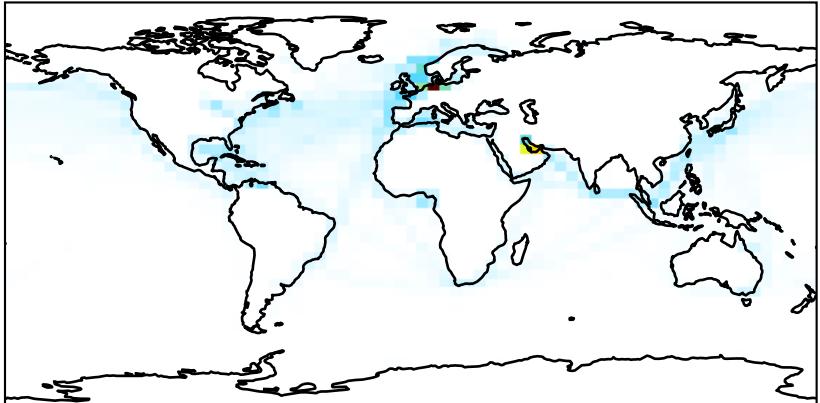


Ratio
Dev/Ref, Fixed Range

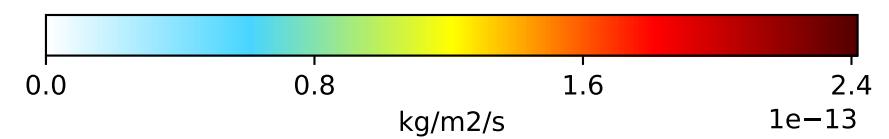
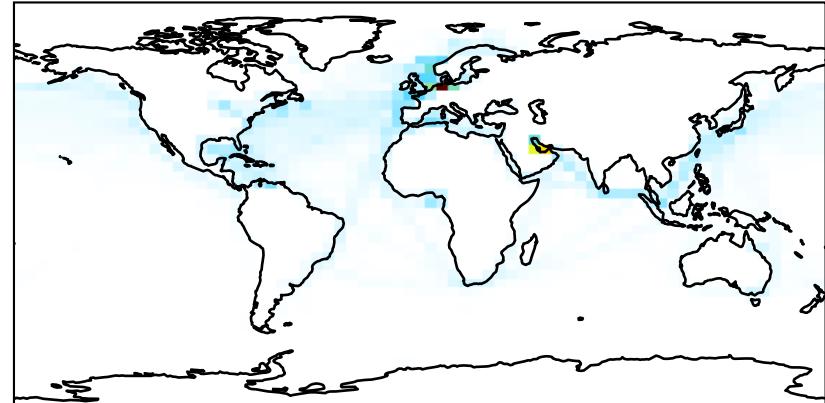


EmisTOLU_Ship

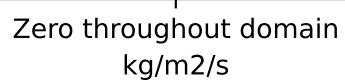
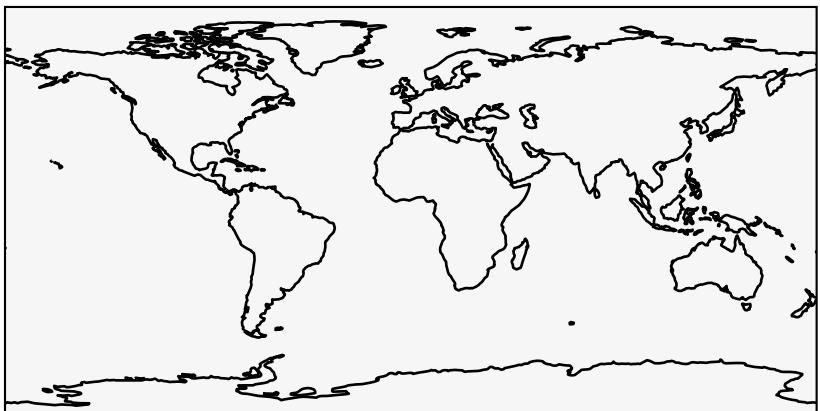
GCC_13.3.0-rc.1 (Ref)
4.0x5.0



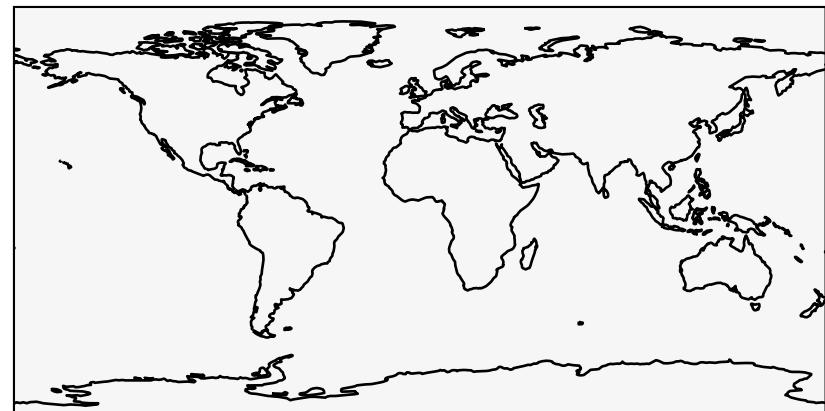
GCC_13.4.0-rc.4 (Dev)
4.0x5.0



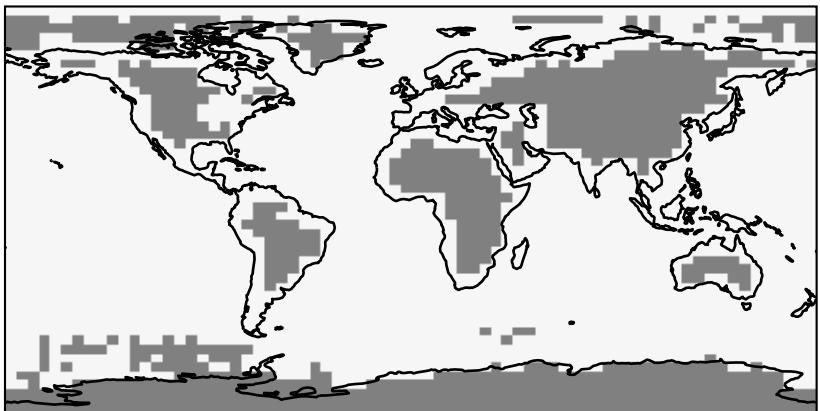
Difference
Dev - Ref, Dynamic Range



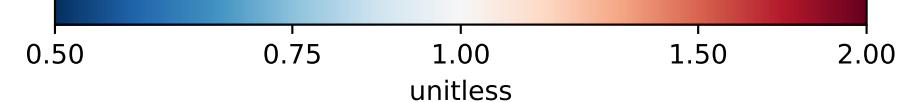
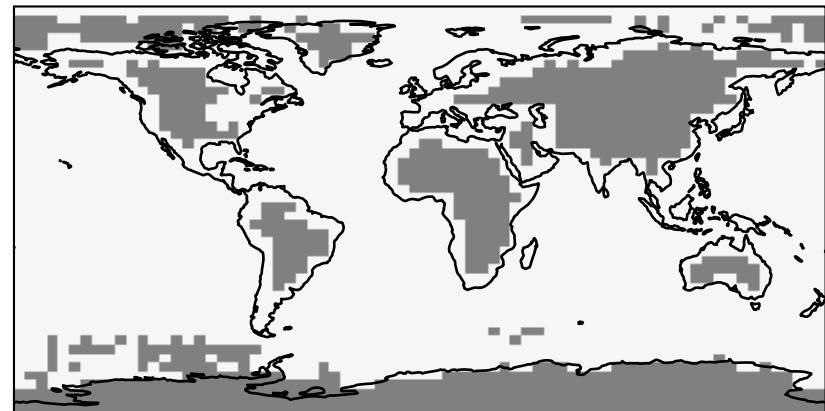
Difference
Dev - Ref, Restricted Range [5%, 95%]



Ratio
Dev/Ref, Dynamic Range

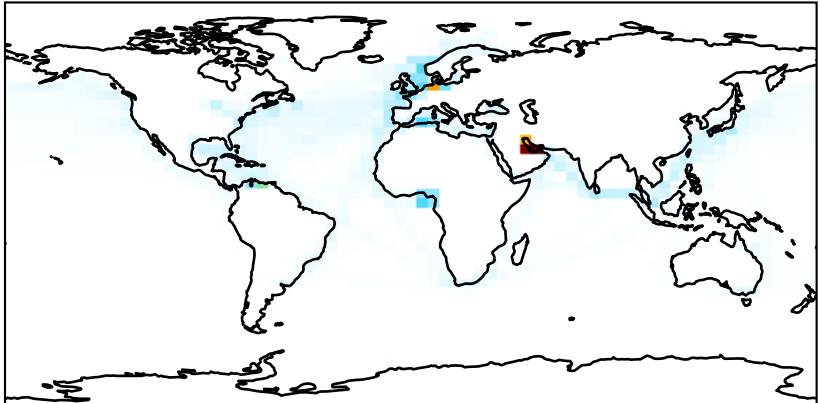


Ratio
Dev/Ref, Fixed Range

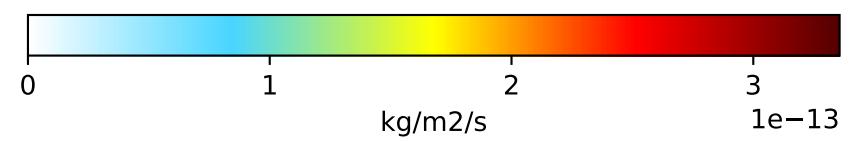
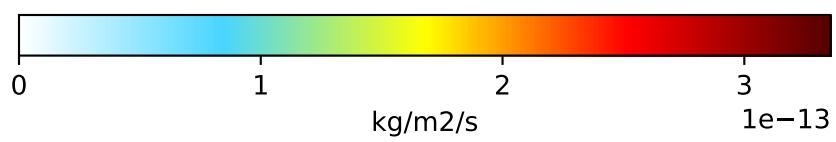
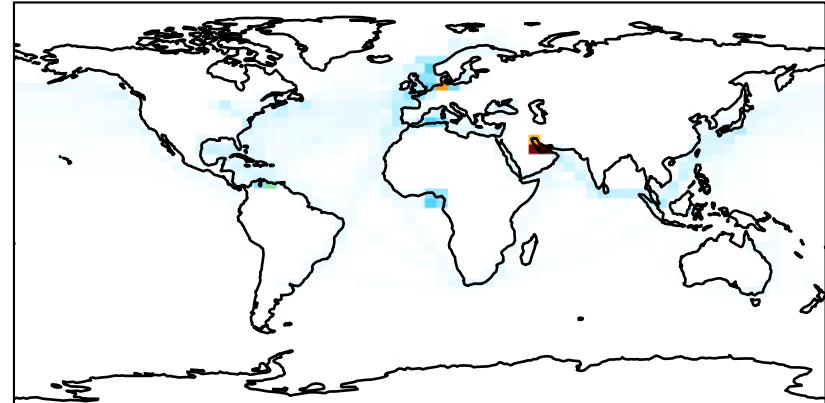


EmisXYLE_Ship

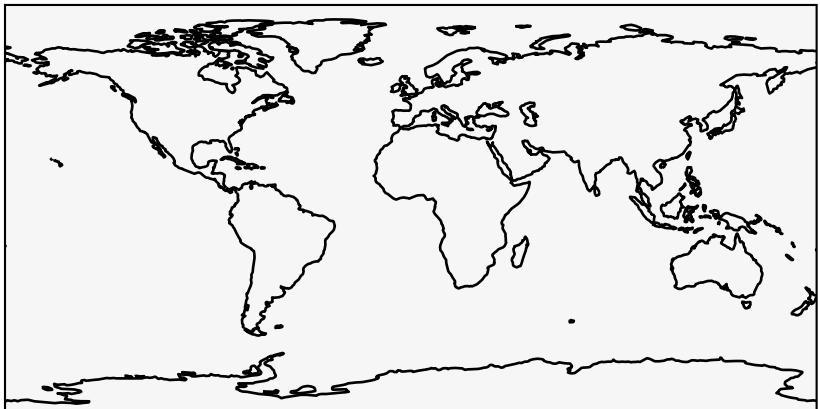
GCC_13.3.0-rc.1 (Ref)
4.0x5.0



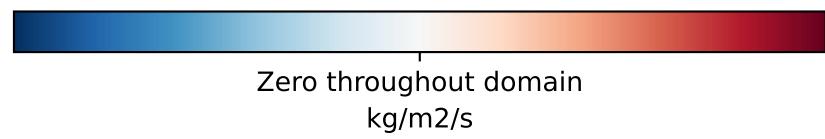
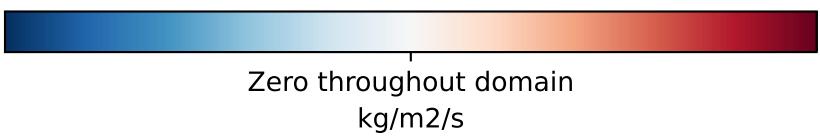
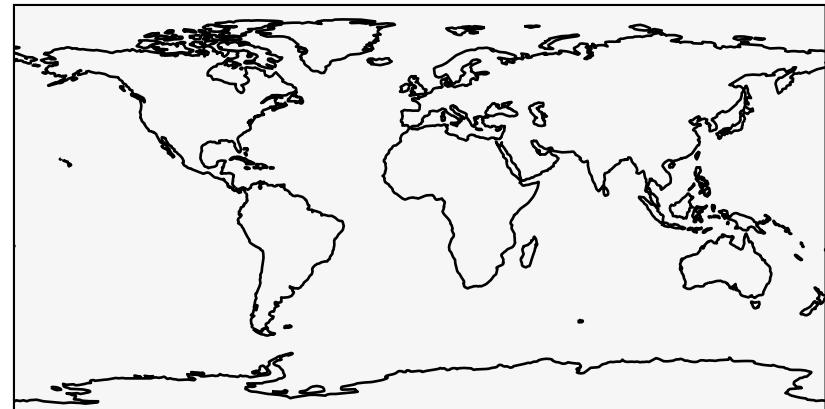
GCC_13.4.0-rc.4 (Dev)
4.0x5.0



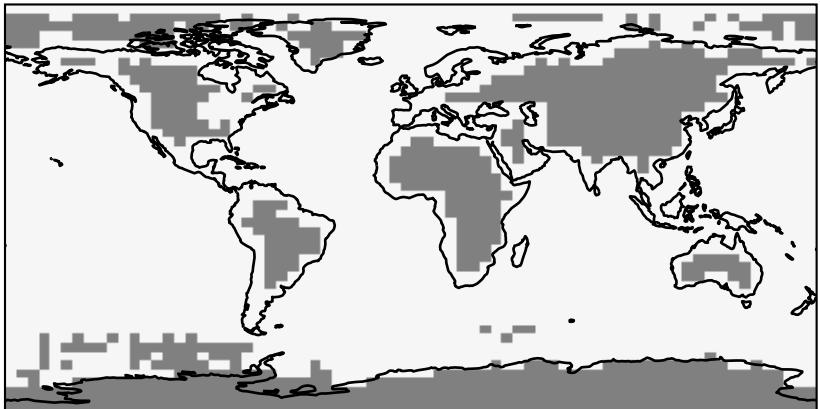
Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%, 95%]



Ratio
Dev/Ref, Dynamic Range



Ratio
Dev/Ref, Fixed Range

