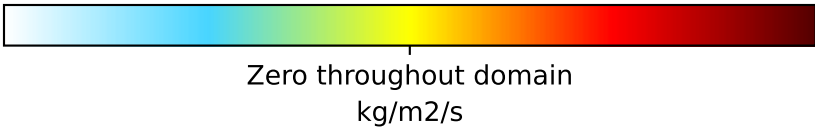
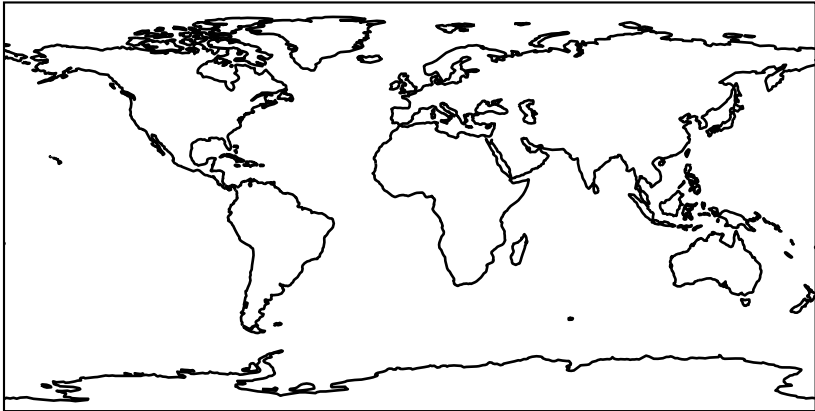
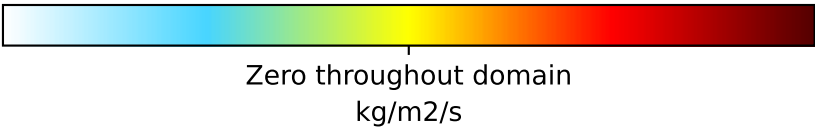
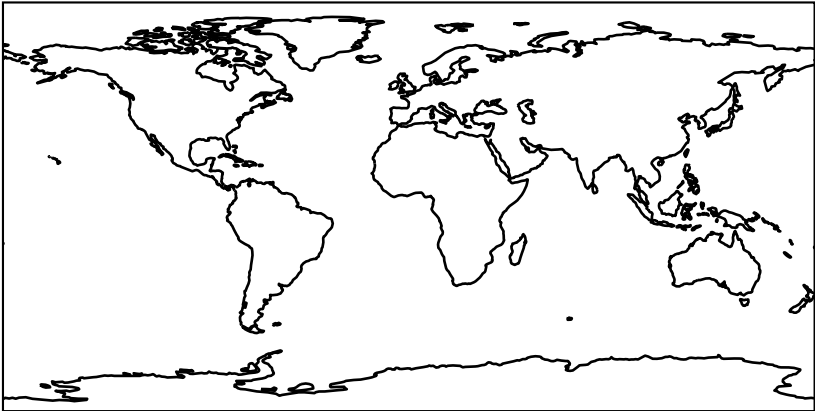


# EmisBrSALA\_Natural

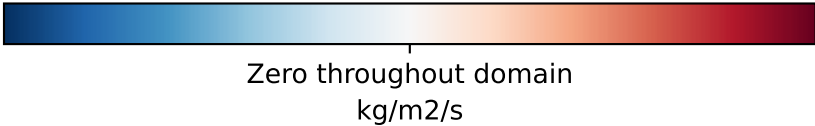
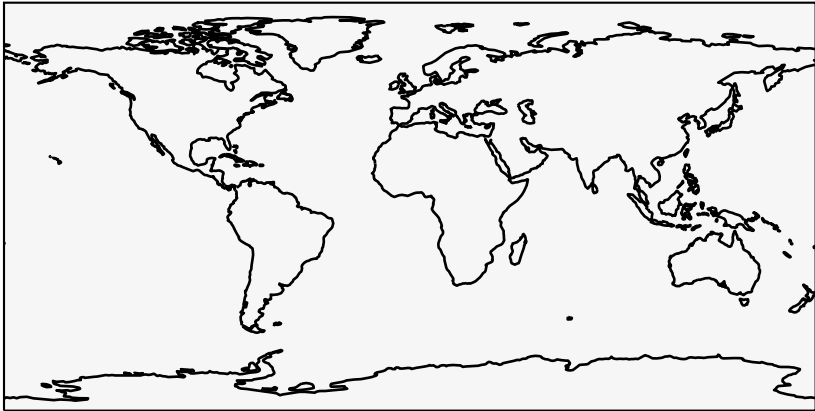
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



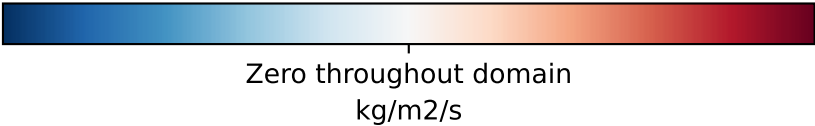
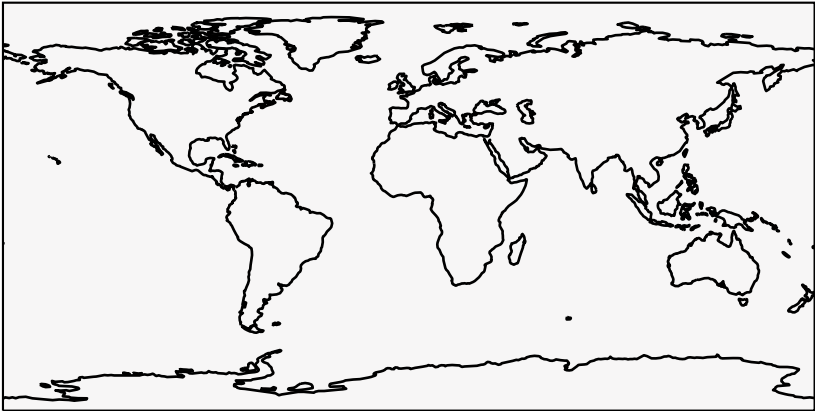
GCHP\_13.4.0-rc.4 (Dev)  
c48



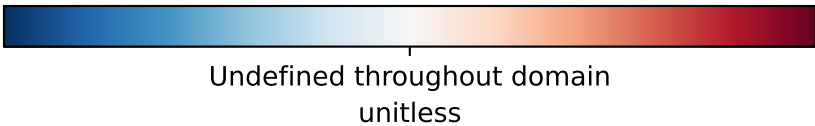
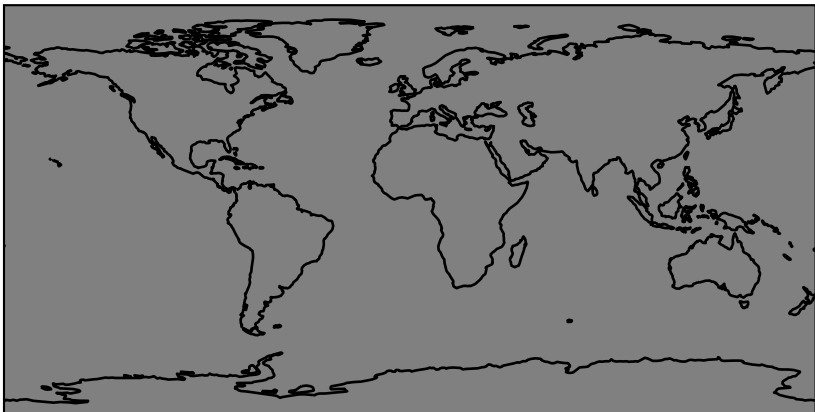
Difference (1x1.25)  
Dev - Ref, Dynamic Range



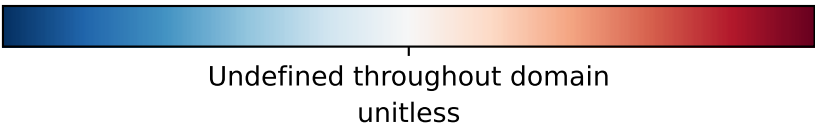
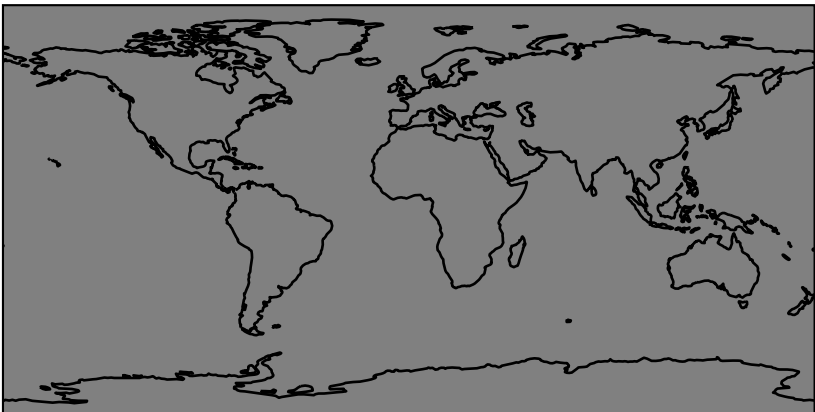
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

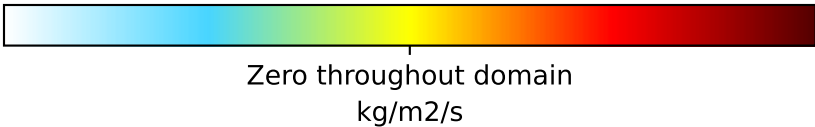
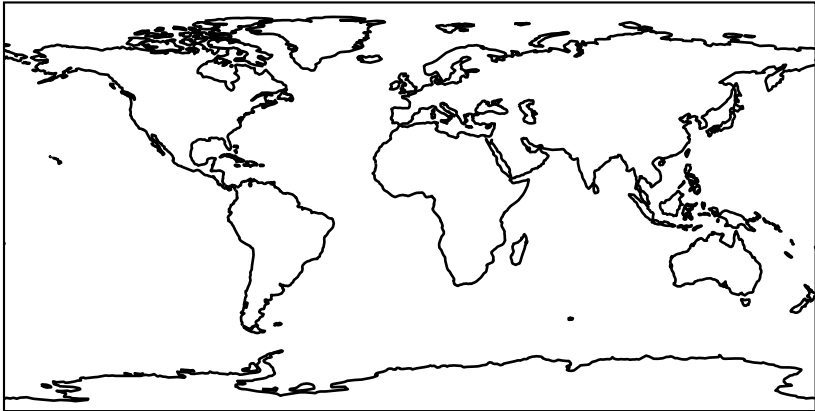


Ratio (1x1.25)  
Dev/Ref, Fixed Range

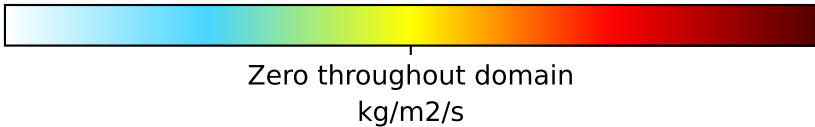
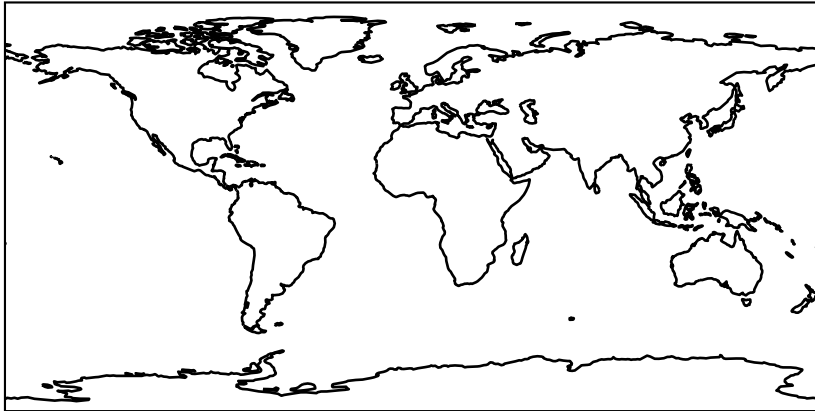


# EmisBrSALC\_Natural

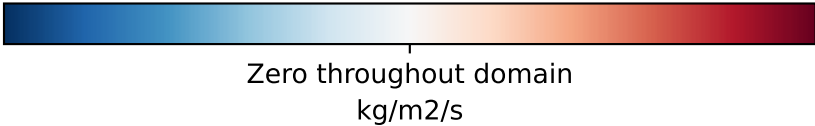
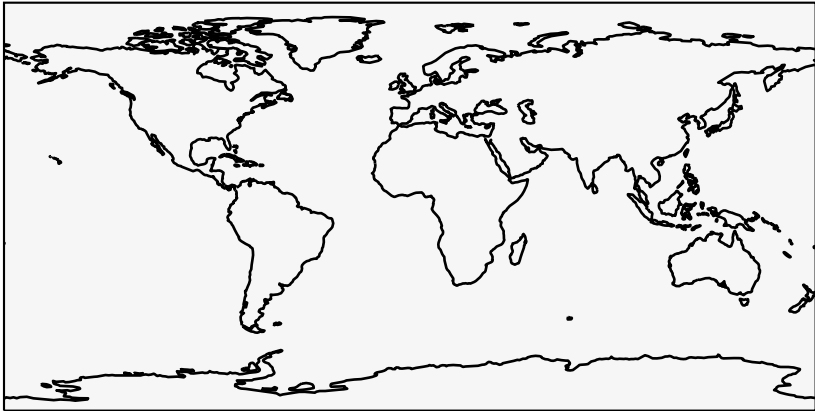
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



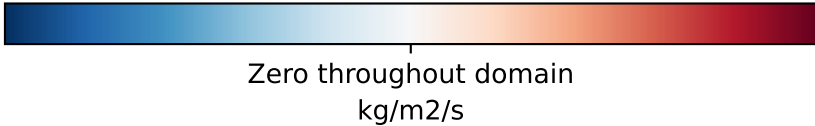
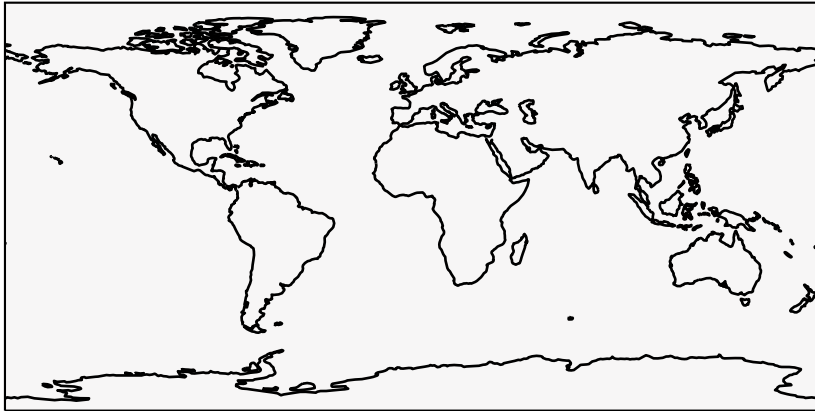
GCHP\_13.4.0-rc.4 (Dev)  
c48



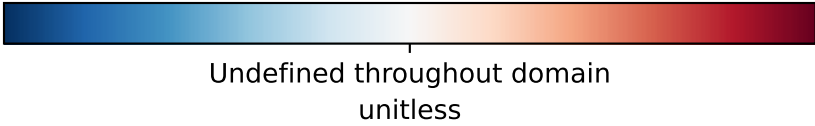
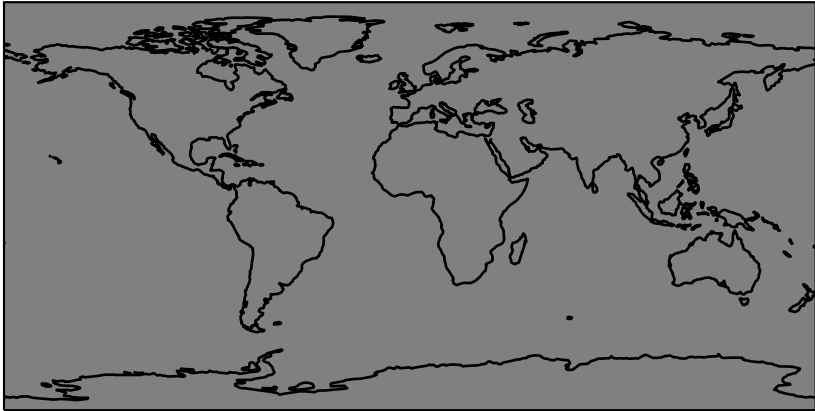
Difference (1x1.25)  
Dev - Ref, Dynamic Range



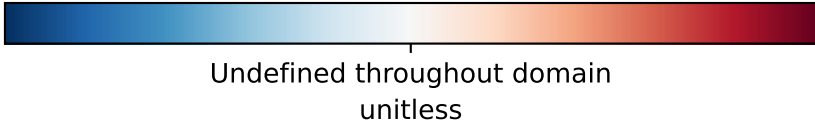
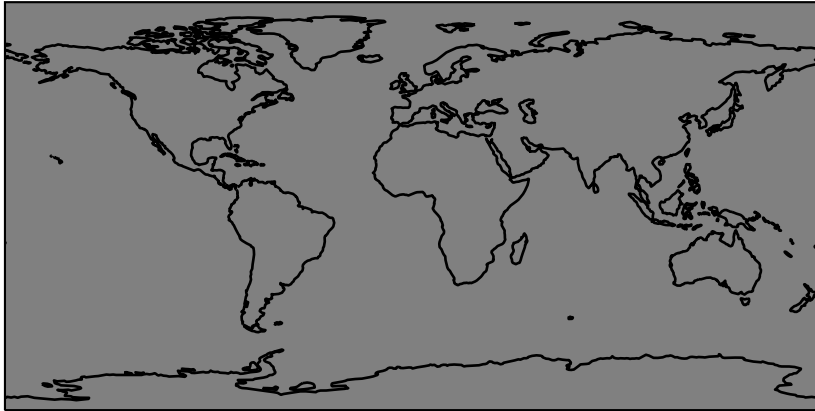
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

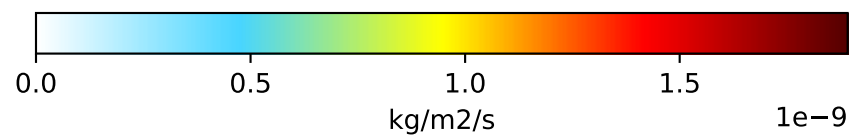
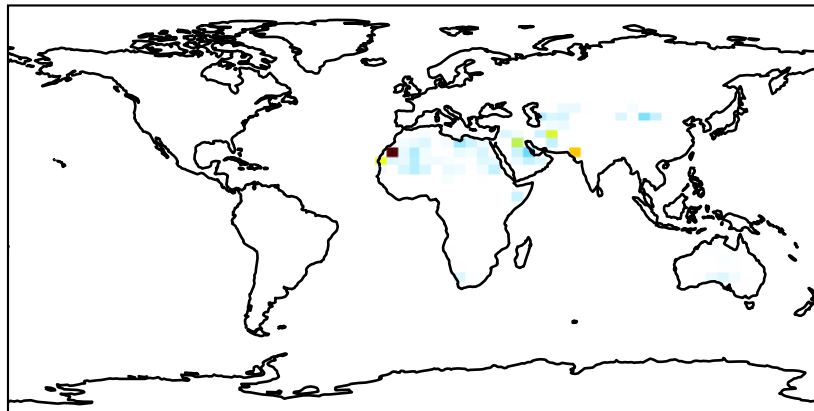


Ratio (1x1.25)  
Dev/Ref, Fixed Range

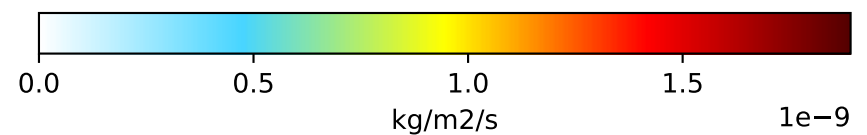
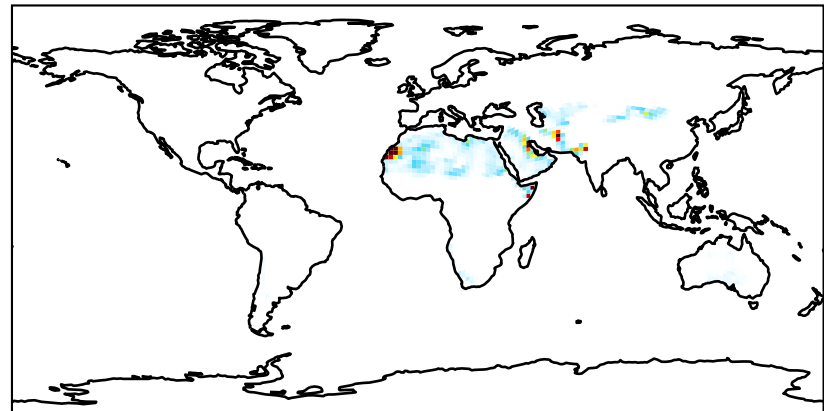


# EmisDST1\_Natural

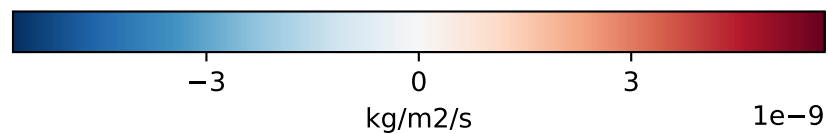
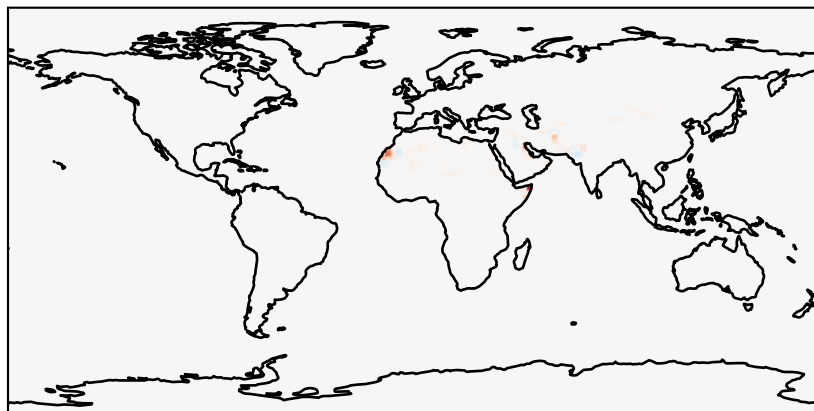
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



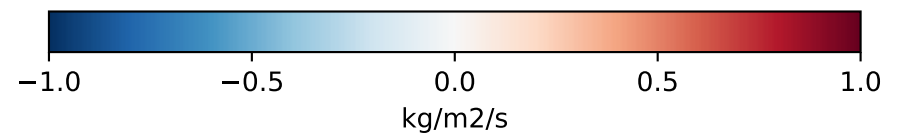
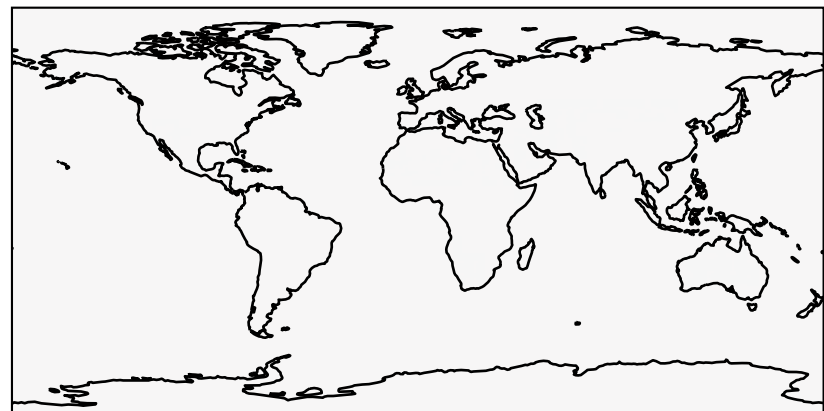
GCHP\_13.4.0-rc.4 (Dev)  
c48



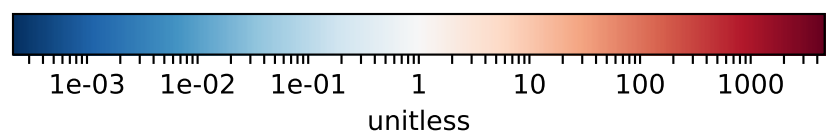
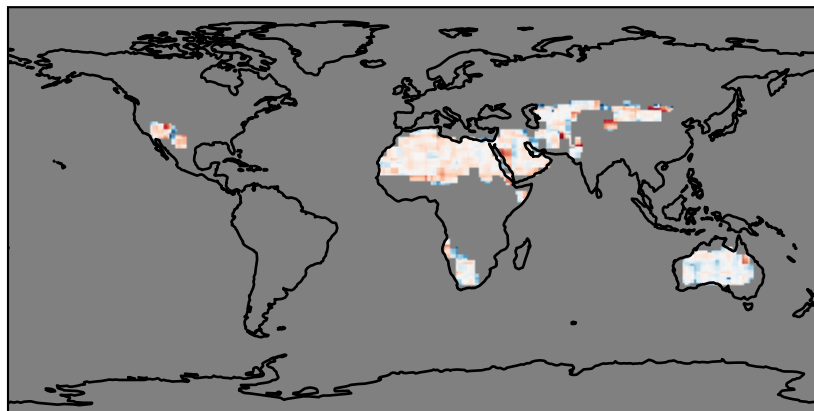
Difference (1x1.25)  
Dev - Ref, Dynamic Range



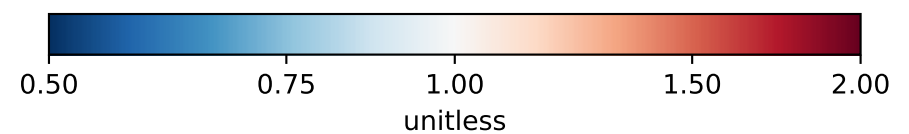
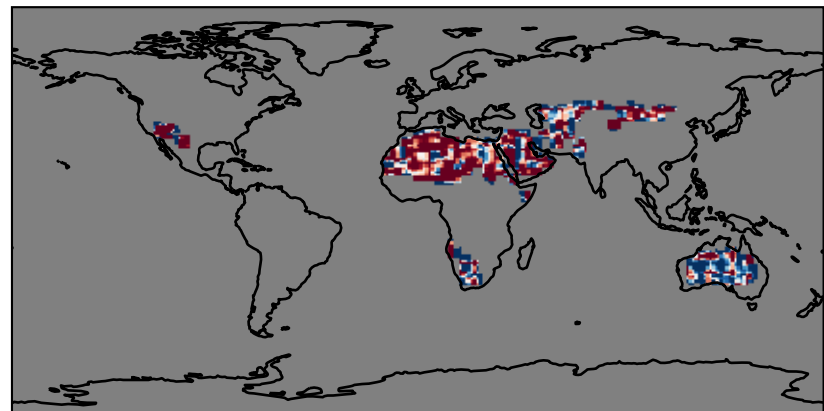
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

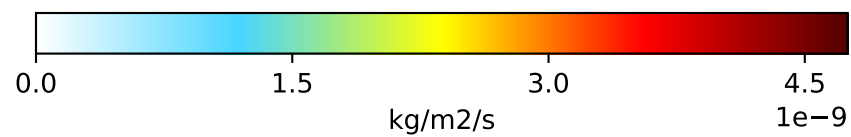
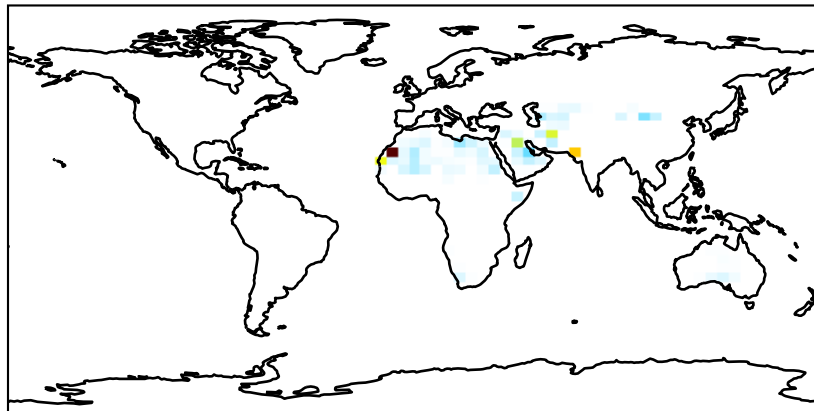


Ratio (1x1.25)  
Dev/Ref, Fixed Range

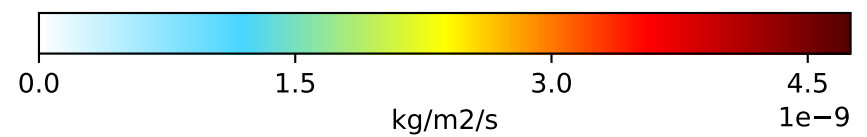
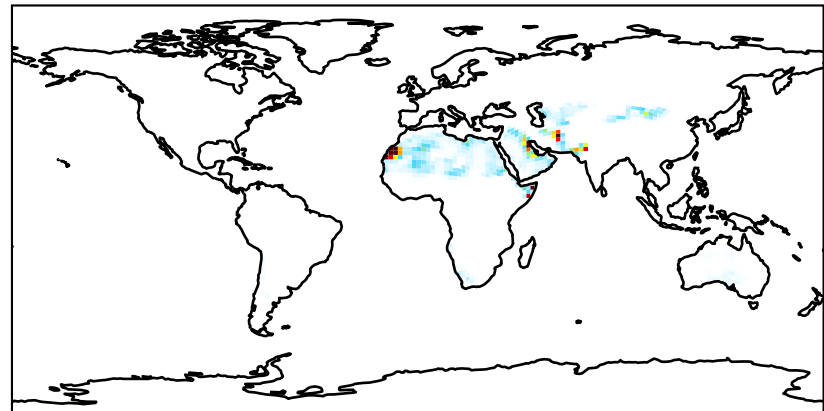


# EmisDST2\_Natural

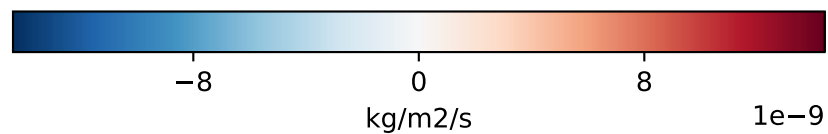
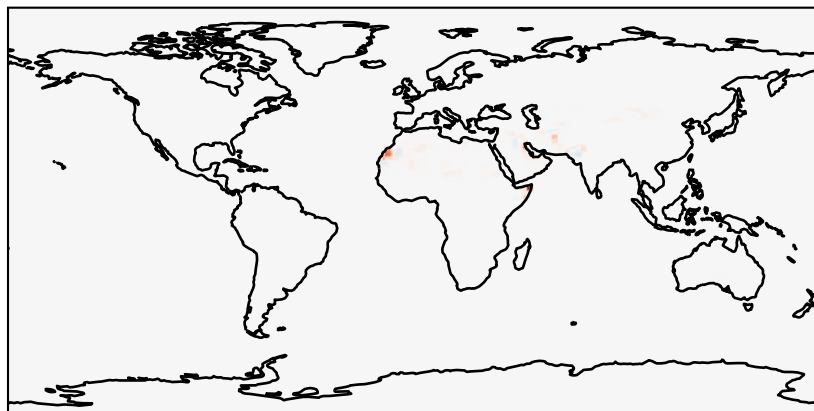
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



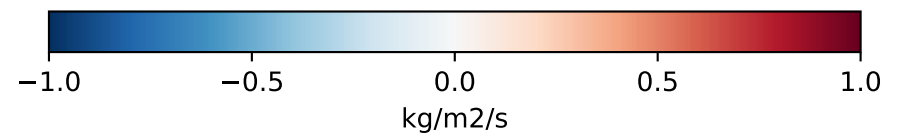
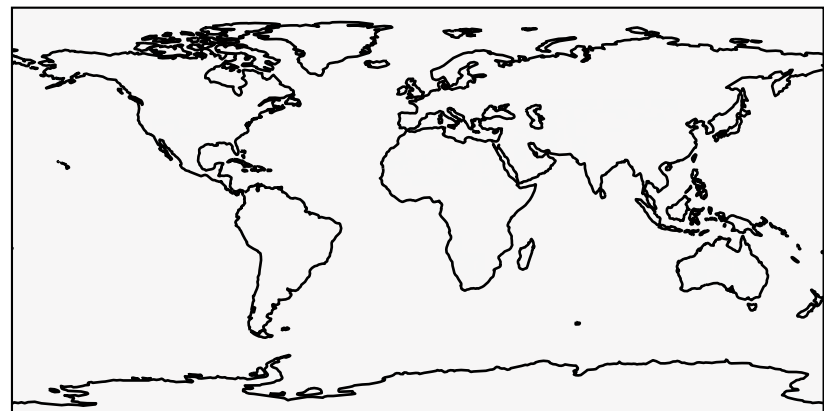
GCHP\_13.4.0-rc.4 (Dev)  
c48



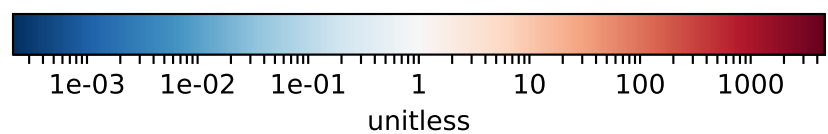
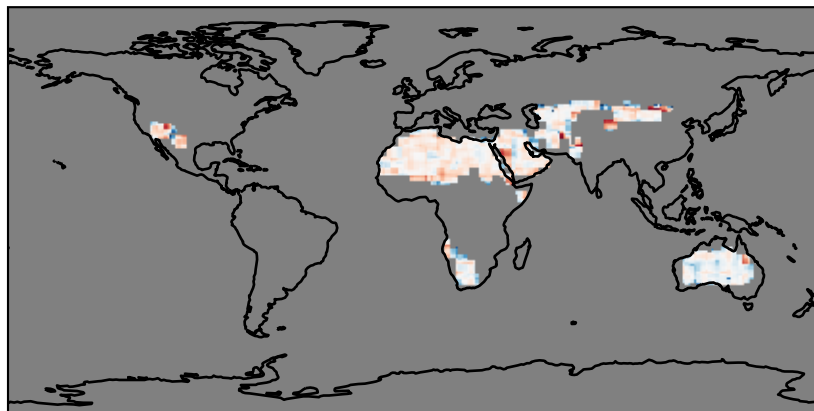
Difference (1x1.25)  
Dev - Ref, Dynamic Range



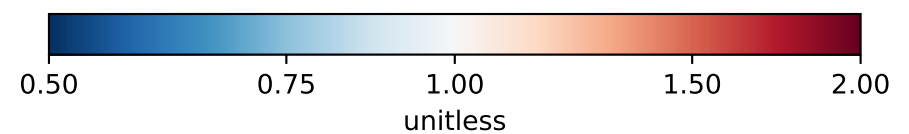
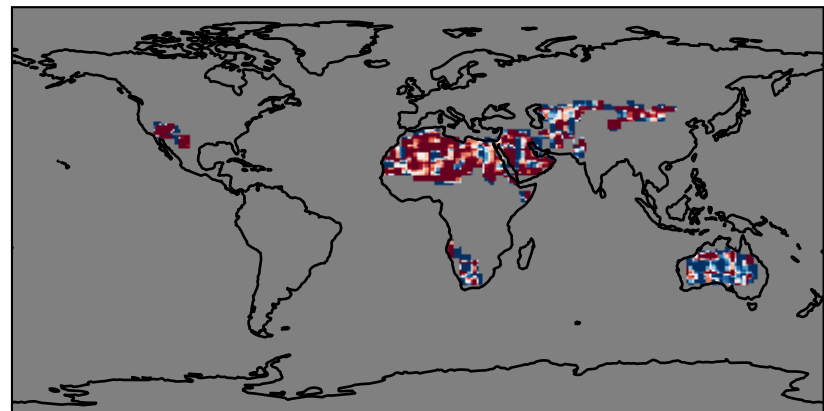
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

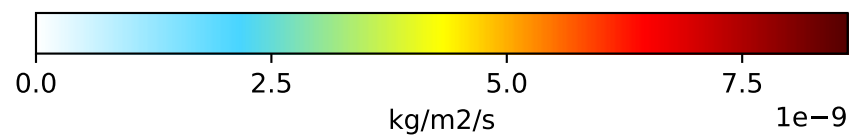
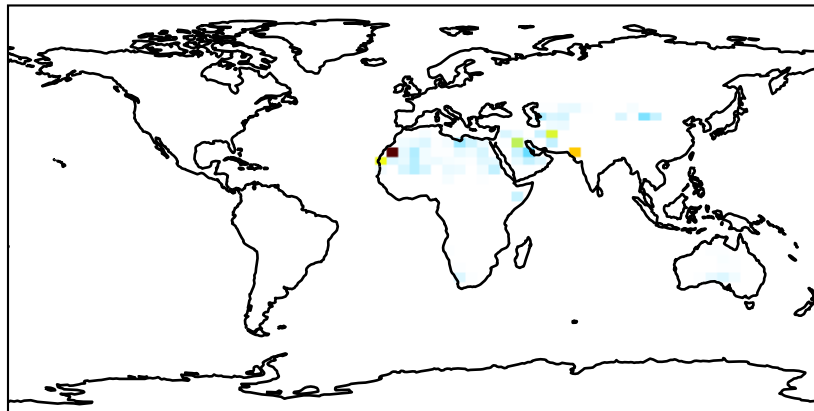


Ratio (1x1.25)  
Dev/Ref, Fixed Range

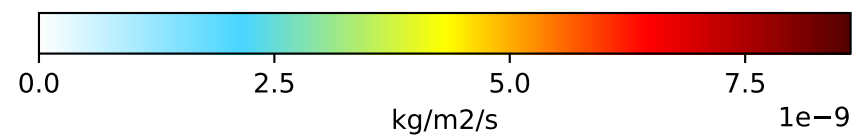
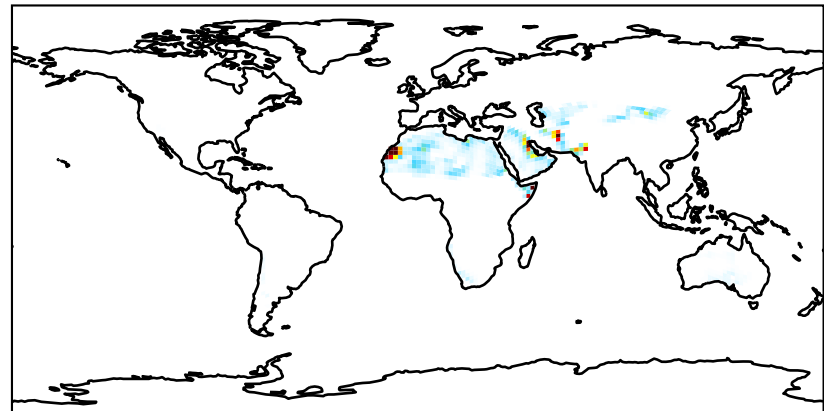


# EmisDST3\_Natural

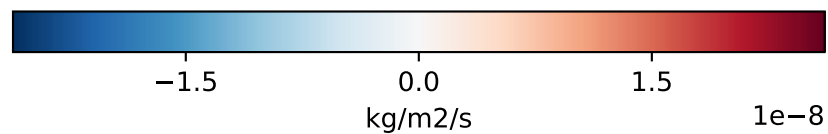
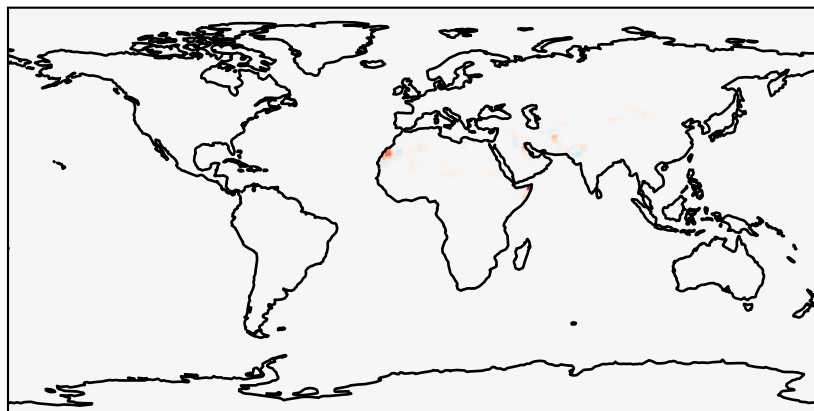
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



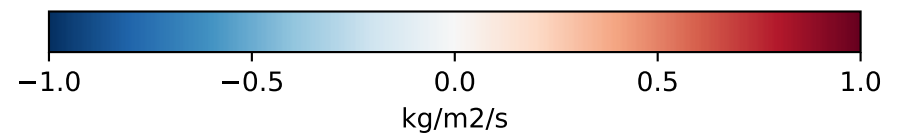
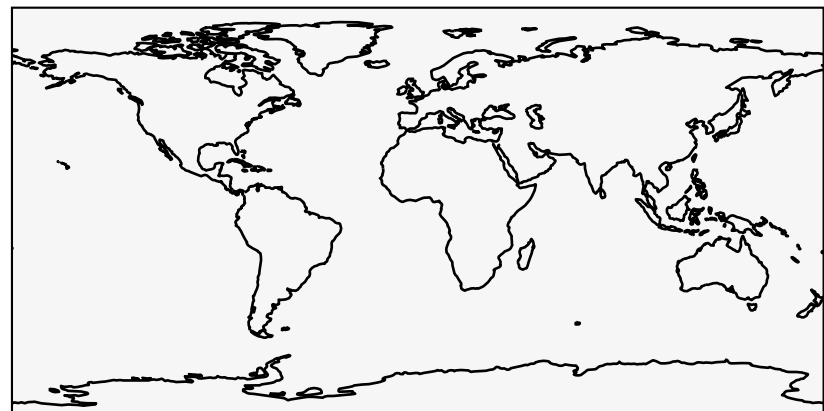
GCHP\_13.4.0-rc.4 (Dev)  
c48



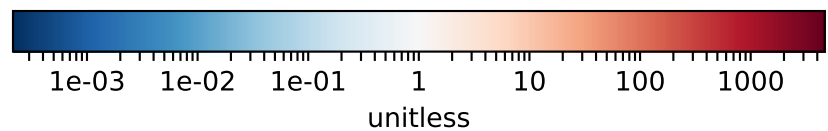
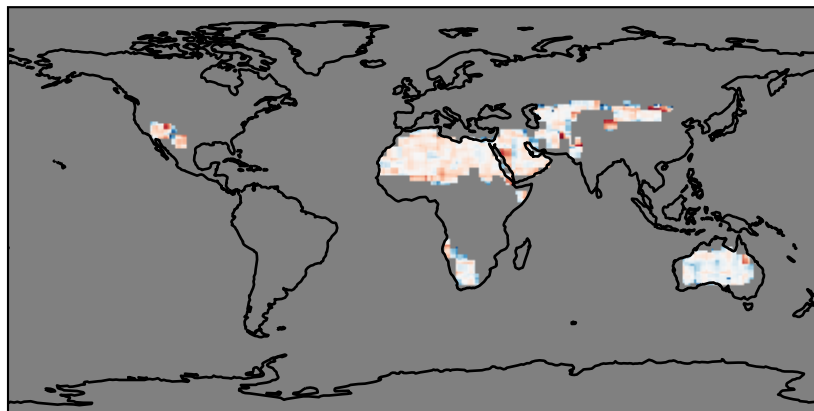
Difference (1x1.25)  
Dev - Ref, Dynamic Range



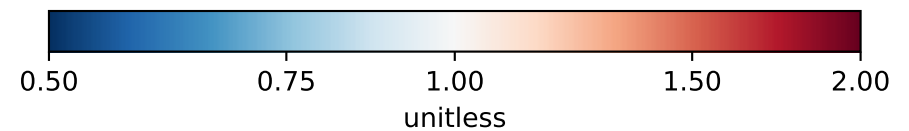
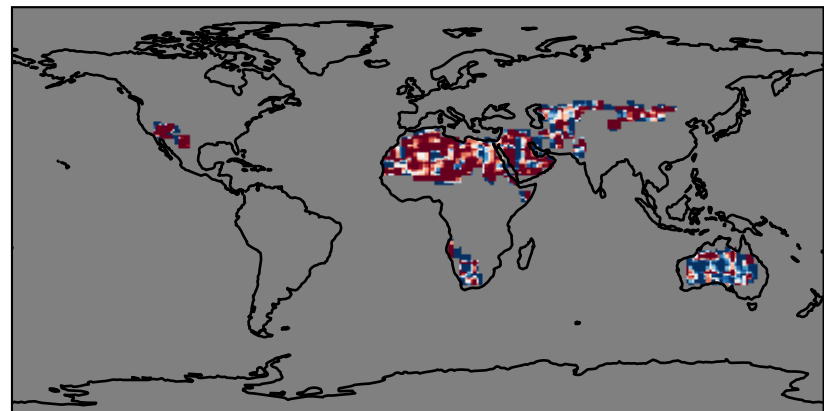
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

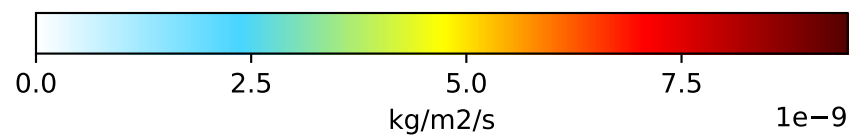
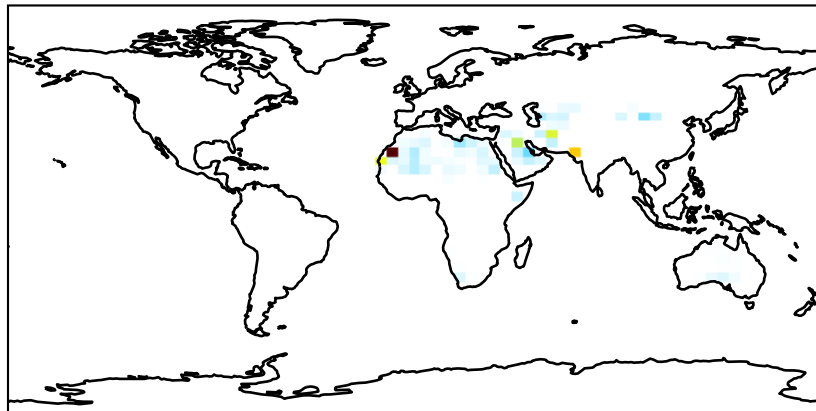


Ratio (1x1.25)  
Dev/Ref, Fixed Range

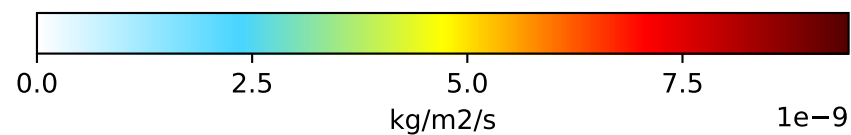
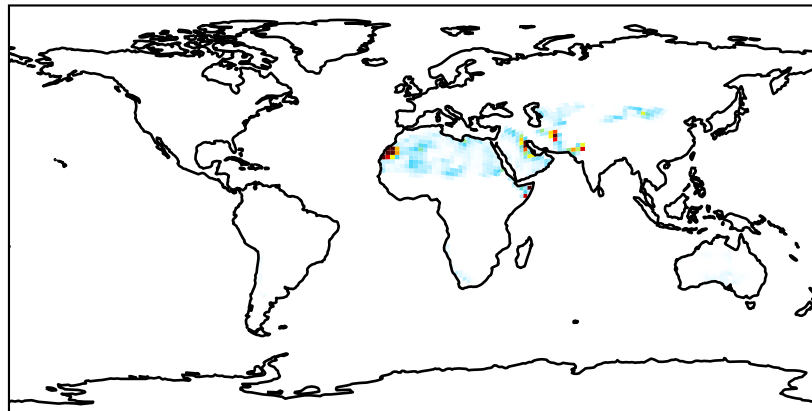


# EmisDST4\_Natural

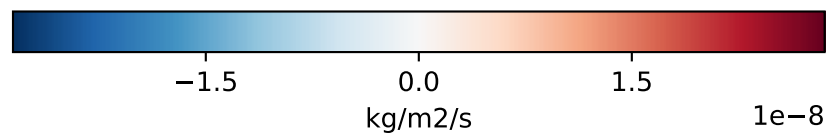
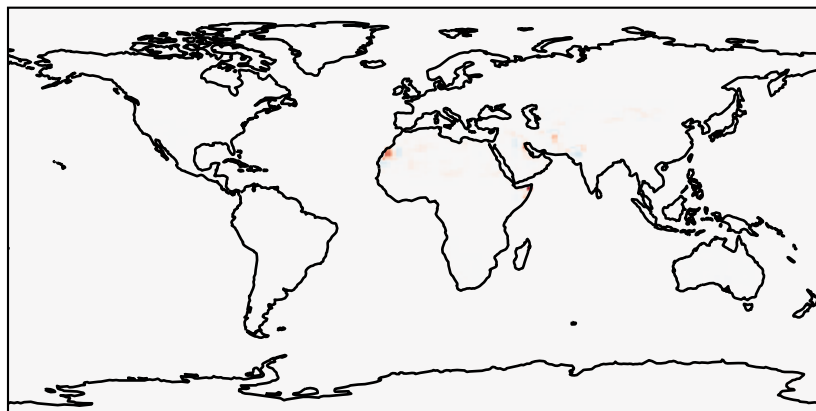
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



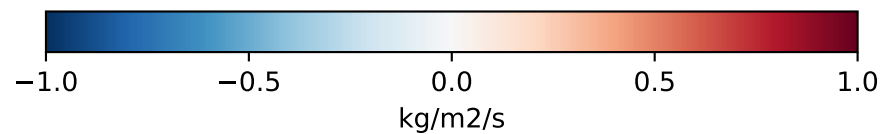
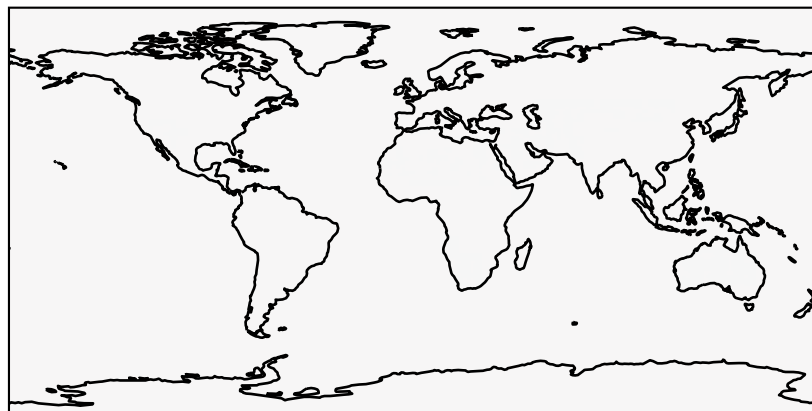
GCHP\_13.4.0-rc.4 (Dev)  
c48



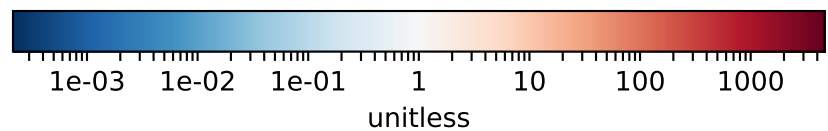
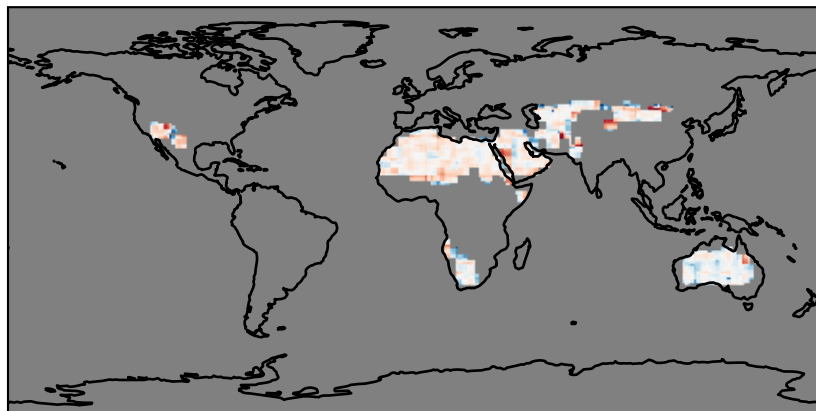
Difference (1x1.25)  
Dev - Ref, Dynamic Range



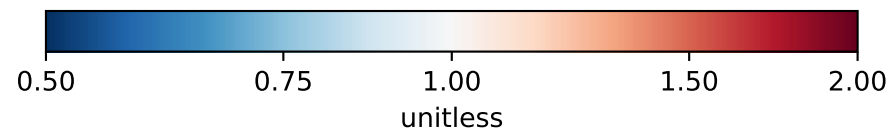
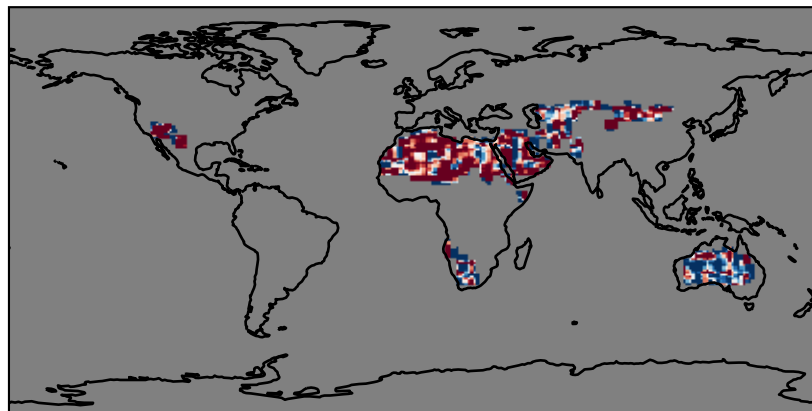
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



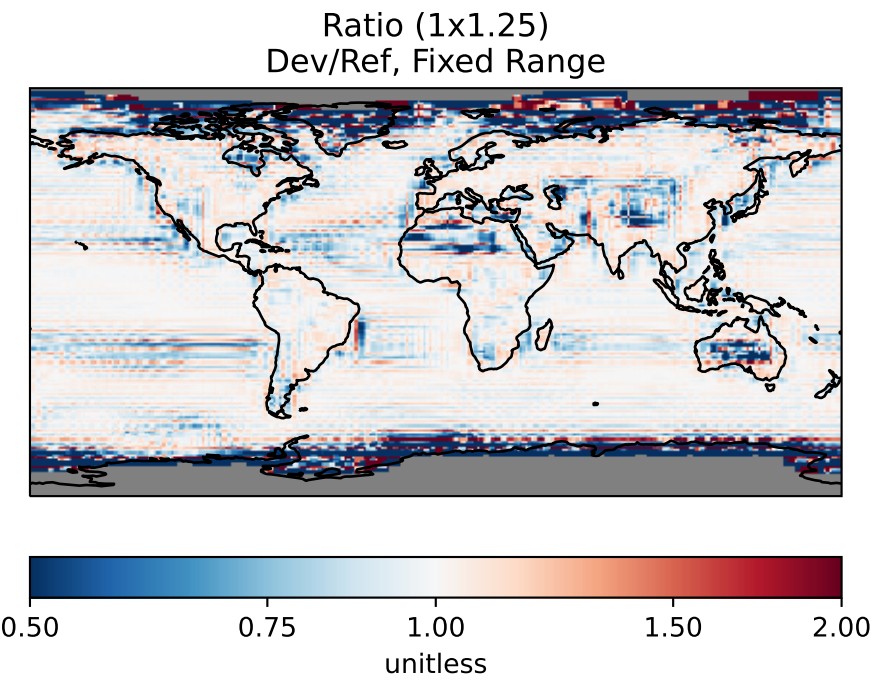
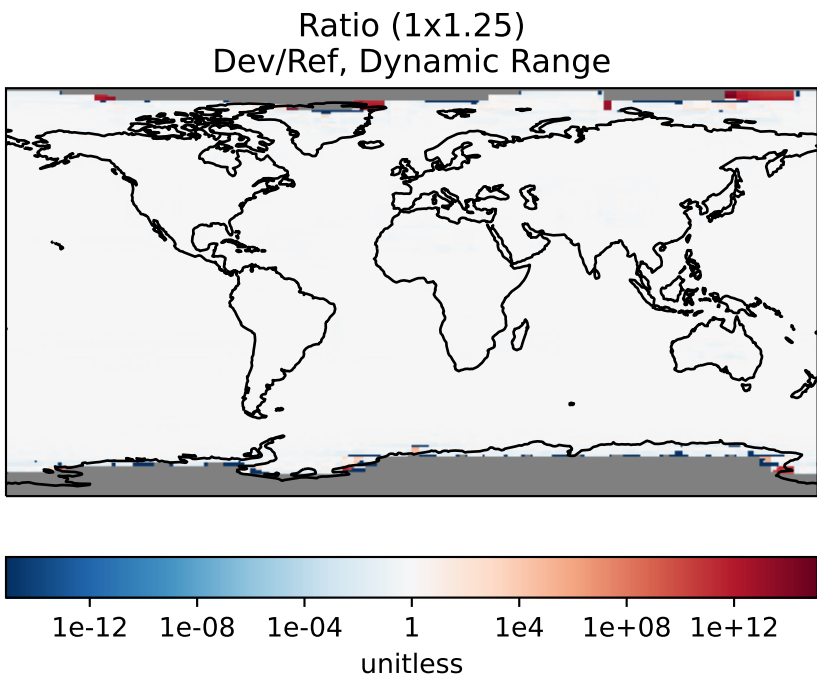
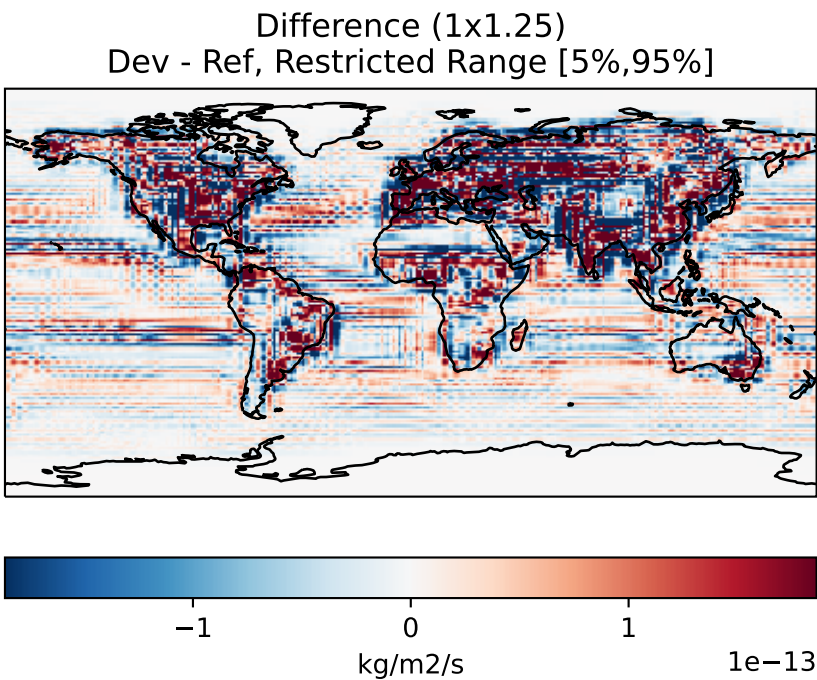
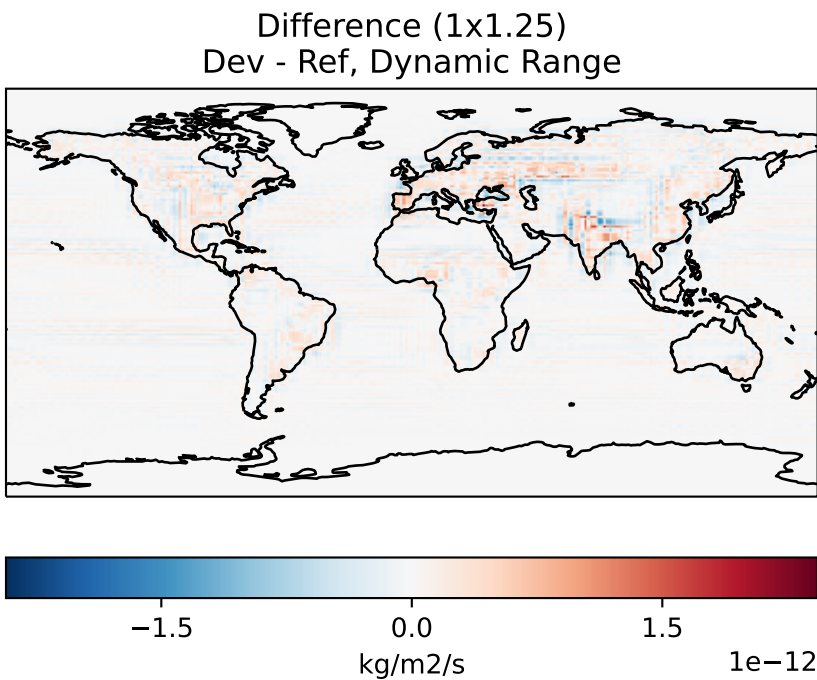
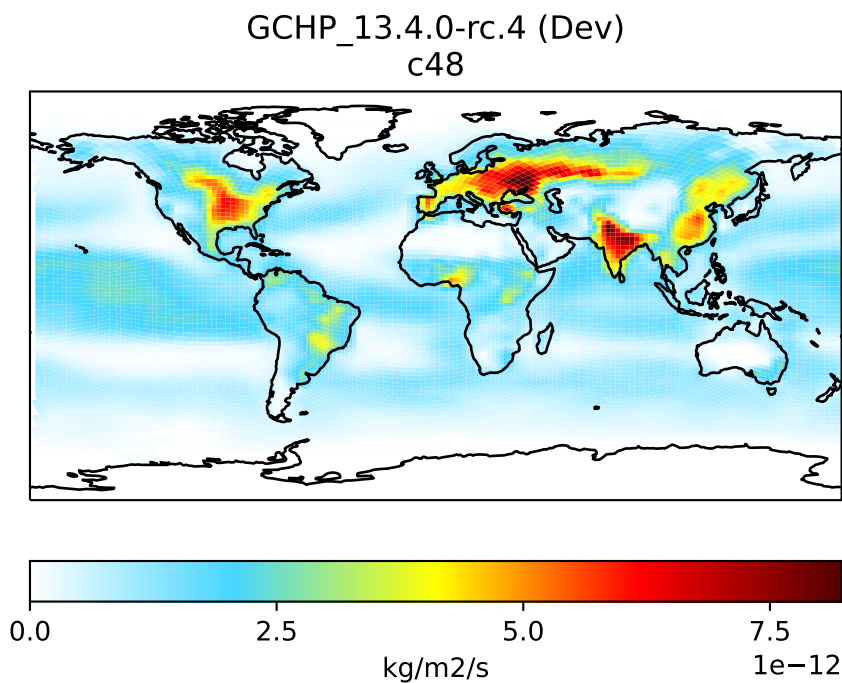
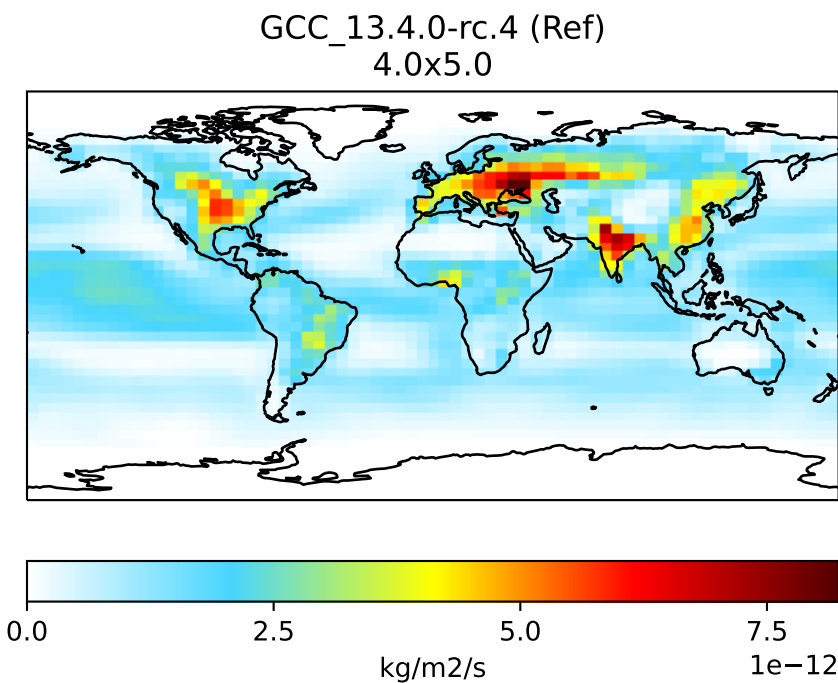
Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Ratio (1x1.25)  
Dev/Ref, Fixed Range

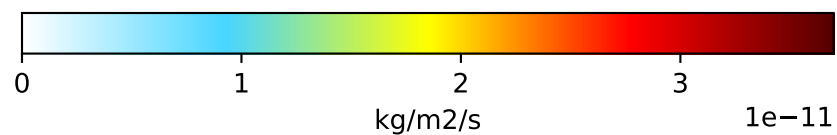
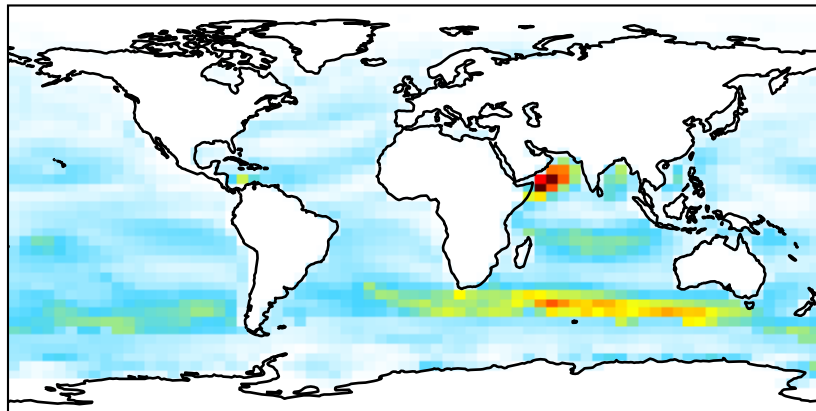


# EmisNH3\_Natural

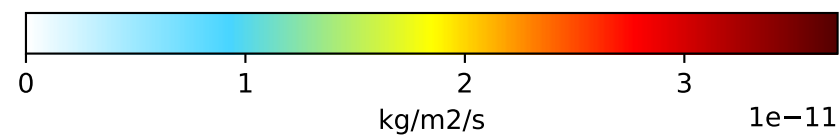
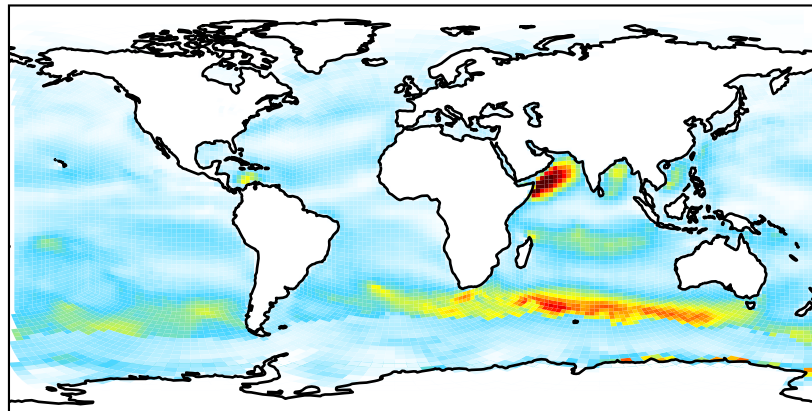


# EmisSALA\_Natural

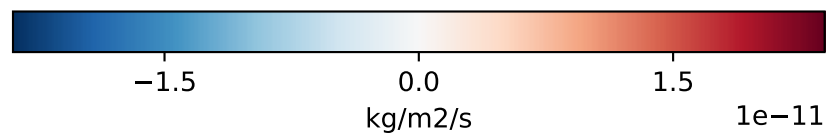
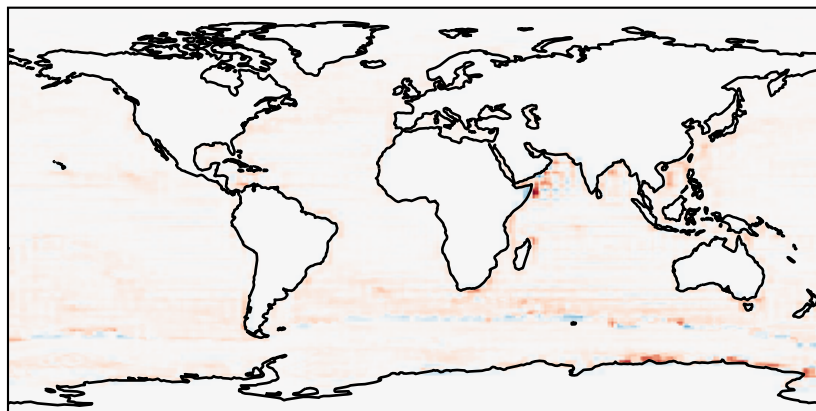
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



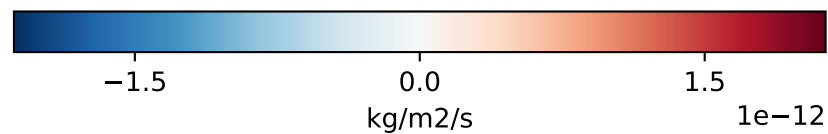
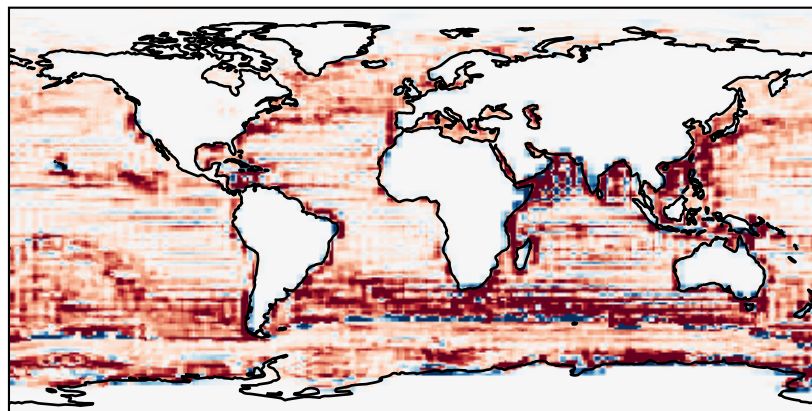
GCHP\_13.4.0-rc.4 (Dev)  
c48



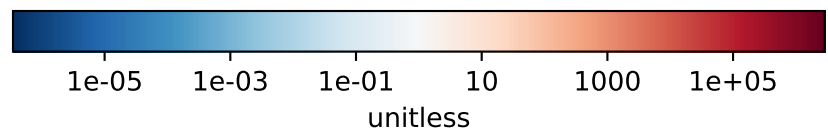
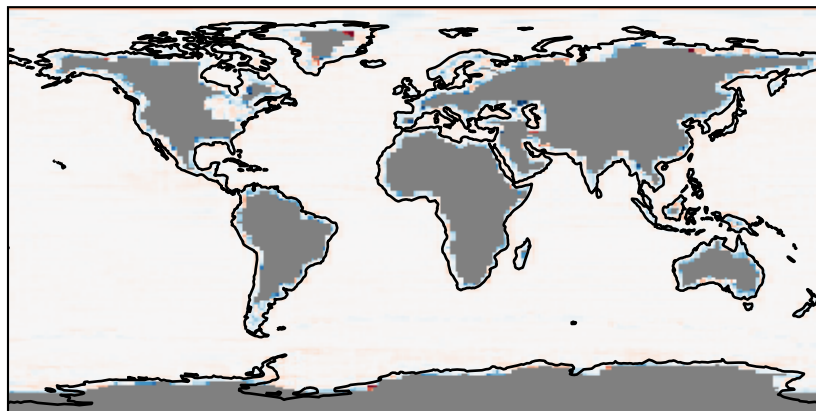
Difference (1x1.25)  
Dev - Ref, Dynamic Range



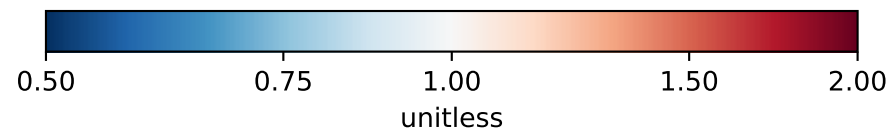
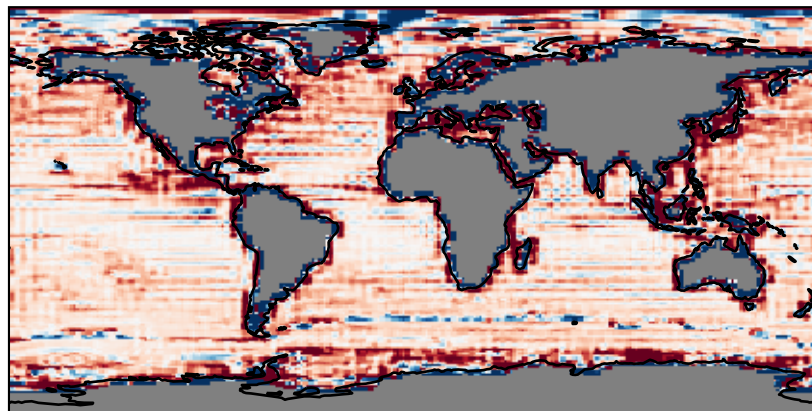
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

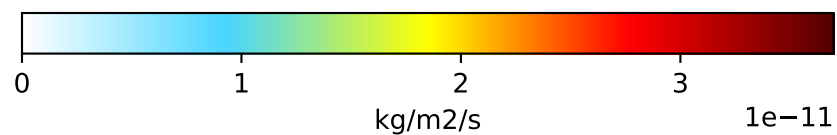
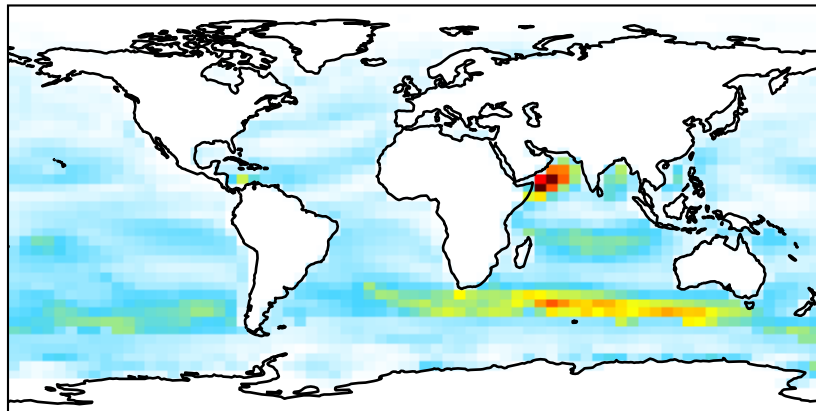


Ratio (1x1.25)  
Dev/Ref, Fixed Range

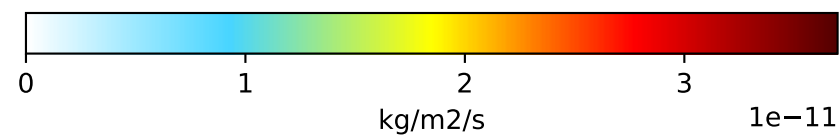
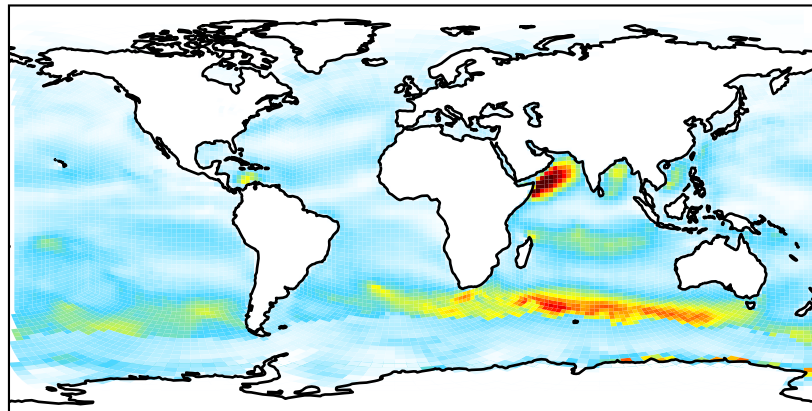


# EmisSALAAL\_Natural

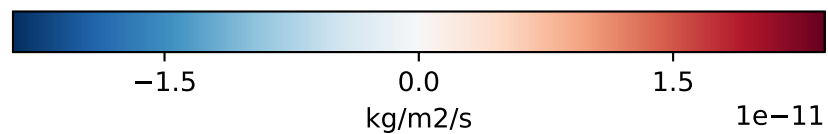
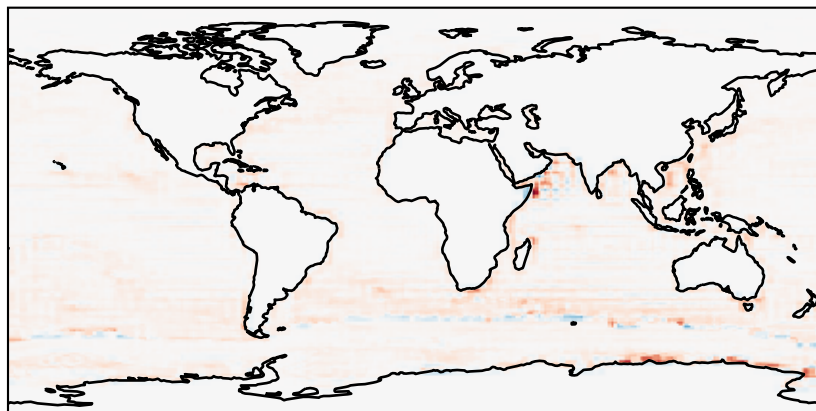
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



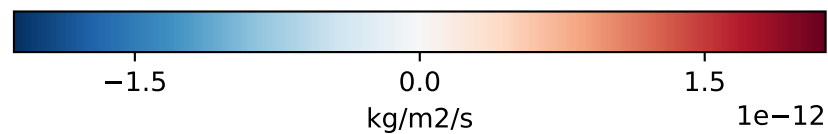
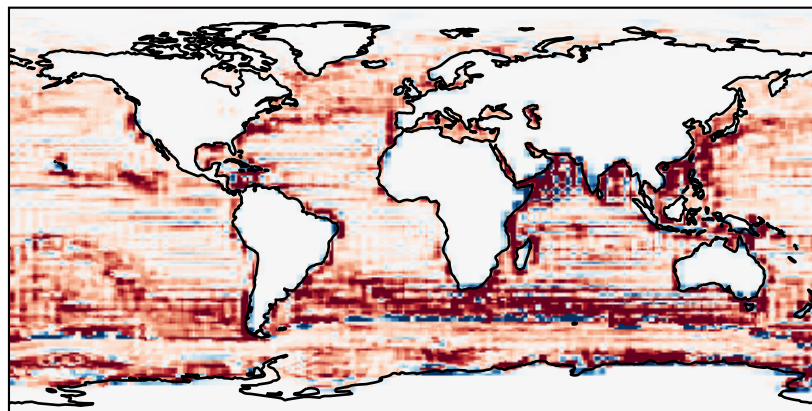
GCHP\_13.4.0-rc.4 (Dev)  
c48



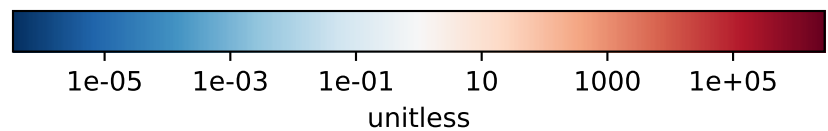
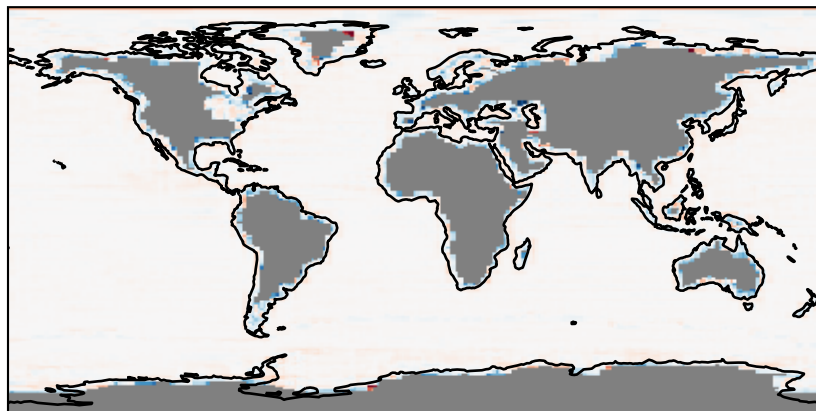
Difference (1x1.25)  
Dev - Ref, Dynamic Range



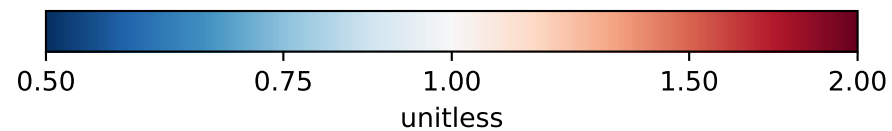
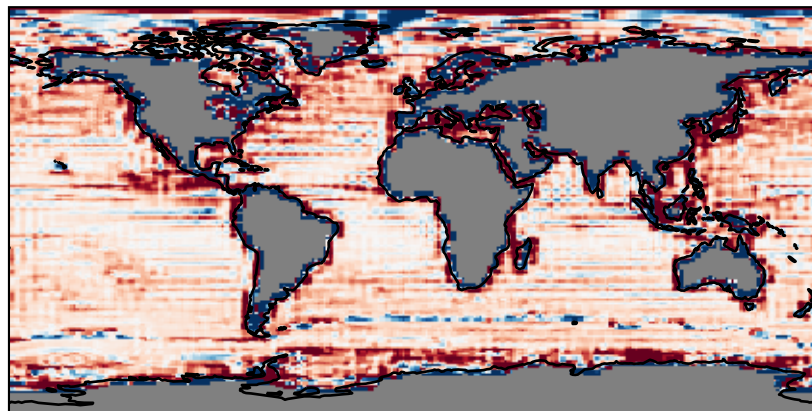
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

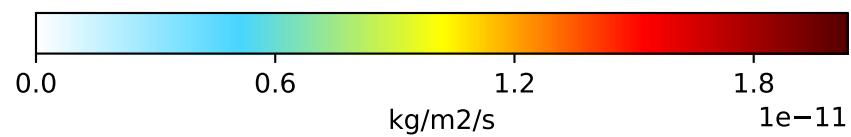
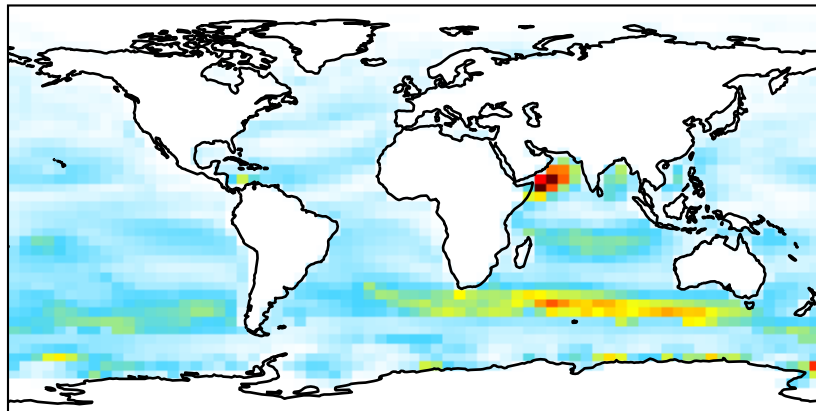


Ratio (1x1.25)  
Dev/Ref, Fixed Range

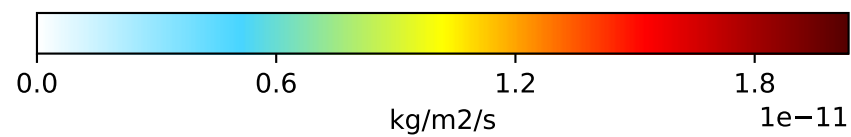
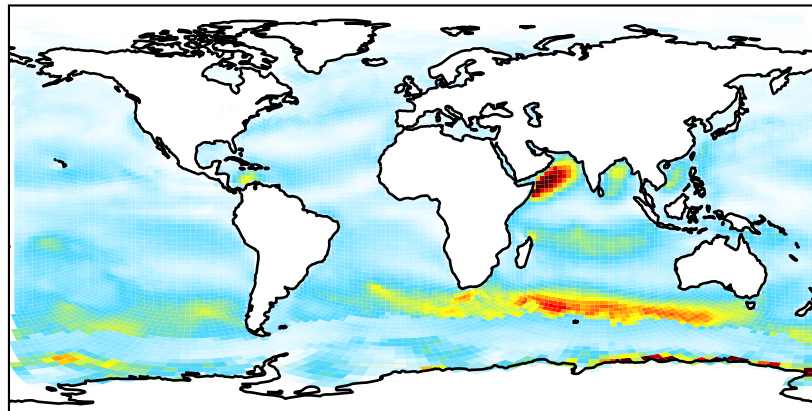


# EmisSALACL\_Natural

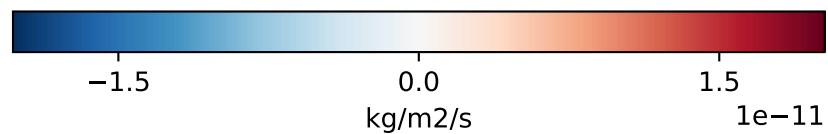
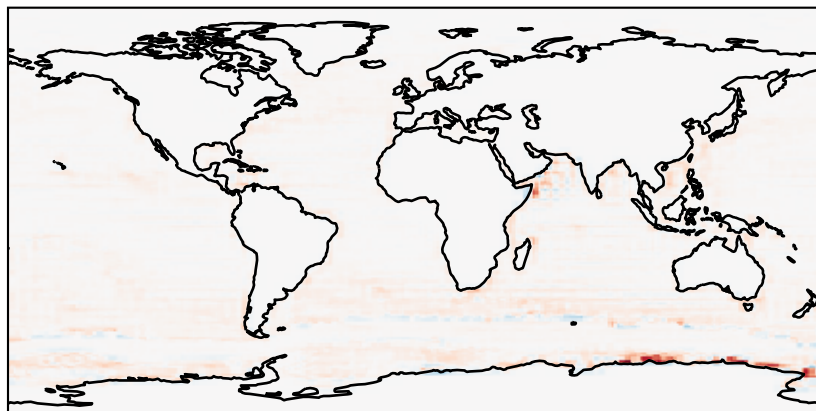
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



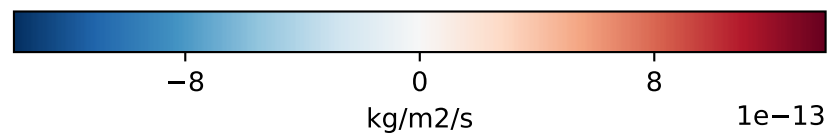
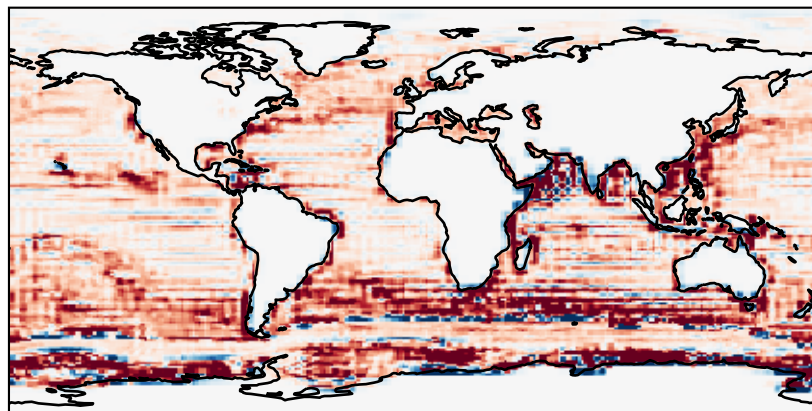
GCHP\_13.4.0-rc.4 (Dev)  
c48



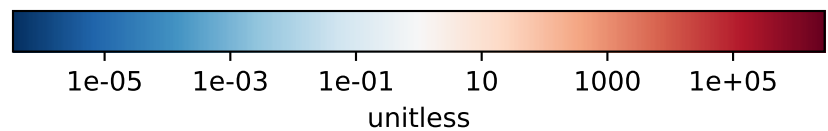
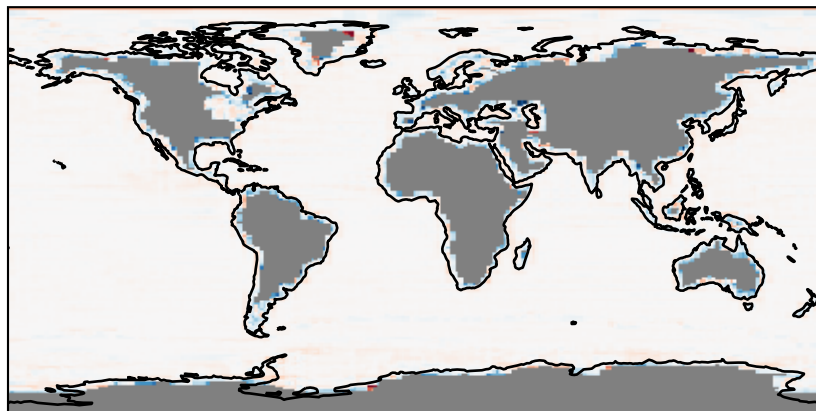
Difference (1x1.25)  
Dev - Ref, Dynamic Range



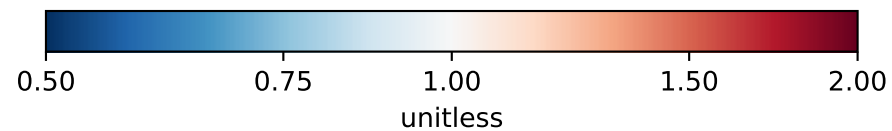
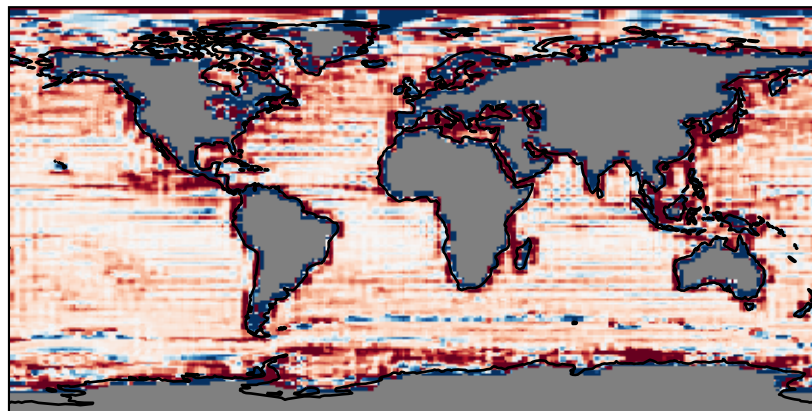
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

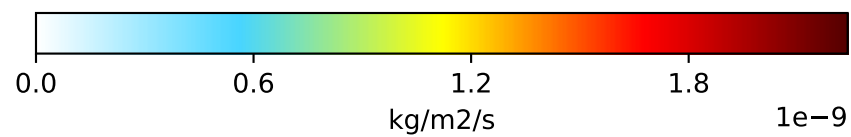
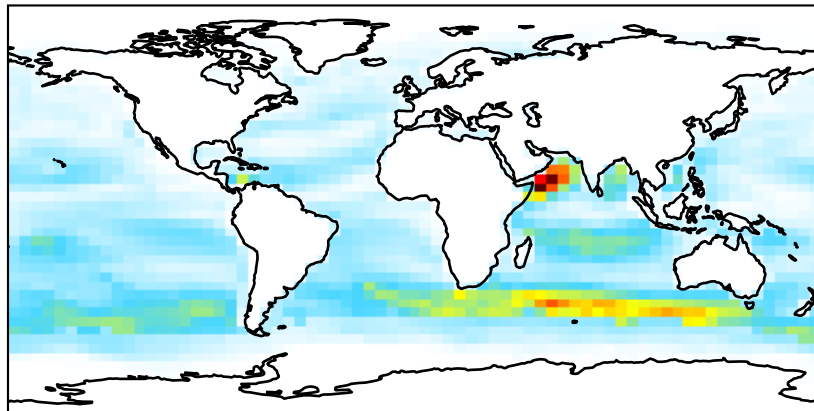


Ratio (1x1.25)  
Dev/Ref, Fixed Range

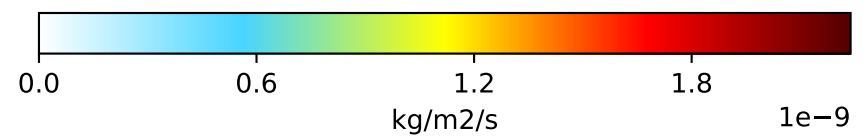
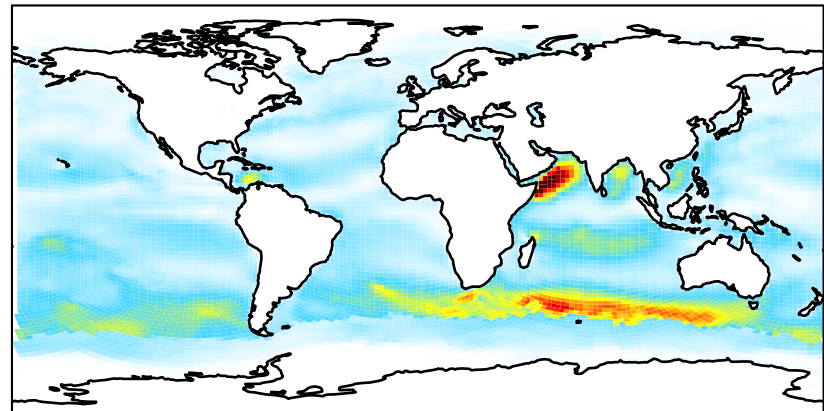


# EmisSALC\_Natural

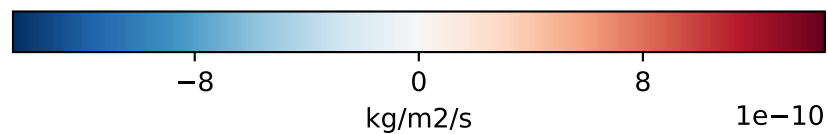
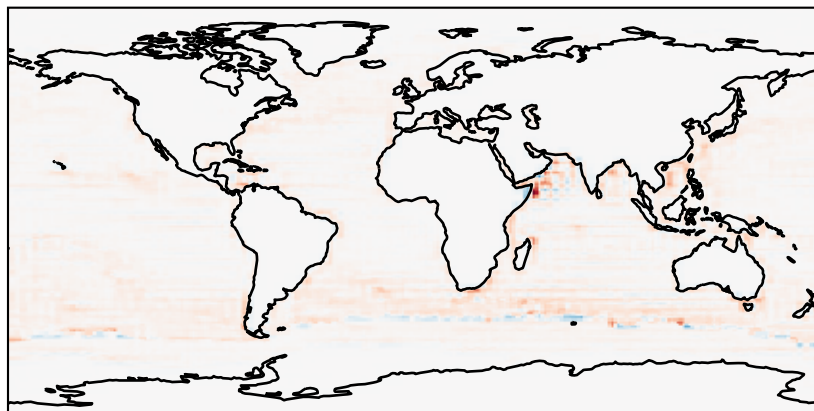
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



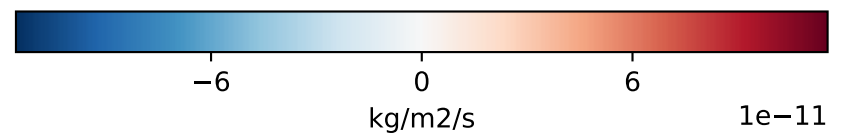
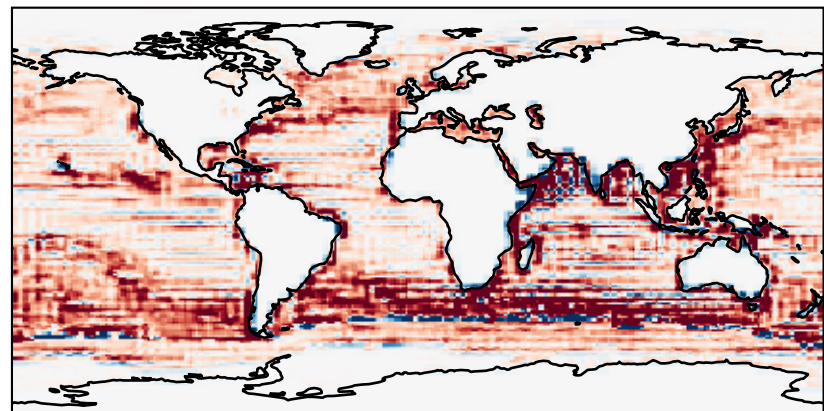
GCHP\_13.4.0-rc.4 (Dev)  
c48



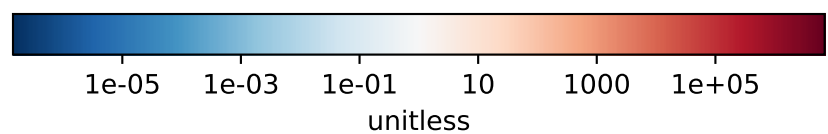
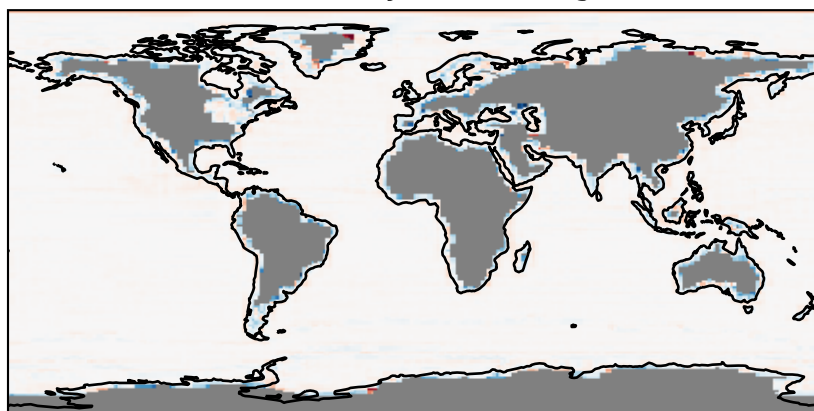
Difference (1x1.25)  
Dev - Ref, Dynamic Range



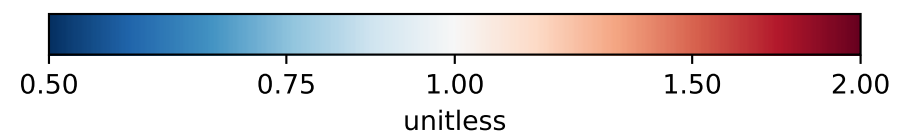
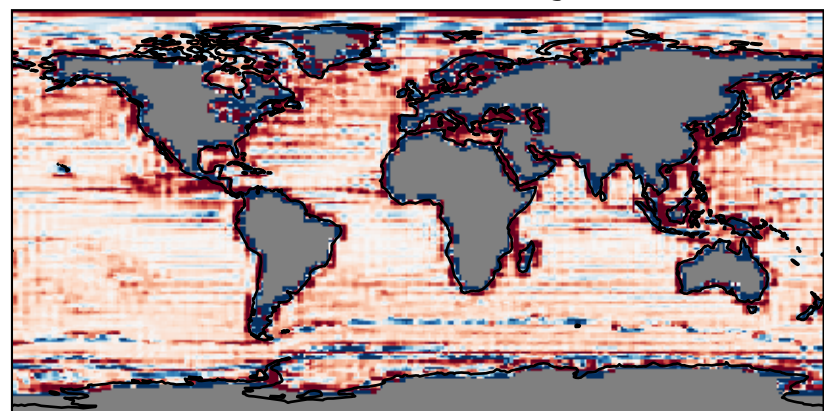
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

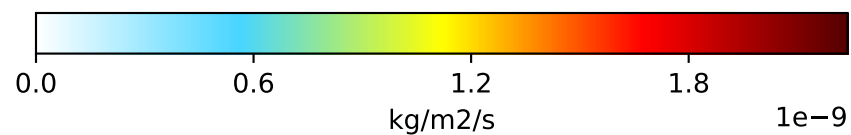
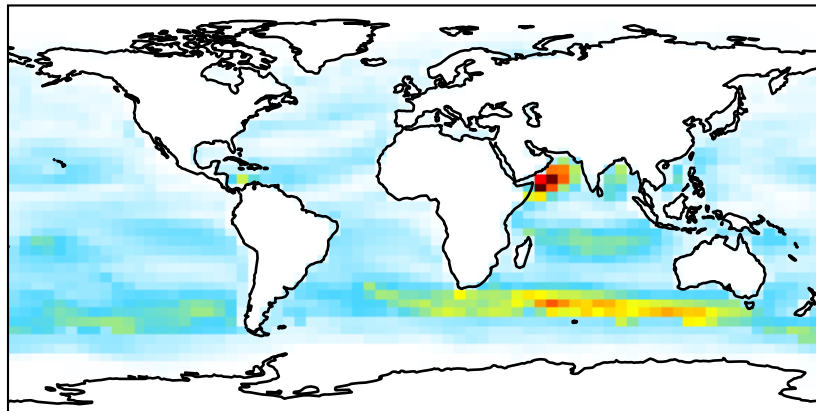


Ratio (1x1.25)  
Dev/Ref, Fixed Range

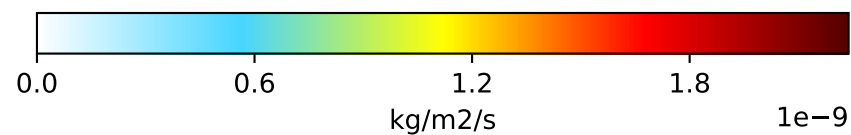
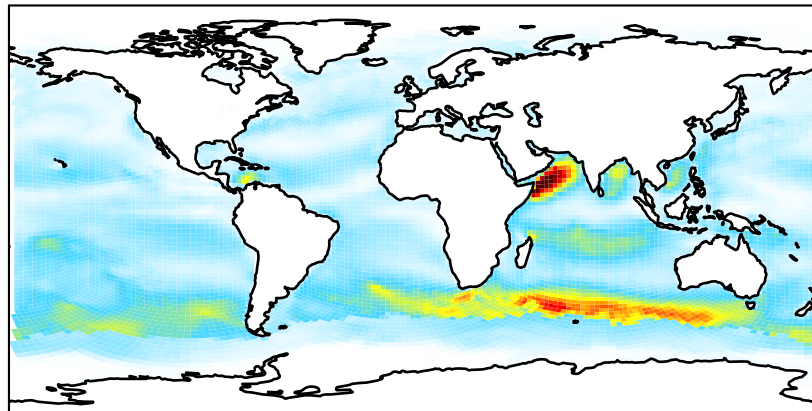


# EmisSALCAL\_Natural

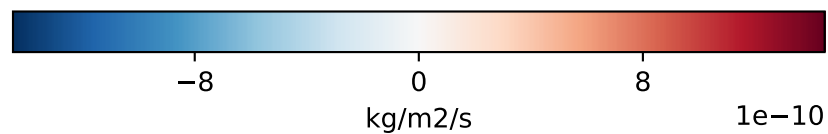
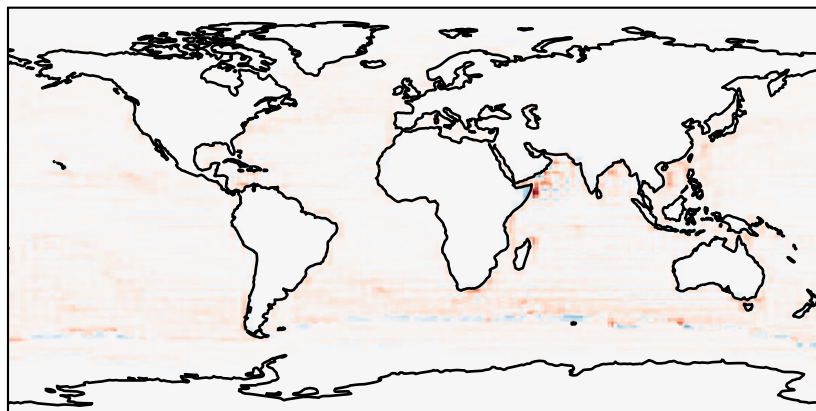
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



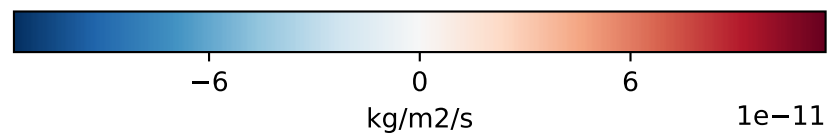
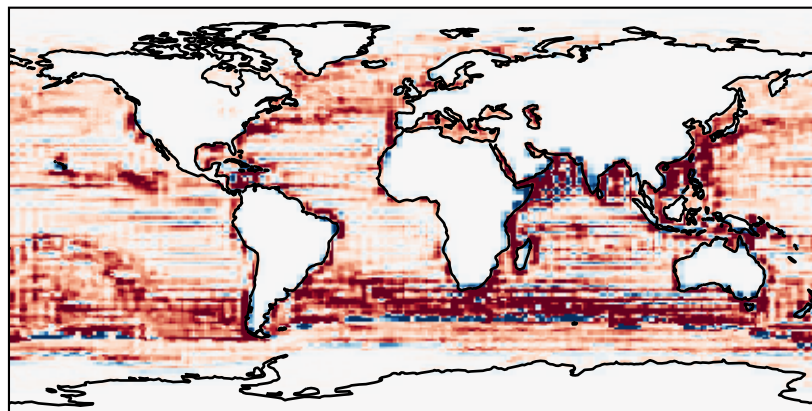
GCHP\_13.4.0-rc.4 (Dev)  
c48



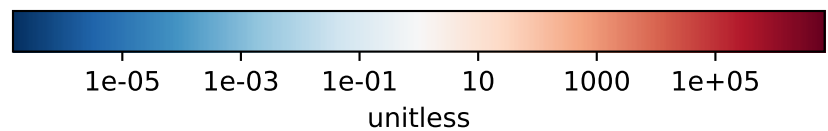
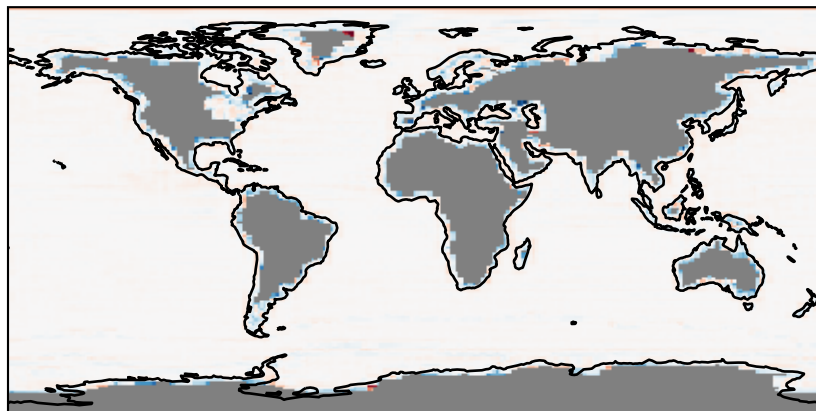
Difference (1x1.25)  
Dev - Ref, Dynamic Range



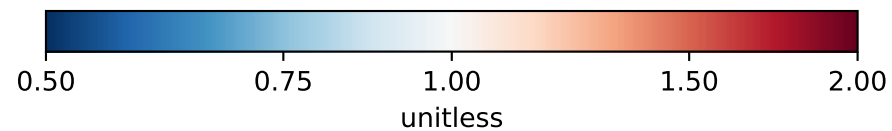
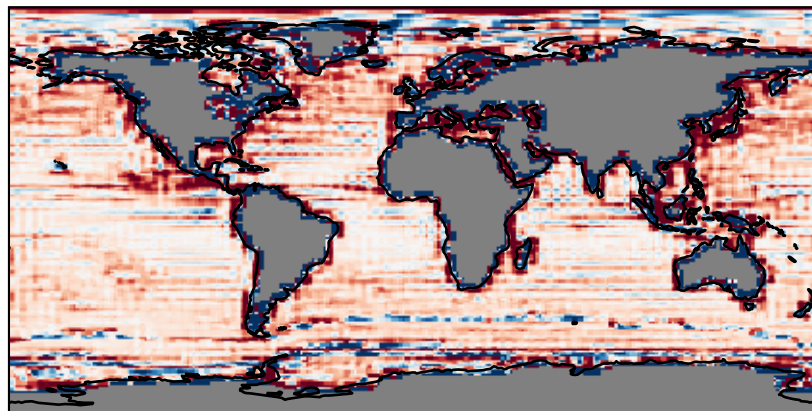
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

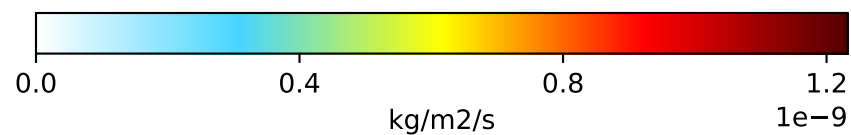
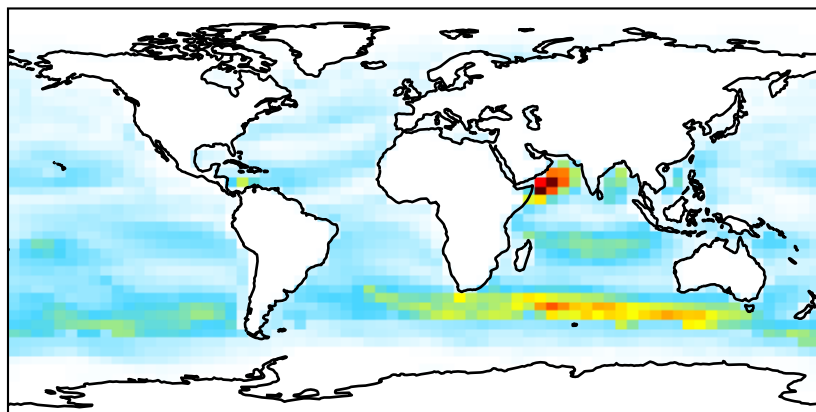


Ratio (1x1.25)  
Dev/Ref, Fixed Range

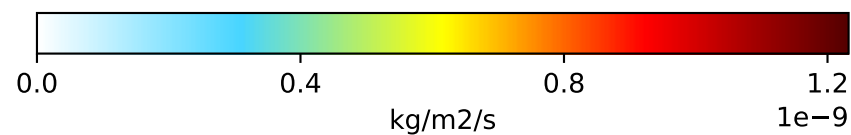
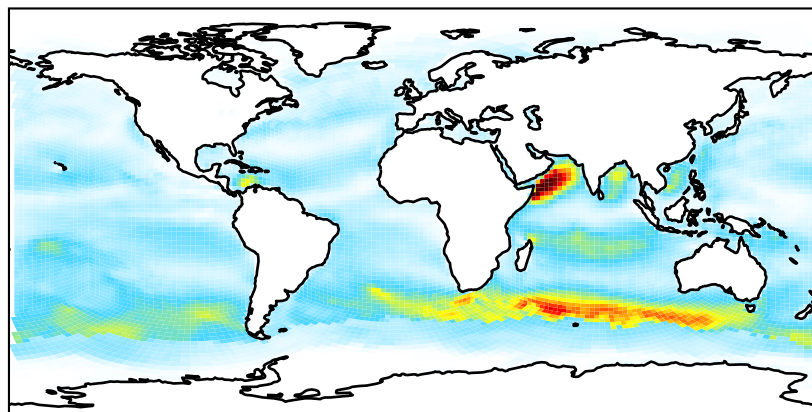


# EmisSALCCL\_Natural

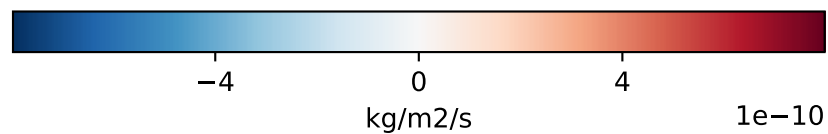
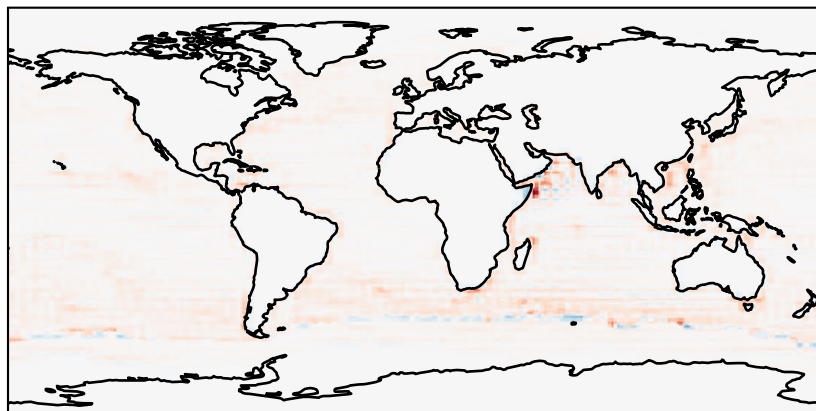
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



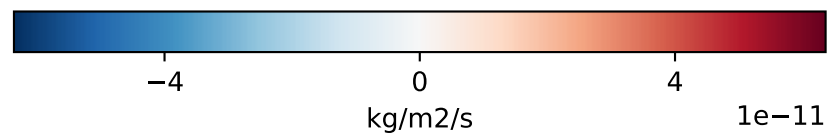
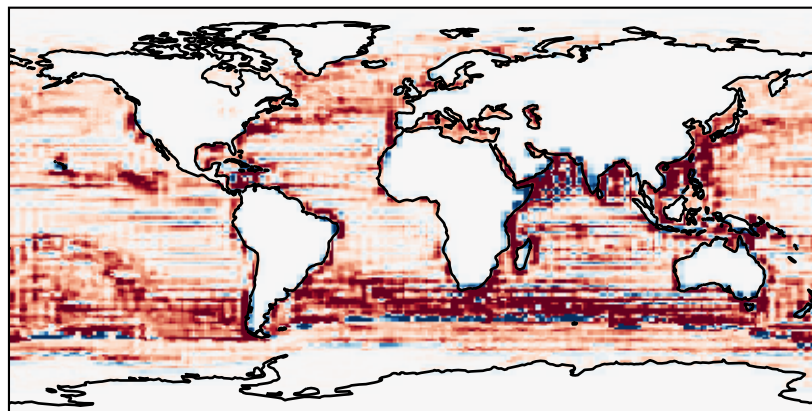
GCHP\_13.4.0-rc.4 (Dev)  
c48



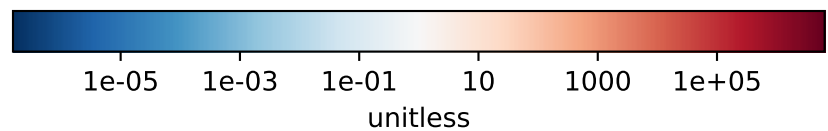
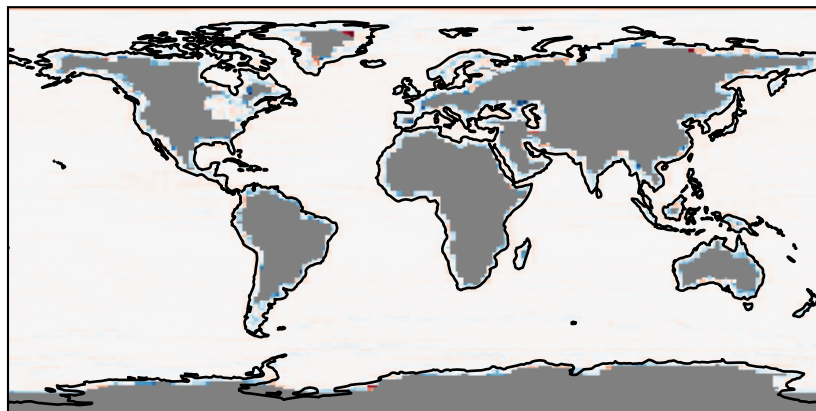
Difference (1x1.25)  
Dev - Ref, Dynamic Range



Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Ratio (1x1.25)  
Dev/Ref, Fixed Range

