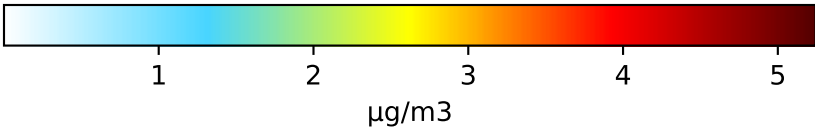
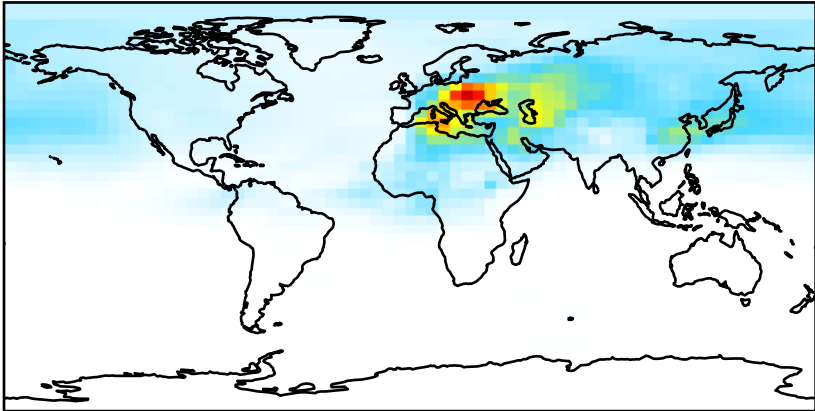
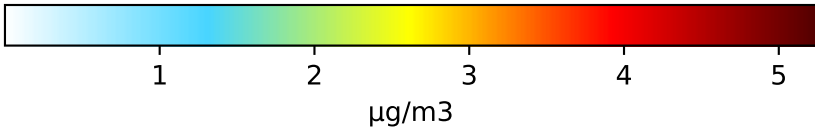
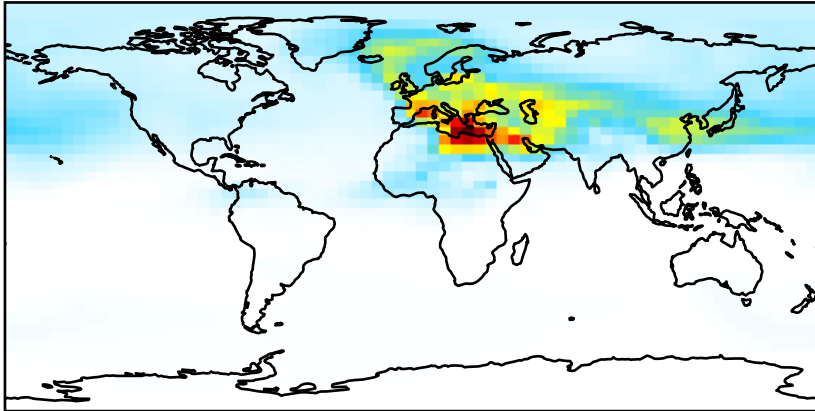


# SpeciesConc\_DST1 (Apr2019)

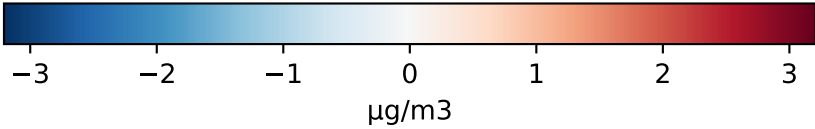
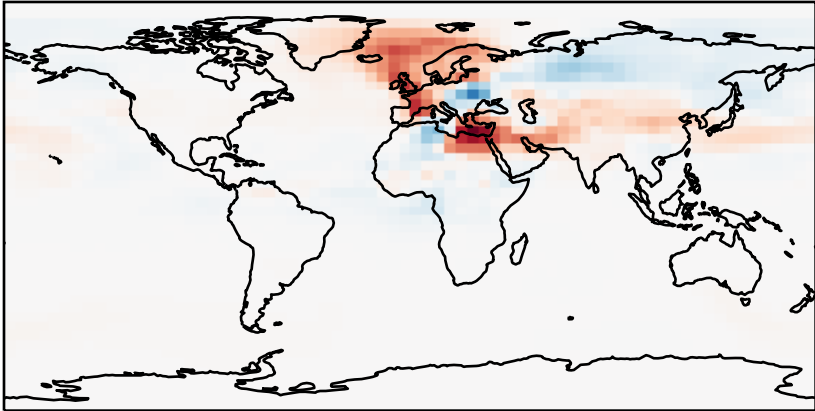
GCC\_12.9.0 (Ref)  
4.0x5.0



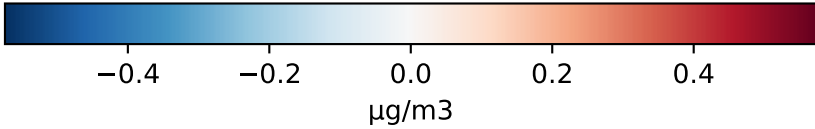
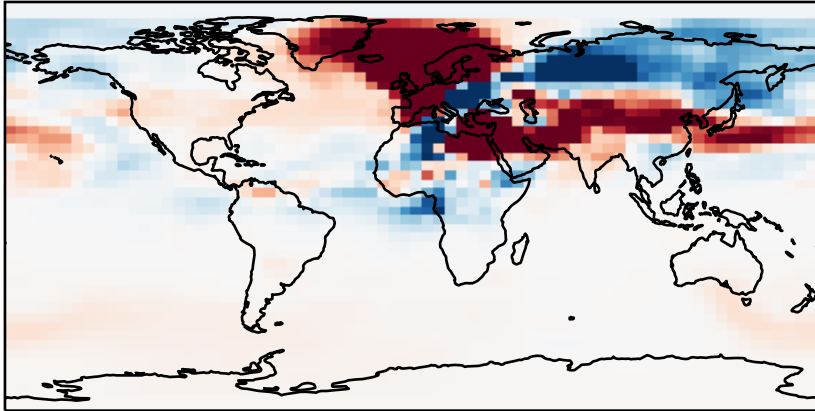
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



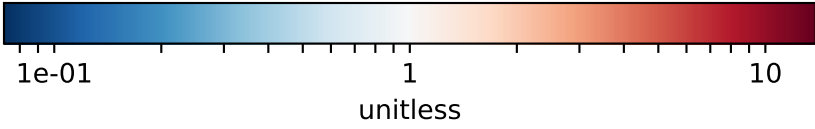
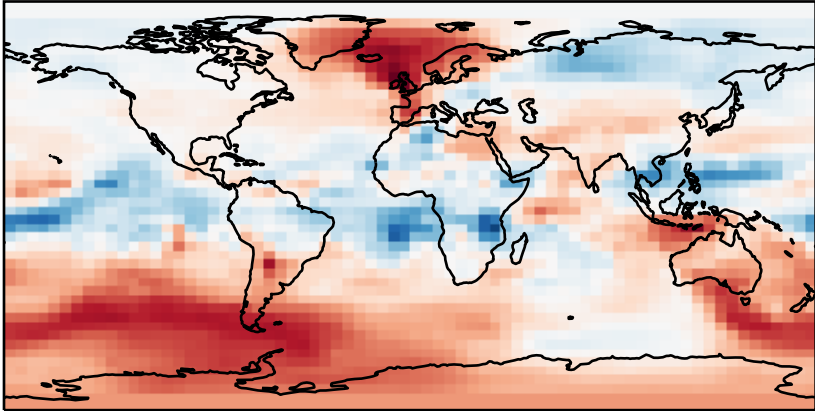
Difference  
Dev - Ref, Dynamic Range



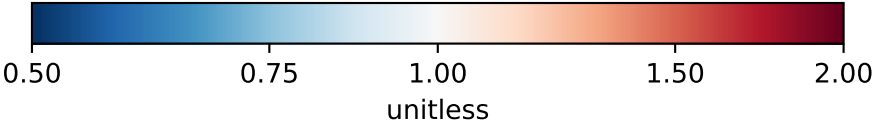
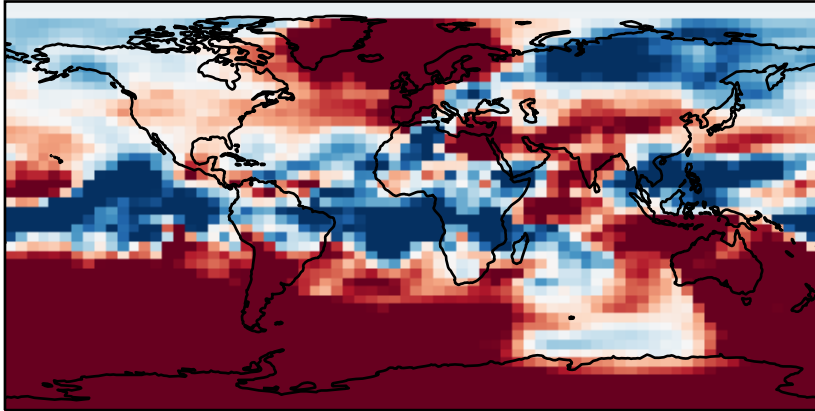
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

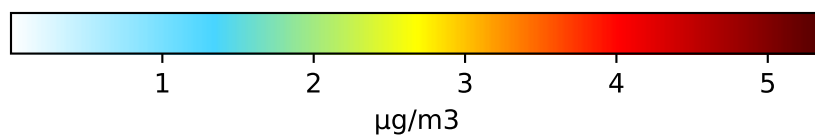
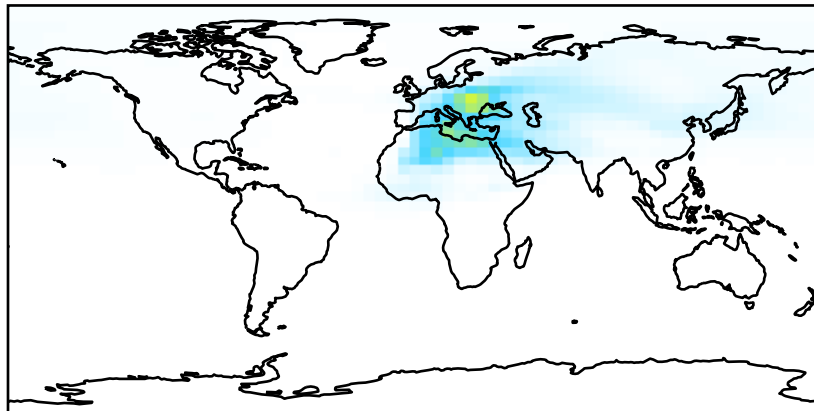


Ratio  
Dev/Ref, Fixed Range

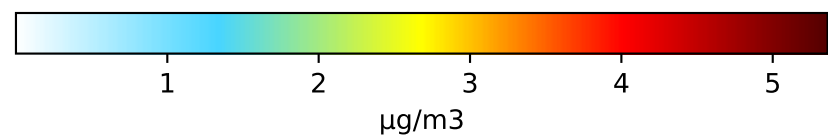
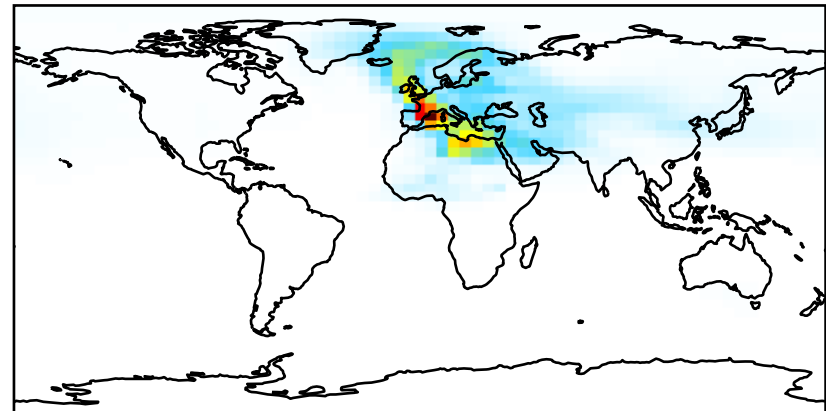


# SpeciesConc\_DST2 (Apr2019)

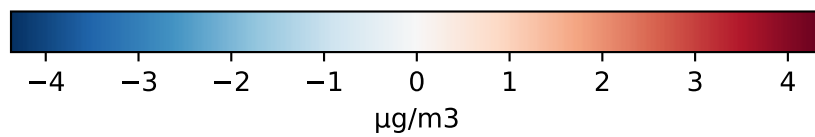
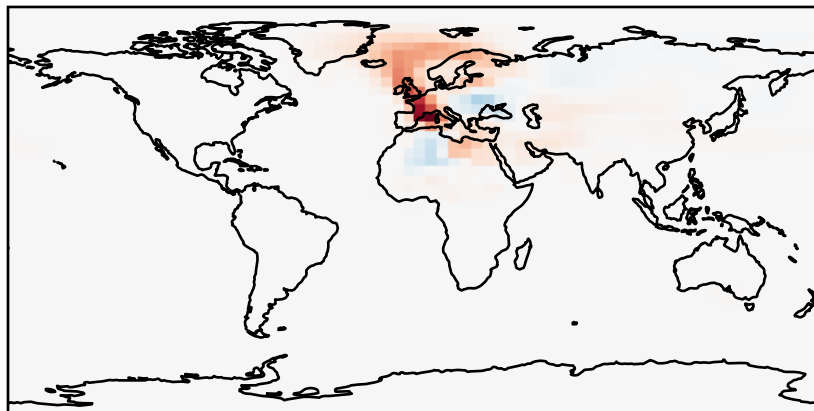
GCC\_12.9.0 (Ref)  
4.0x5.0



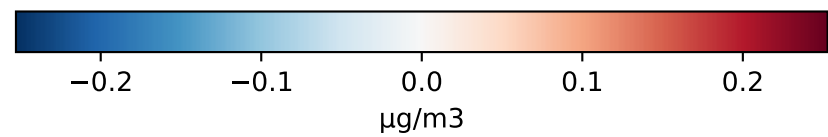
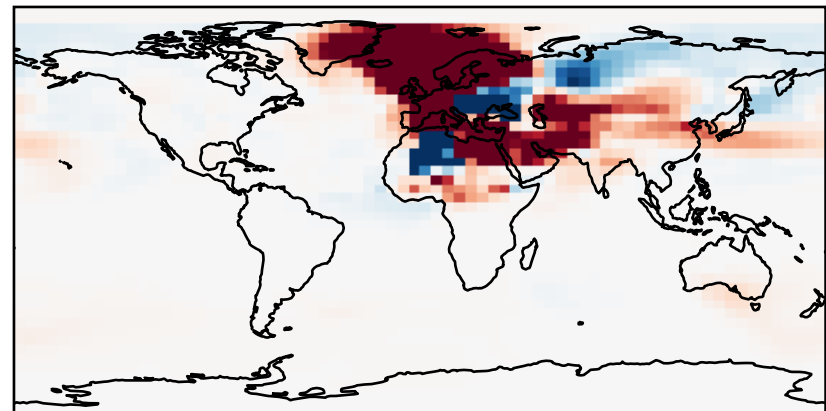
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



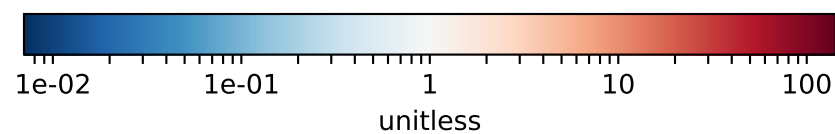
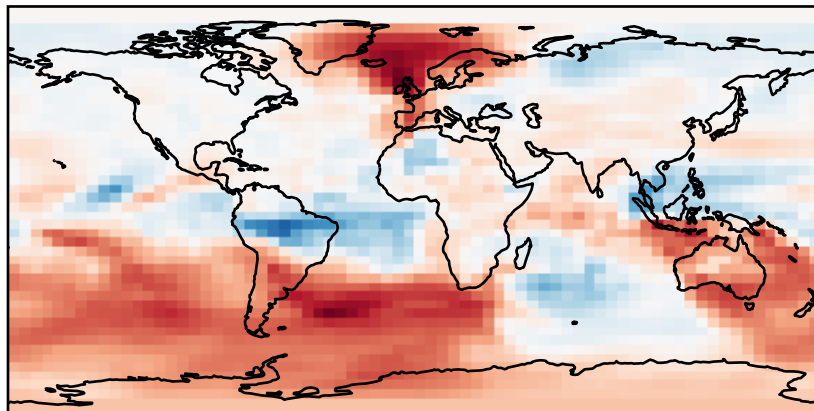
Difference  
Dev - Ref, Dynamic Range



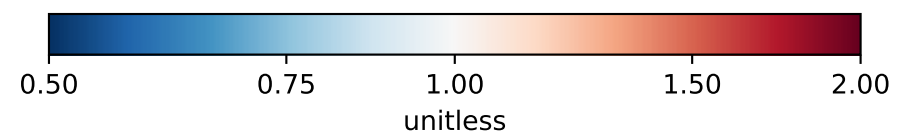
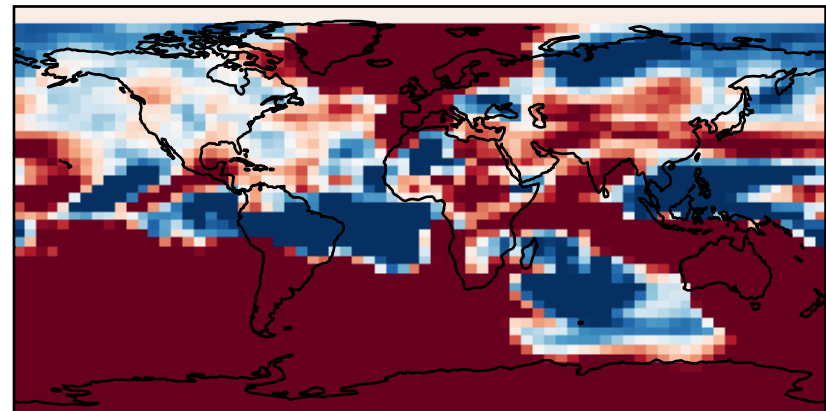
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

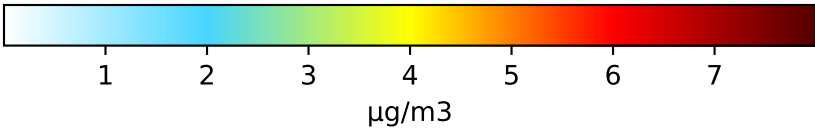
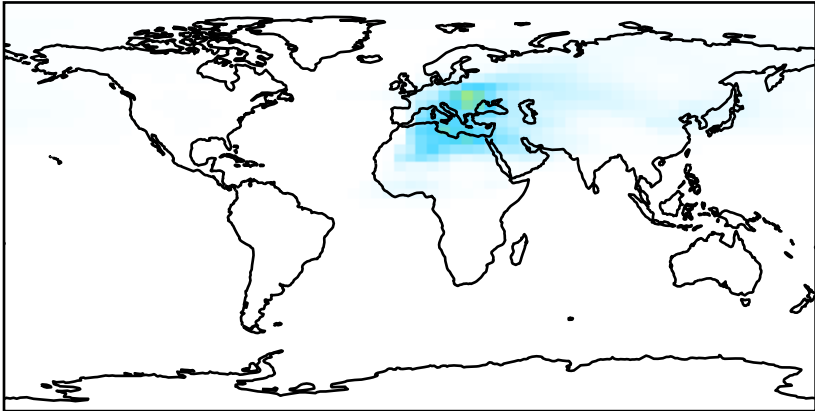


Ratio  
Dev/Ref, Fixed Range

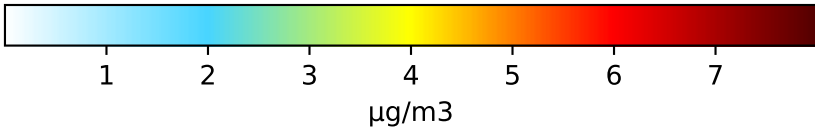
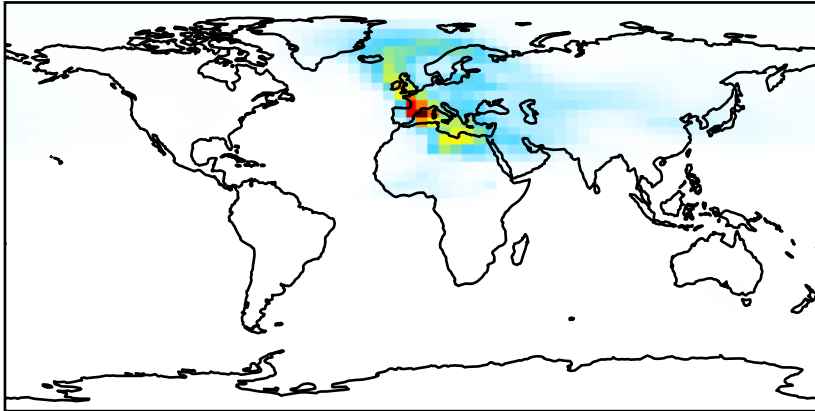


# SpeciesConc\_DST3 (Apr2019)

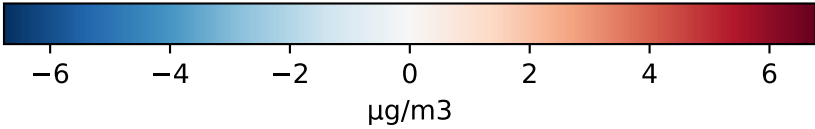
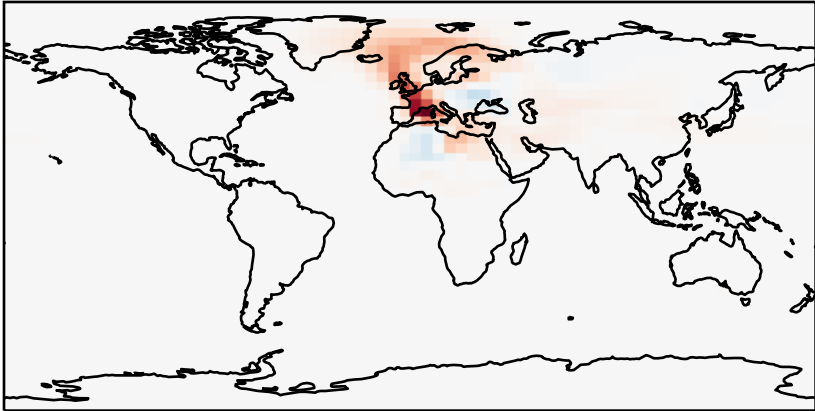
GCC\_12.9.0 (Ref)  
4.0x5.0



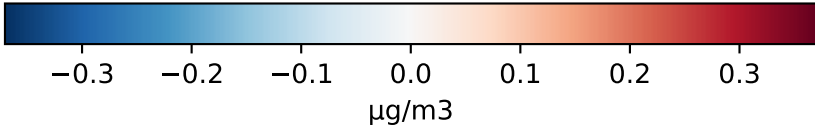
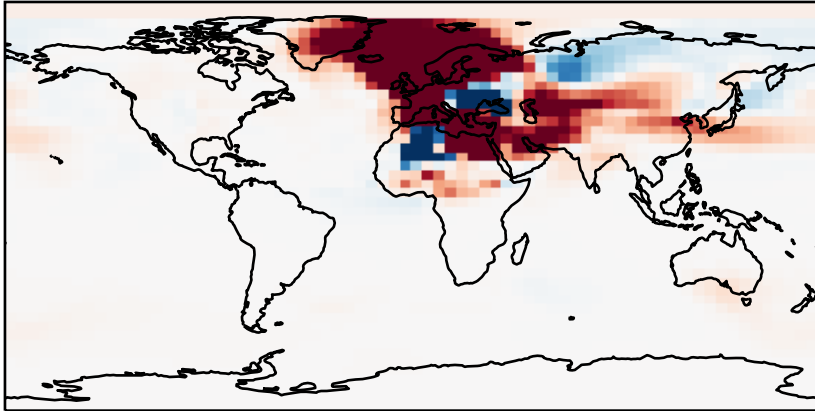
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



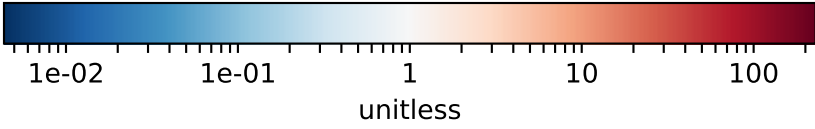
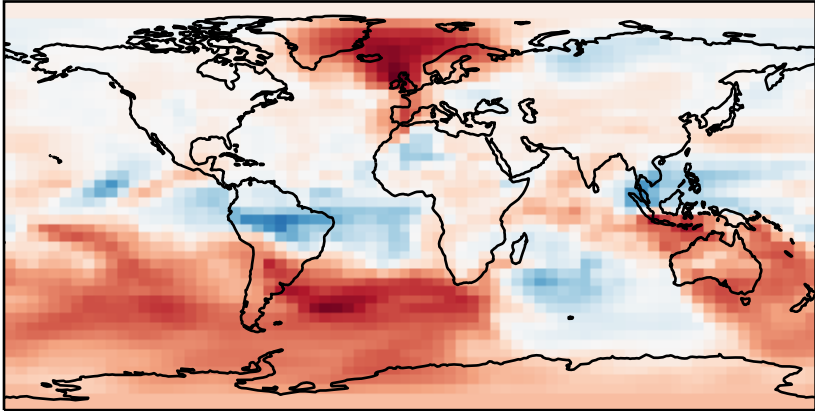
Difference  
Dev - Ref, Dynamic Range



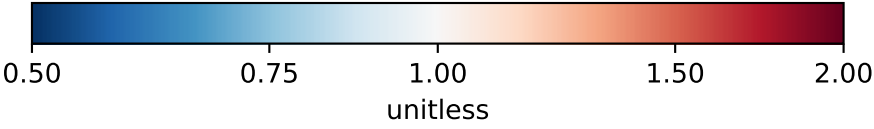
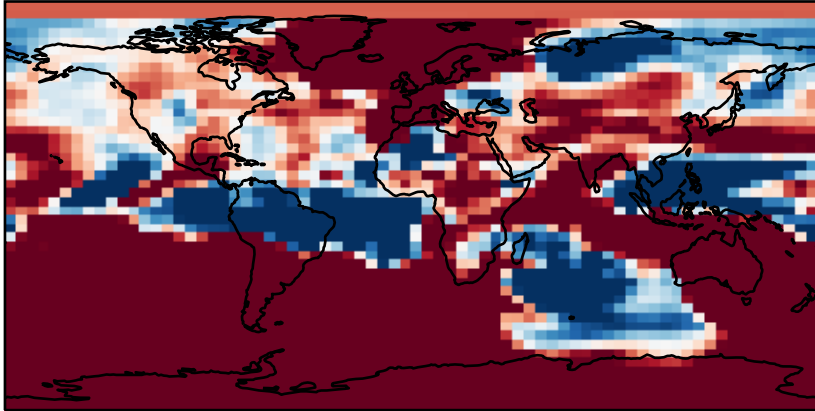
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

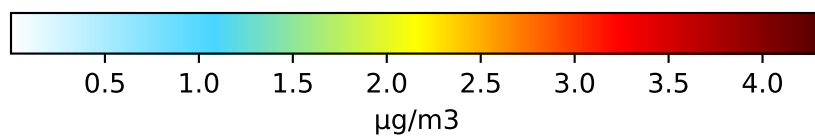
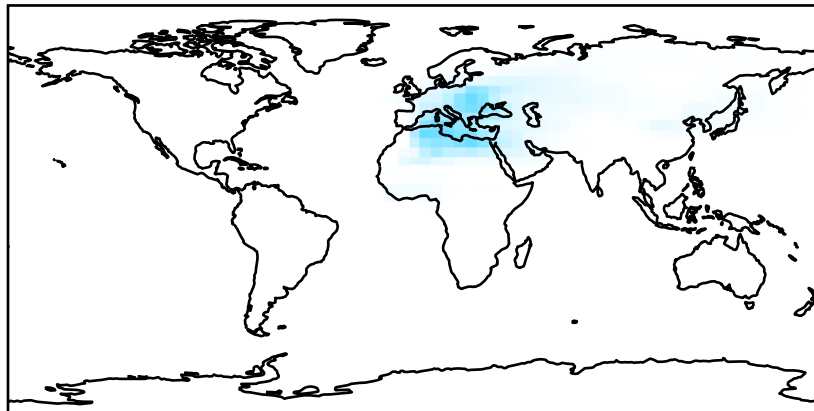


Ratio  
Dev/Ref, Fixed Range

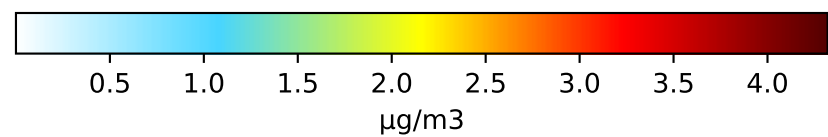
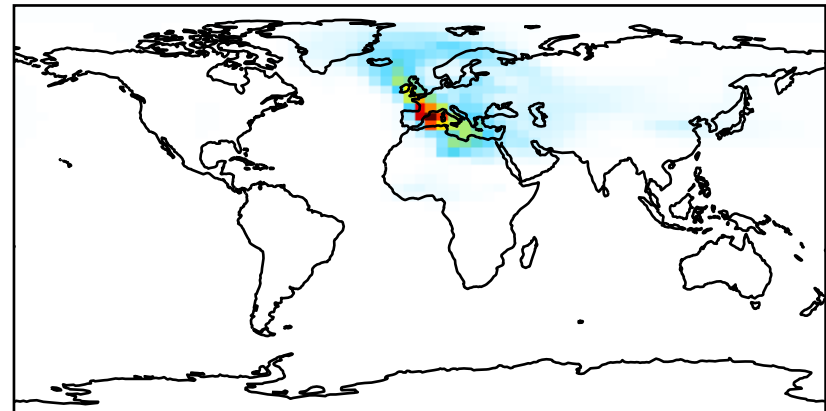


# SpeciesConc\_DST4 (Apr2019)

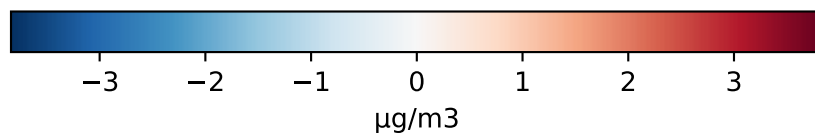
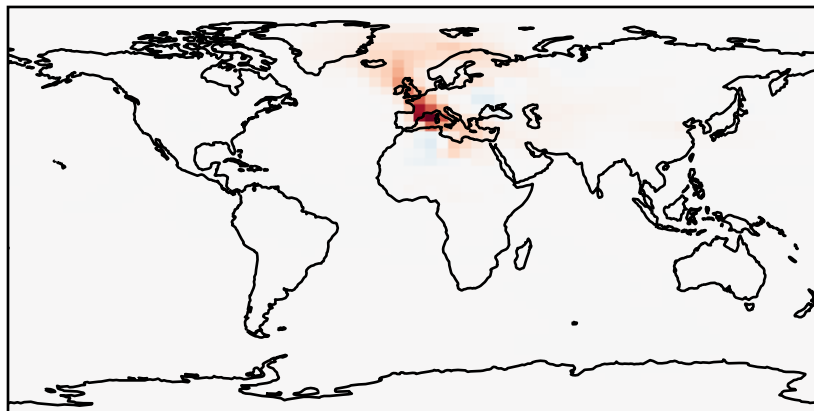
GCC\_12.9.0 (Ref)  
4.0x5.0



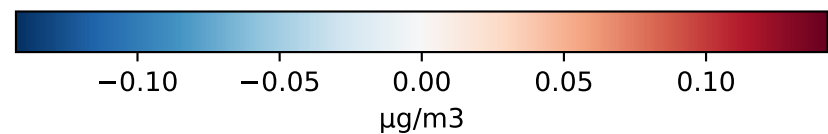
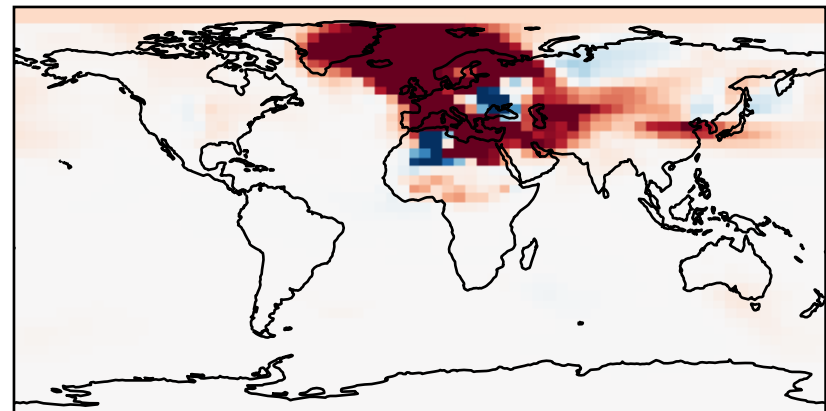
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



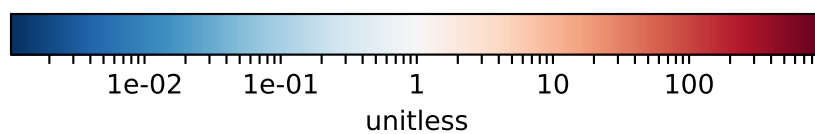
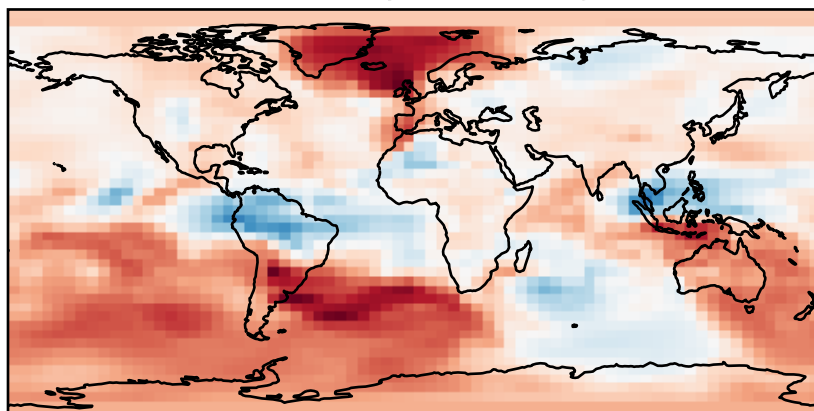
Difference  
Dev - Ref, Dynamic Range



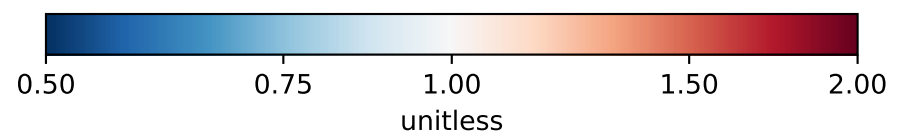
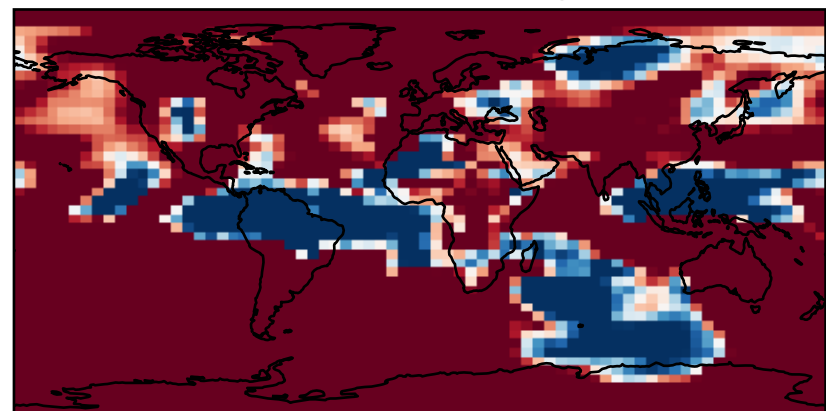
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



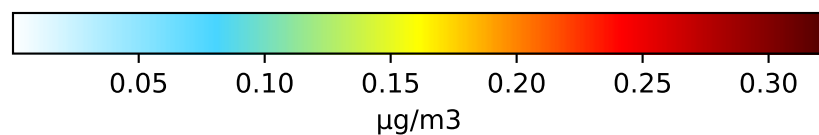
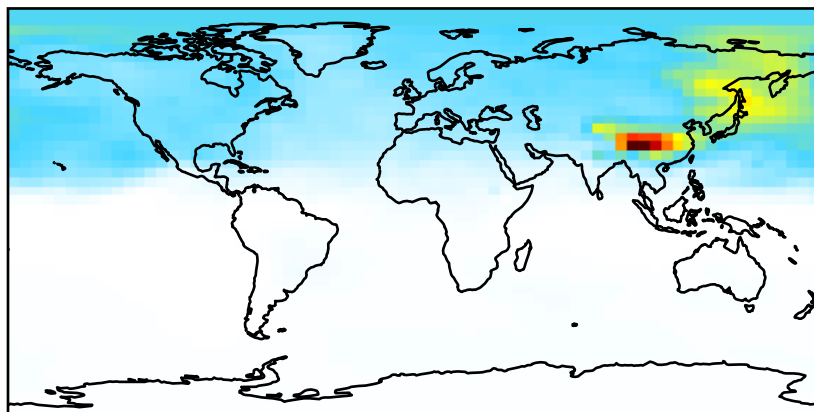
Ratio  
Dev/Ref, Fixed Range



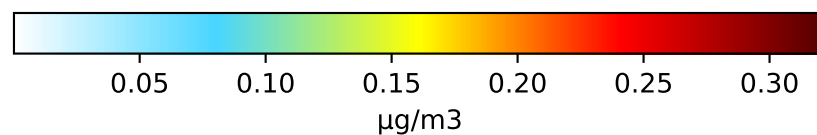
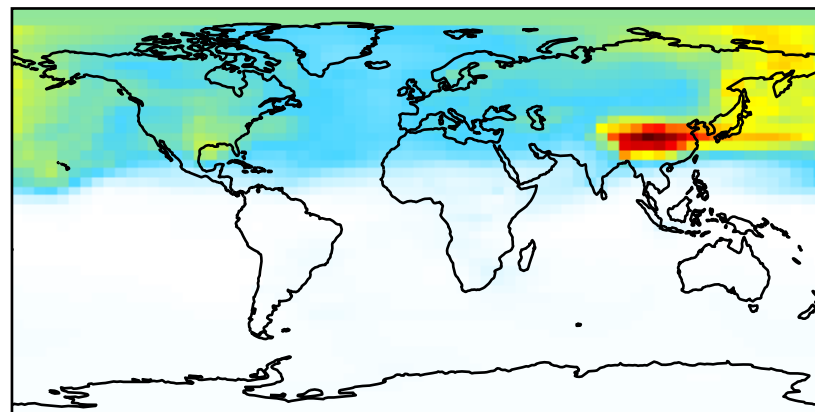


# SpeciesConc\_NH4 (Apr2019)

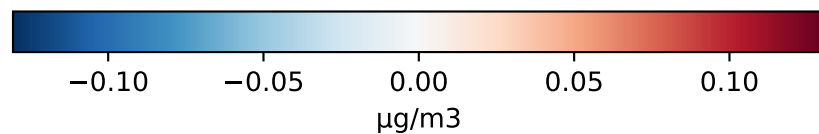
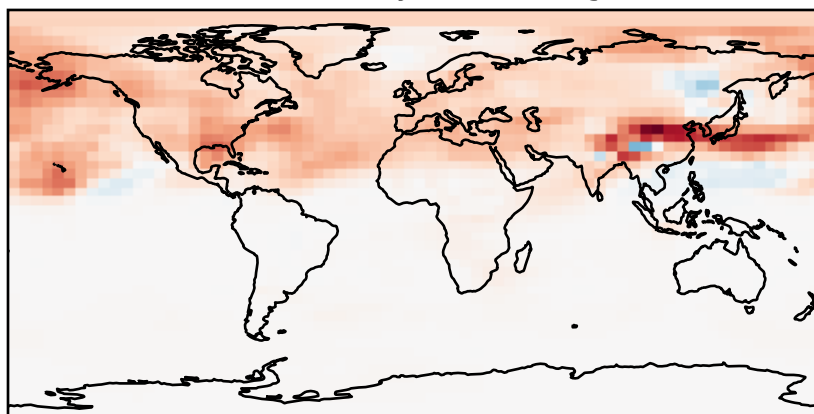
GCC\_12.9.0 (Ref)  
4.0x5.0



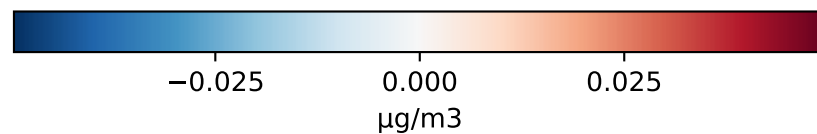
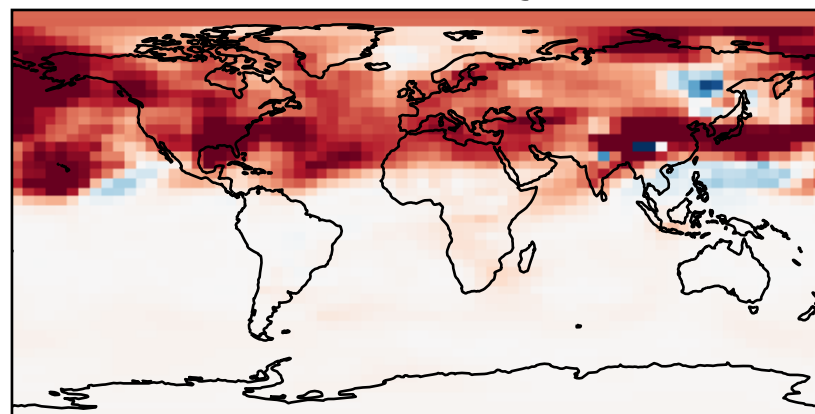
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



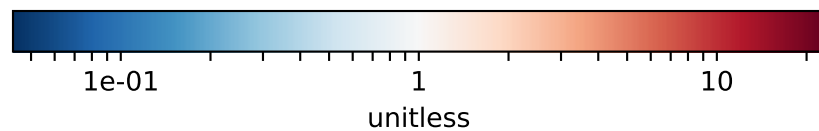
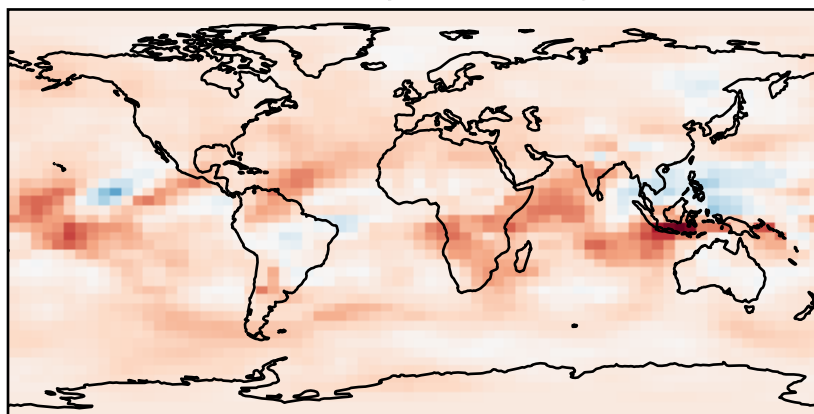
Difference  
Dev - Ref, Dynamic Range



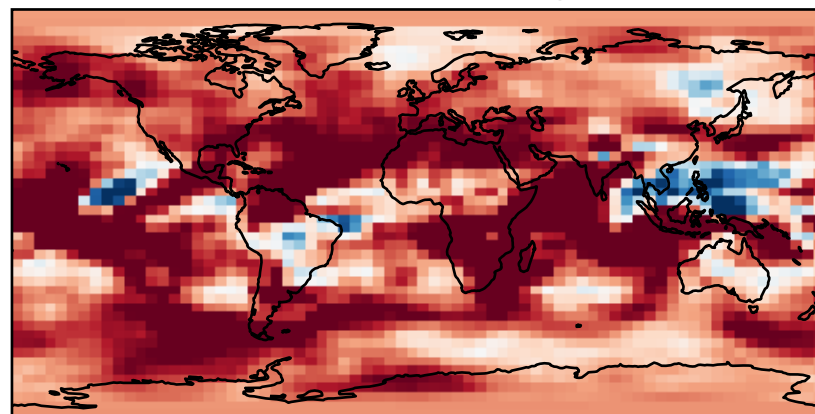
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

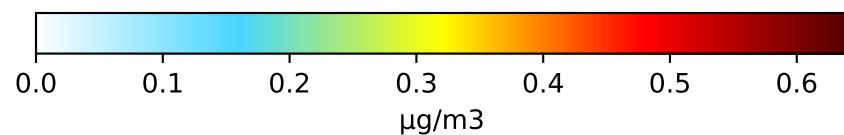
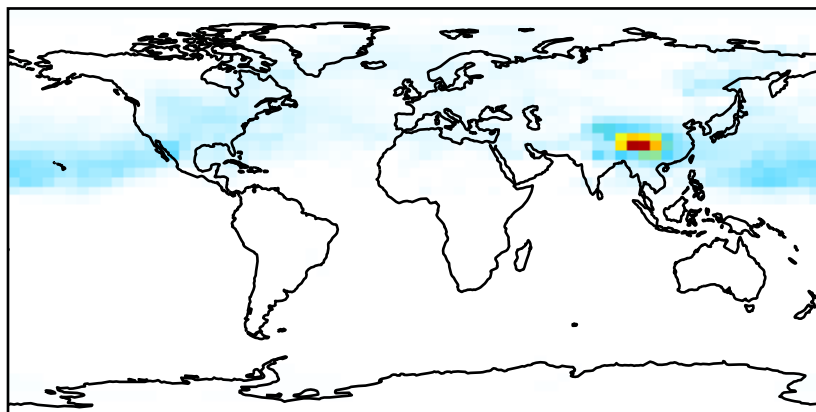


Ratio  
Dev/Ref, Fixed Range

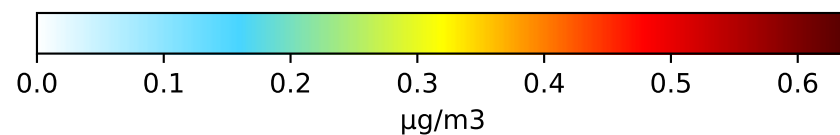
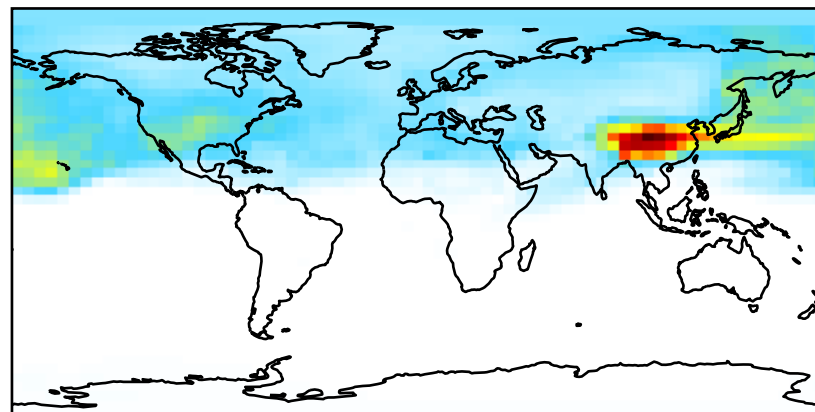


# SpeciesConc\_NIT (Apr2019)

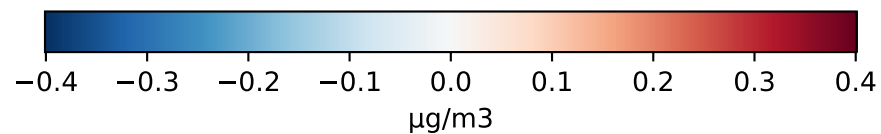
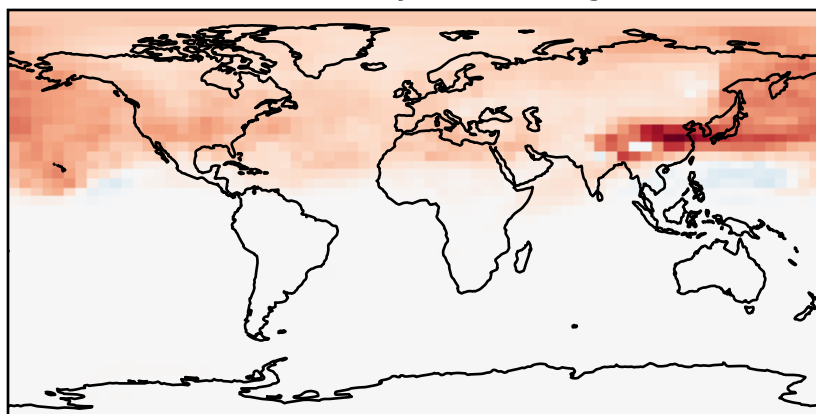
GCC\_12.9.0 (Ref)  
4.0x5.0



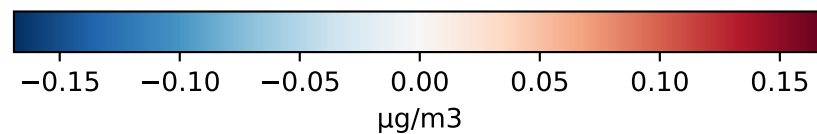
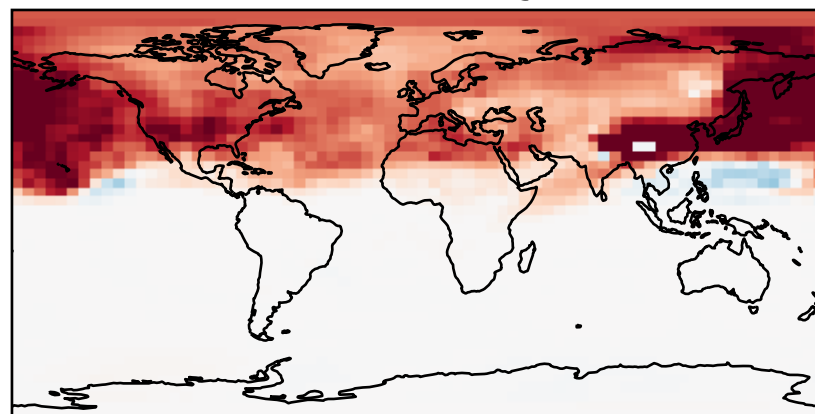
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



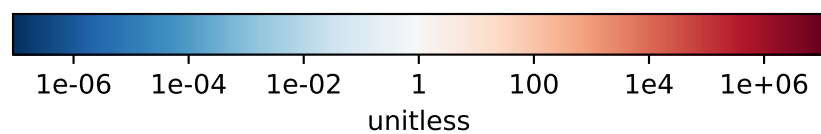
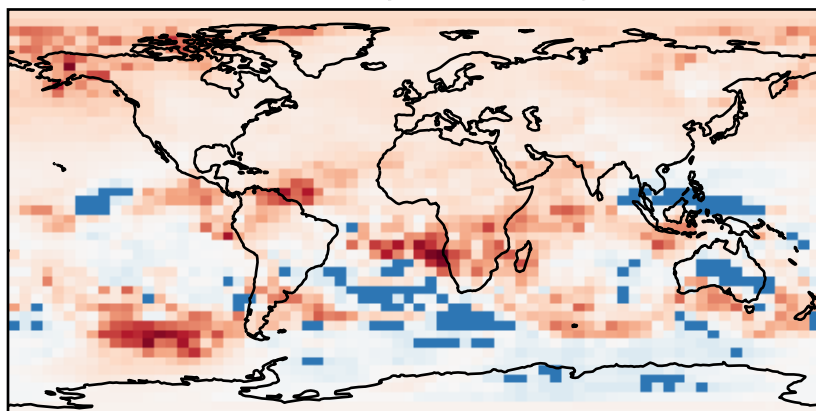
Difference  
Dev - Ref, Dynamic Range



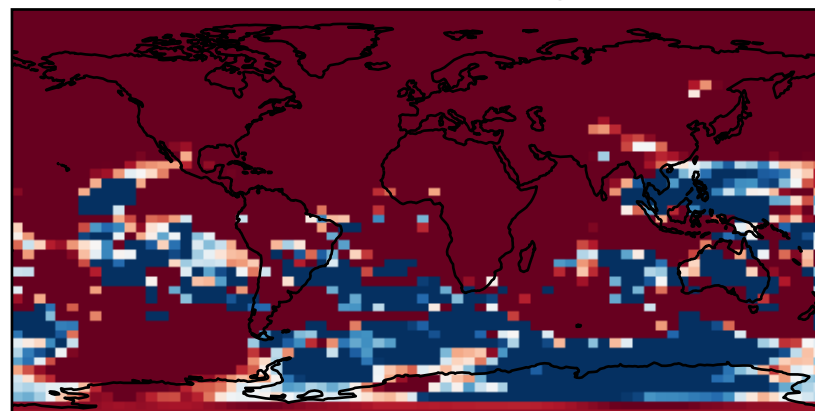
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

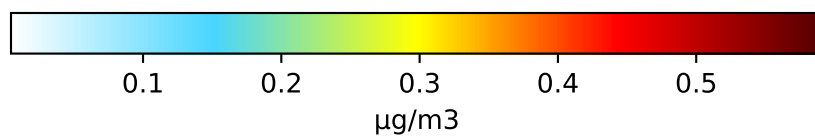
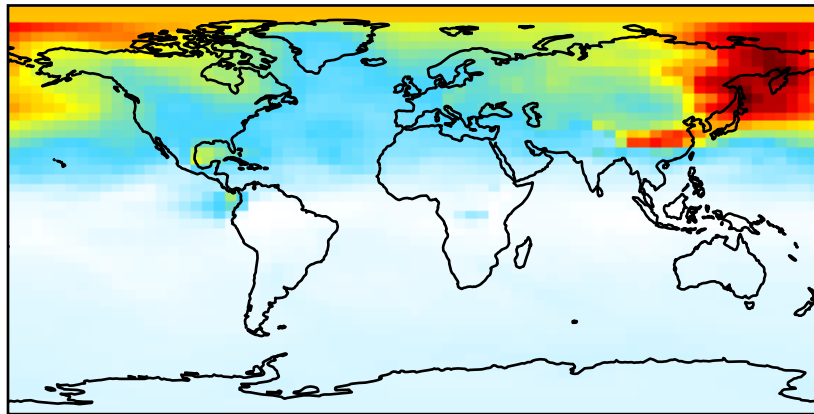


Ratio  
Dev/Ref, Fixed Range

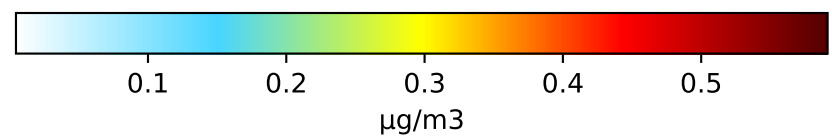
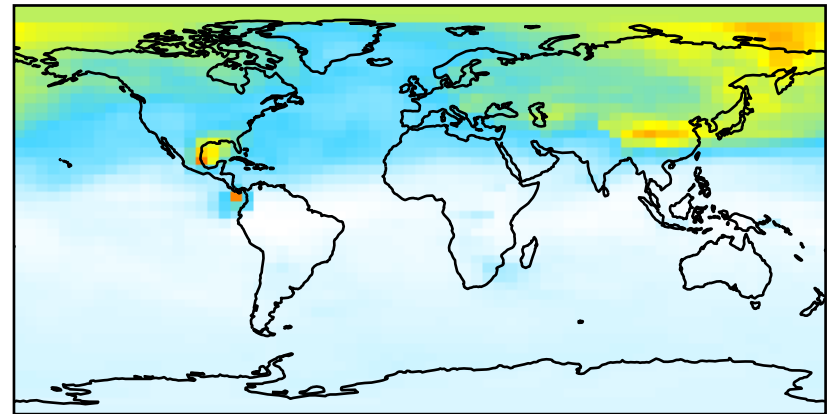


# SpeciesConc\_SO4 (Apr2019)

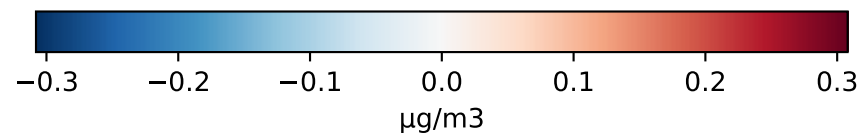
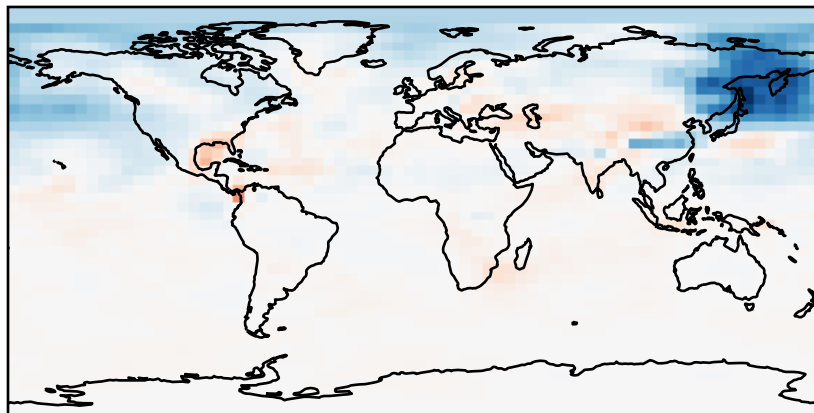
GCC\_12.9.0 (Ref)  
4.0x5.0



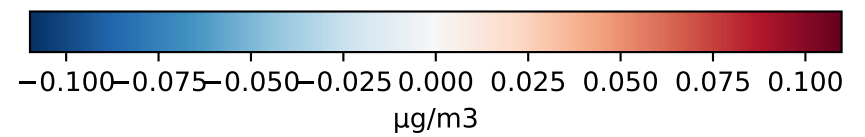
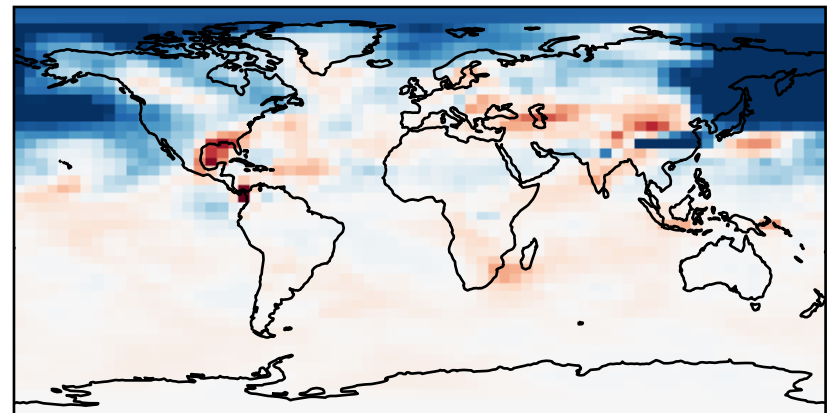
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



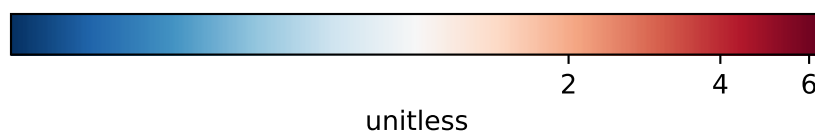
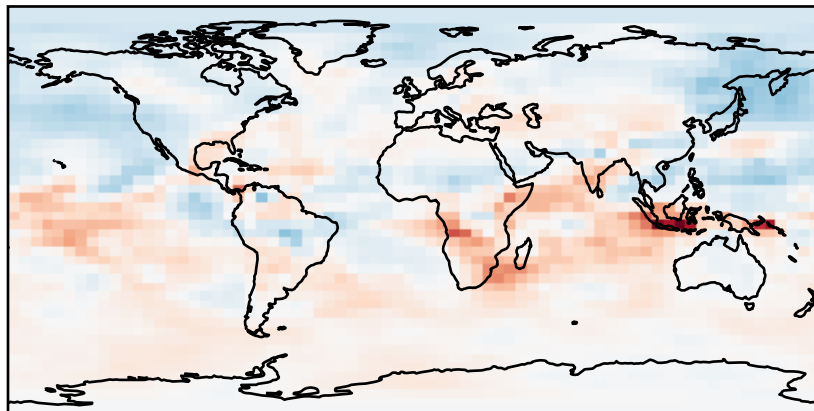
Difference  
Dev - Ref, Dynamic Range



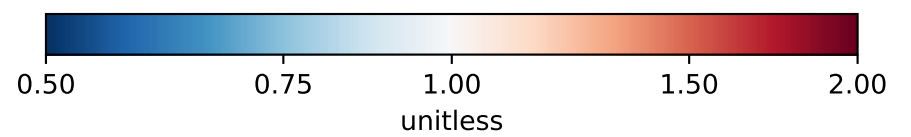
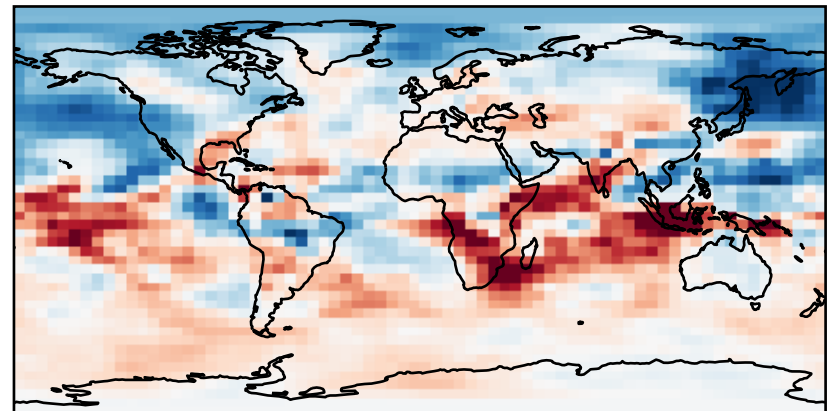
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

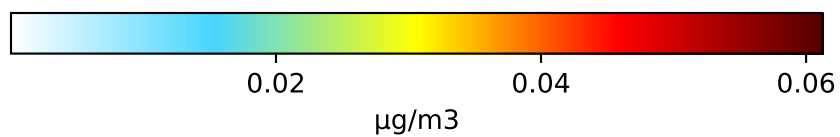
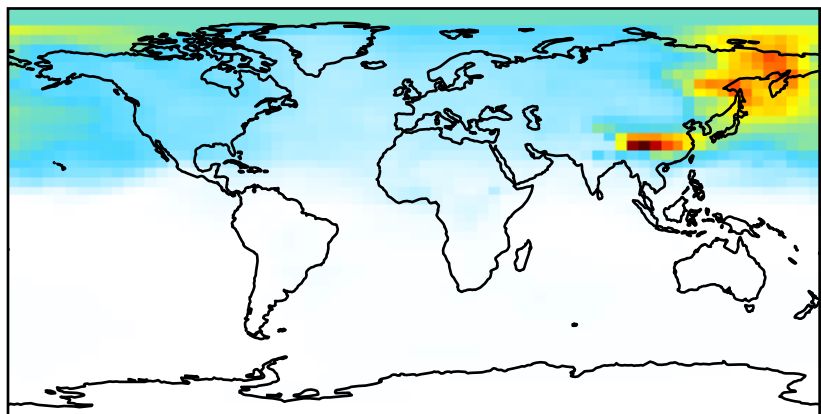


Ratio  
Dev/Ref, Fixed Range

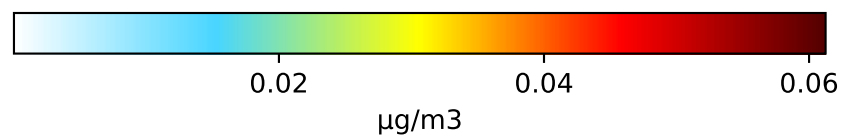
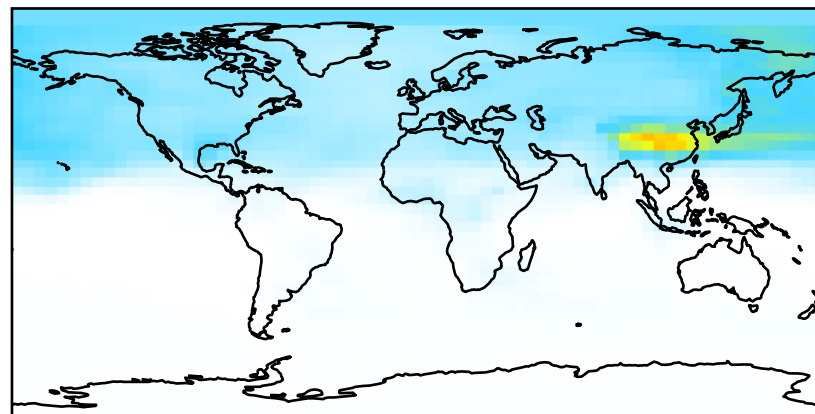


# SpeciesConc\_BCPI (Apr2019)

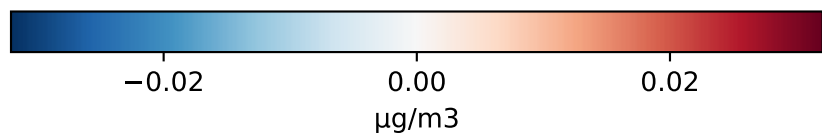
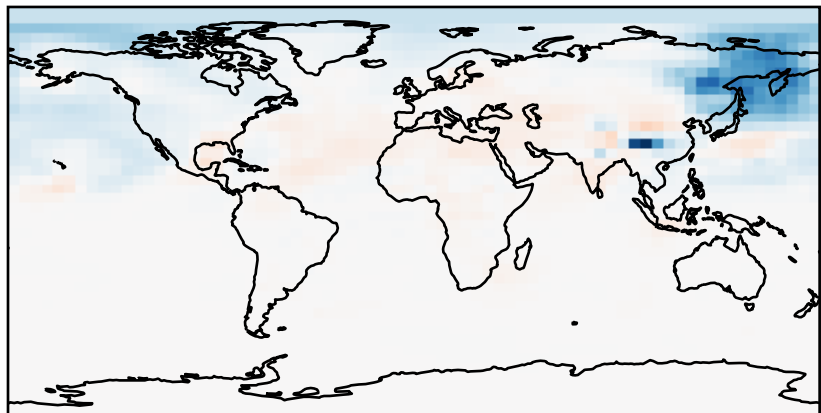
GCC\_12.9.0 (Ref)  
4.0x5.0



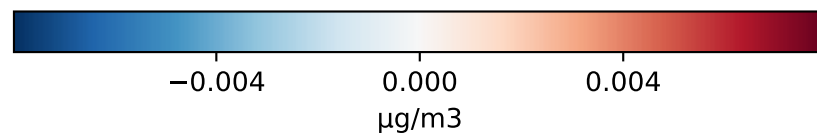
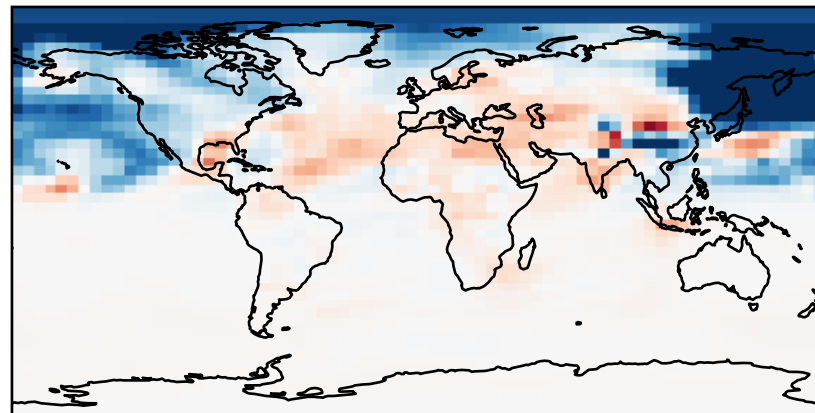
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



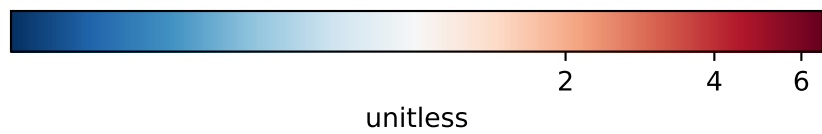
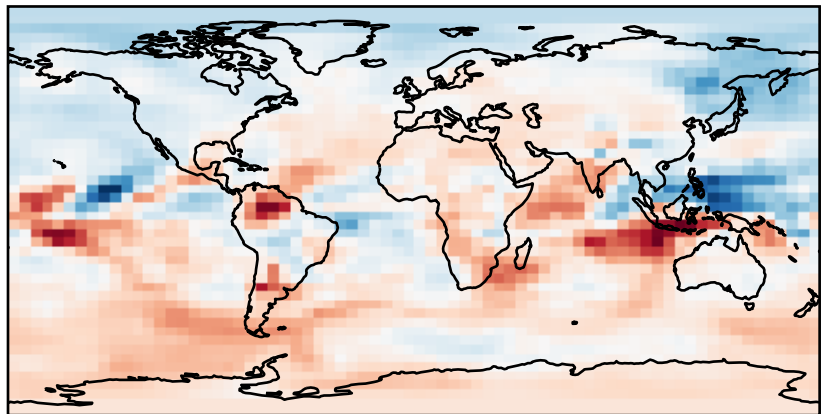
Difference  
Dev - Ref, Dynamic Range



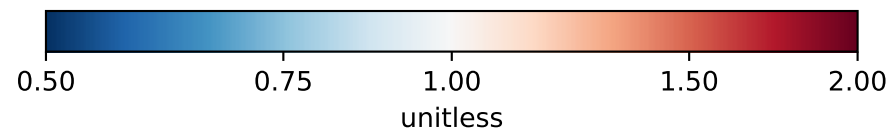
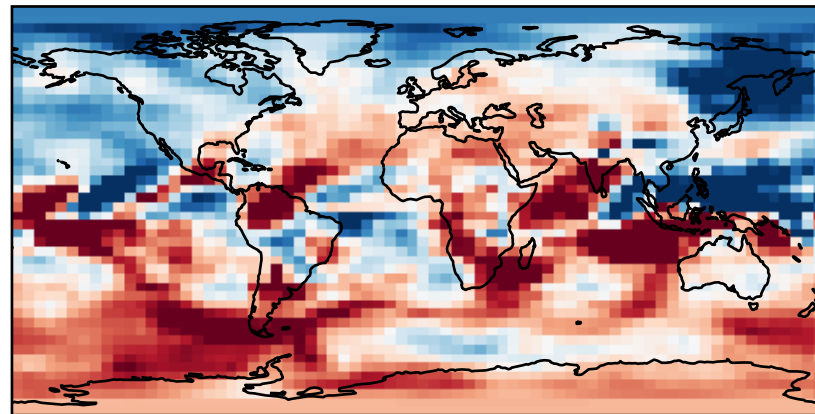
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



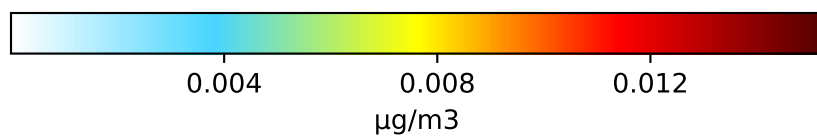
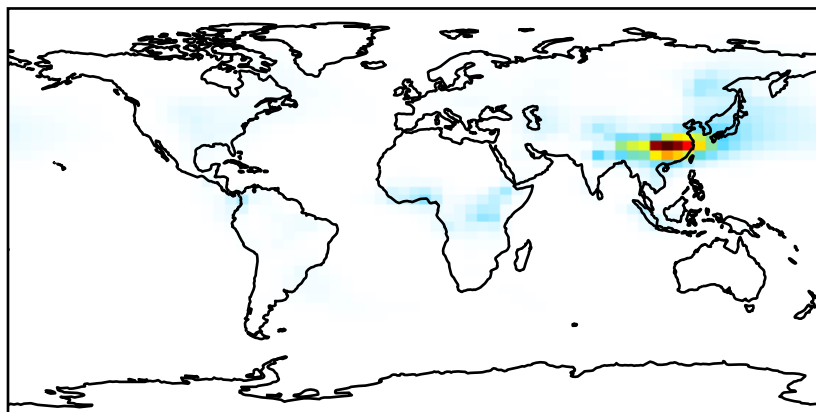
Ratio  
Dev/Ref, Fixed Range



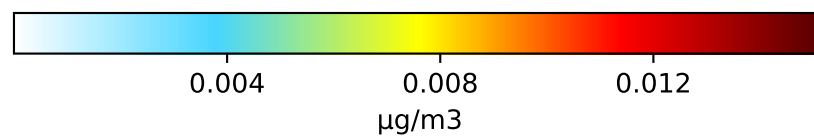
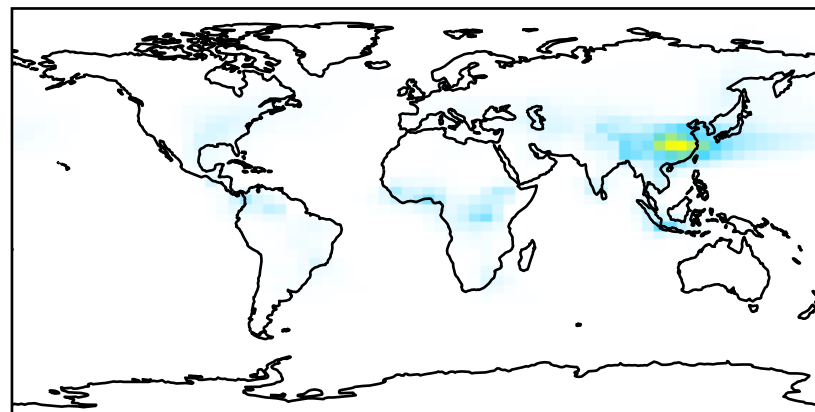


# SpeciesConc\_BCPO (Apr2019)

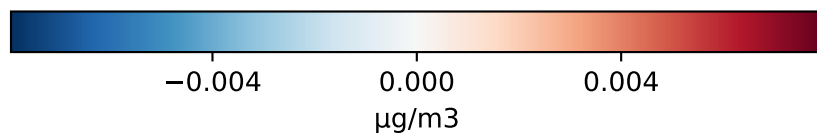
GCC\_12.9.0 (Ref)  
4.0x5.0



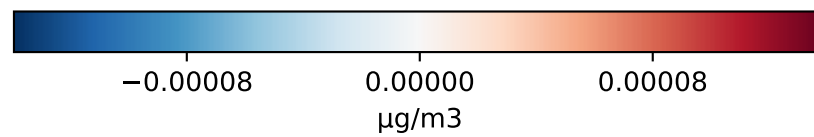
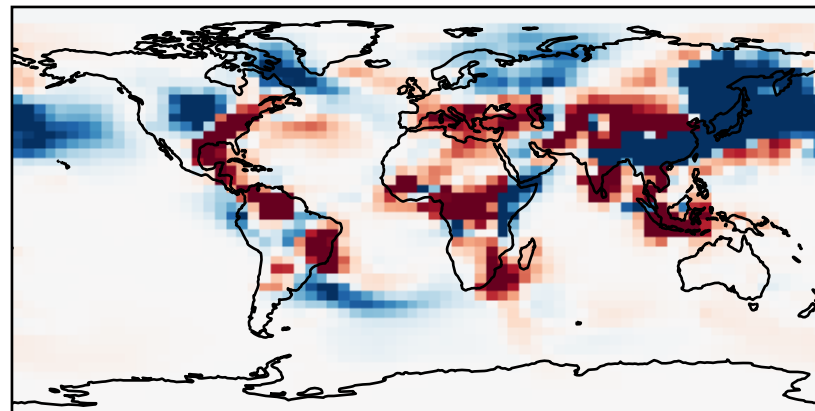
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



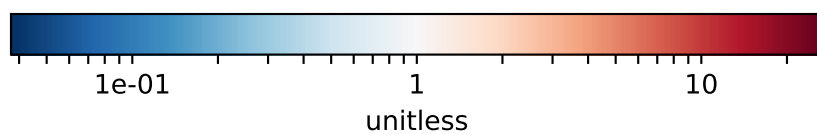
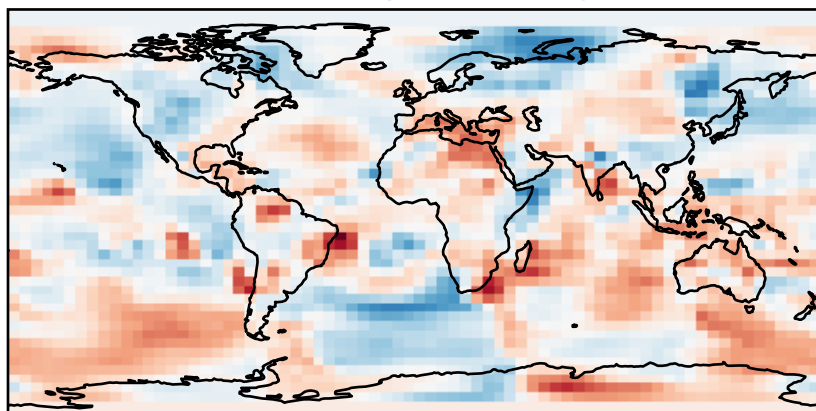
Difference  
Dev - Ref, Dynamic Range



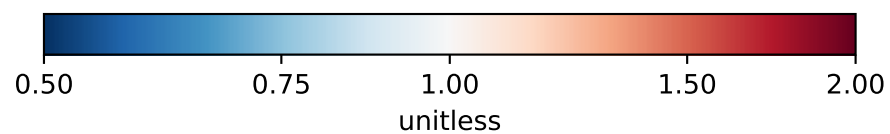
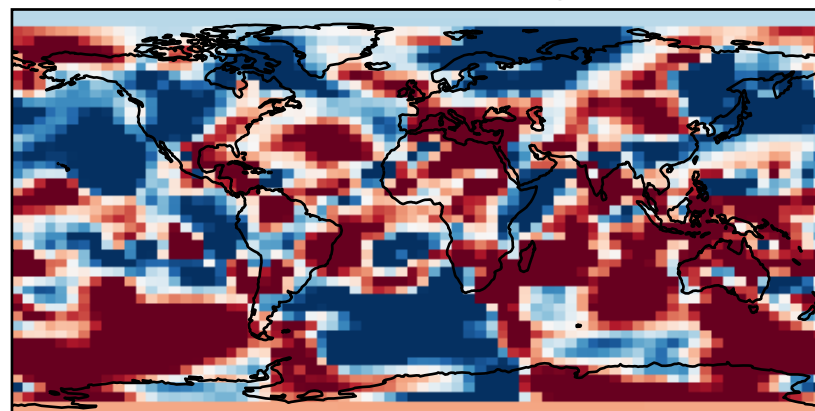
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



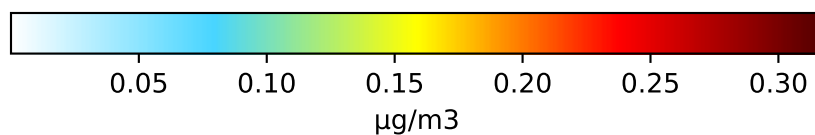
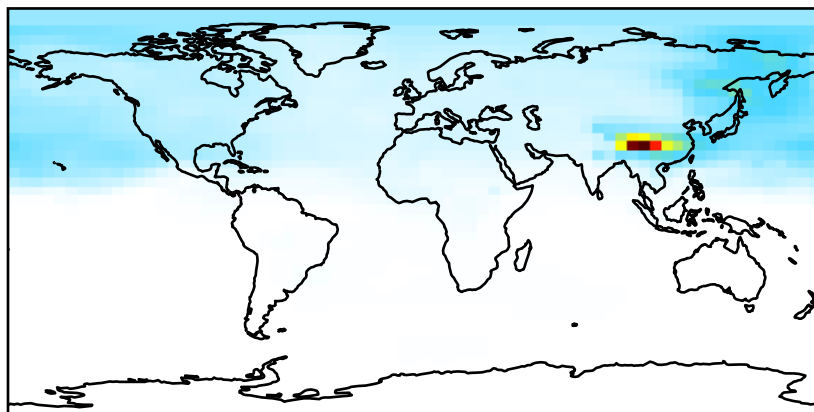
Ratio  
Dev/Ref, Fixed Range



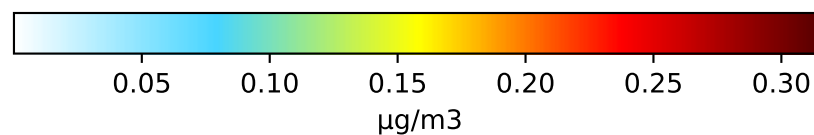
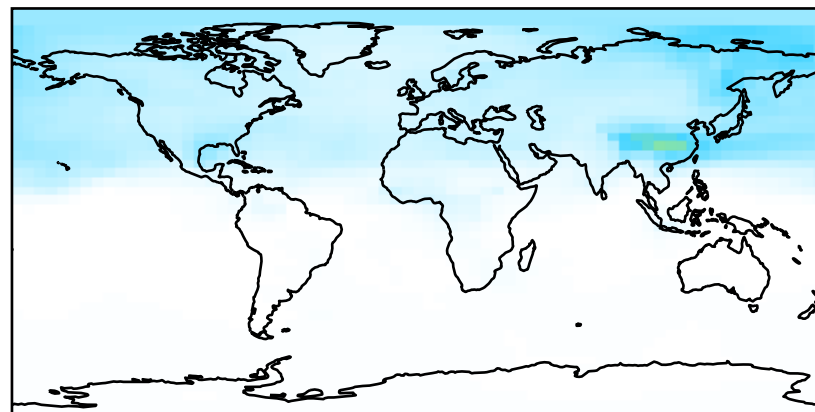


# SpeciesConc\_OCPI (Apr2019)

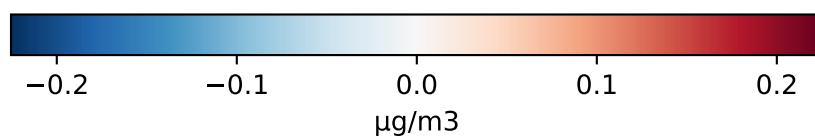
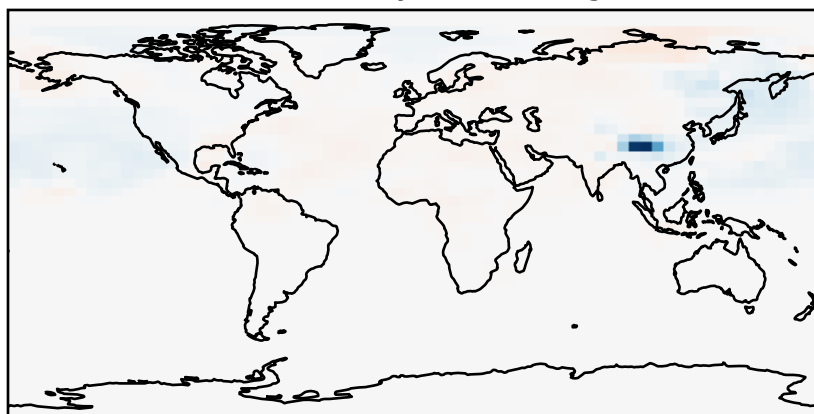
GCC\_12.9.0 (Ref)  
4.0x5.0



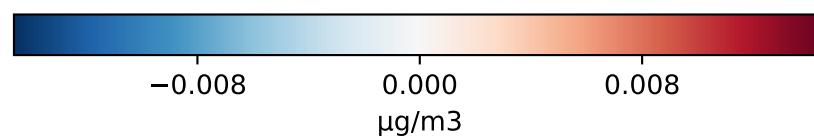
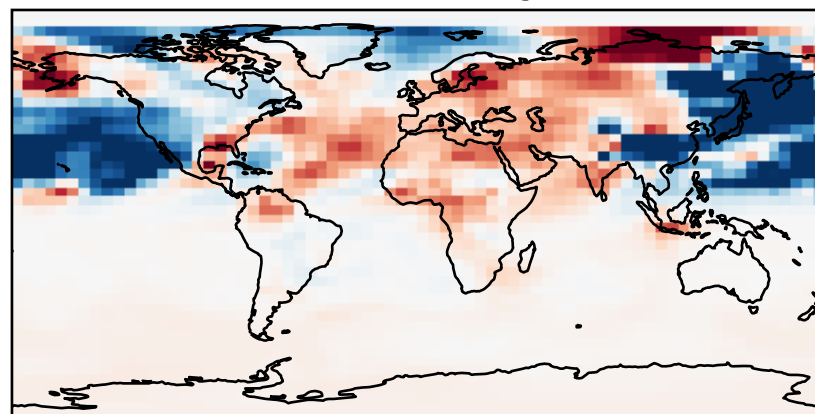
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



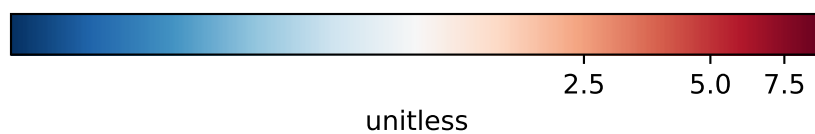
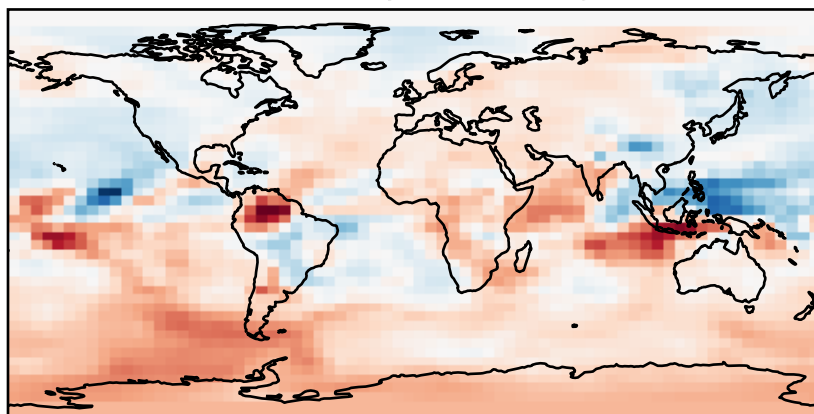
Difference  
Dev - Ref, Dynamic Range



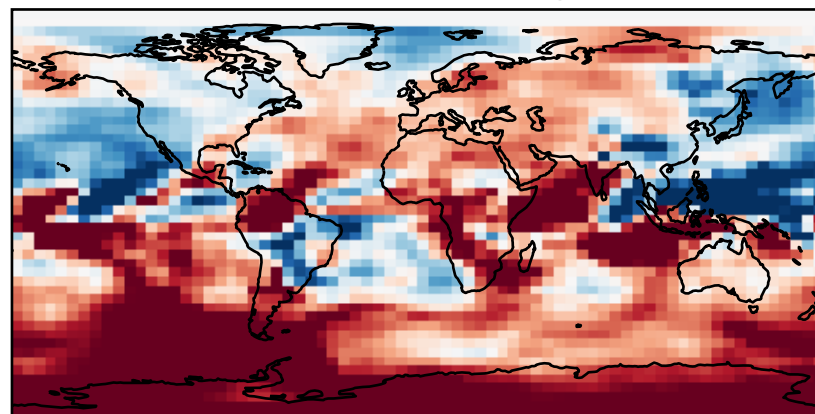
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

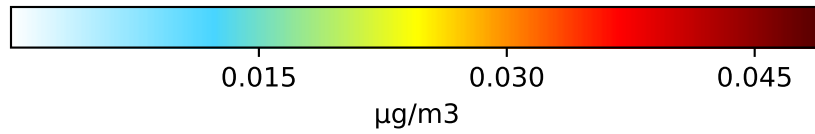
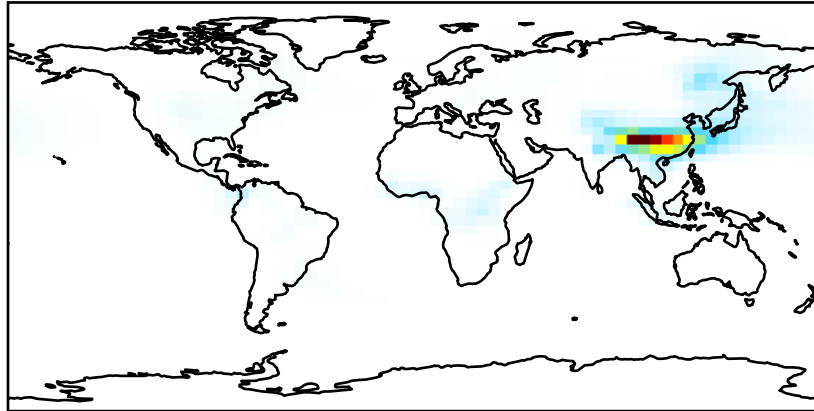


Ratio  
Dev/Ref, Fixed Range

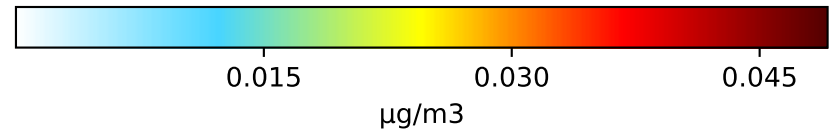
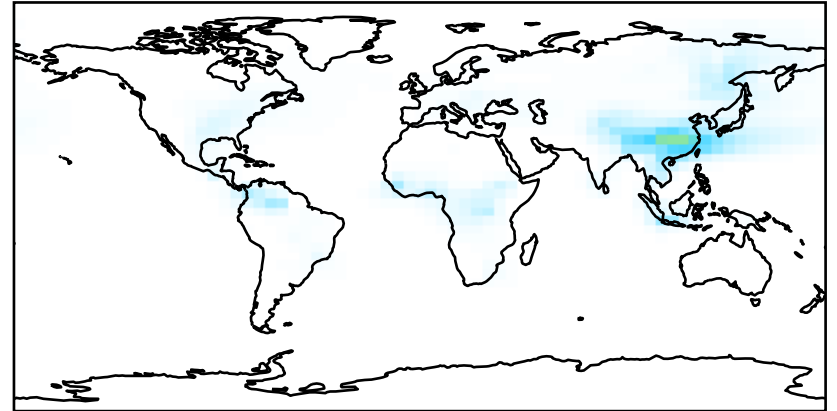


# SpeciesConc\_OCPO (Apr2019)

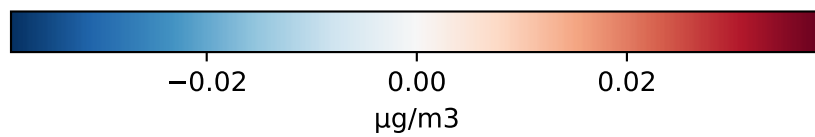
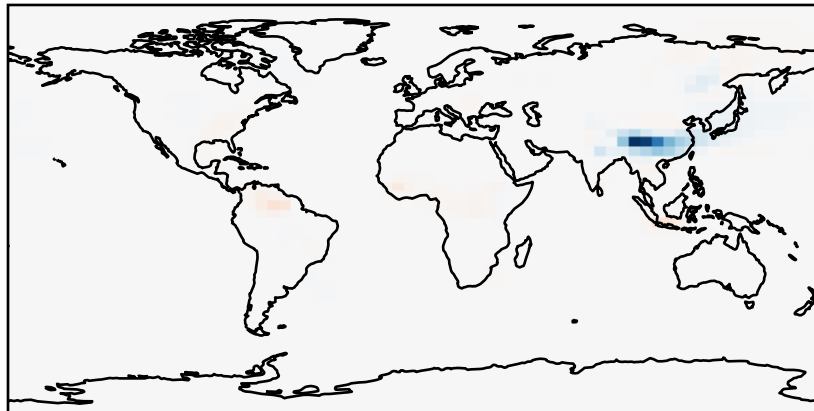
GCC\_12.9.0 (Ref)  
4.0x5.0



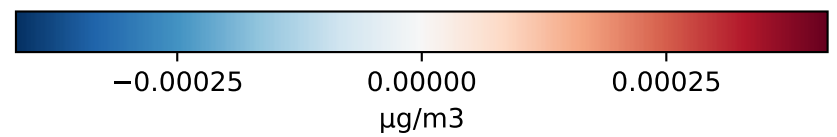
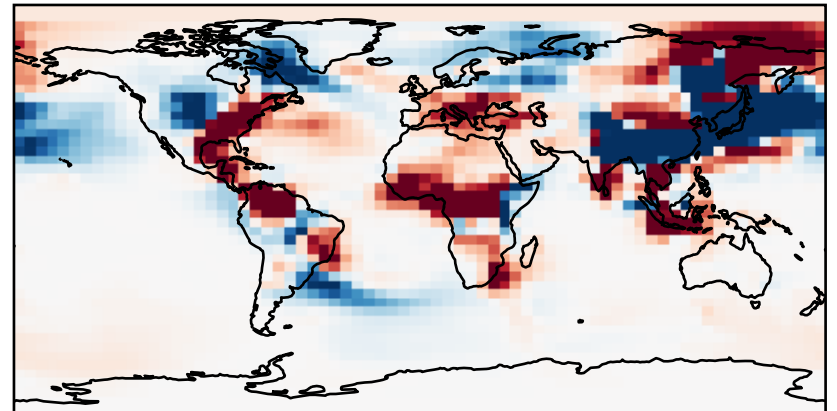
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



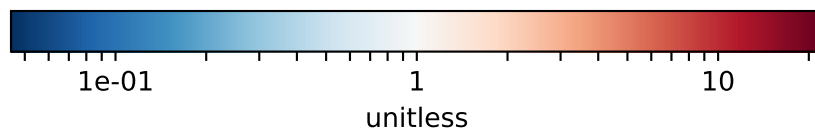
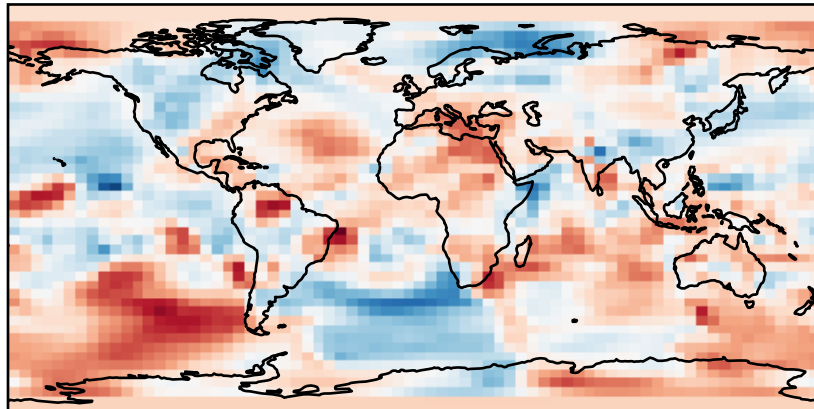
Difference  
Dev - Ref, Dynamic Range



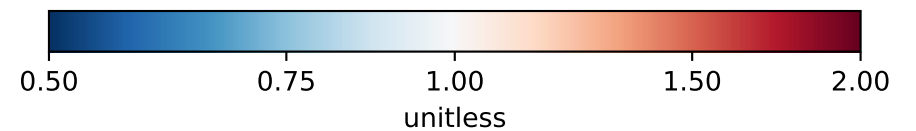
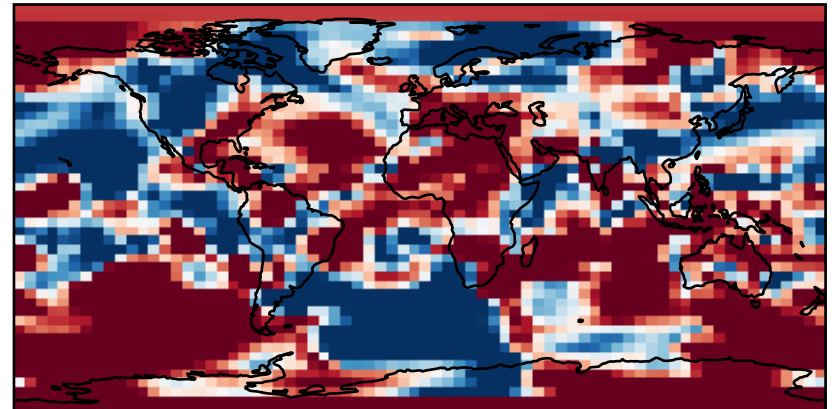
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

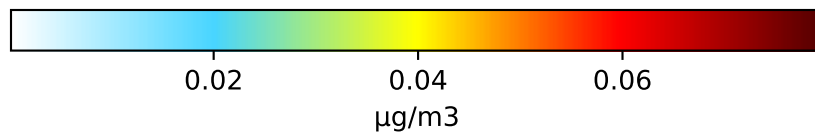
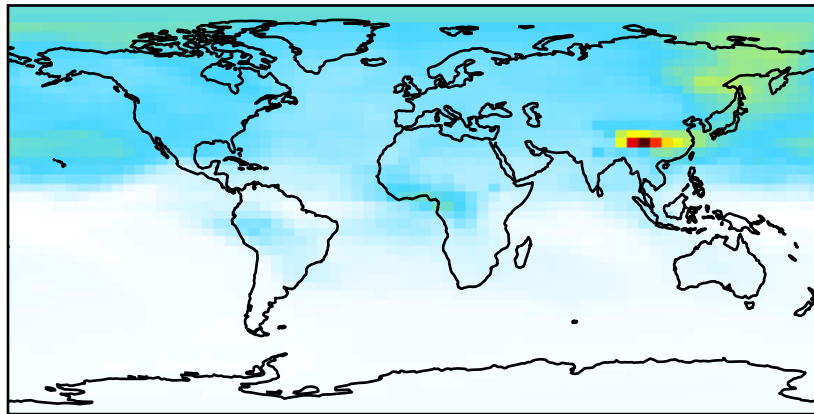


Ratio  
Dev/Ref, Fixed Range

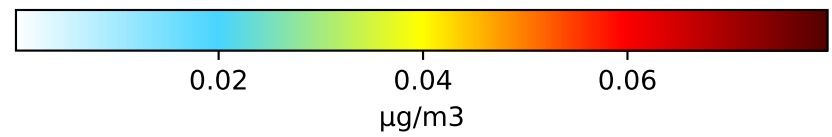
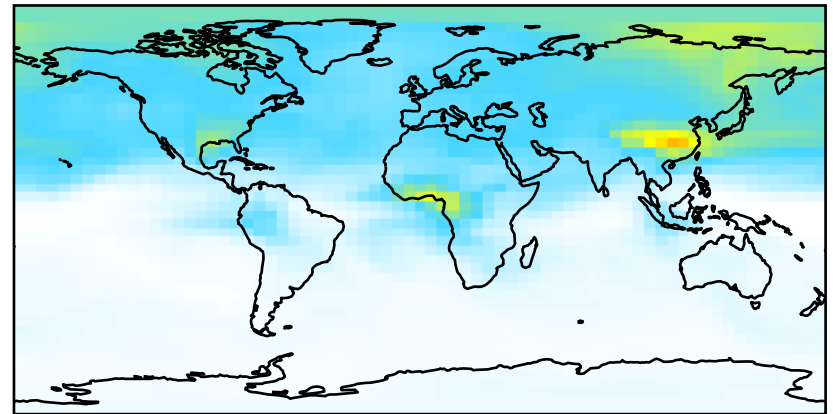


# SpeciesConc\_Complex\_SOA (Apr2019)

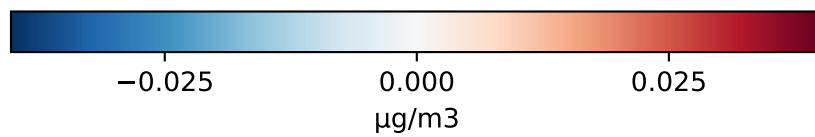
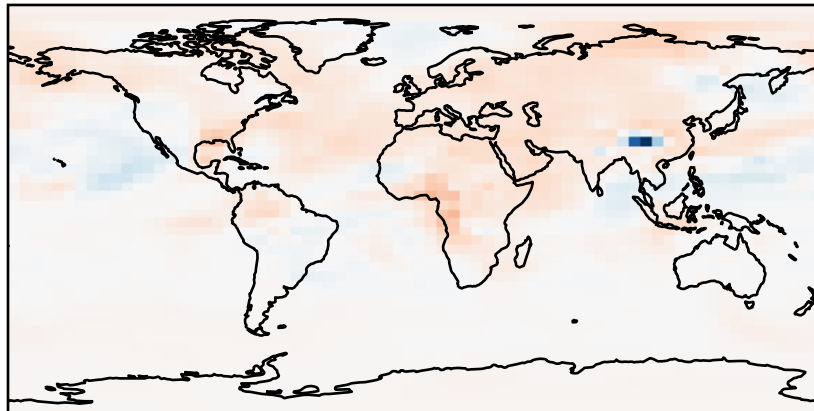
GCC\_12.9.0 (Ref)  
4.0x5.0



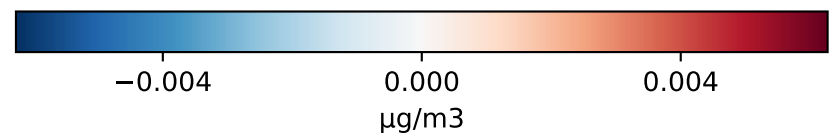
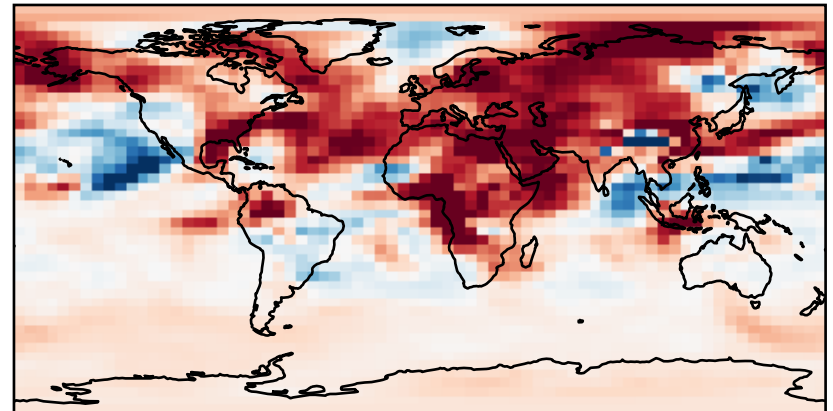
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



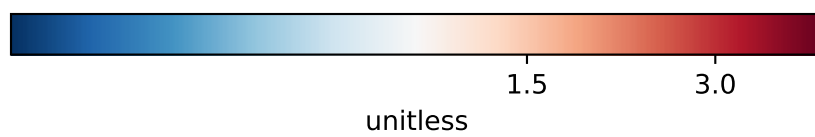
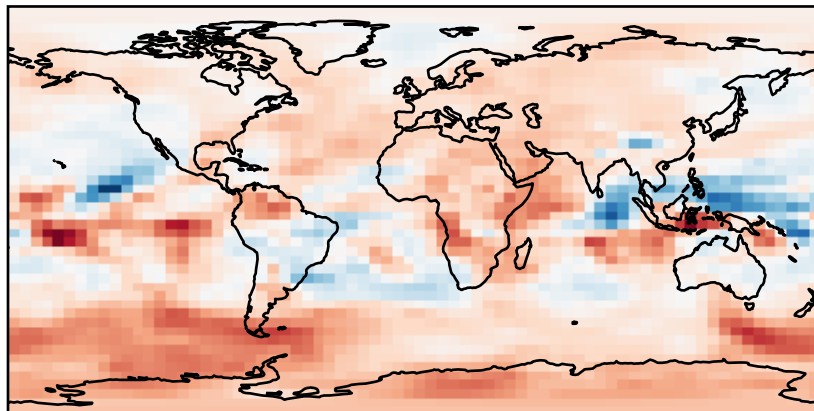
Difference  
Dev - Ref, Dynamic Range



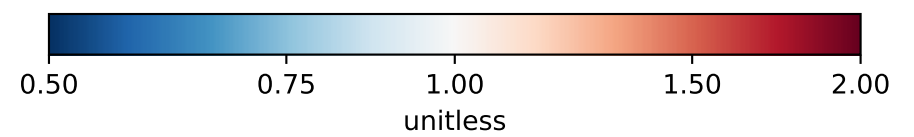
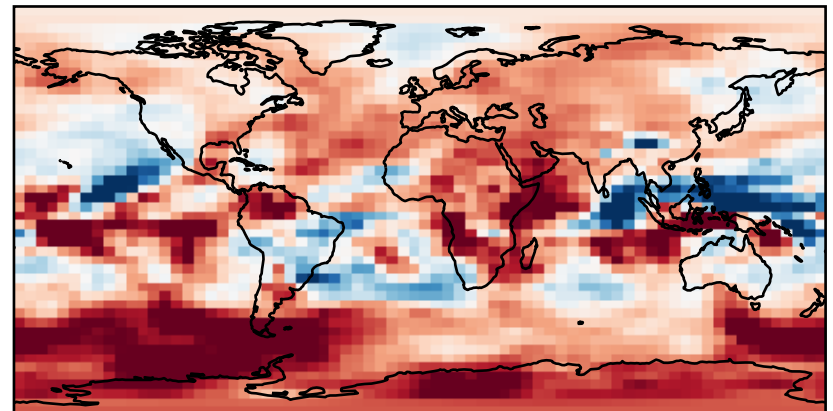
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

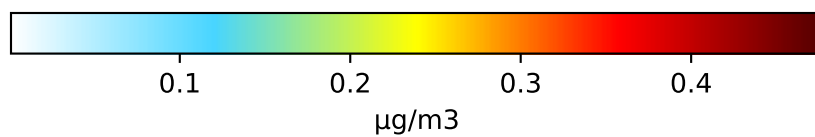
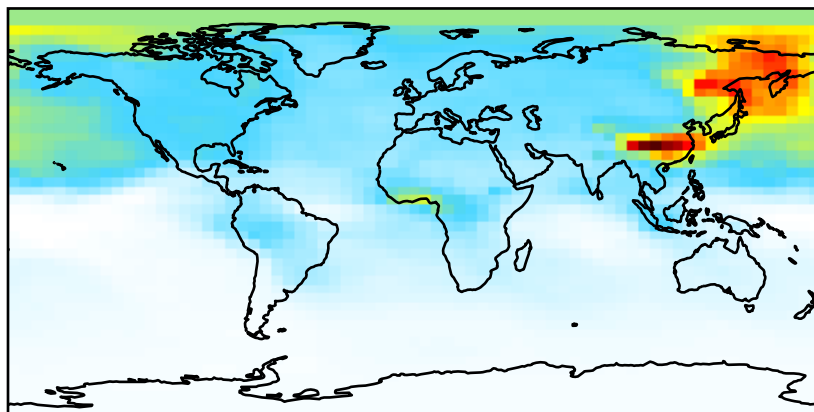


Ratio  
Dev/Ref, Fixed Range

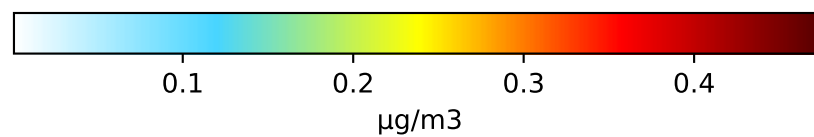
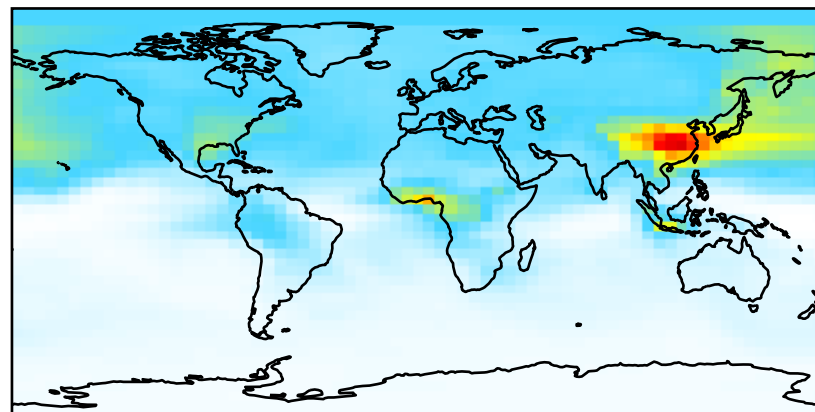


# SpeciesConc\_Simple\_SOA (Apr2019)

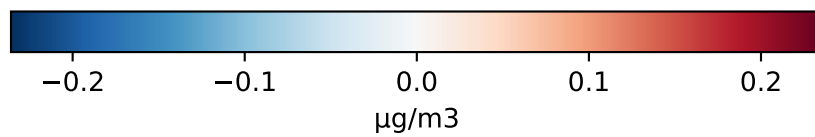
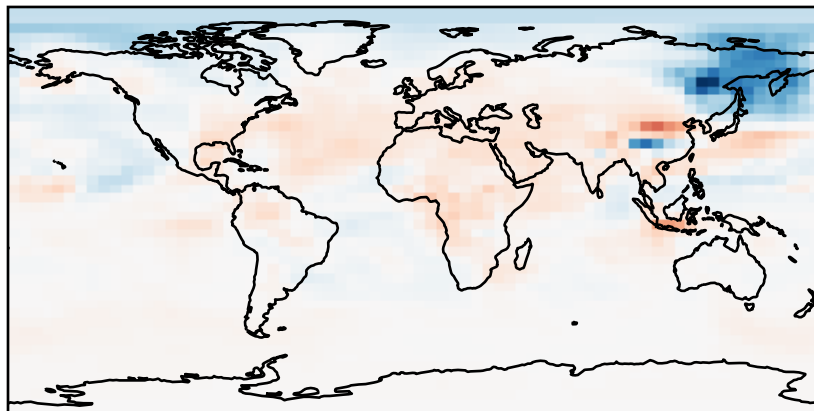
GCC\_12.9.0 (Ref)  
4.0x5.0



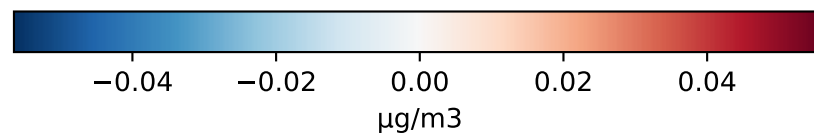
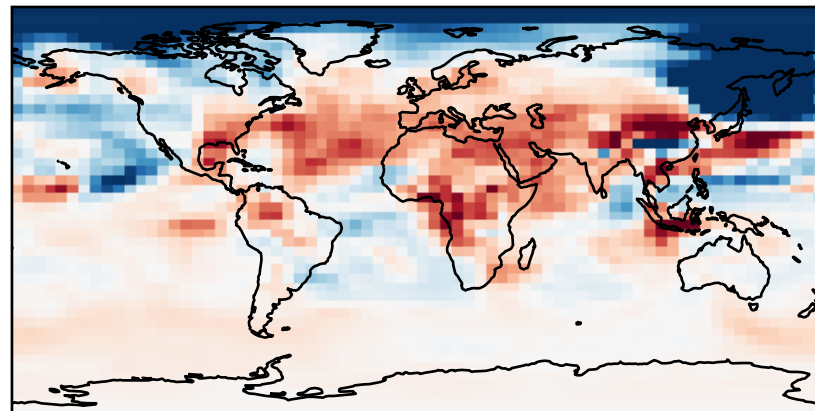
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



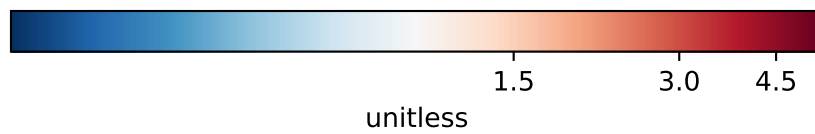
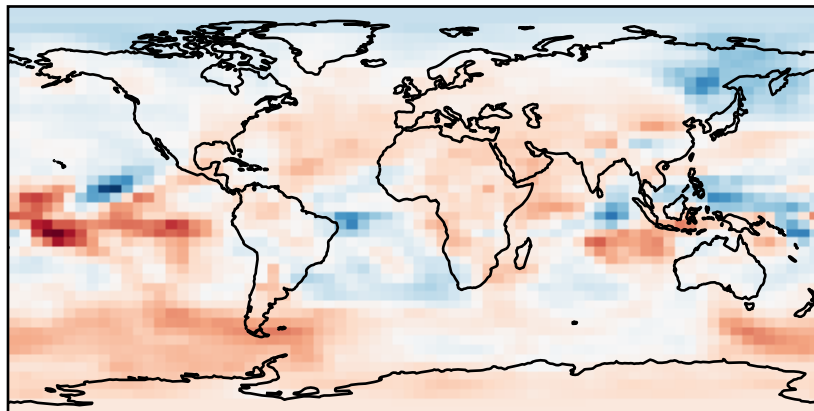
Difference  
Dev - Ref, Dynamic Range



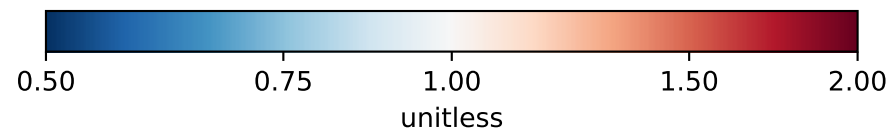
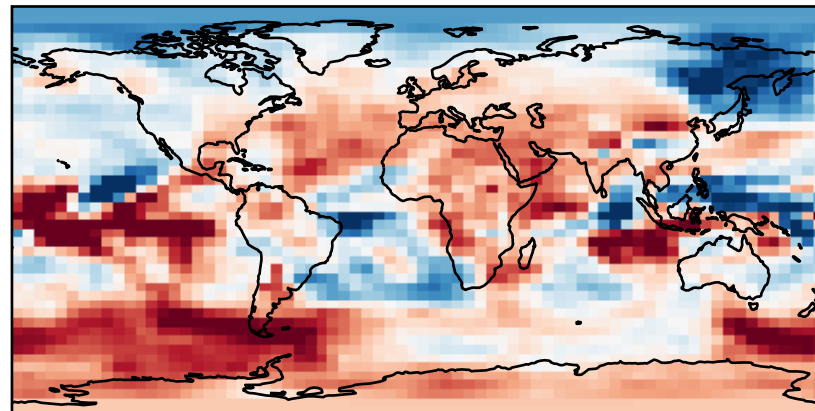
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



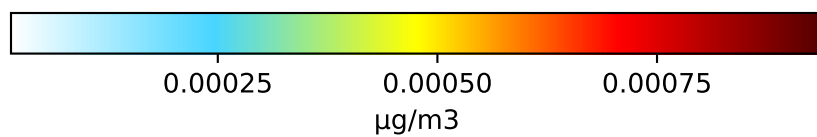
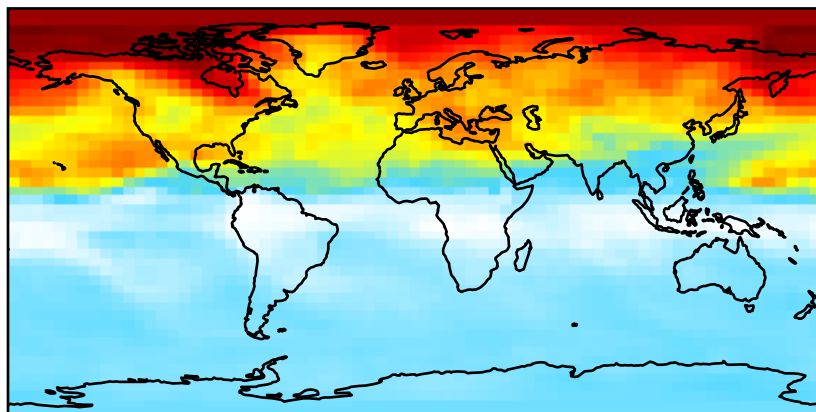
Ratio  
Dev/Ref, Fixed Range



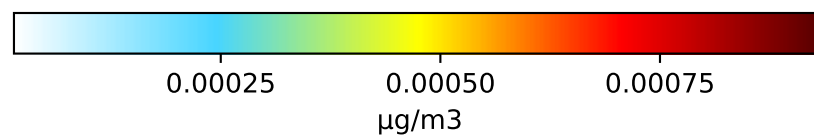
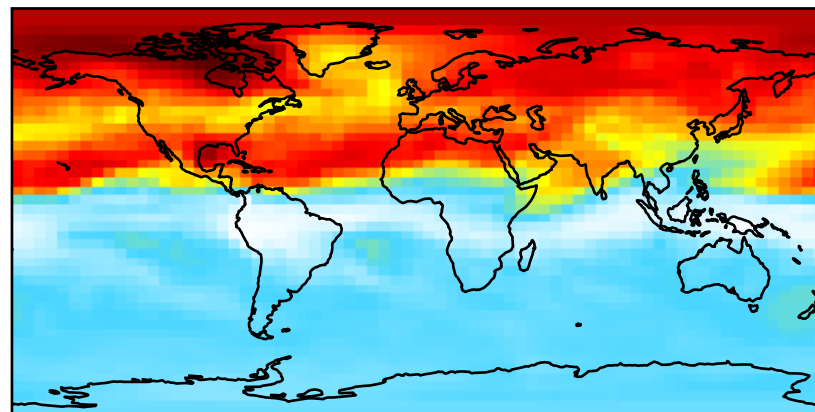


# SpeciesConc\_AERI (Apr2019)

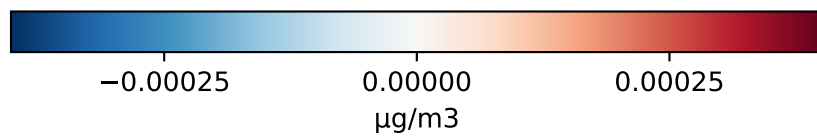
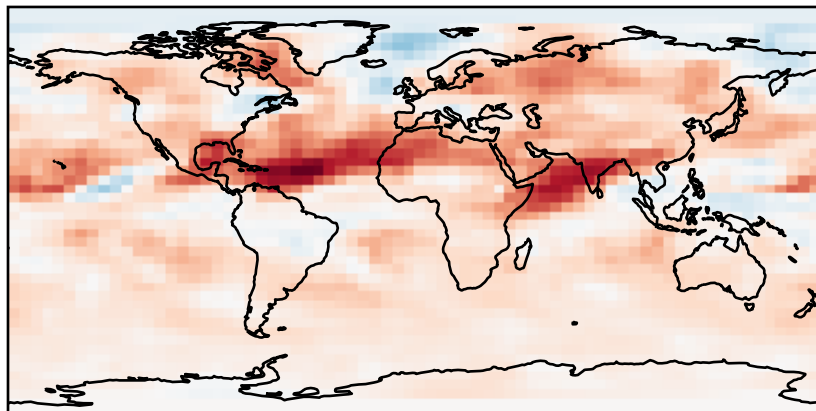
GCC\_12.9.0 (Ref)  
4.0x5.0



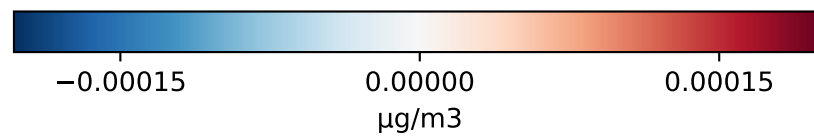
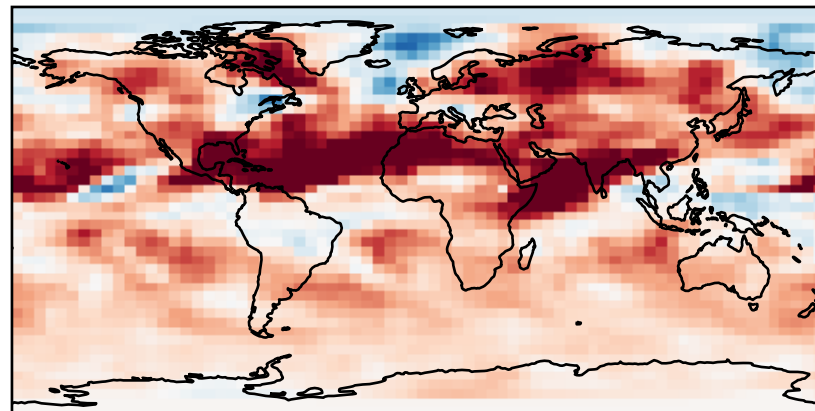
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



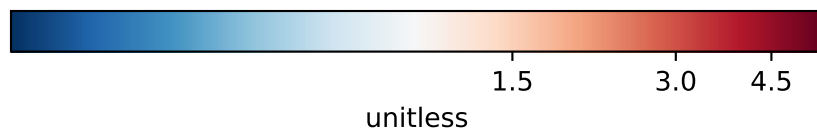
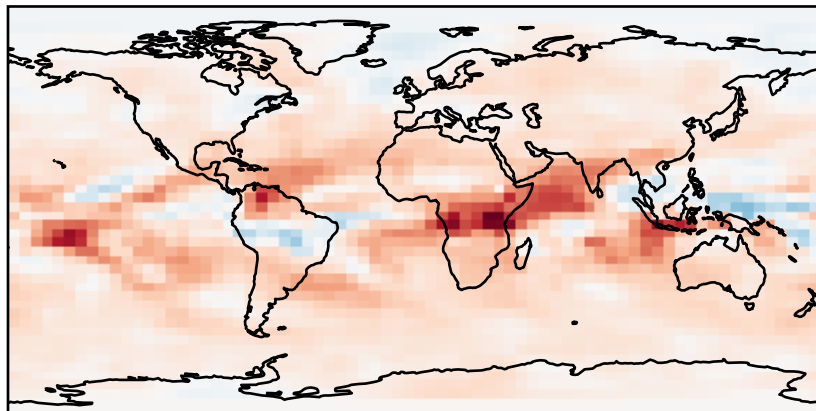
Difference  
Dev - Ref, Dynamic Range



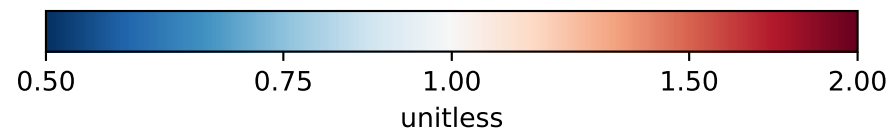
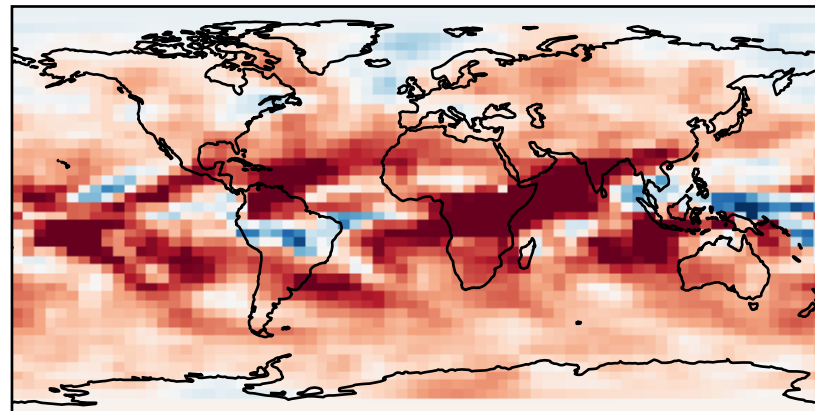
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

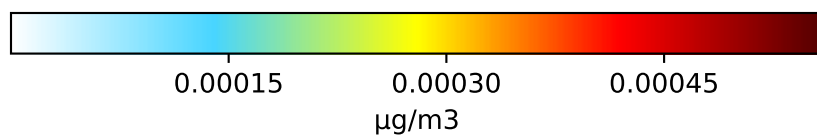
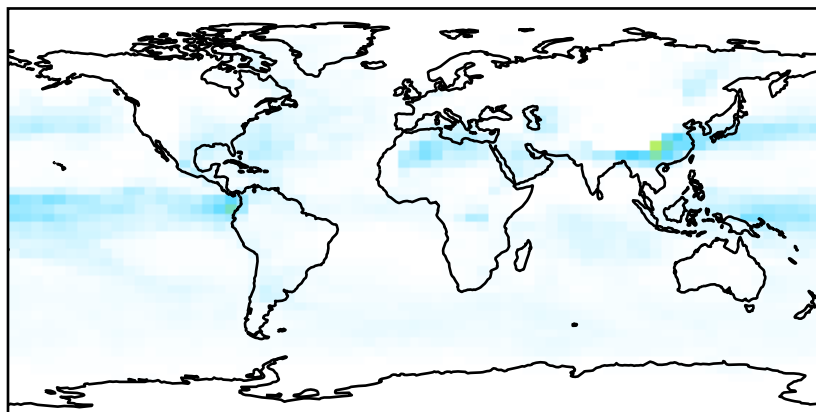


Ratio  
Dev/Ref, Fixed Range

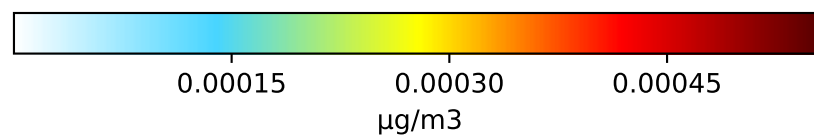
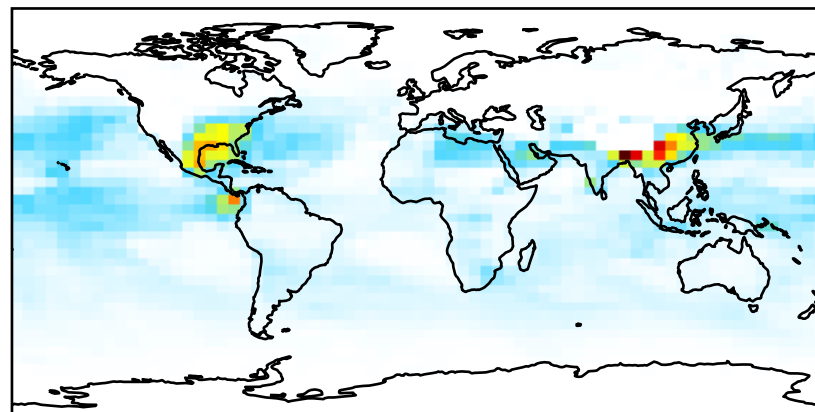


# SpeciesConc\_BrSALA (Apr2019)

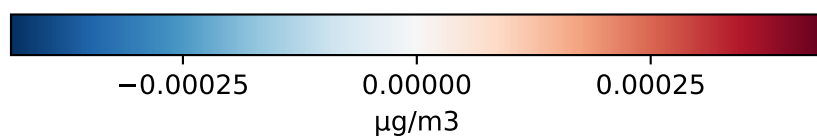
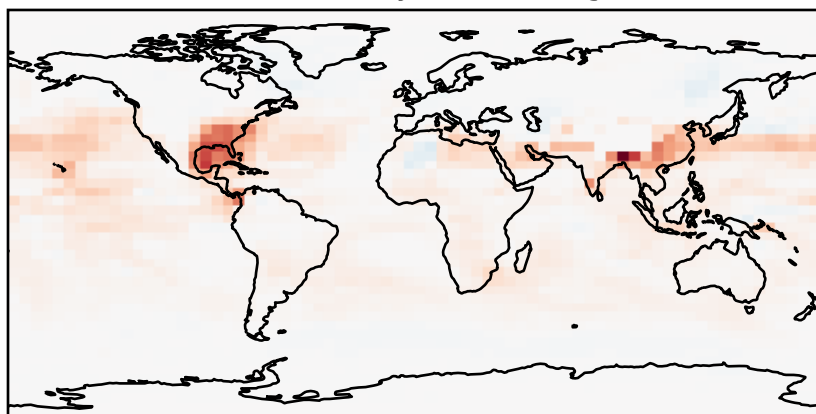
GCC\_12.9.0 (Ref)  
4.0x5.0



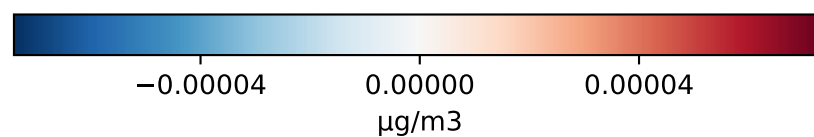
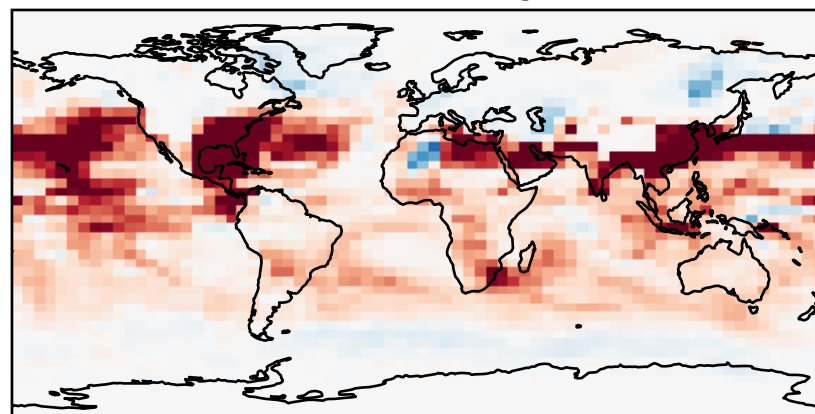
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



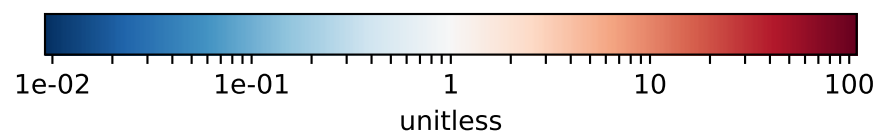
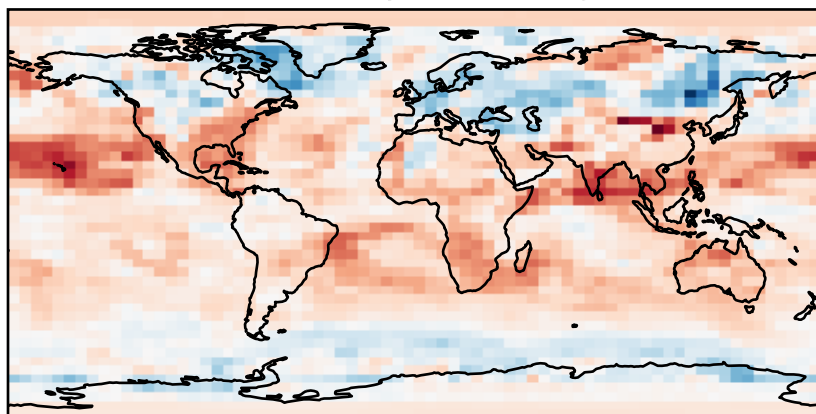
Difference  
Dev - Ref, Dynamic Range



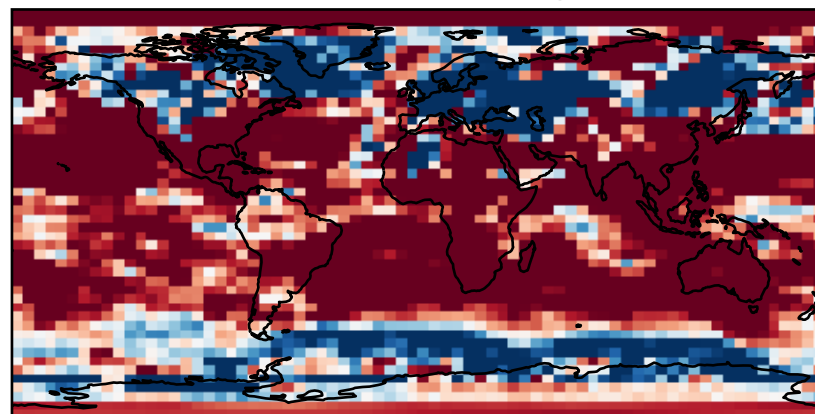
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

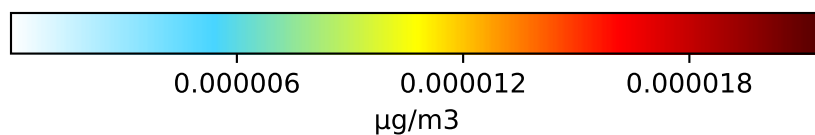
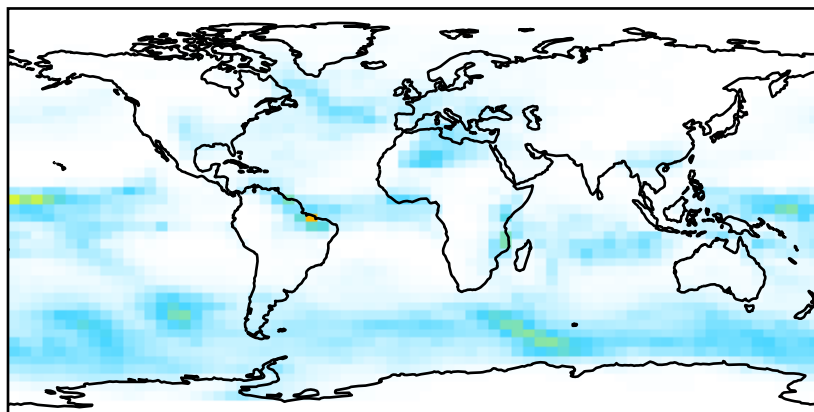


Ratio  
Dev/Ref, Fixed Range

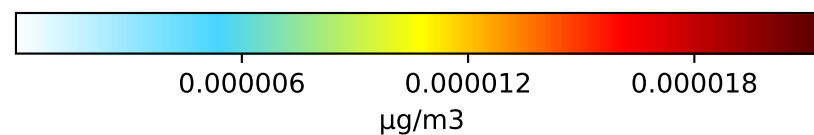
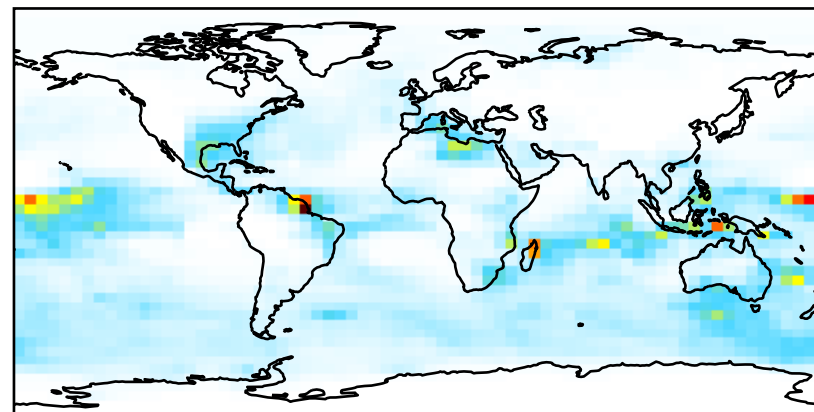


# SpeciesConc\_BrSALC (Apr2019)

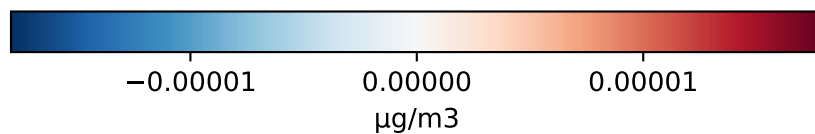
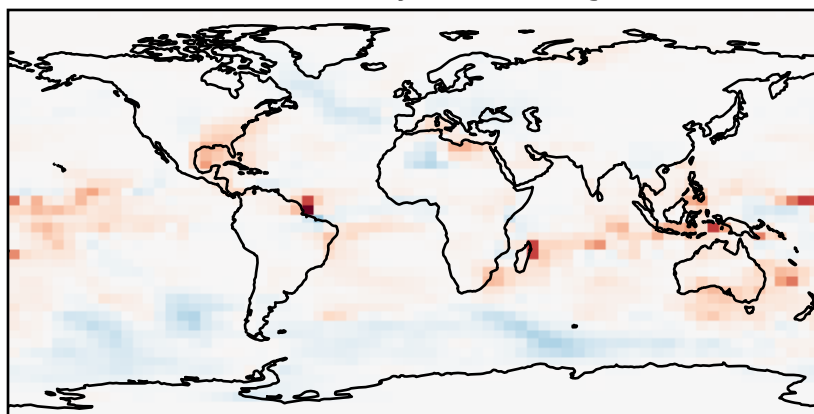
GCC\_12.9.0 (Ref)  
4.0x5.0



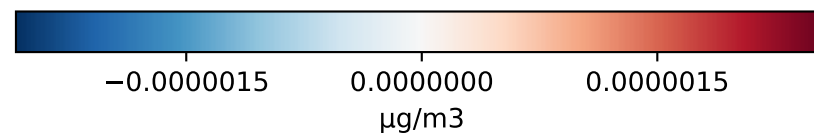
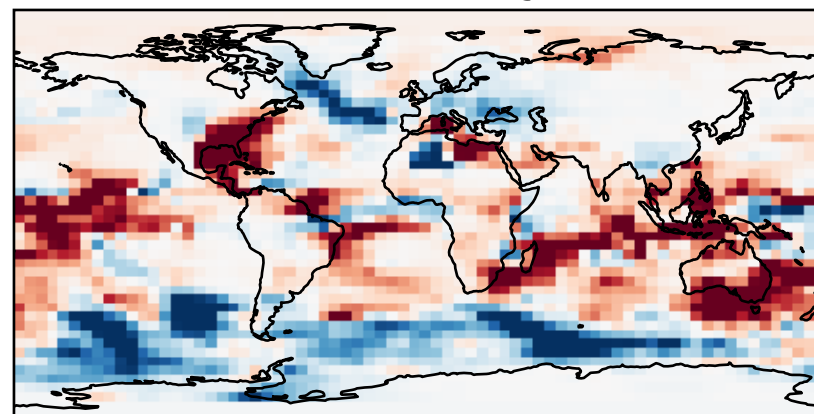
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



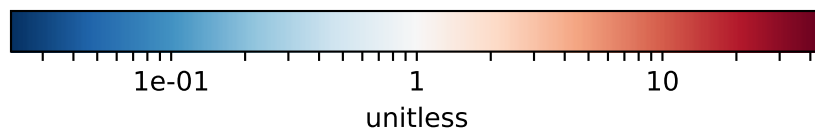
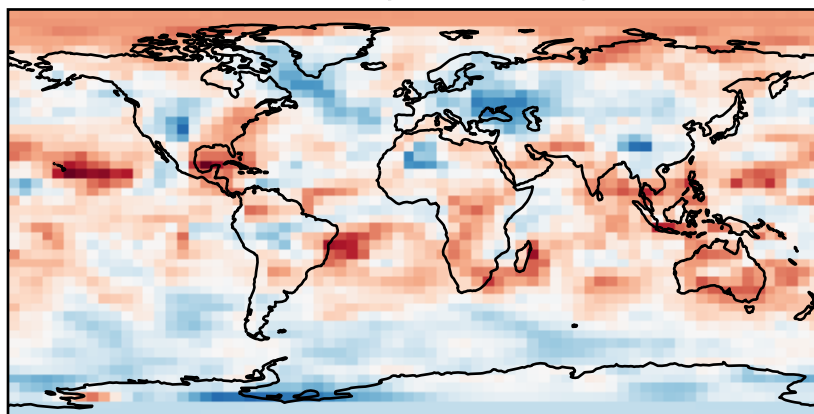
Difference  
Dev - Ref, Dynamic Range



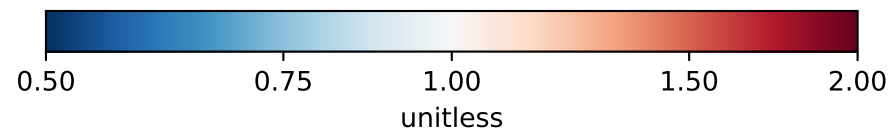
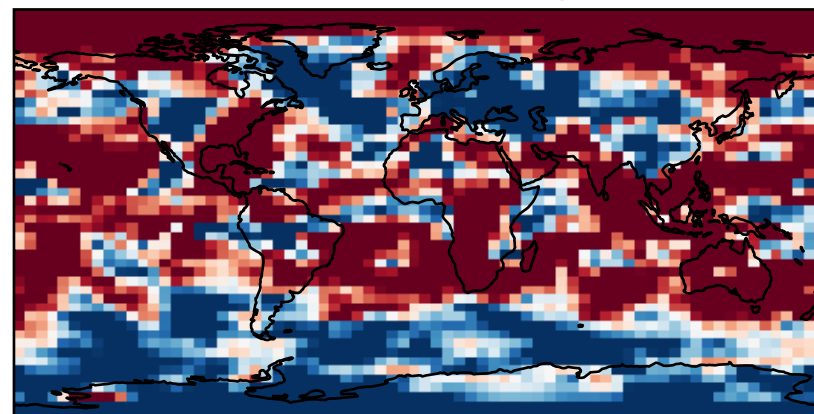
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

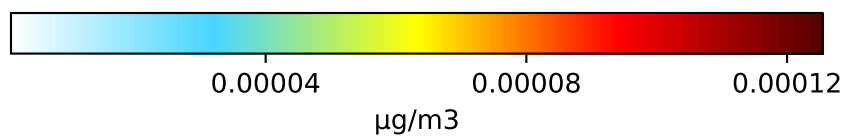
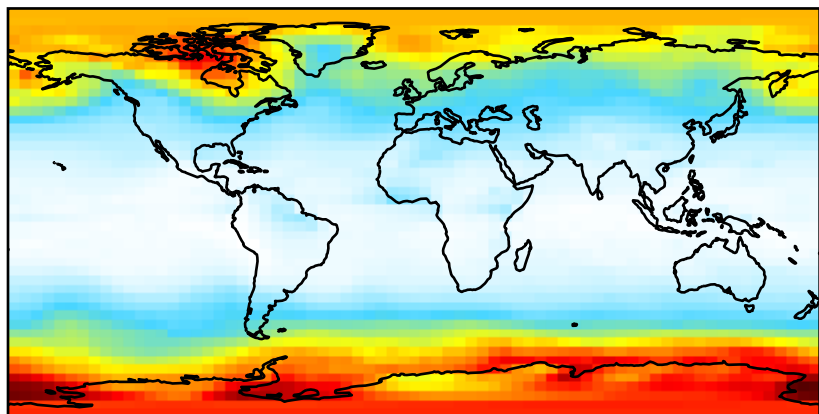


Ratio  
Dev/Ref, Fixed Range

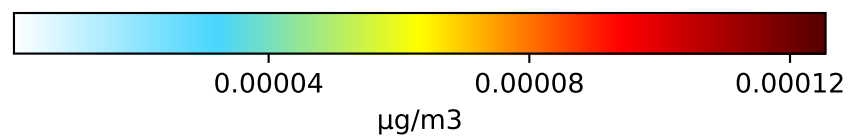
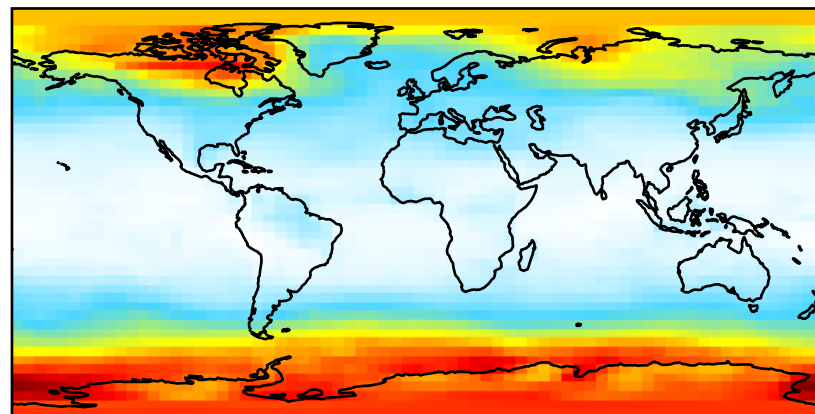


# SpeciesConc\_ISALA (Apr2019)

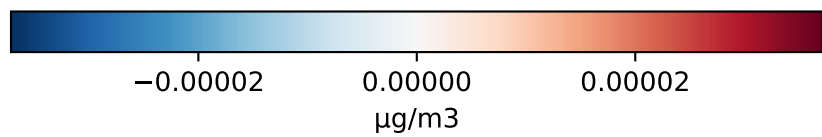
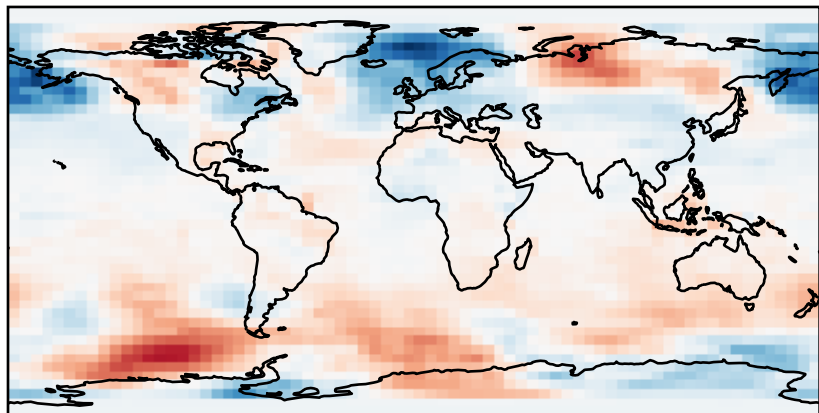
GCC\_12.9.0 (Ref)  
4.0x5.0



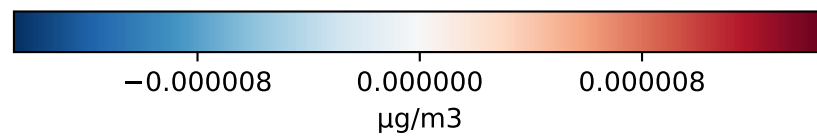
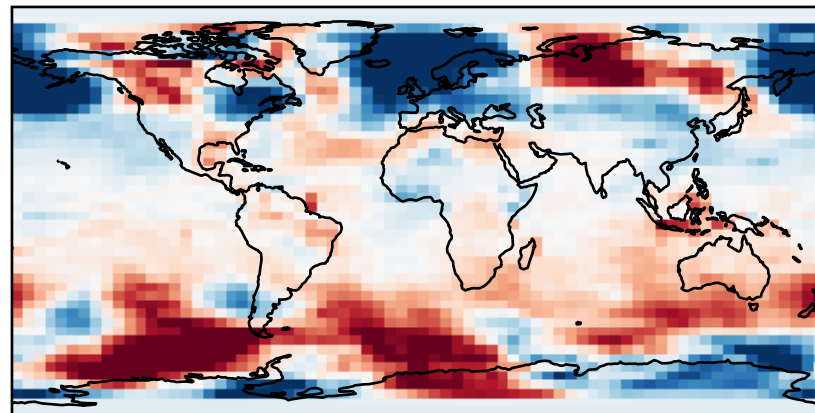
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



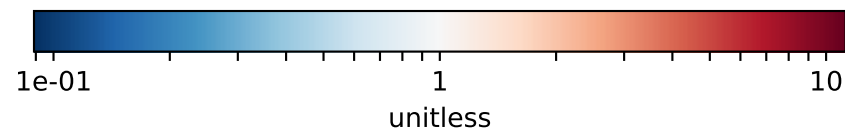
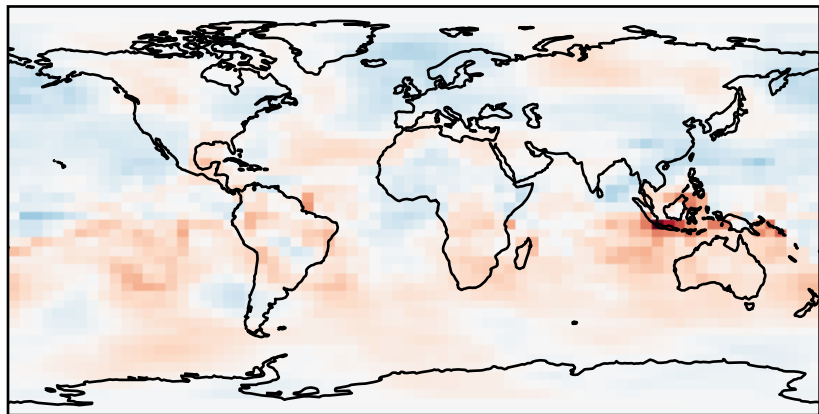
Difference  
Dev - Ref, Dynamic Range



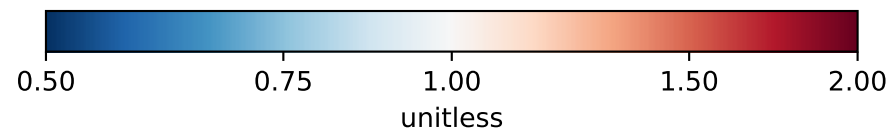
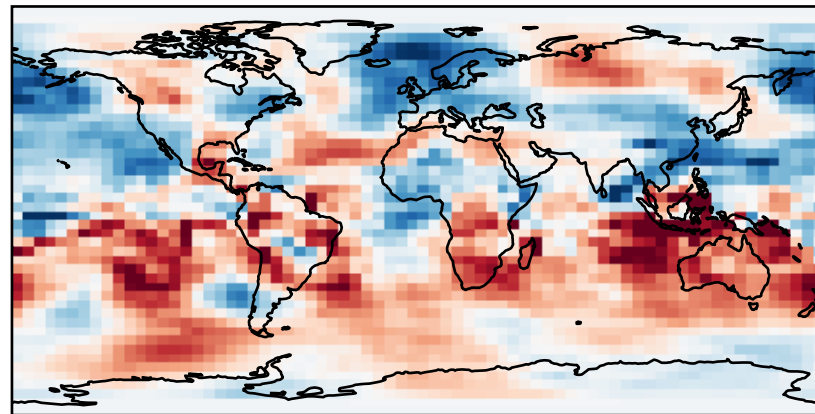
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



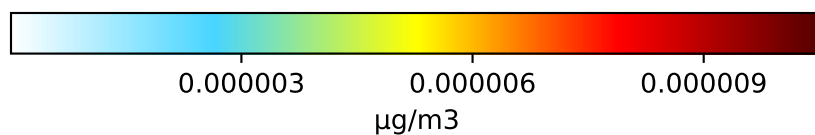
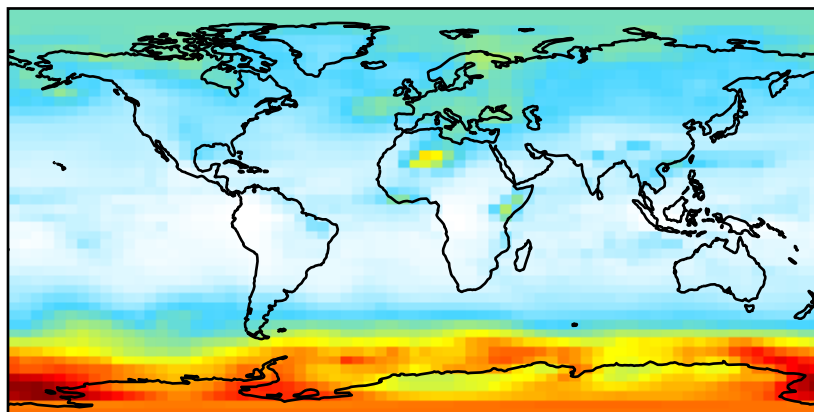
Ratio  
Dev/Ref, Fixed Range



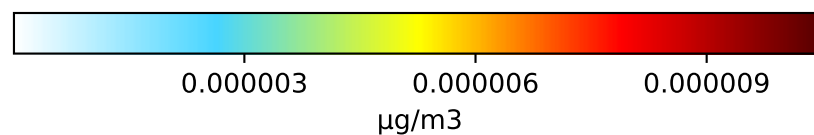
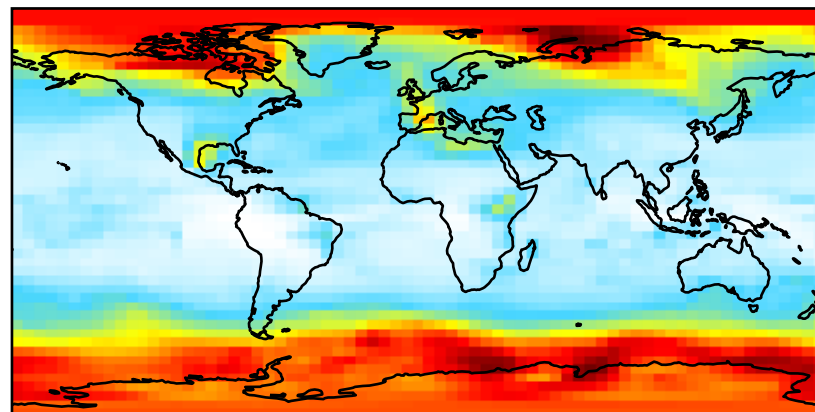


# SpeciesConc\_ISALC (Apr2019)

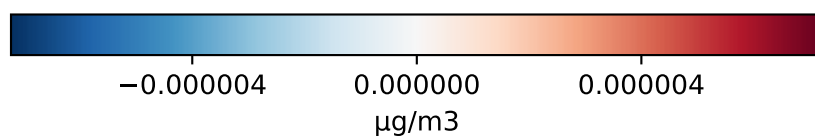
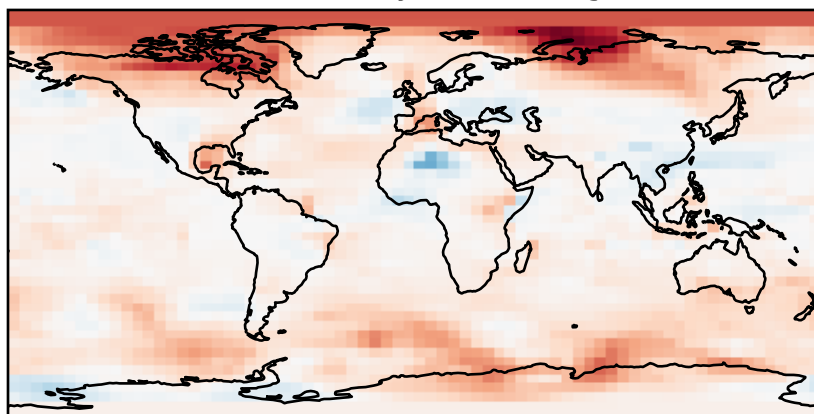
GCC\_12.9.0 (Ref)  
4.0x5.0



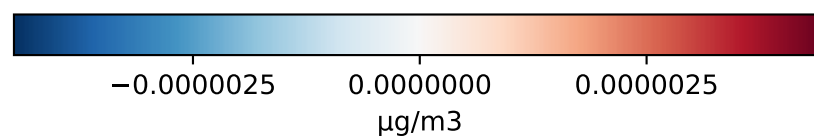
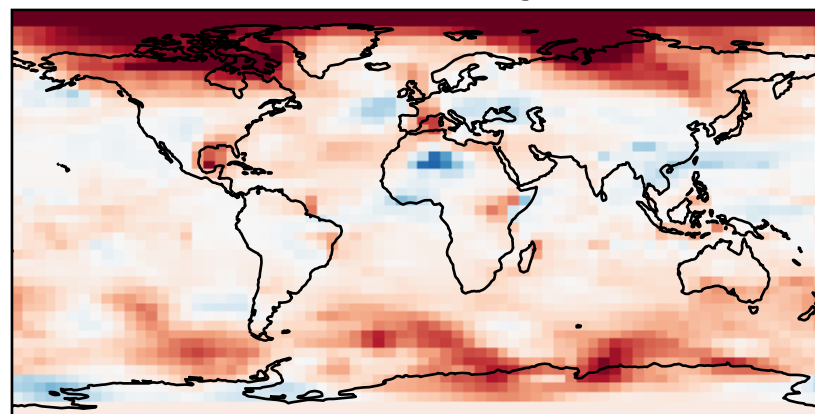
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



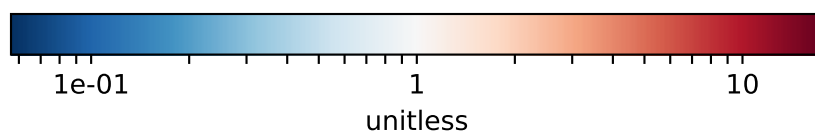
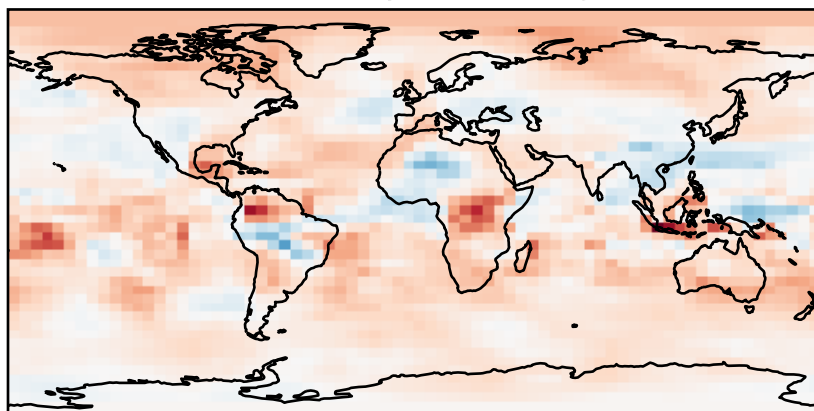
Difference  
Dev - Ref, Dynamic Range



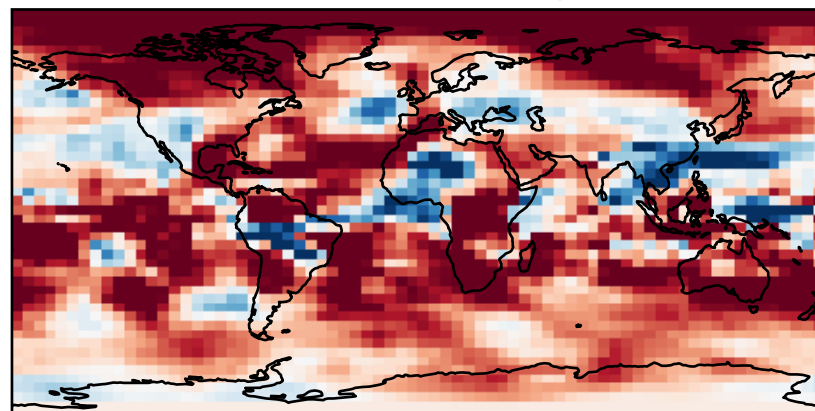
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

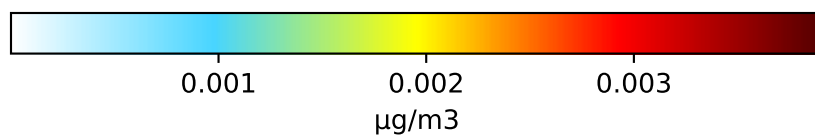
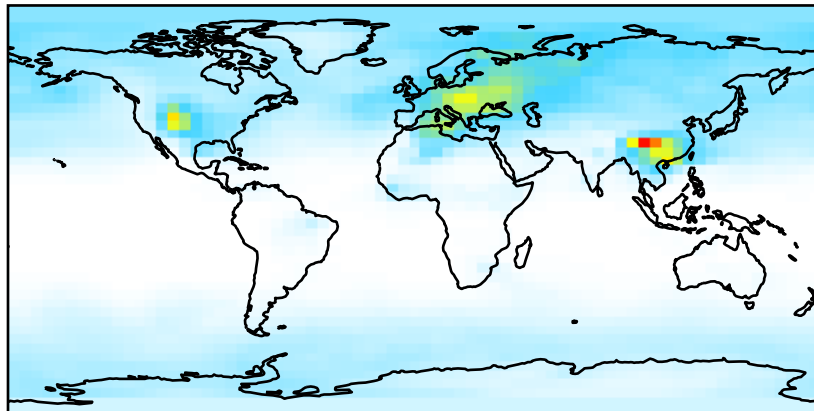


Ratio  
Dev/Ref, Fixed Range

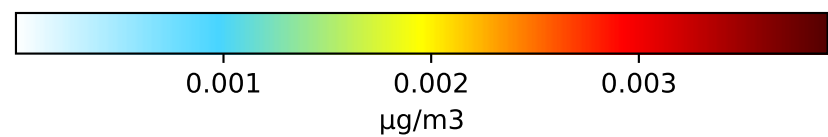
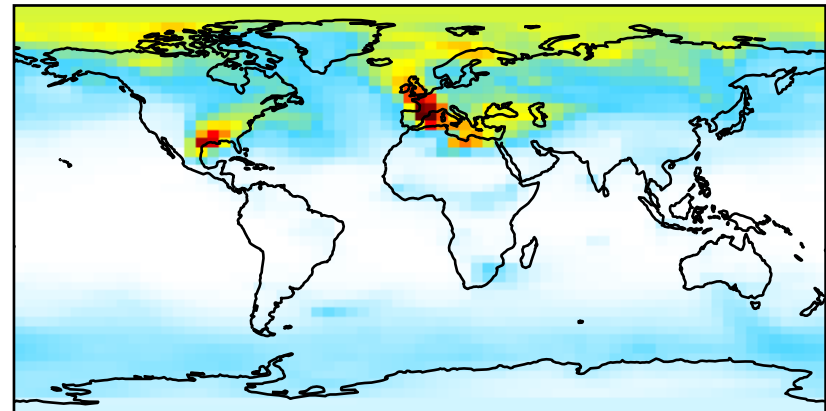


# SpeciesConc\_NITs (Apr2019)

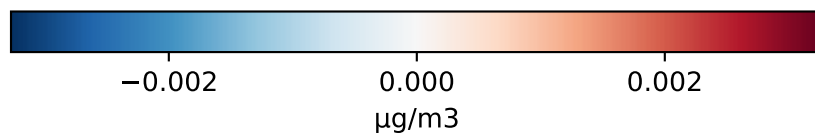
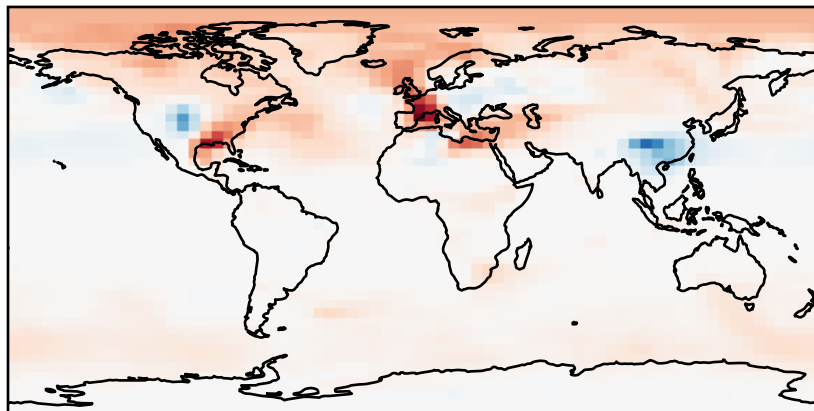
GCC\_12.9.0 (Ref)  
4.0x5.0



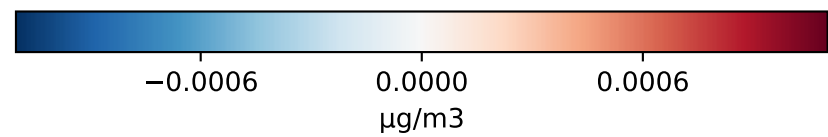
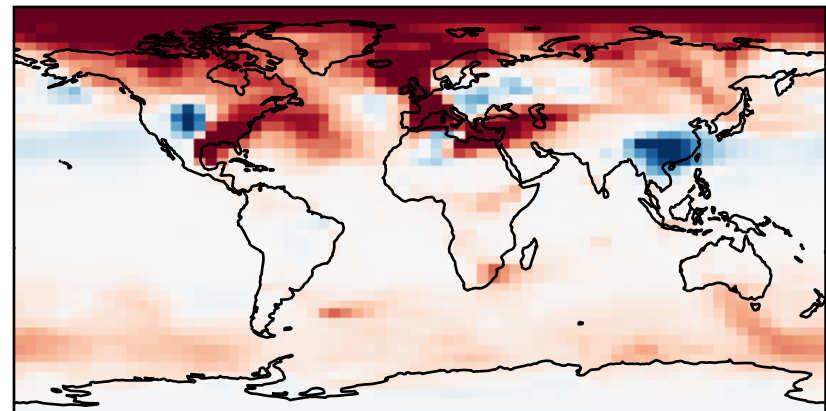
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



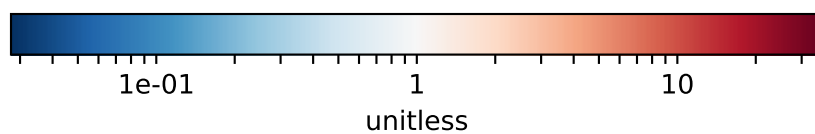
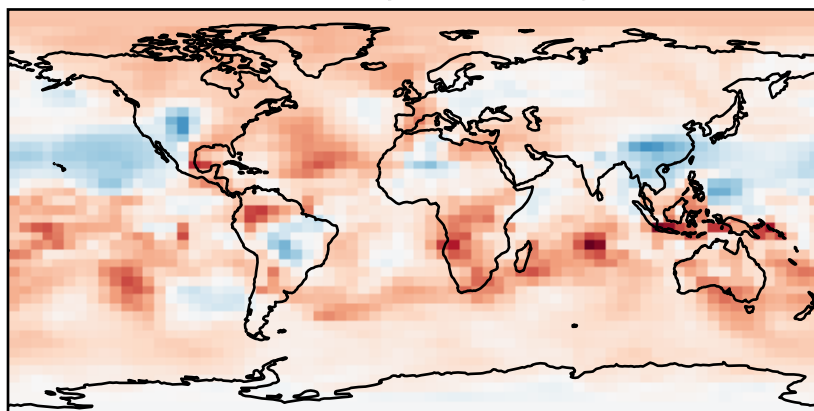
Difference  
Dev - Ref, Dynamic Range



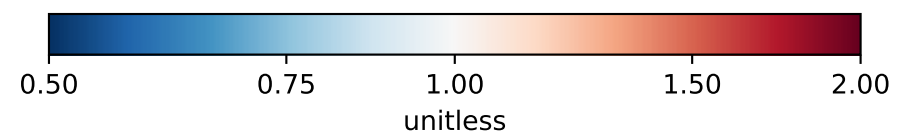
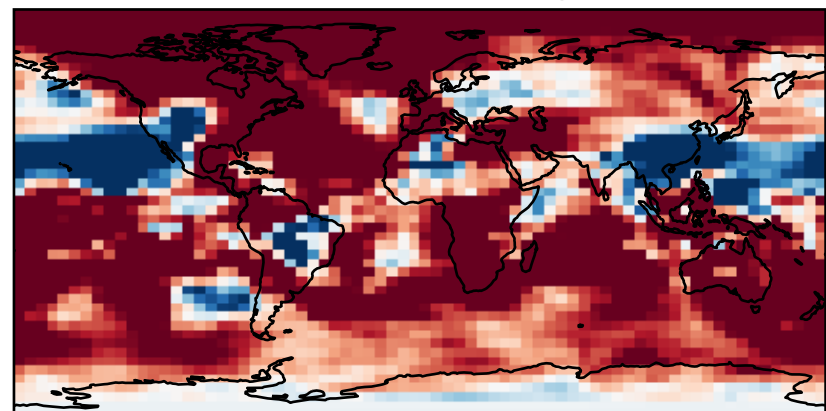
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

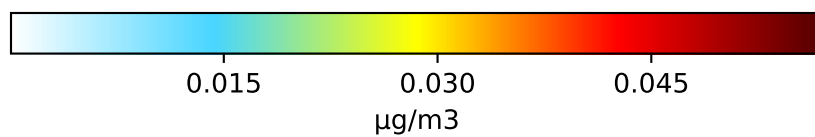
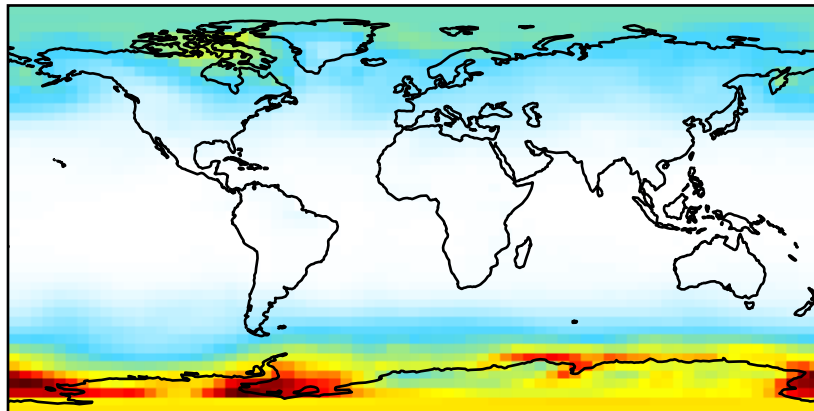


Ratio  
Dev/Ref, Fixed Range

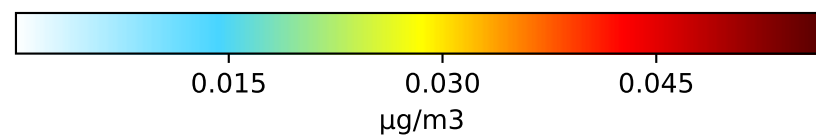
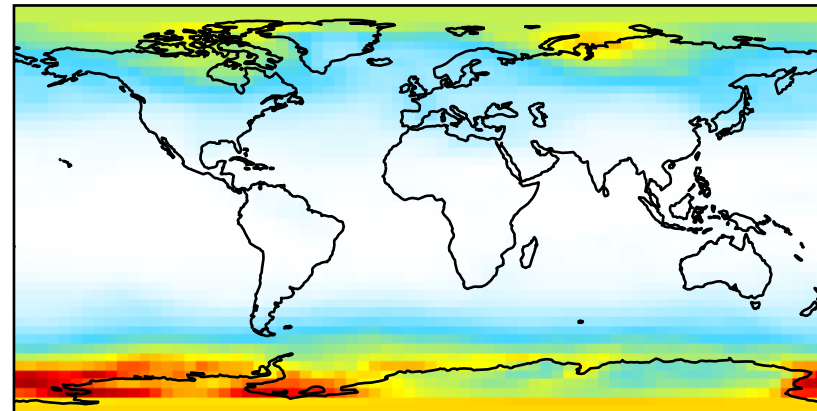


# SpeciesConc\_SALA (Apr2019)

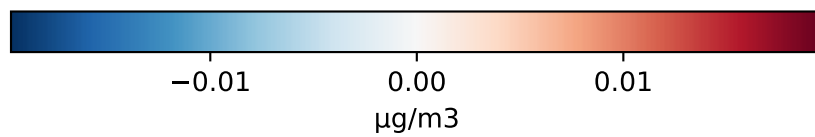
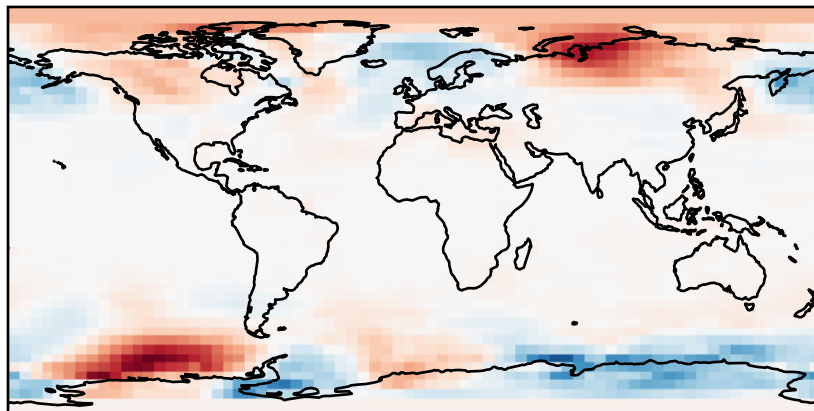
GCC\_12.9.0 (Ref)  
4.0x5.0



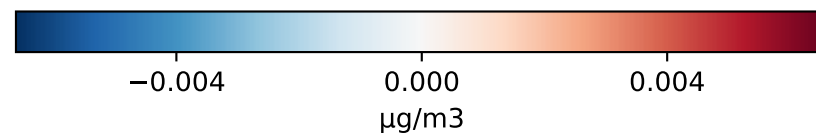
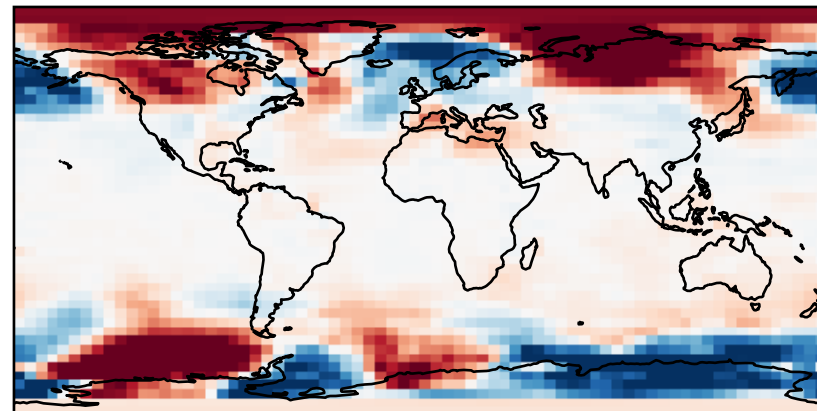
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



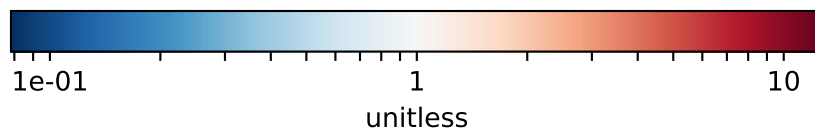
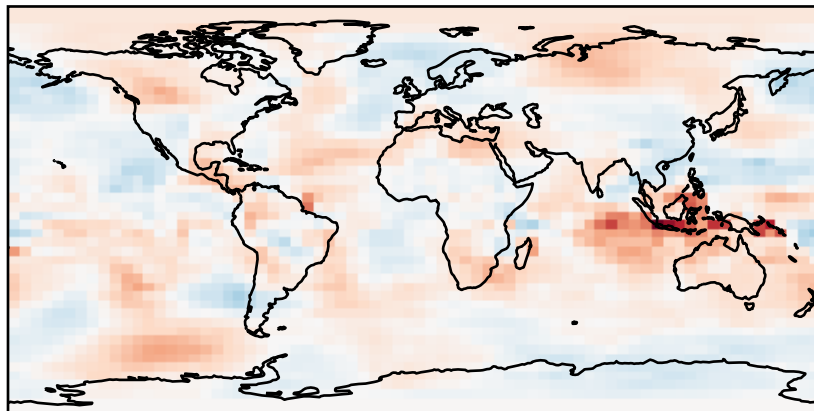
Difference  
Dev - Ref, Dynamic Range



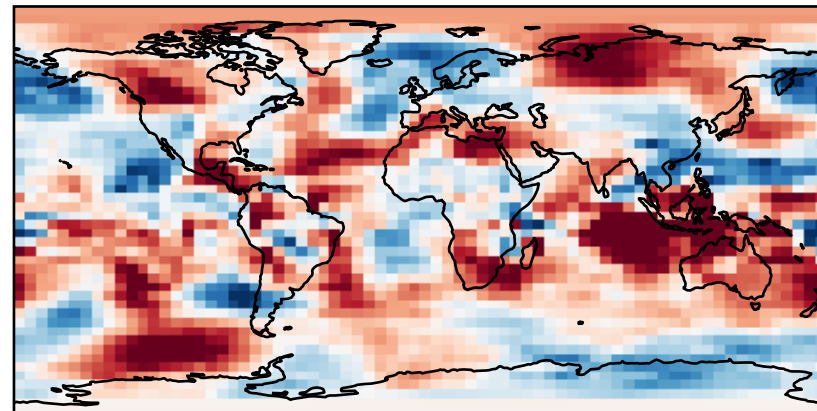
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

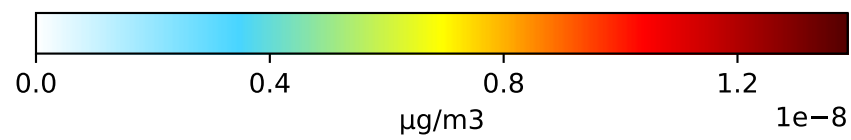
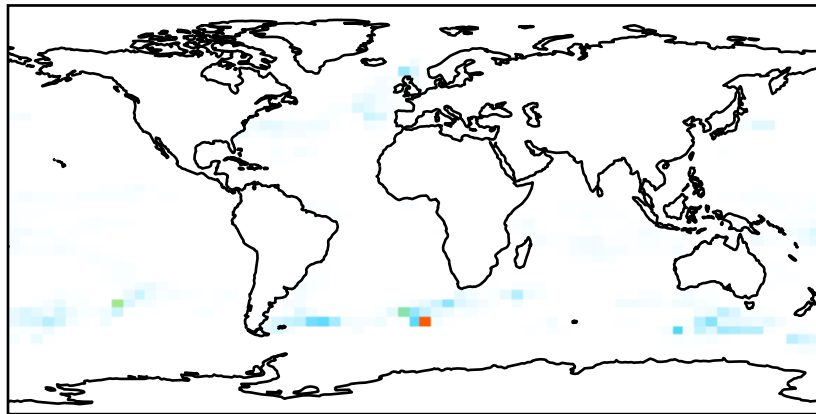


Ratio  
Dev/Ref, Fixed Range

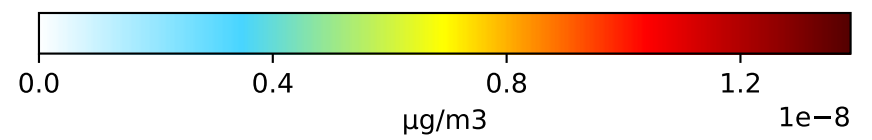
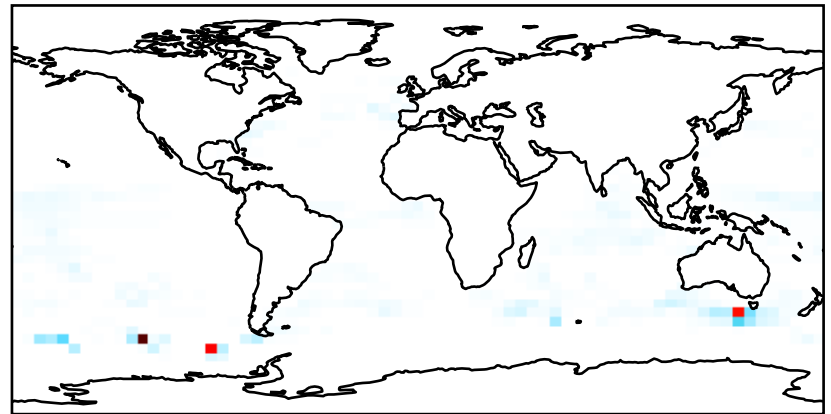


# SpeciesConc\_SALAAL (Apr2019)

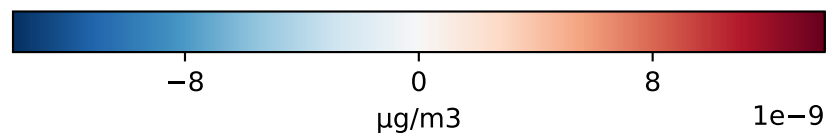
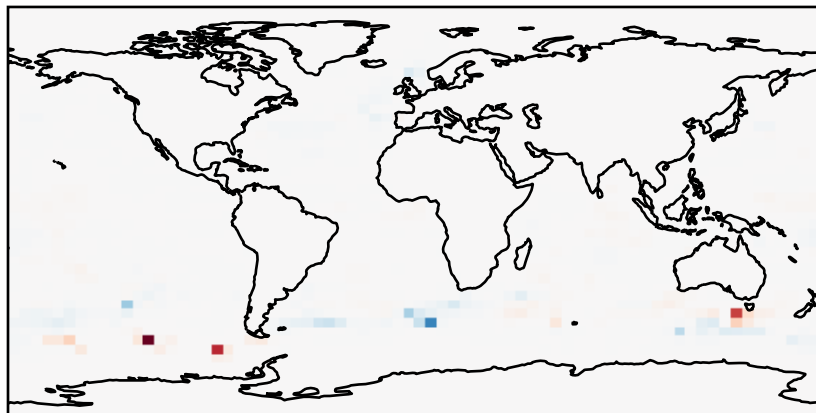
GCC\_12.9.0 (Ref)  
4.0x5.0



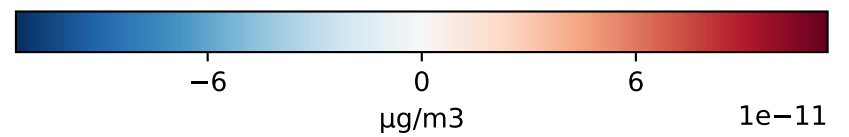
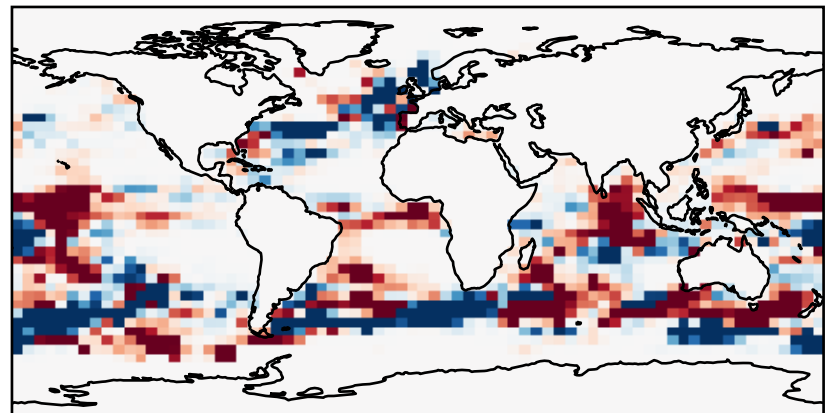
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



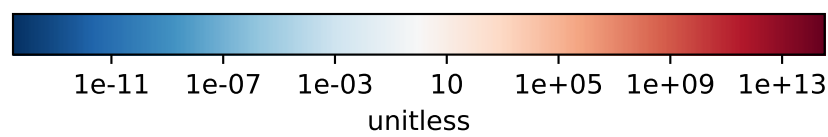
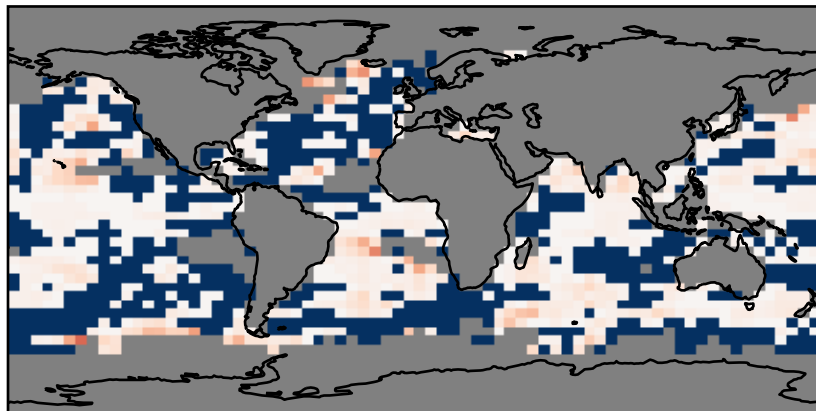
Difference  
Dev - Ref, Dynamic Range



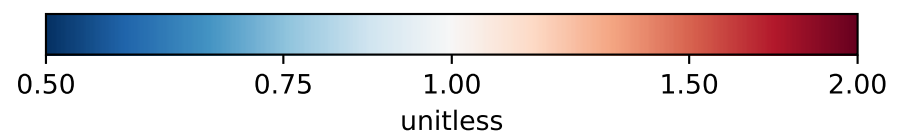
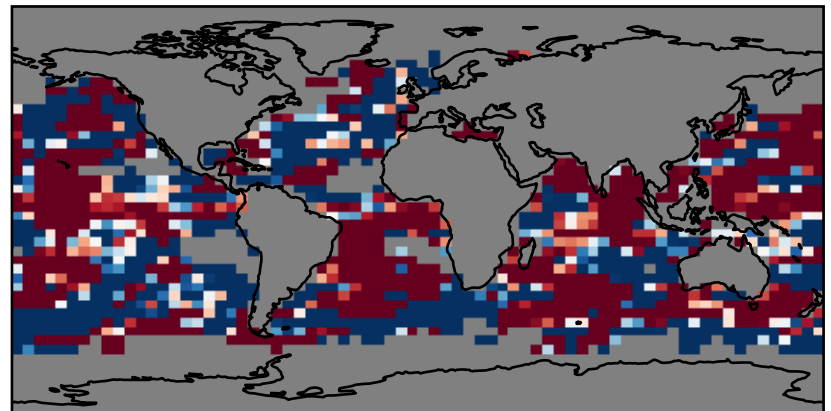
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



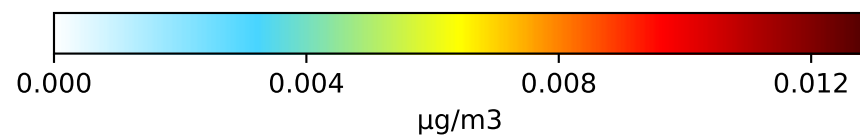
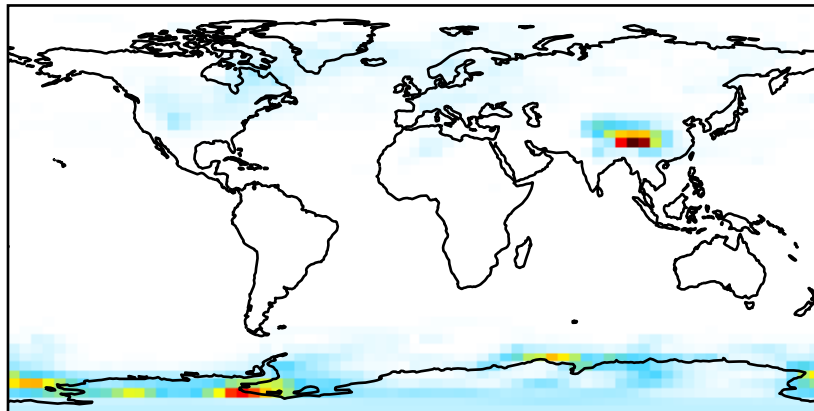
Ratio  
Dev/Ref, Fixed Range



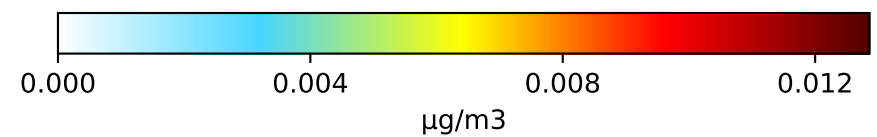
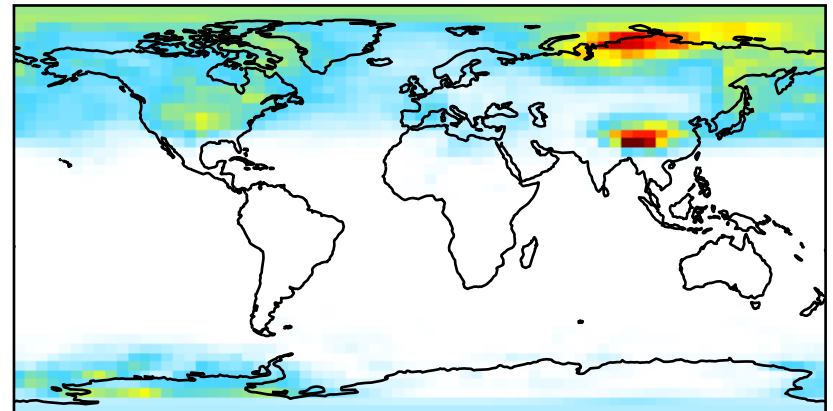


# SpeciesConc\_SALACL (Apr2019)

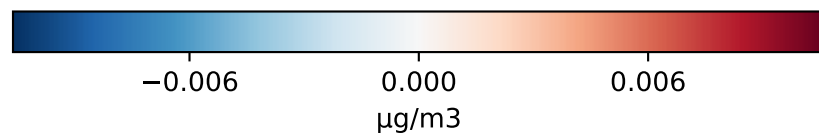
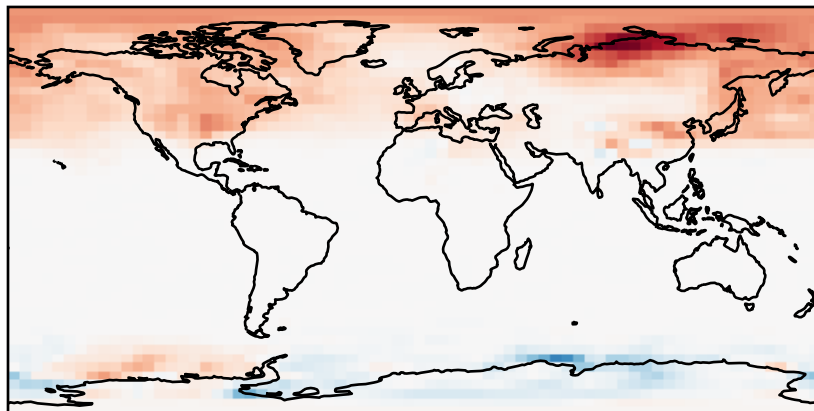
GCC\_12.9.0 (Ref)  
4.0x5.0



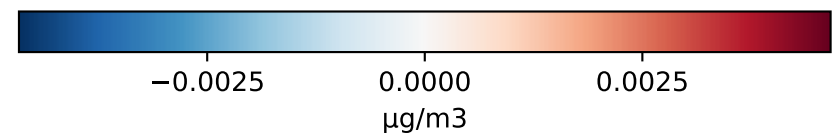
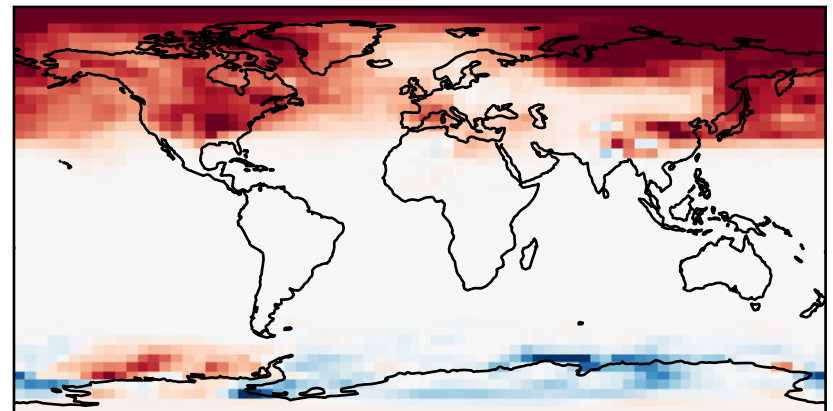
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



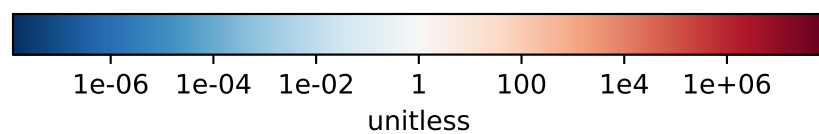
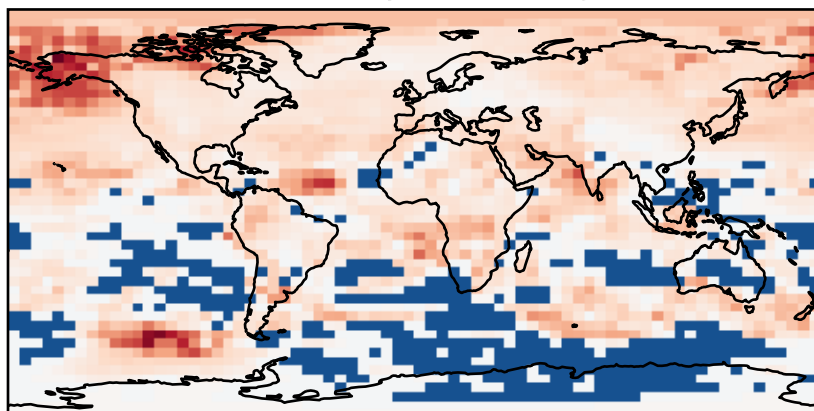
Difference  
Dev - Ref, Dynamic Range



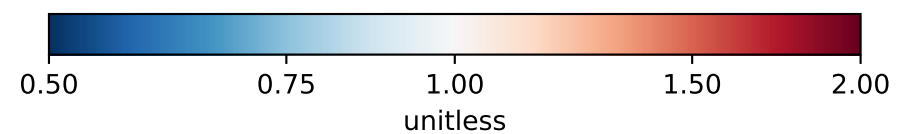
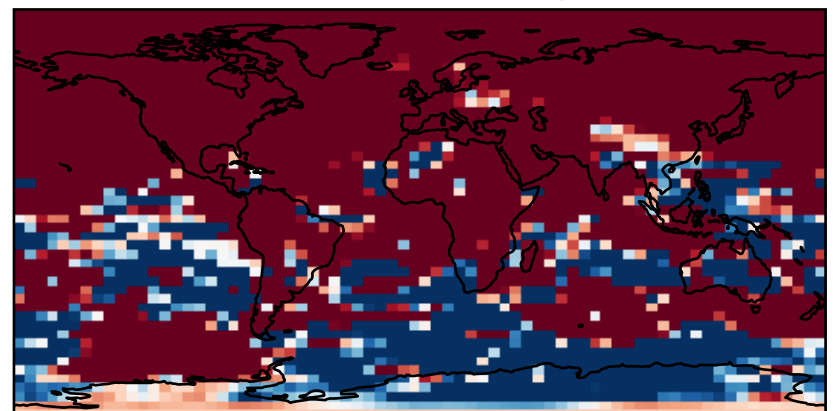
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

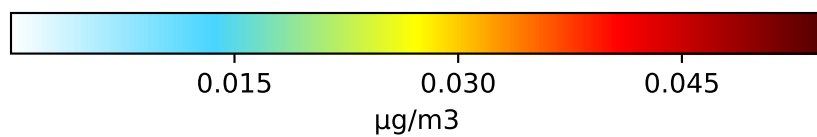
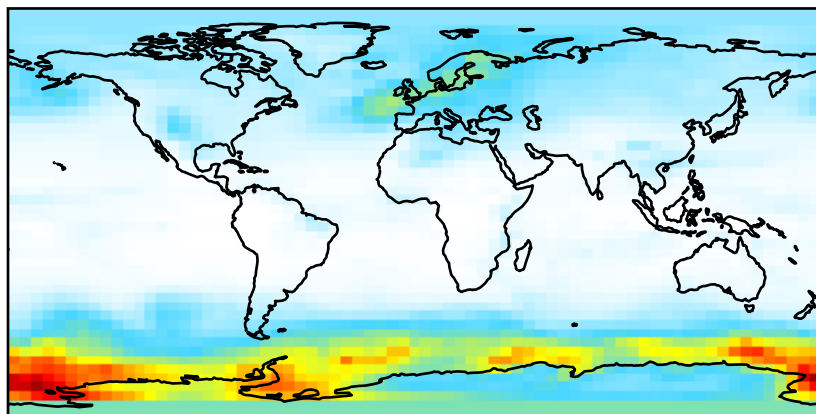


Ratio  
Dev/Ref, Fixed Range

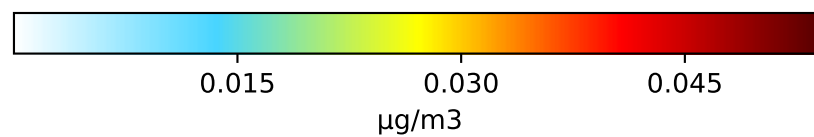
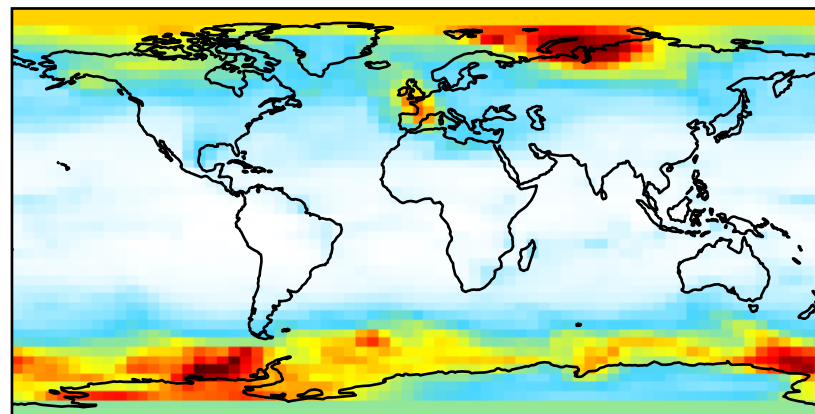


# SpeciesConc\_SALC (Apr2019)

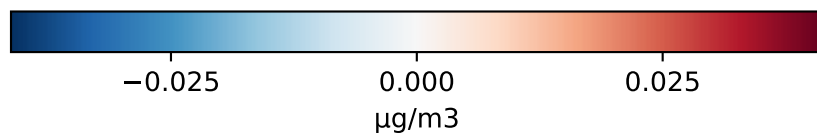
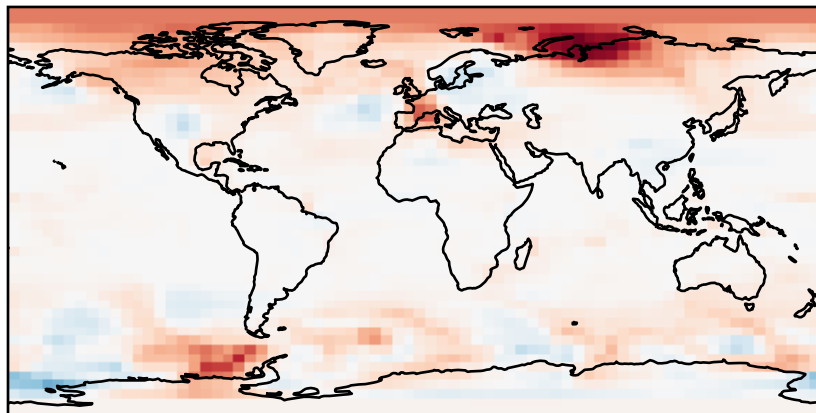
GCC\_12.9.0 (Ref)  
4.0x5.0



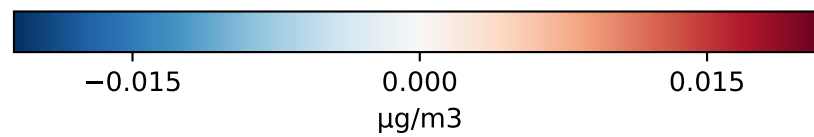
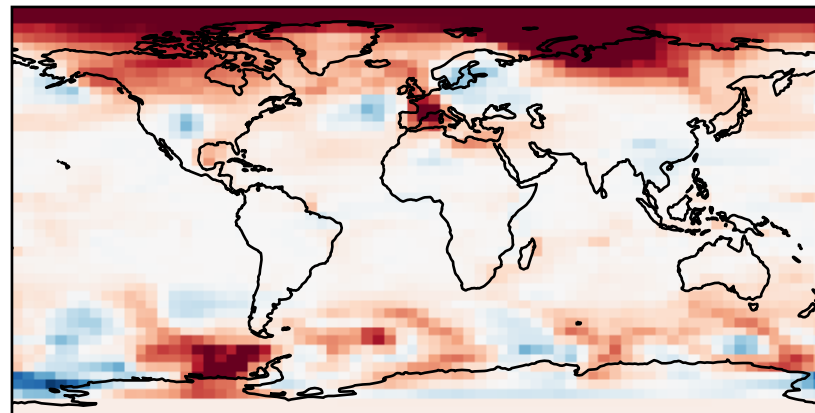
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



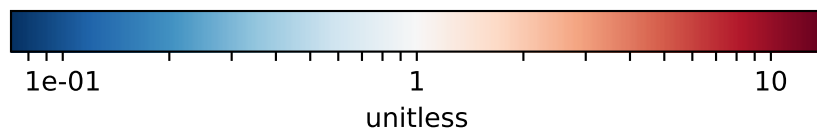
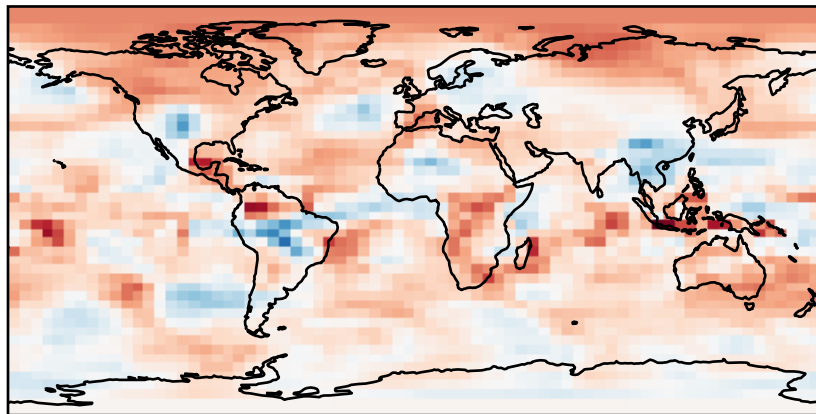
Difference  
Dev - Ref, Dynamic Range



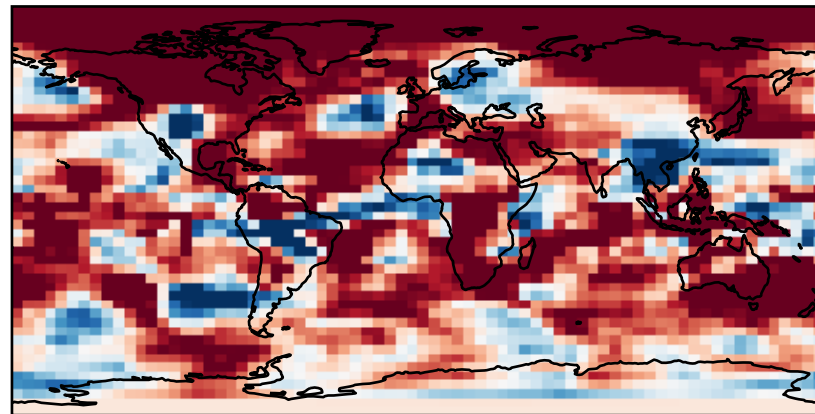
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

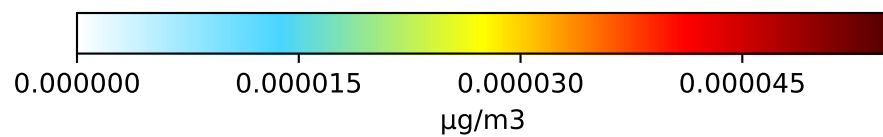
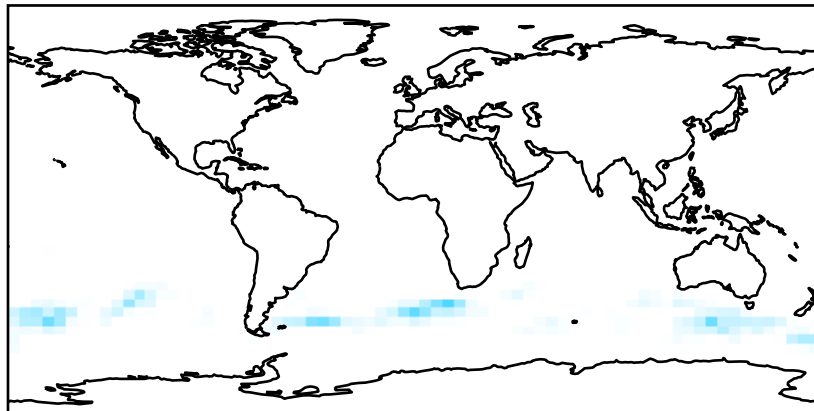


Ratio  
Dev/Ref, Fixed Range

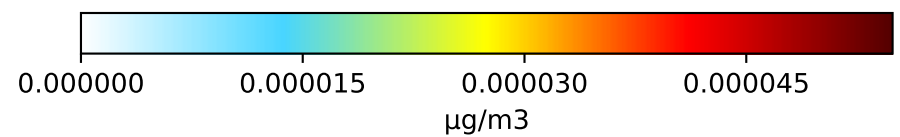
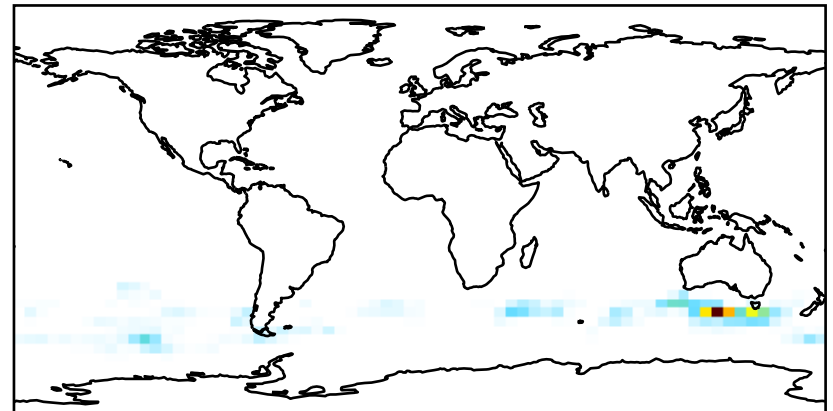


# SpeciesConc\_SALCAL (Apr2019)

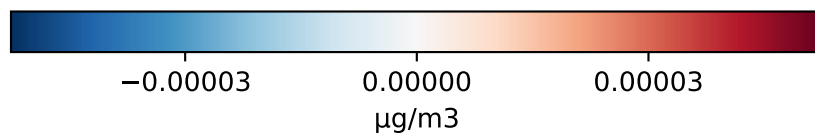
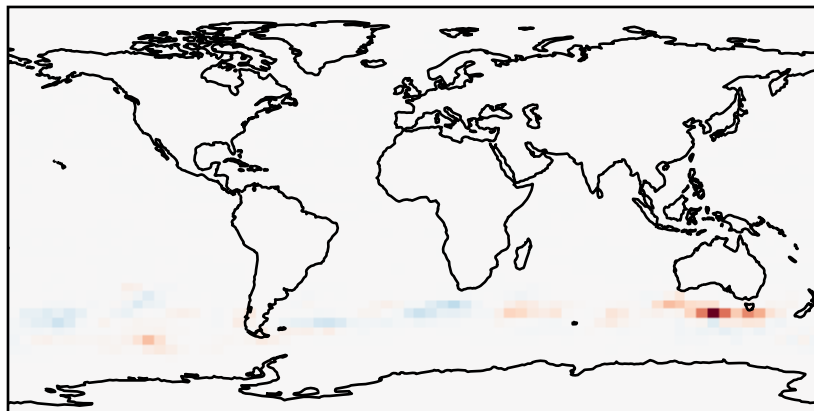
GCC\_12.9.0 (Ref)  
4.0x5.0



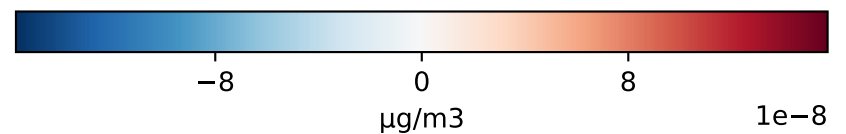
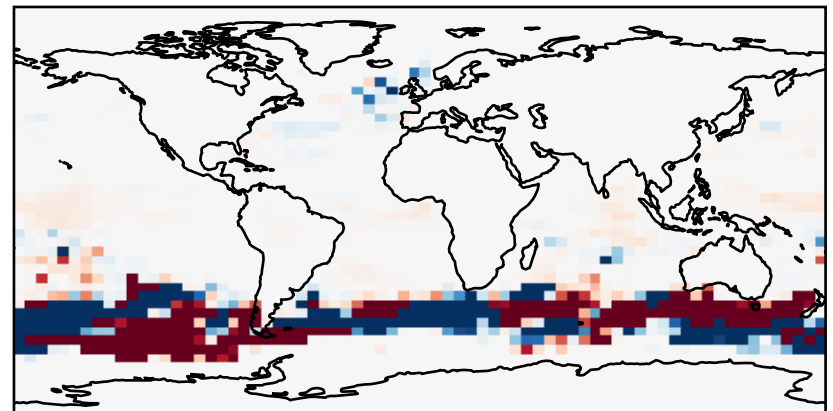
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



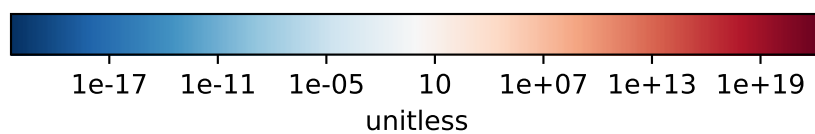
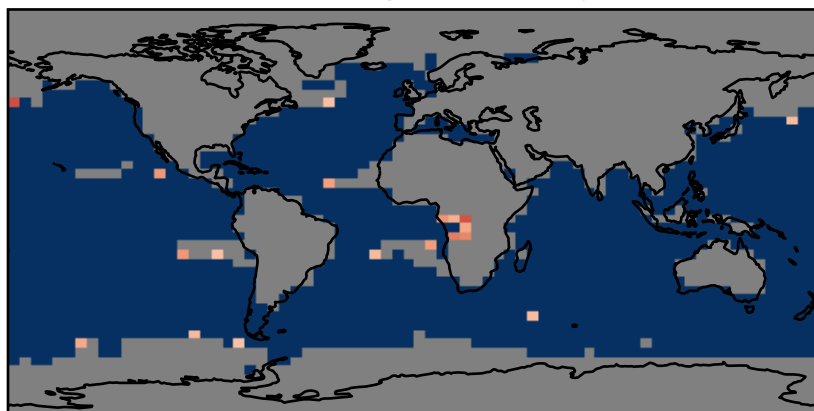
Difference  
Dev - Ref, Dynamic Range



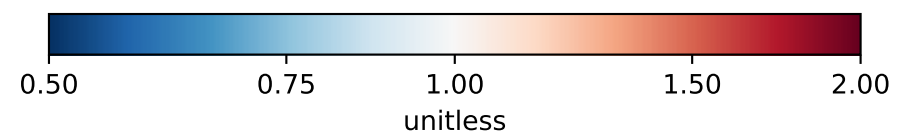
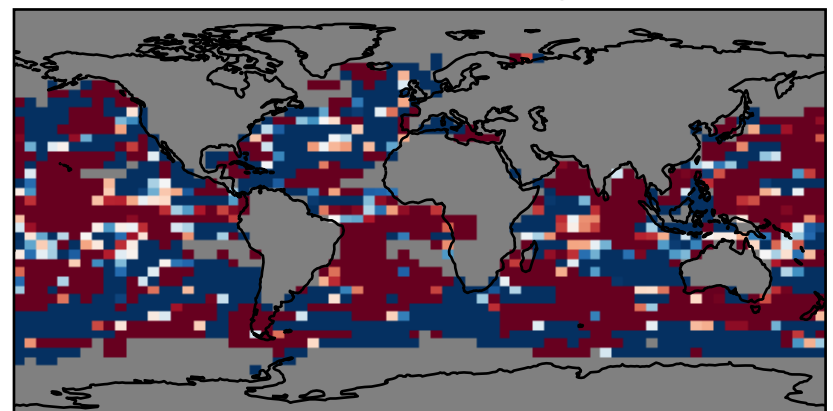
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

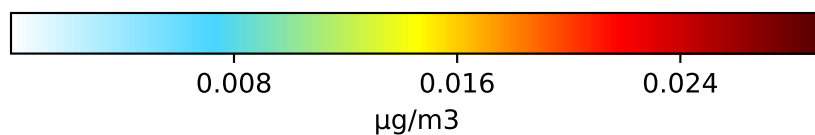
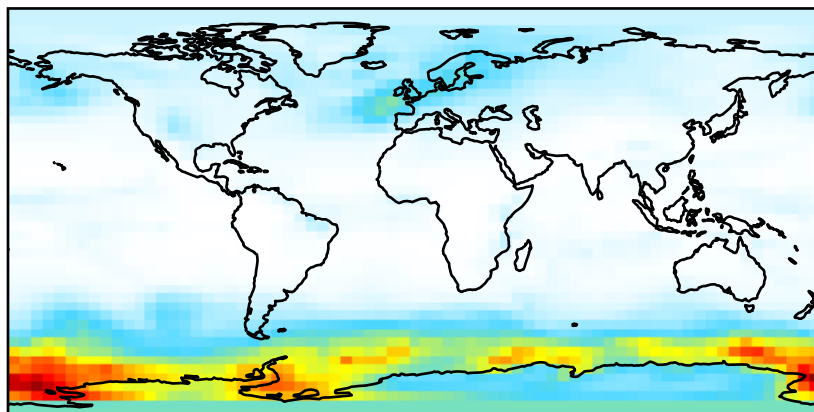


Ratio  
Dev/Ref, Fixed Range

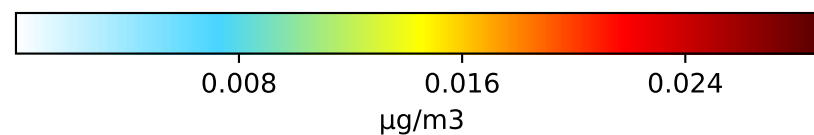
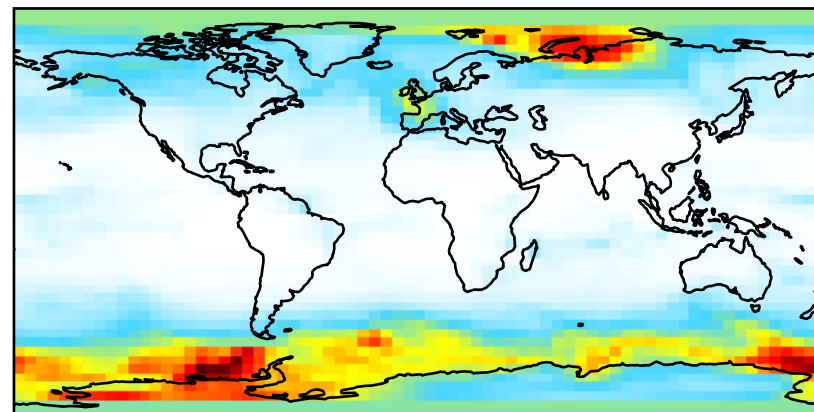


# SpeciesConc\_SALCCL (Apr2019)

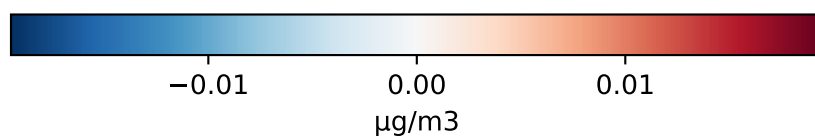
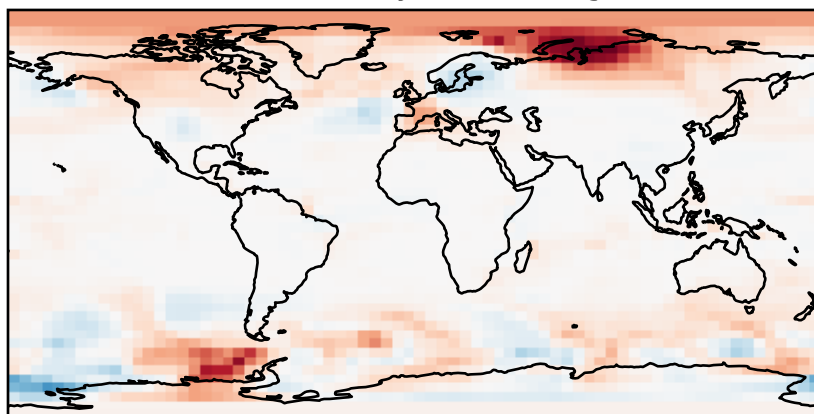
GCC\_12.9.0 (Ref)  
4.0x5.0



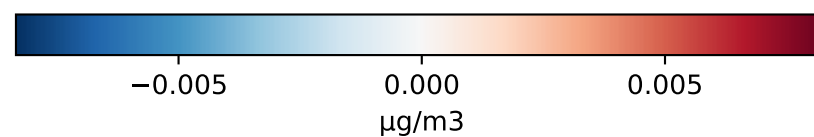
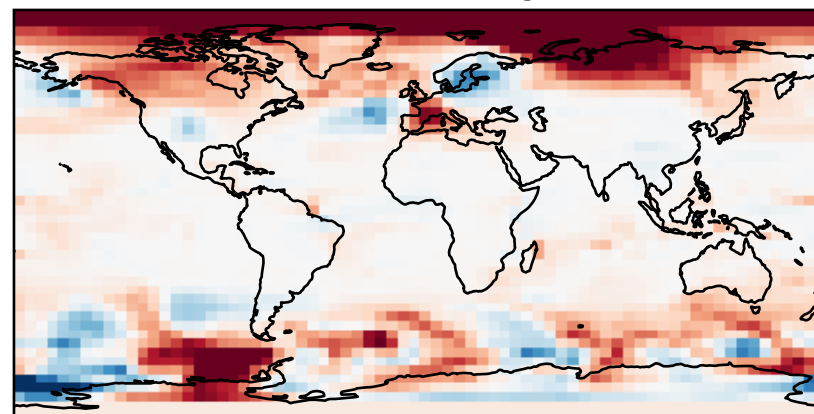
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



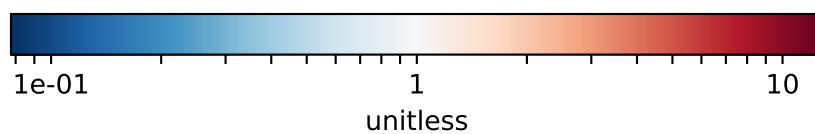
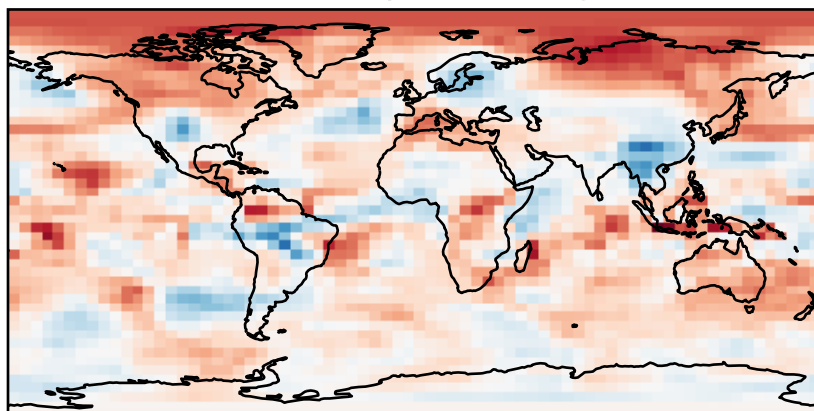
Difference  
Dev - Ref, Dynamic Range



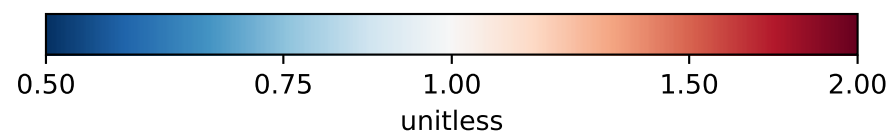
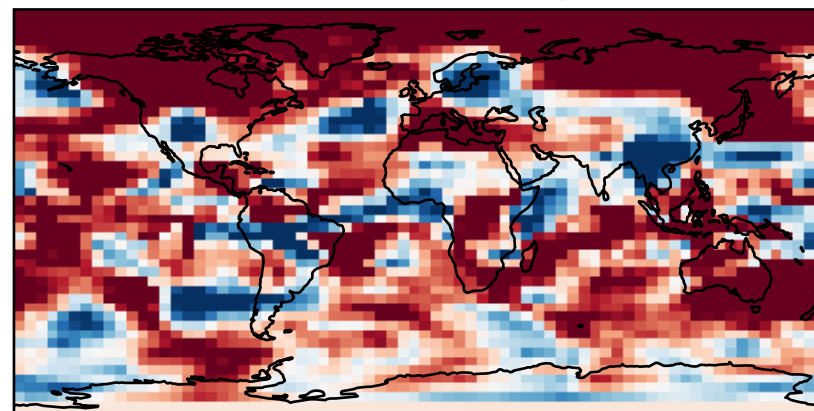
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



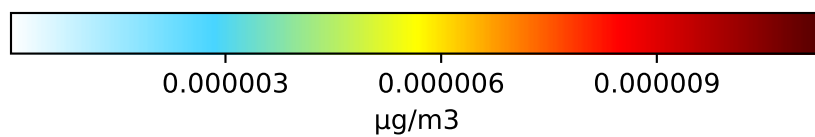
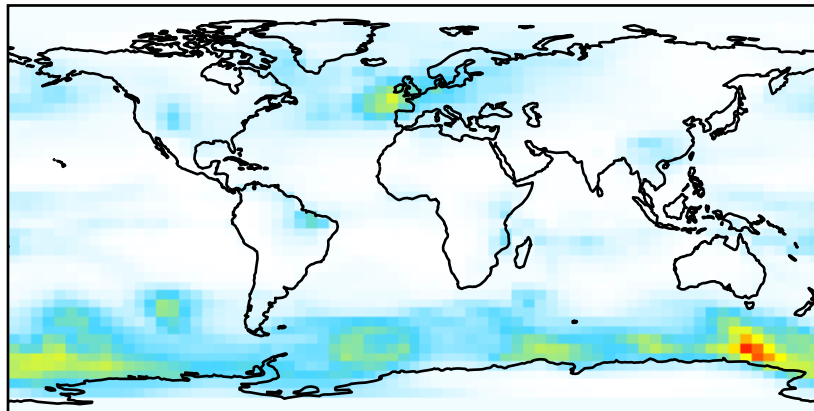
Ratio  
Dev/Ref, Fixed Range



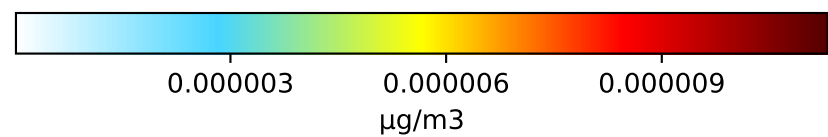
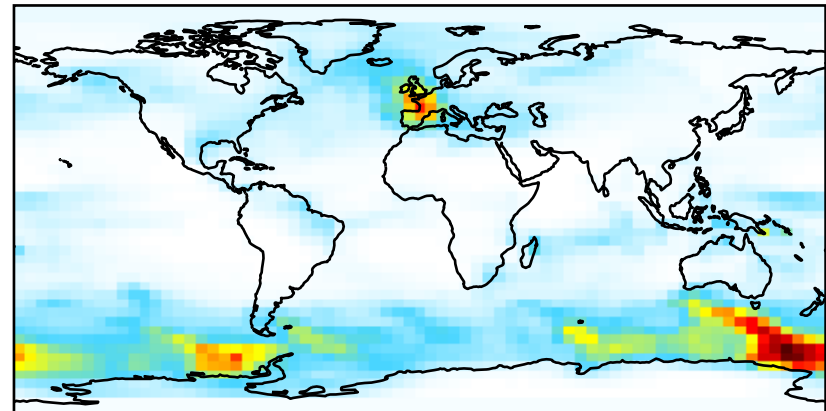


# SpeciesConc\_SO4s (Apr2019)

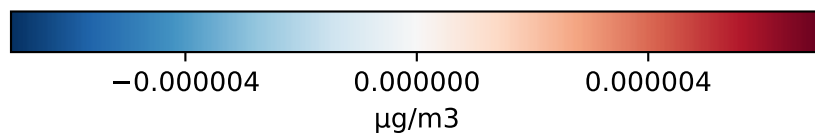
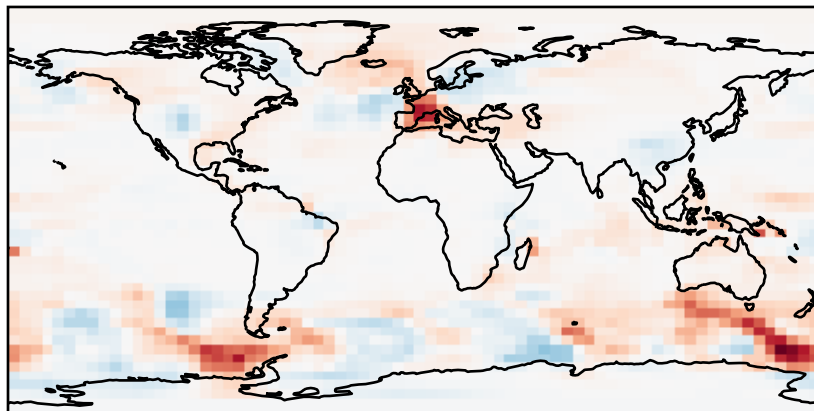
GCC\_12.9.0 (Ref)  
4.0x5.0



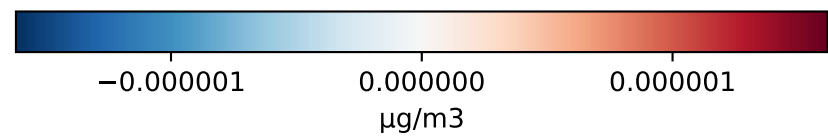
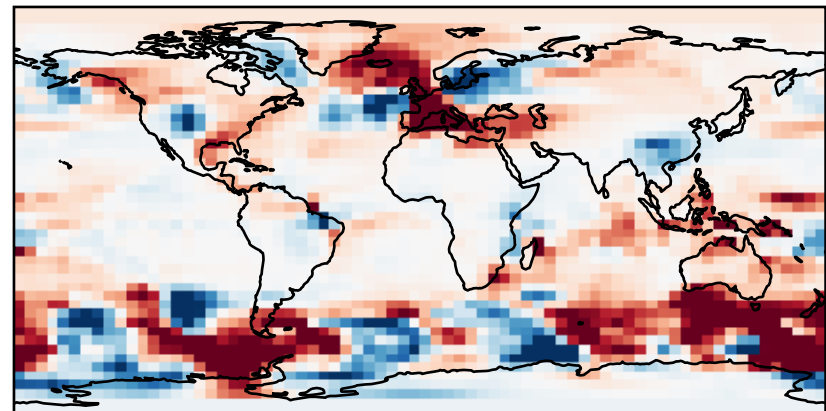
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



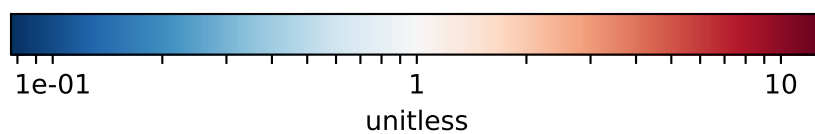
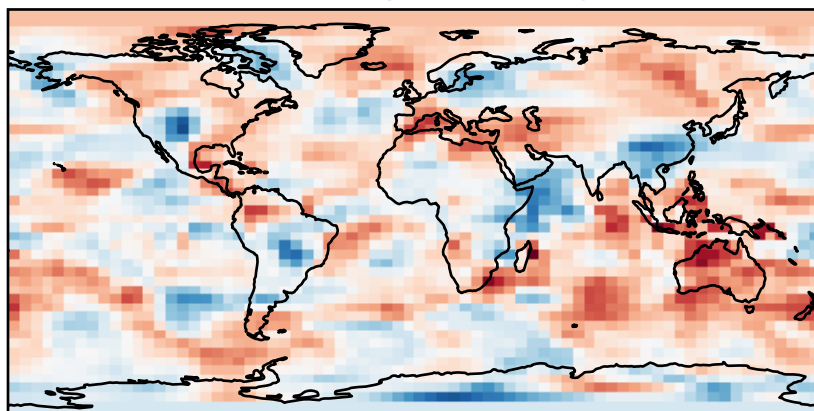
Difference  
Dev - Ref, Dynamic Range



Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

