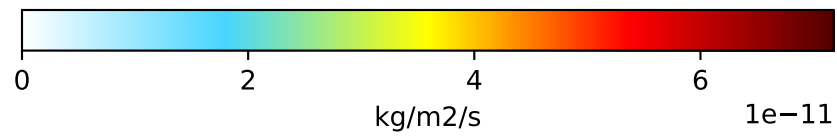
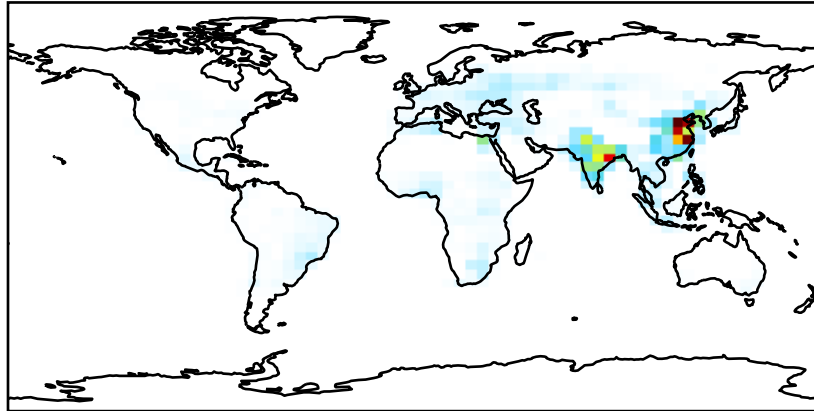
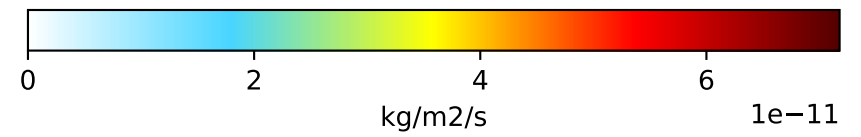
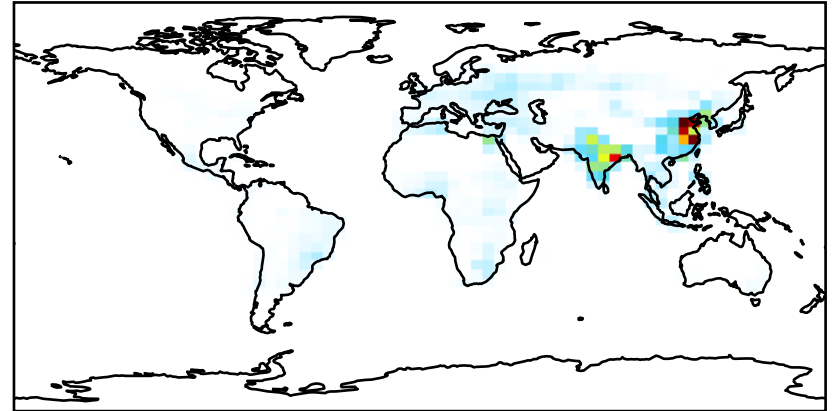


EmisDST1_Anthro (Jul2019)

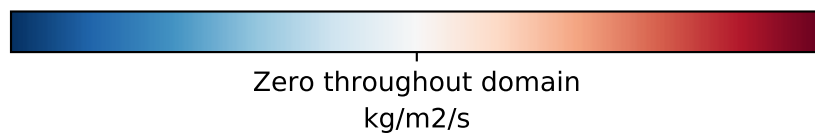
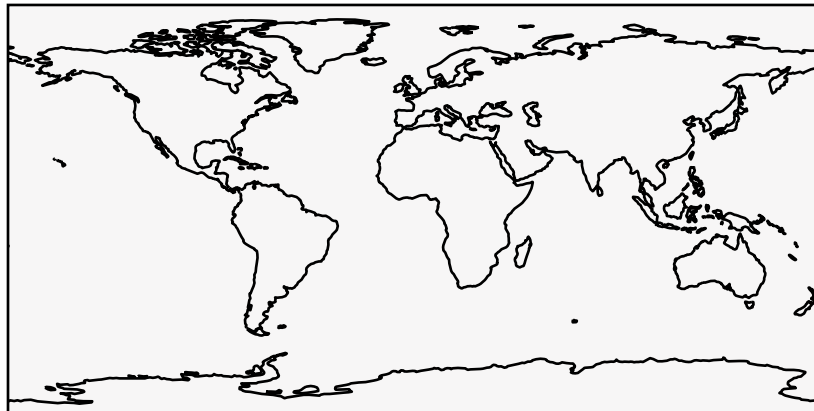
GCC_12.9.0 (Ref)
4.0x5.0



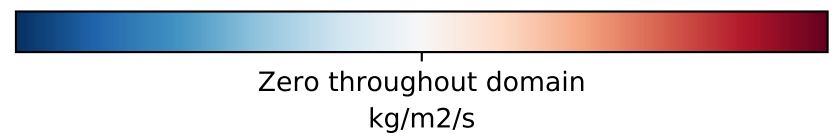
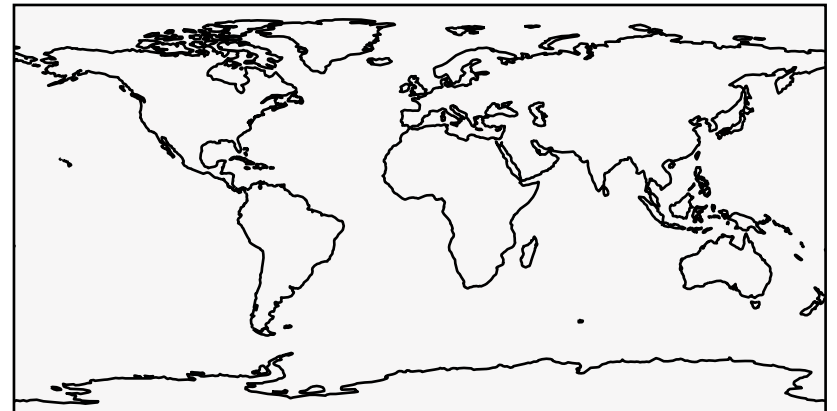
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



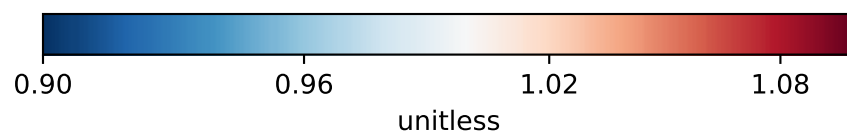
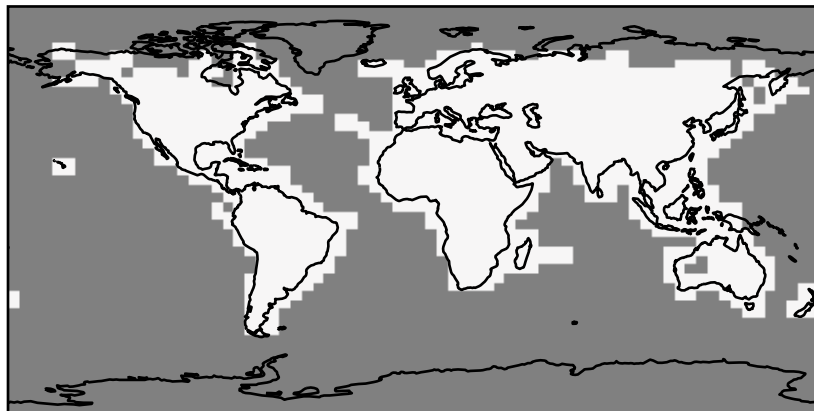
Difference
Dev - Ref, Dynamic Range



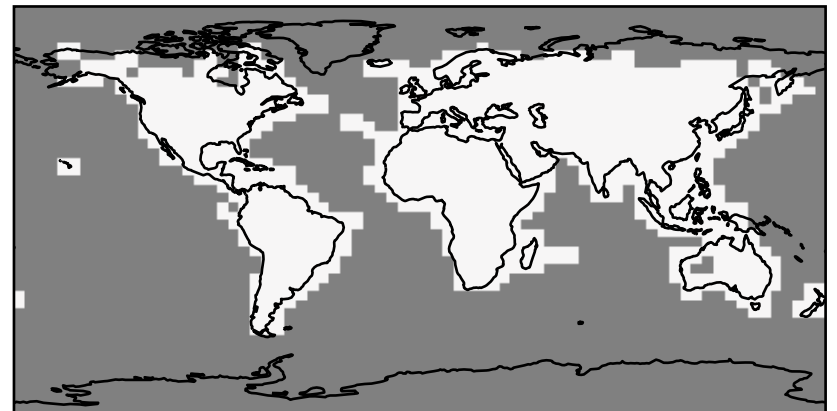
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

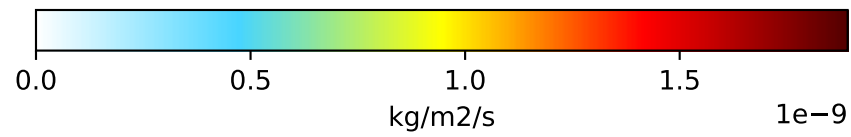
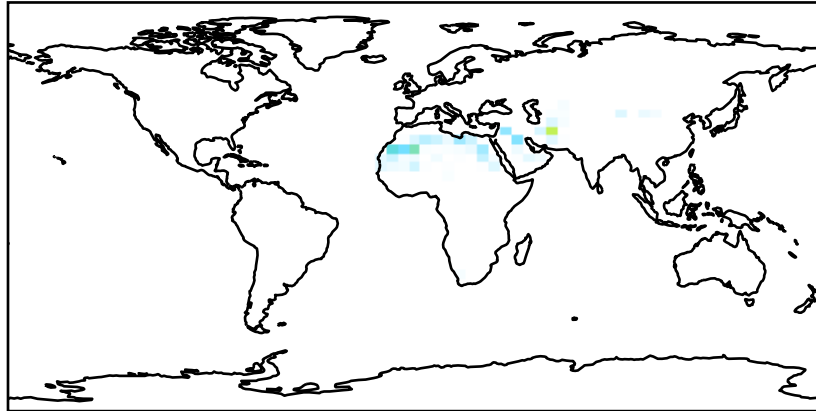


Ratio
Dev/Ref, Fixed Range

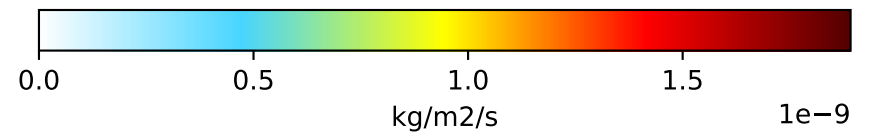
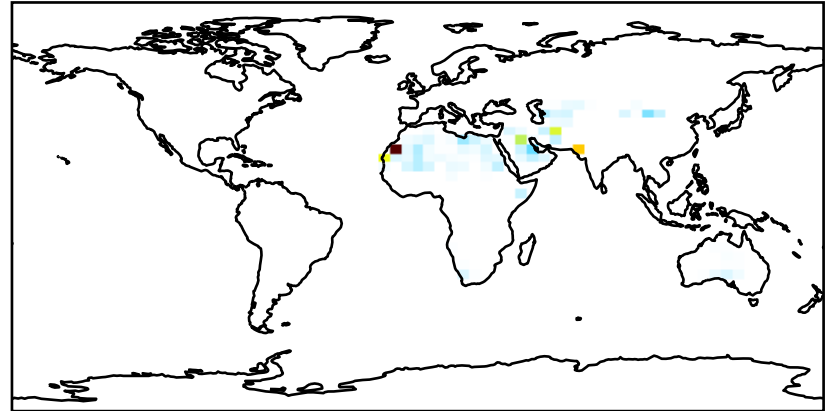


EmisDST1_Natural (Jul2019)

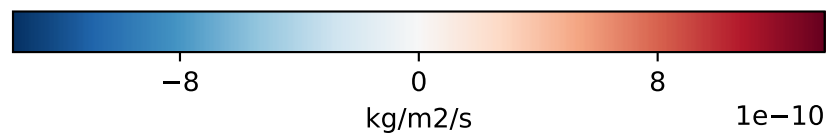
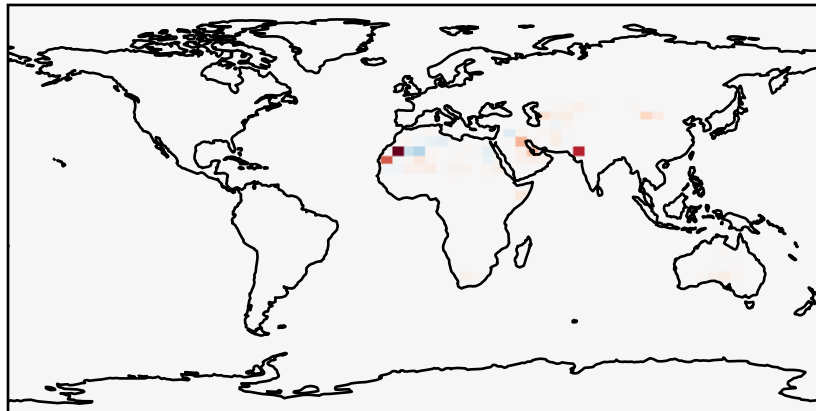
GCC_12.9.0 (Ref)
4.0x5.0



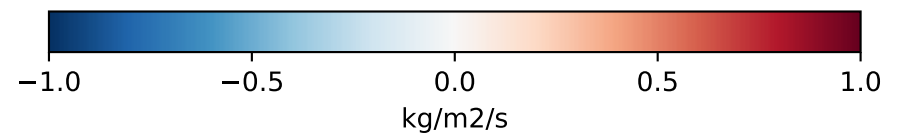
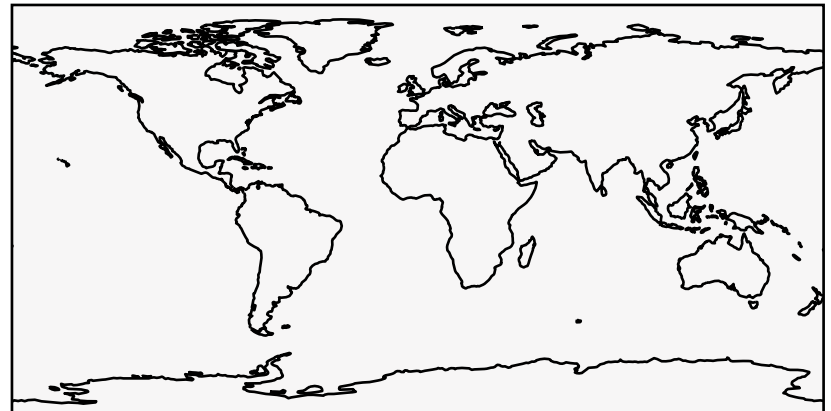
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



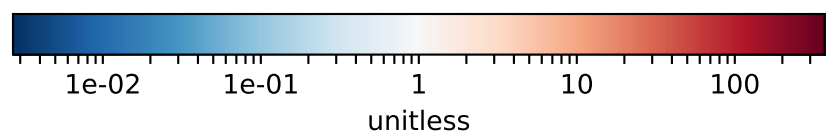
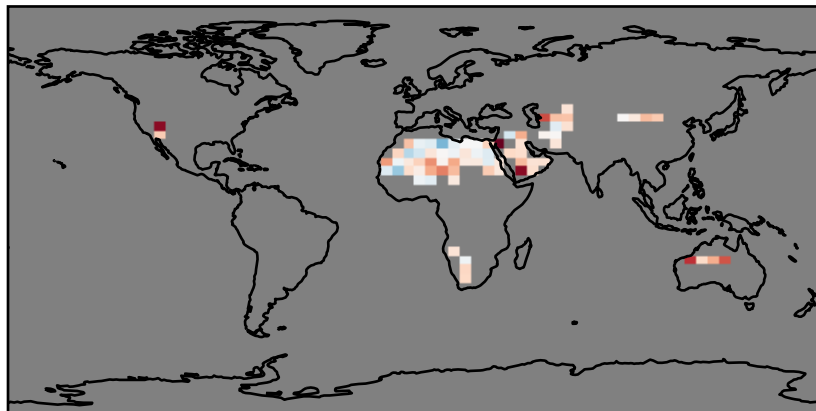
Difference
Dev - Ref, Dynamic Range



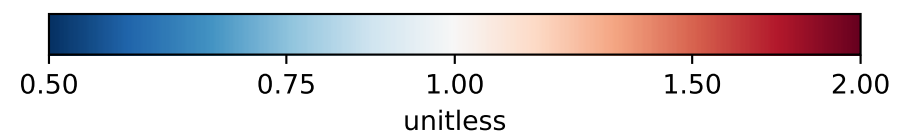
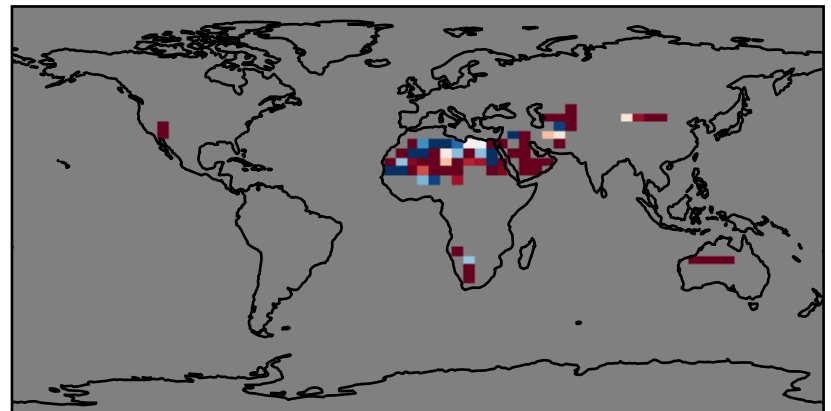
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

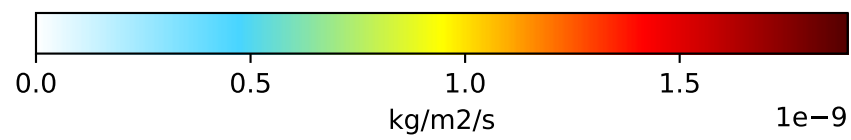
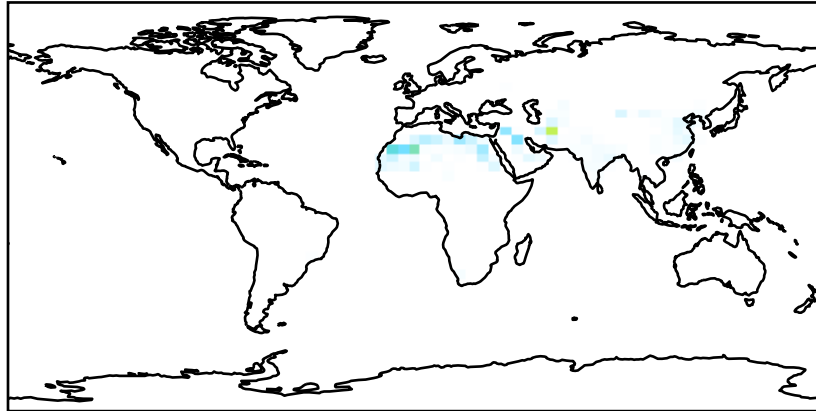


Ratio
Dev/Ref, Fixed Range

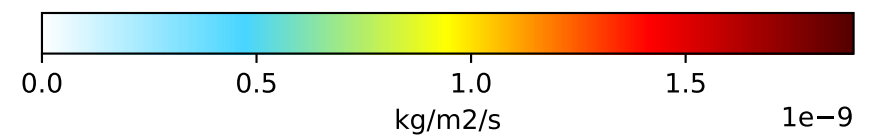
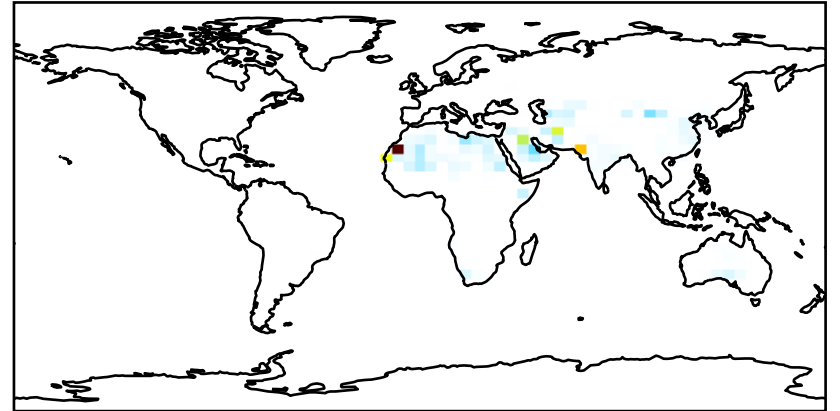


EmisDST1_Total (Jul2019)

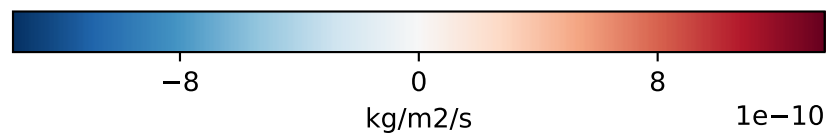
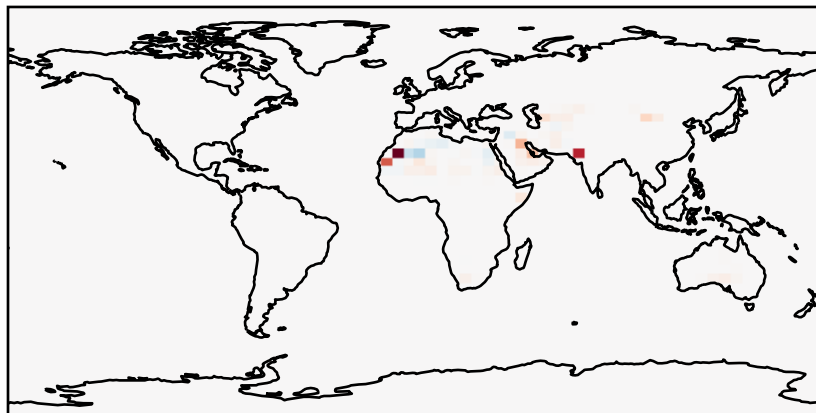
GCC_12.9.0 (Ref)
4.0x5.0



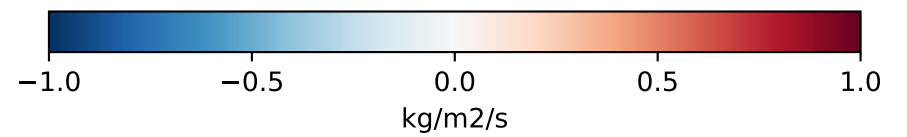
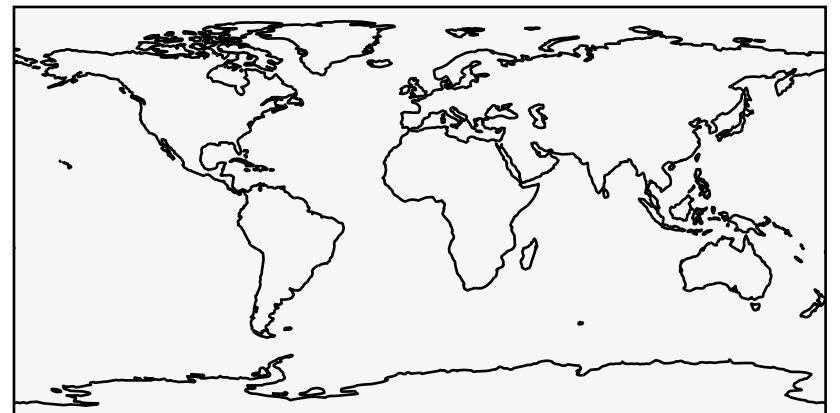
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



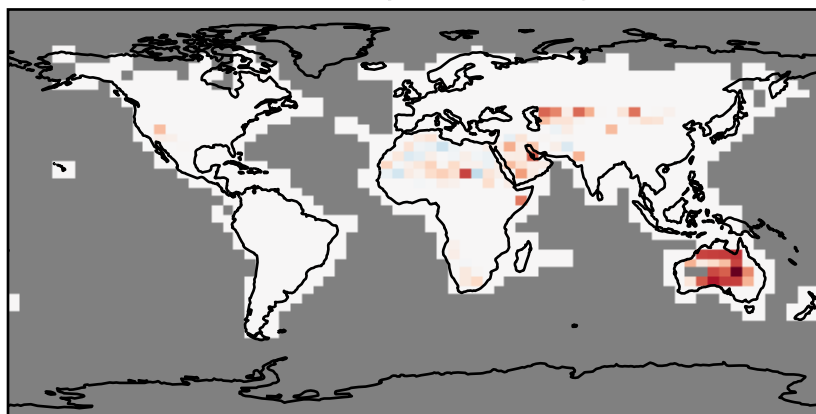
Difference
Dev - Ref, Dynamic Range



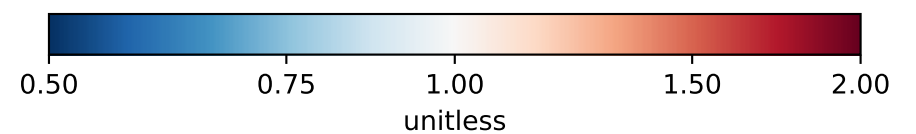
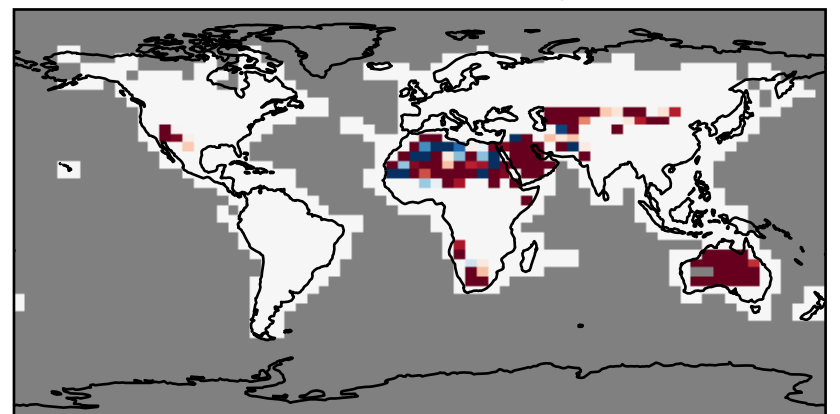
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

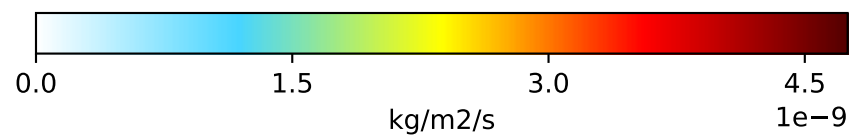
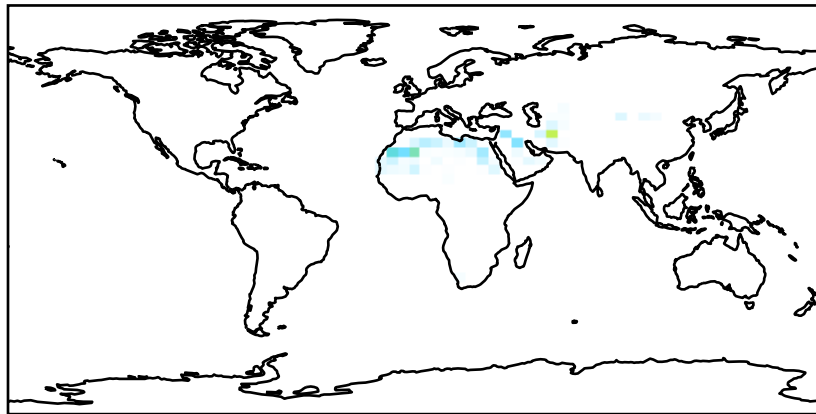


Ratio
Dev/Ref, Fixed Range

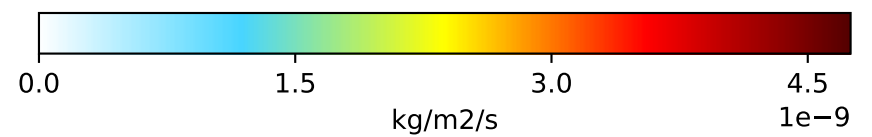
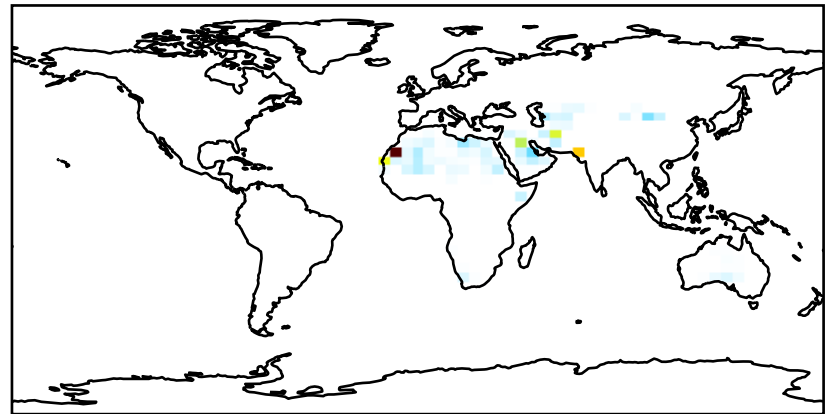


EmisDST2_Natural (Jul2019)

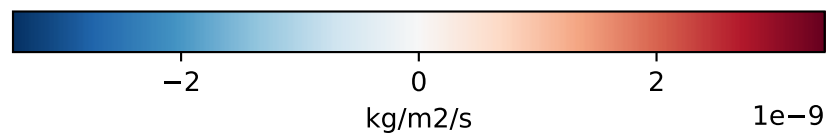
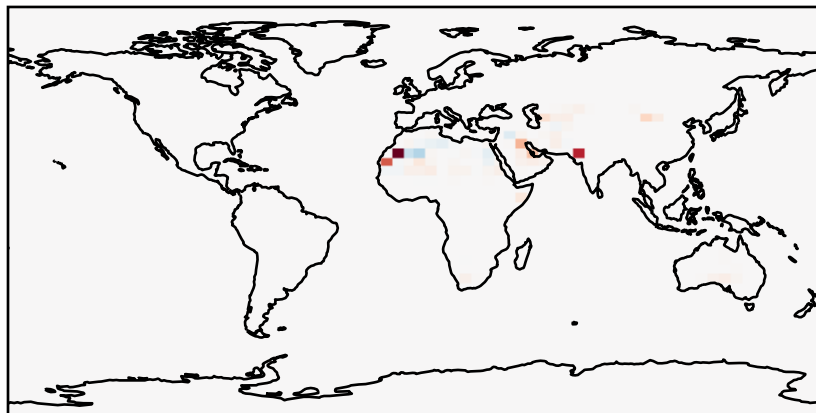
GCC_12.9.0 (Ref)
4.0x5.0



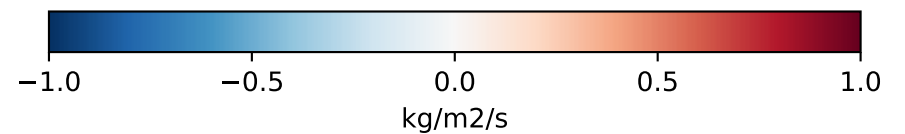
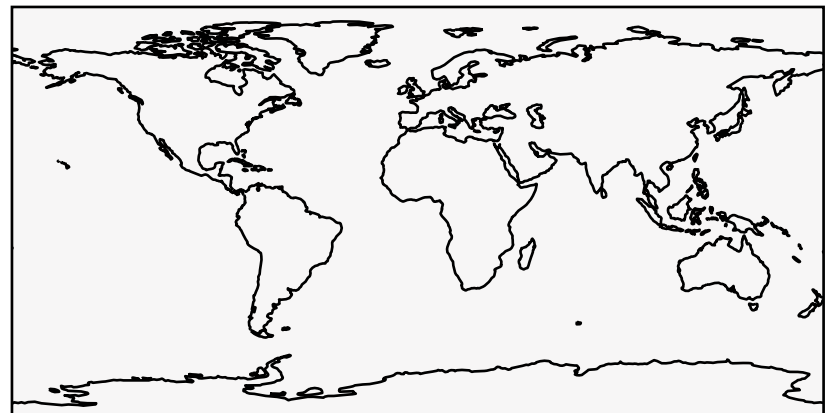
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



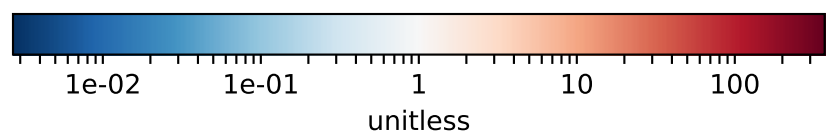
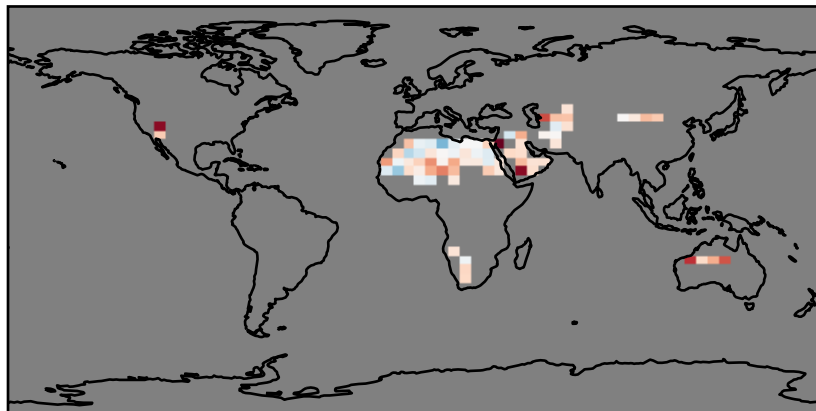
Difference
Dev - Ref, Dynamic Range



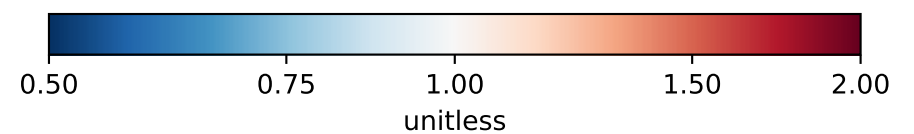
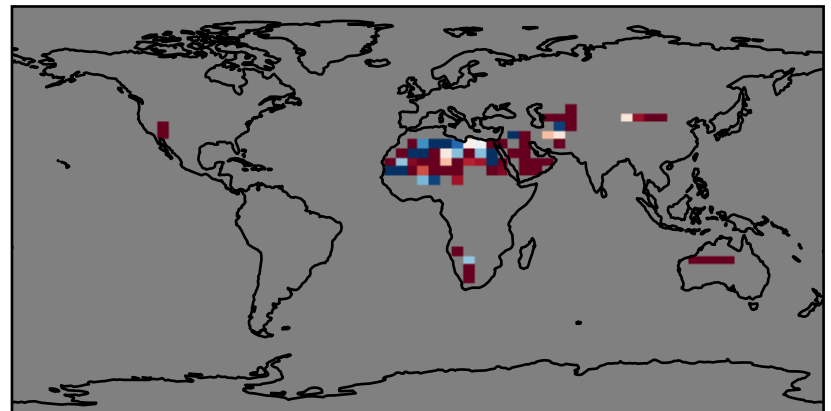
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

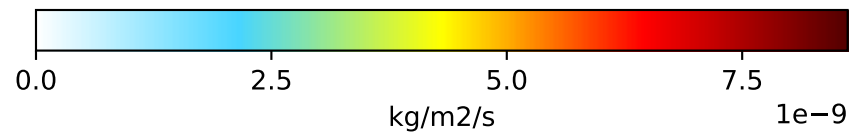
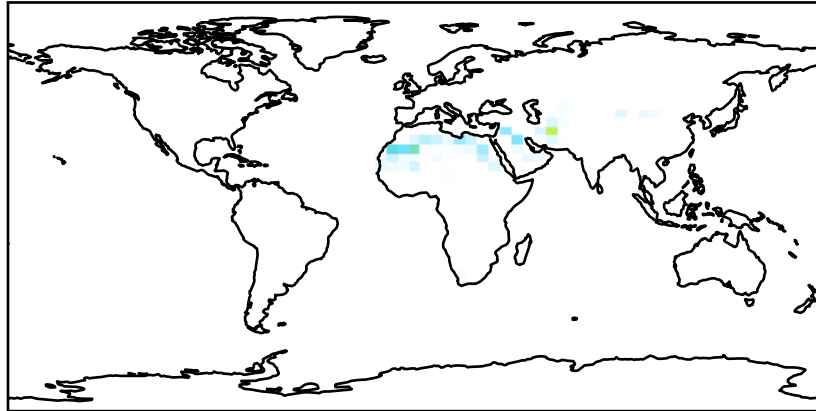


Ratio
Dev/Ref, Fixed Range

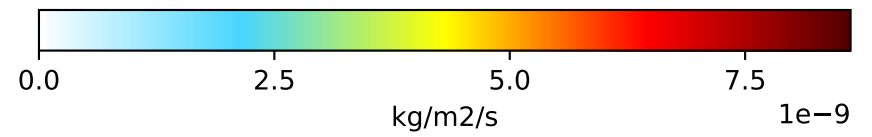
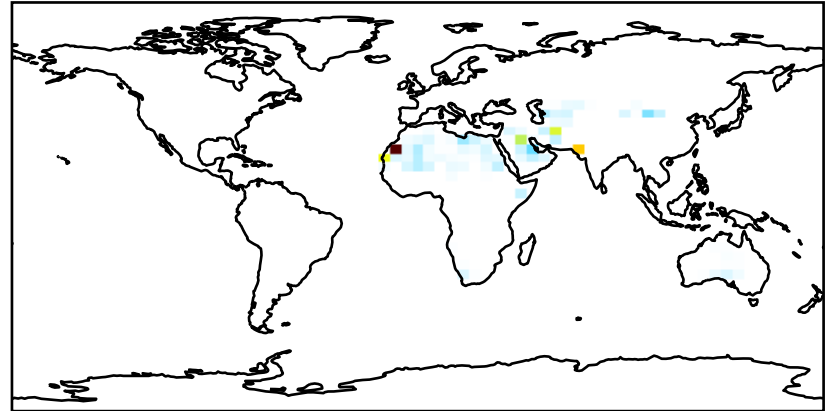


EmisDST3_Natural (Jul2019)

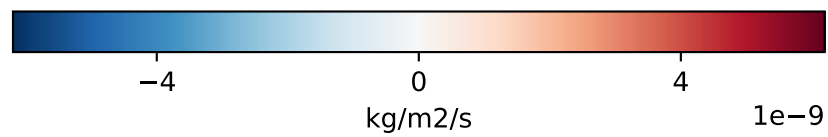
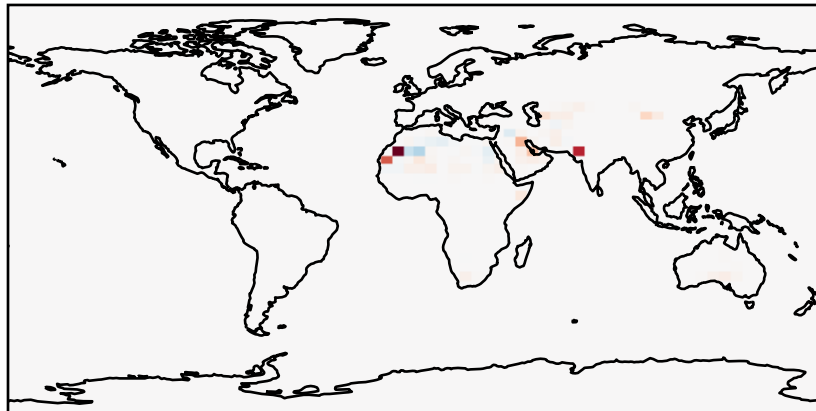
GCC_12.9.0 (Ref)
4.0x5.0



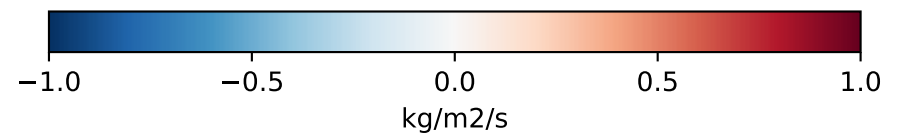
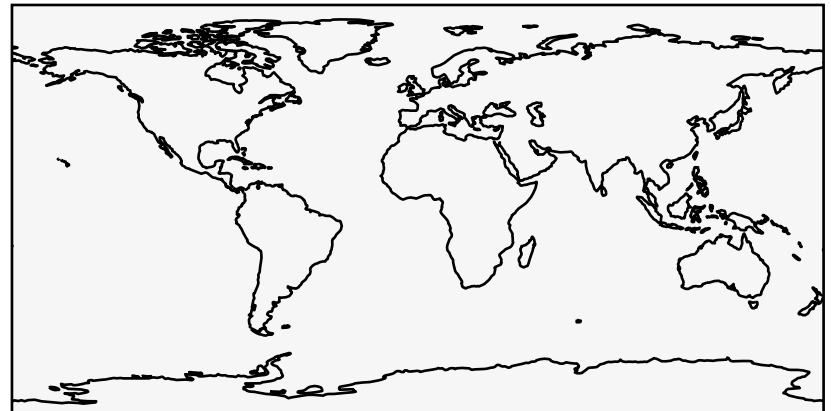
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



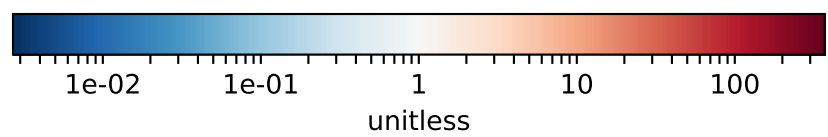
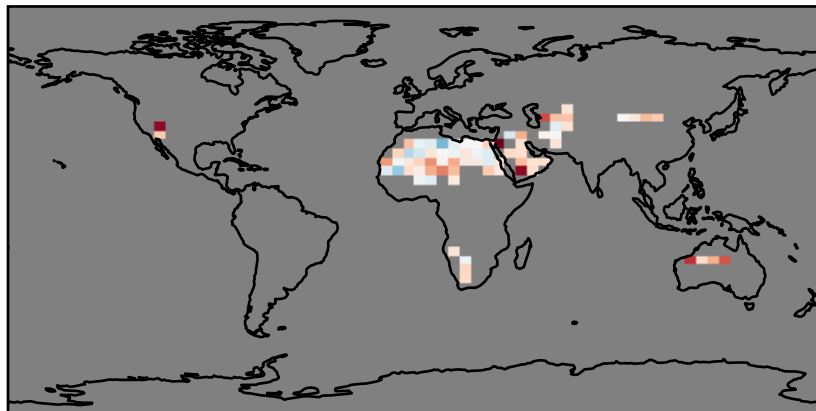
Difference
Dev - Ref, Dynamic Range



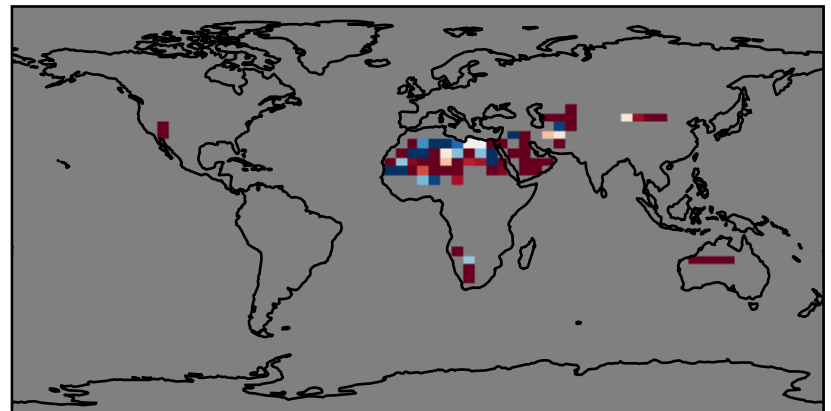
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

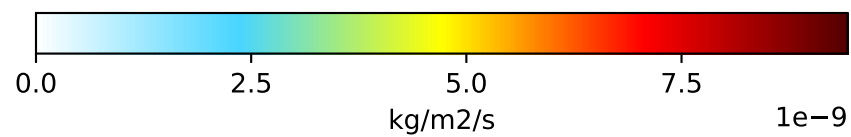
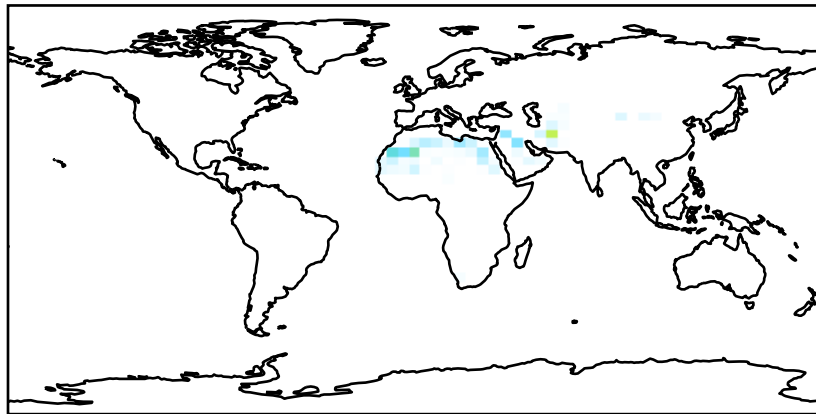


Ratio
Dev/Ref, Fixed Range

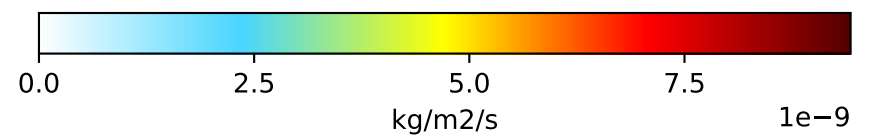
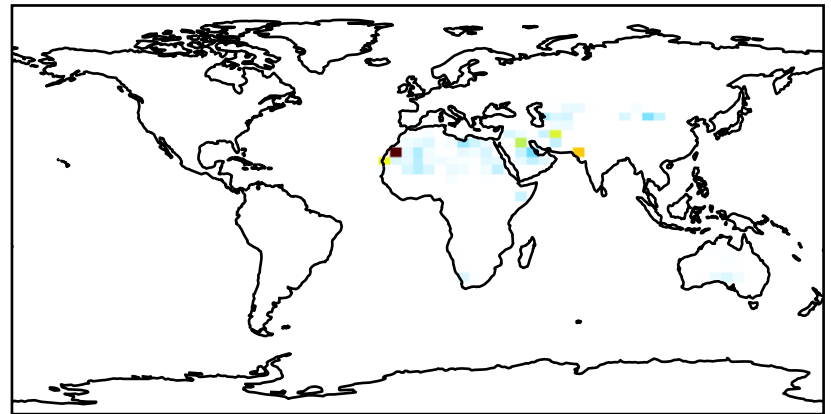


EmisDST4_Natural (Jul2019)

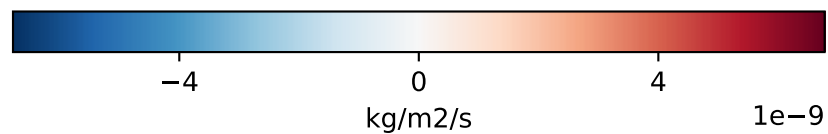
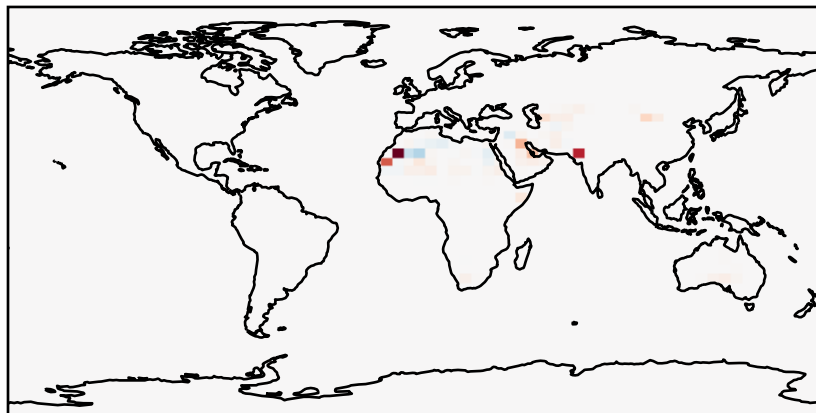
GCC_12.9.0 (Ref)
4.0x5.0



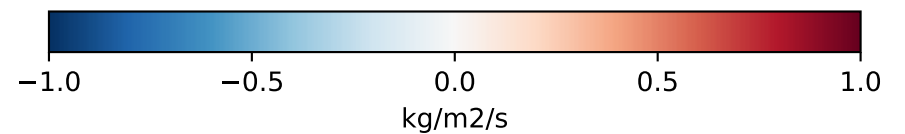
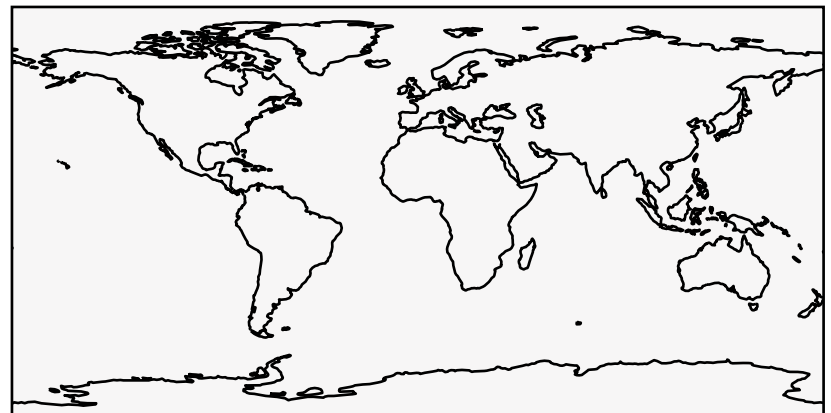
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



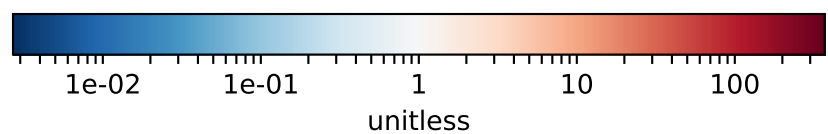
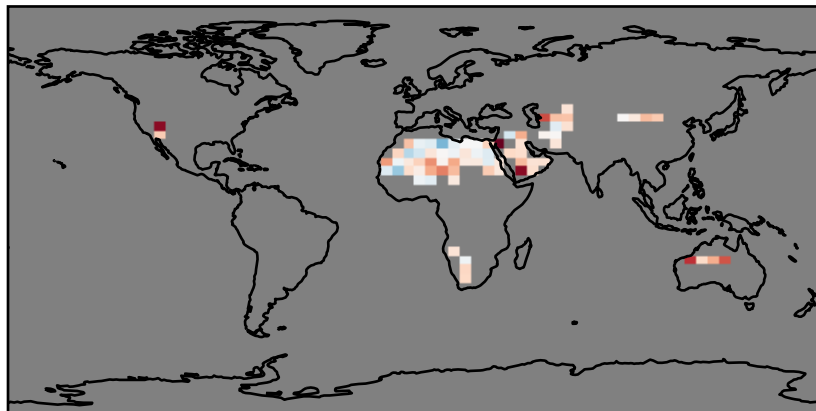
Difference
Dev - Ref, Dynamic Range



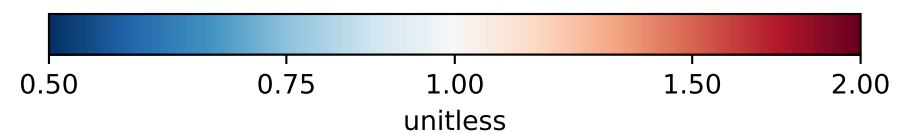
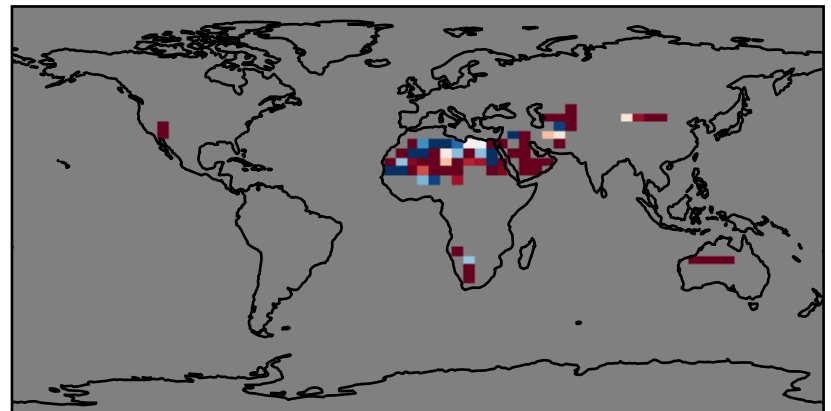
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

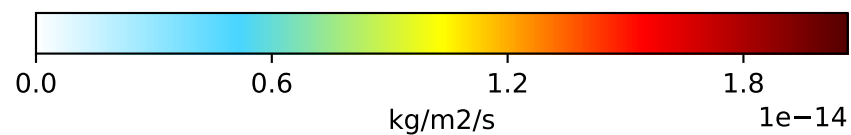
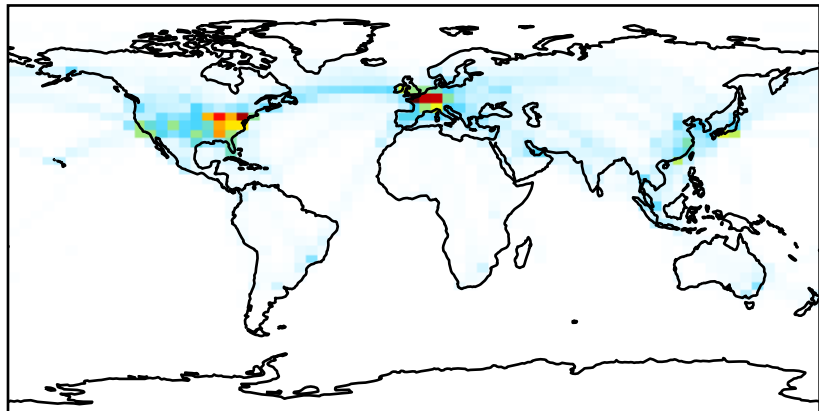


Ratio
Dev/Ref, Fixed Range

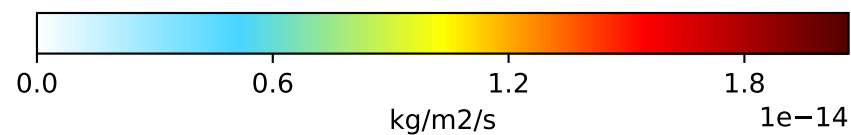
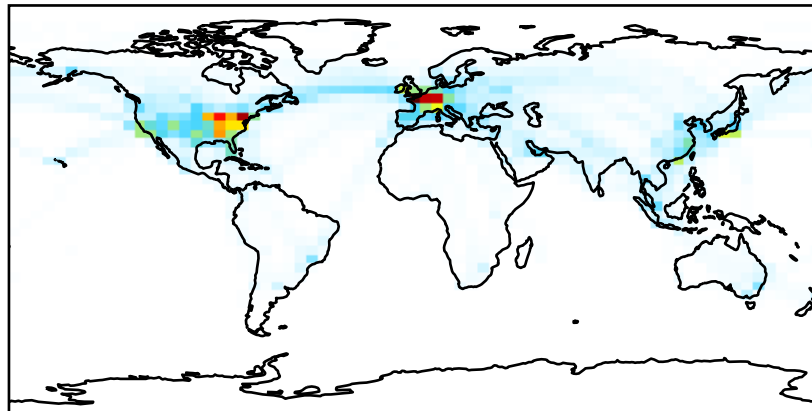


EmisSO4_Aircraft (Jul2019)

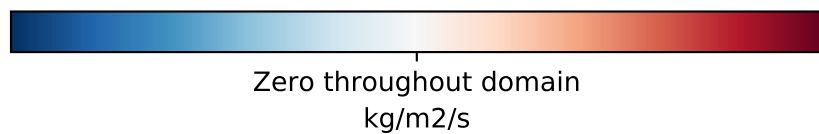
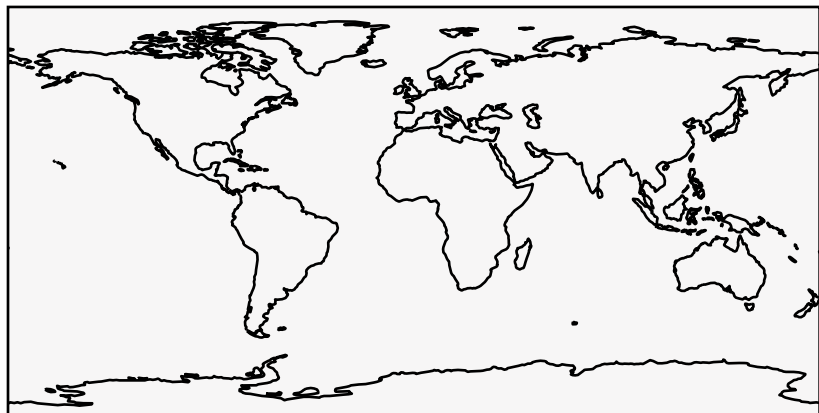
GCC_12.9.0 (Ref)
4.0x5.0



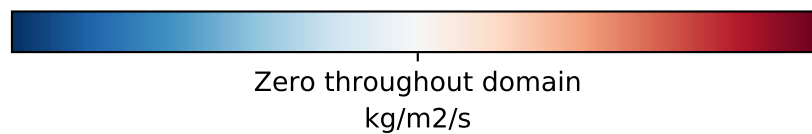
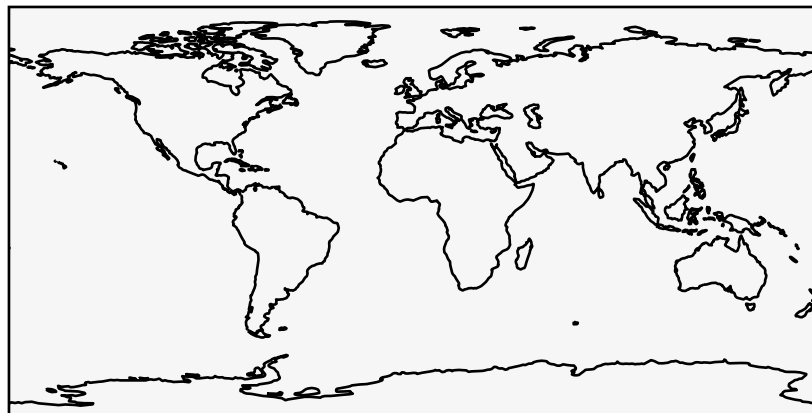
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



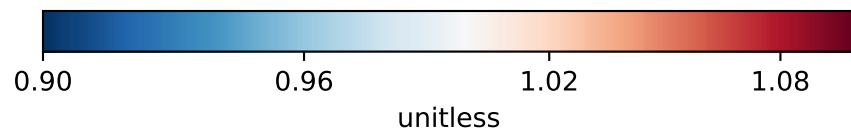
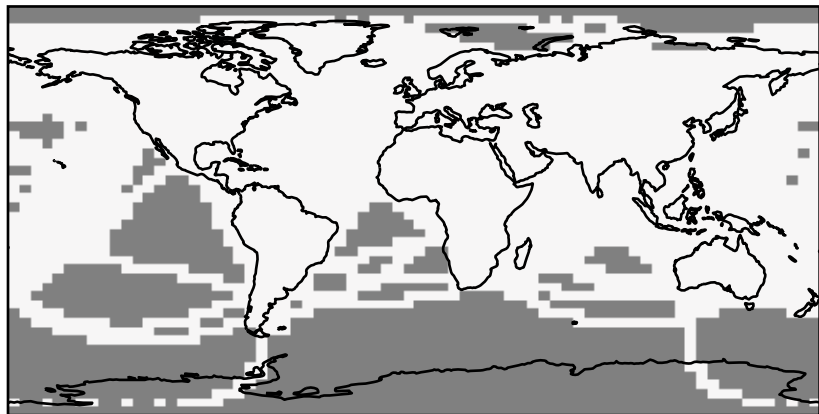
Difference
Dev - Ref, Dynamic Range



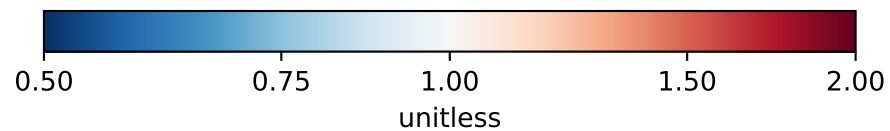
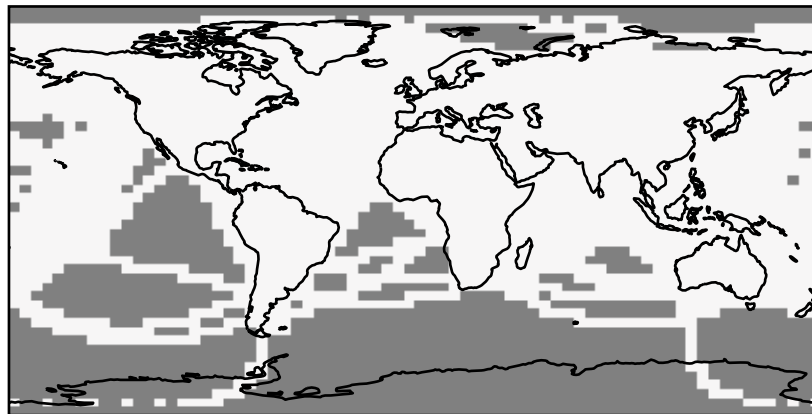
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

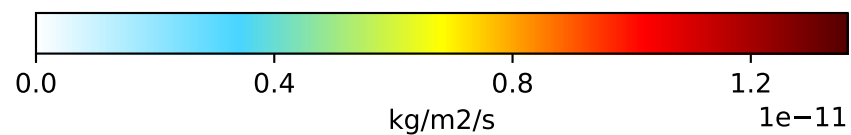
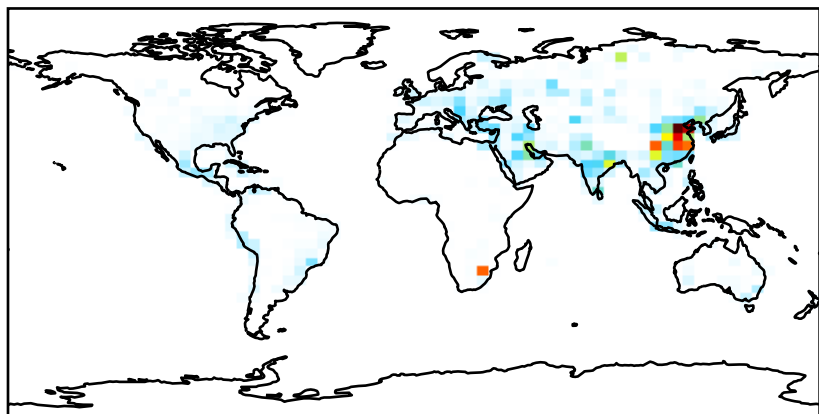


Ratio
Dev/Ref, Fixed Range

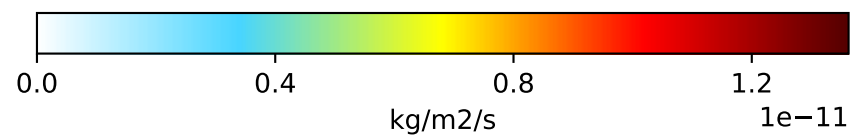
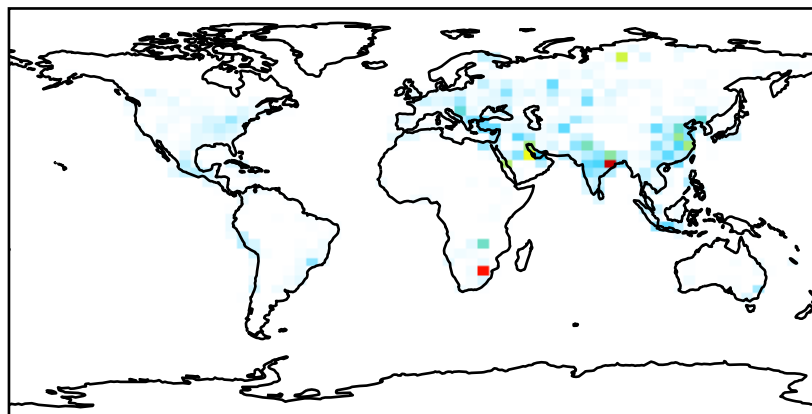


EmisSO4_Anthro (Jul2019)

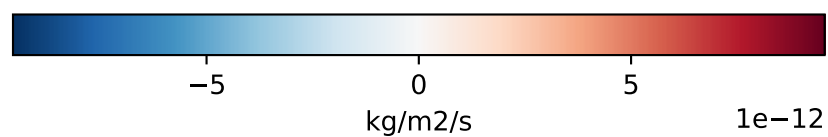
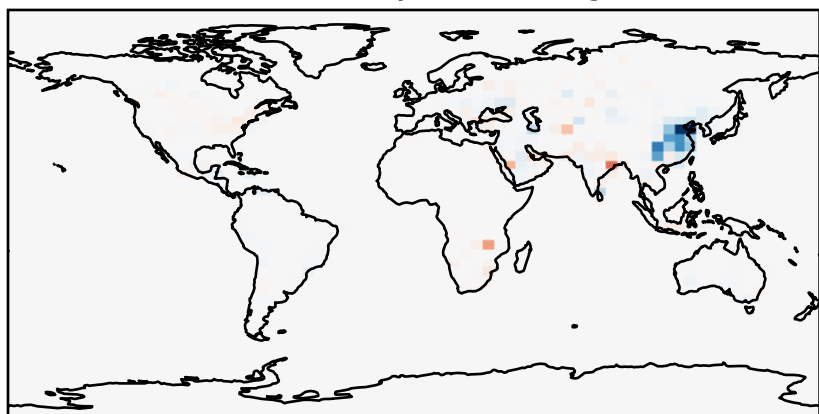
GCC_12.9.0 (Ref)
4.0x5.0



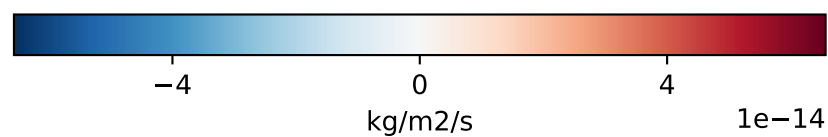
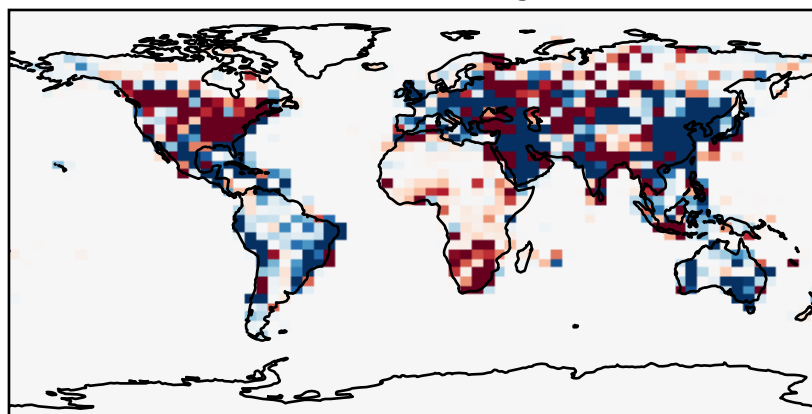
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



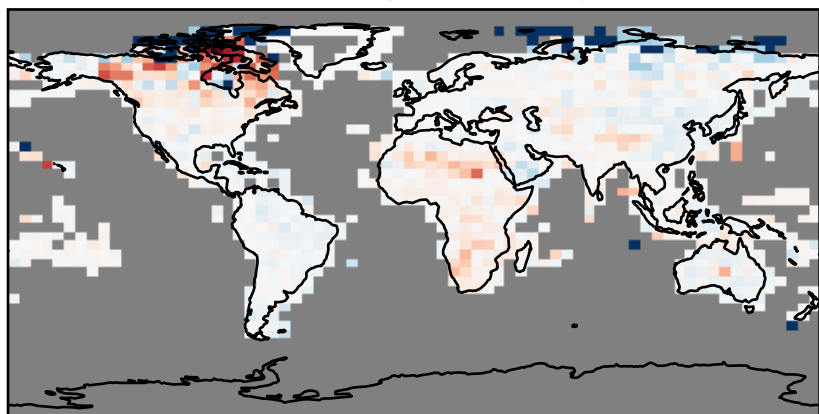
Difference
Dev - Ref, Dynamic Range



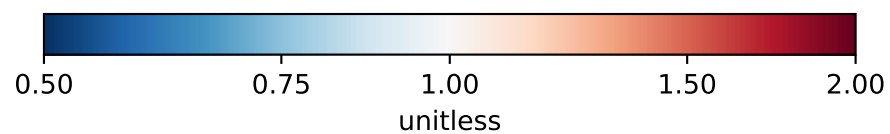
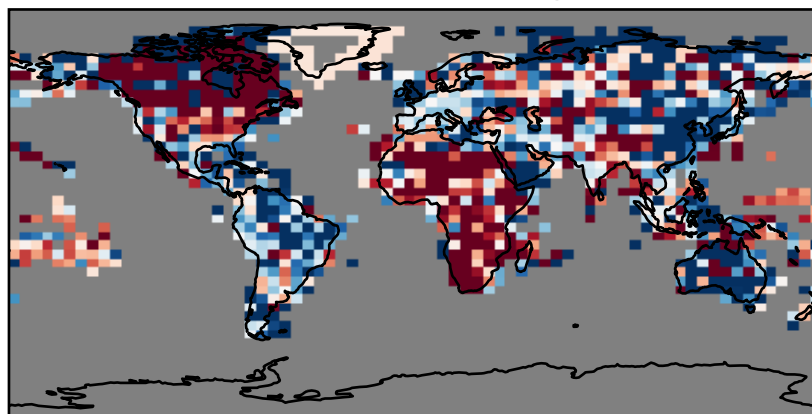
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

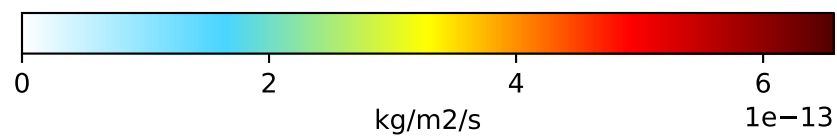
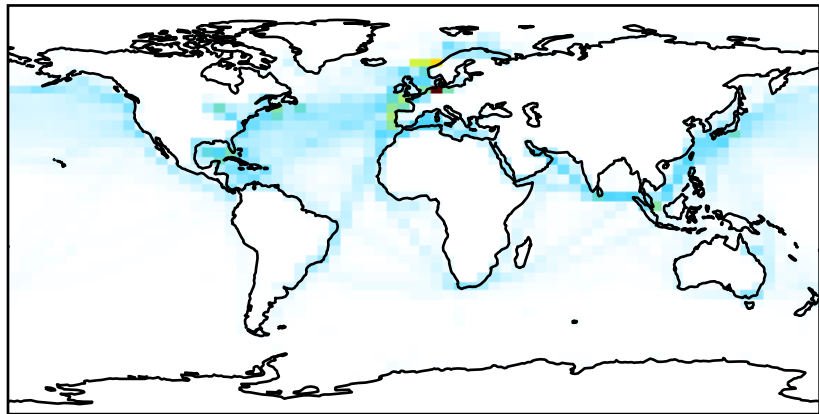


Ratio
Dev/Ref, Fixed Range

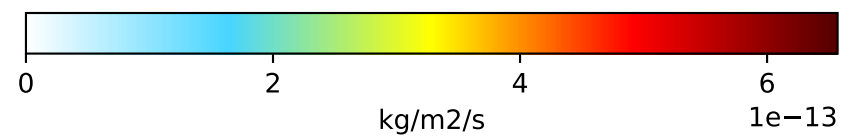
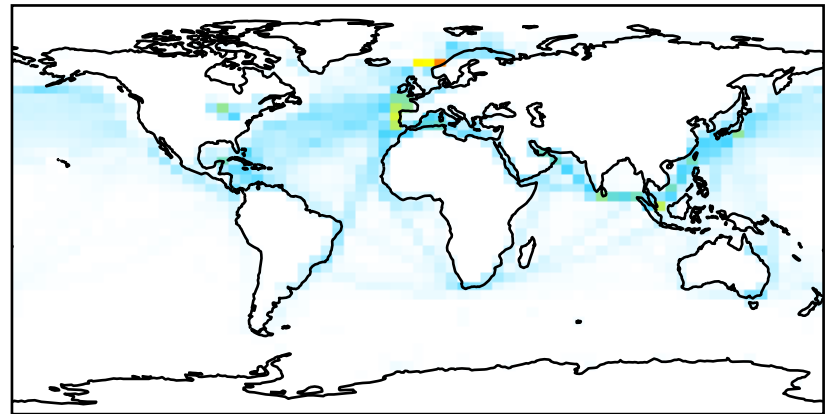


EmisSO4_Ship (Jul2019)

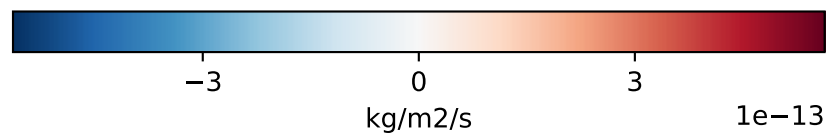
GCC_12.9.0 (Ref)
4.0x5.0



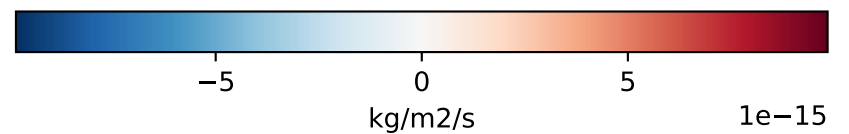
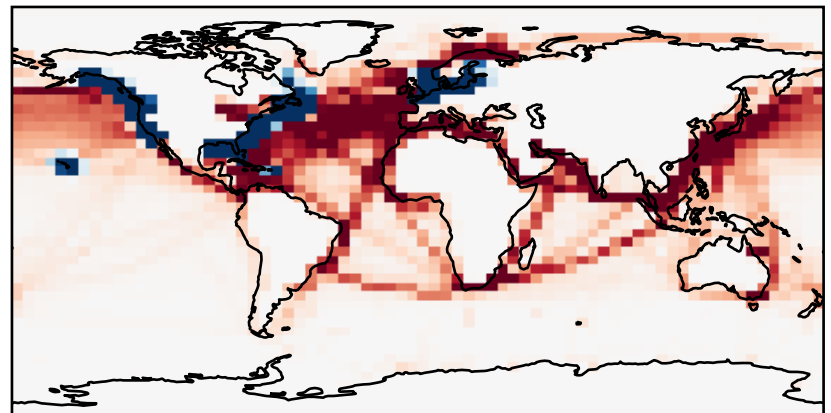
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



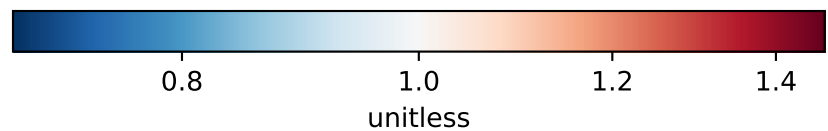
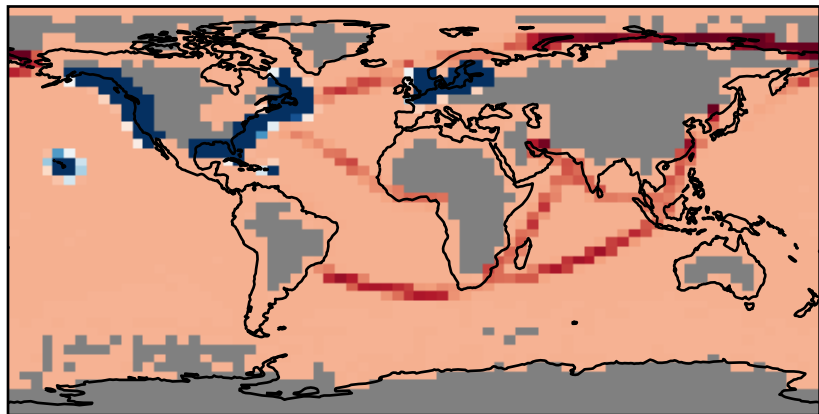
Difference
Dev - Ref, Dynamic Range



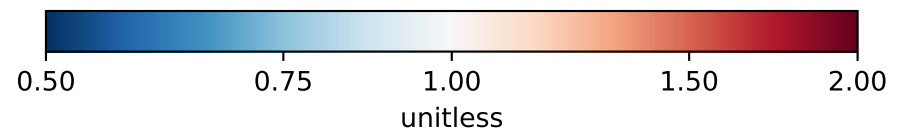
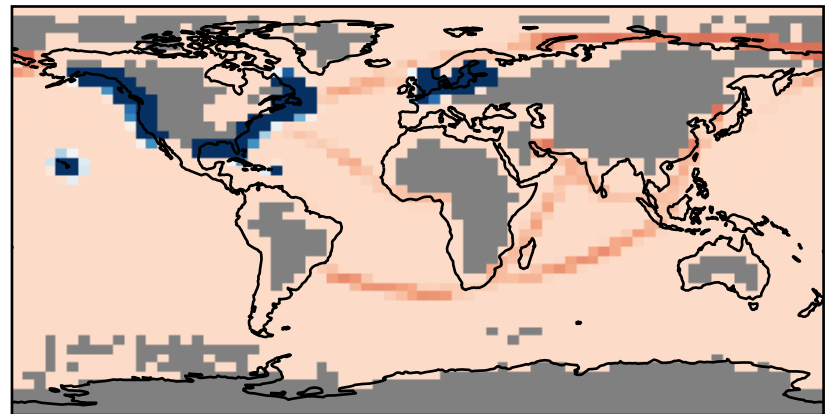
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

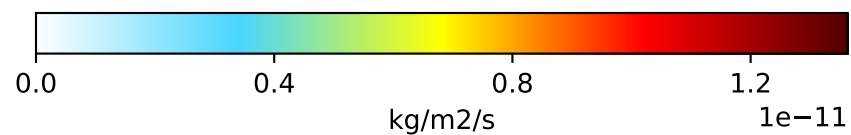
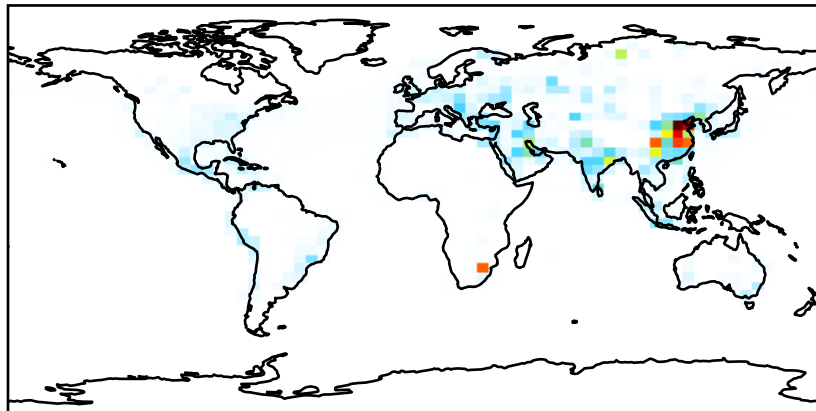


Ratio
Dev/Ref, Fixed Range

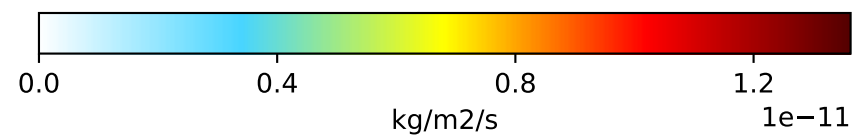
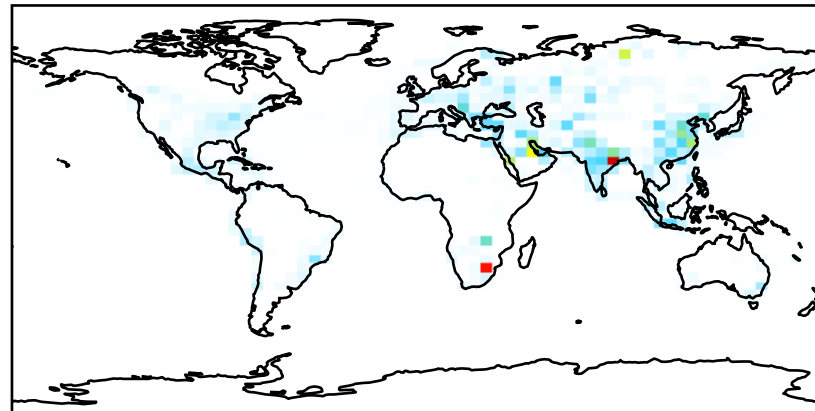


EmisSO4_Total (Jul2019)

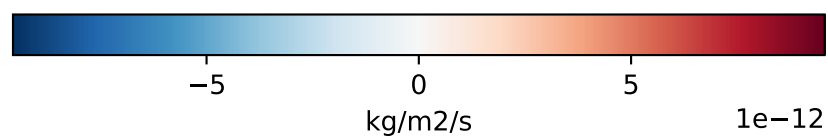
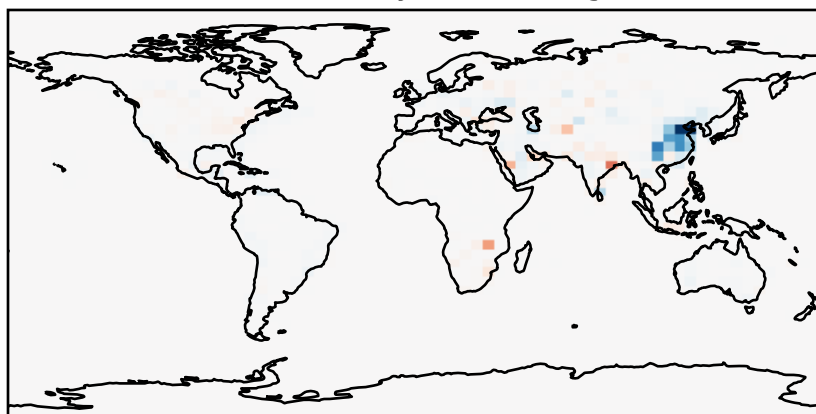
GCC_12.9.0 (Ref)
4.0x5.0



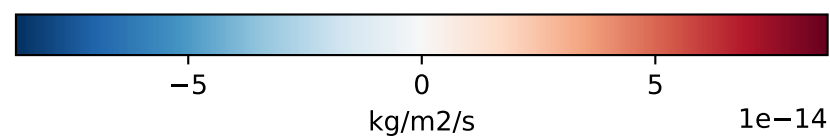
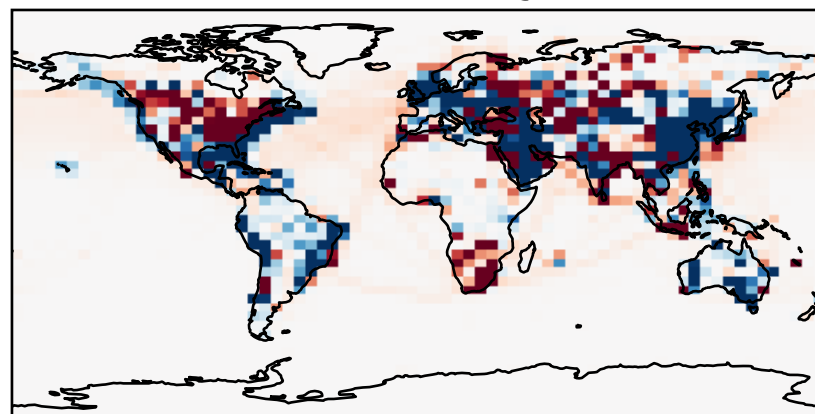
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



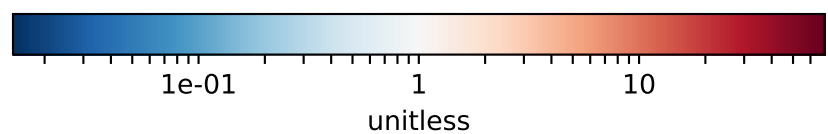
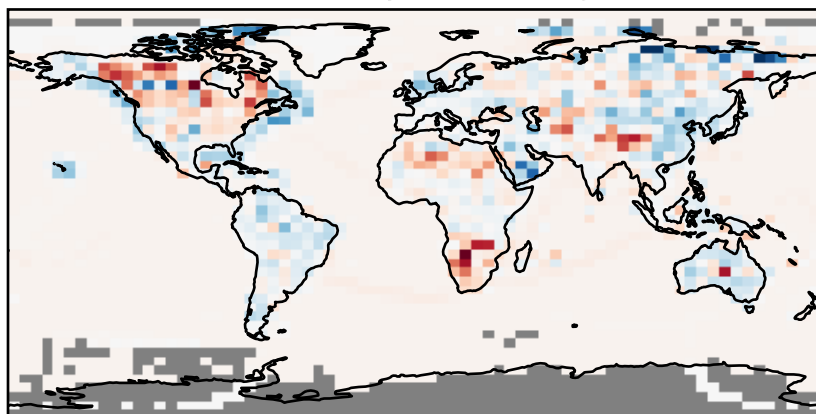
Difference
Dev - Ref, Dynamic Range



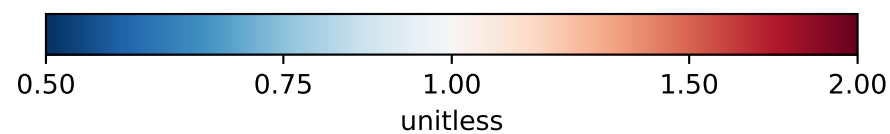
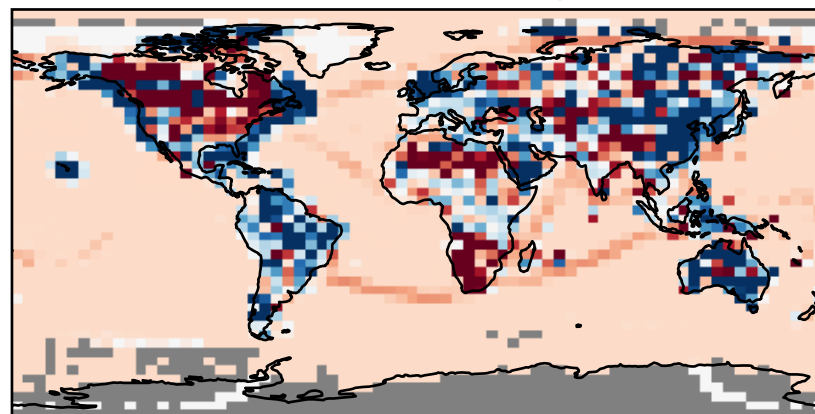
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

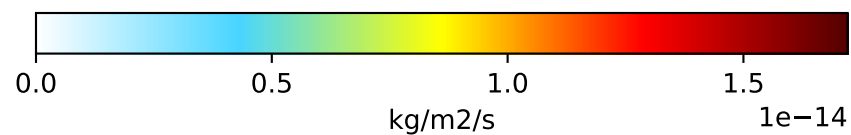
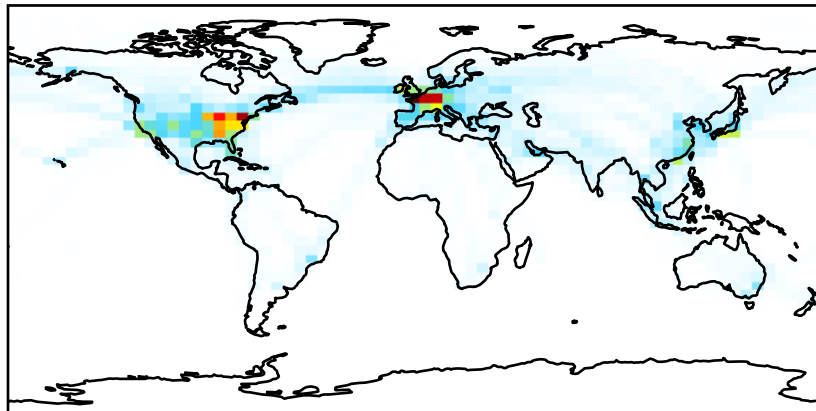


Ratio
Dev/Ref, Fixed Range

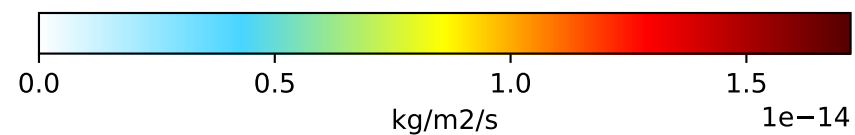
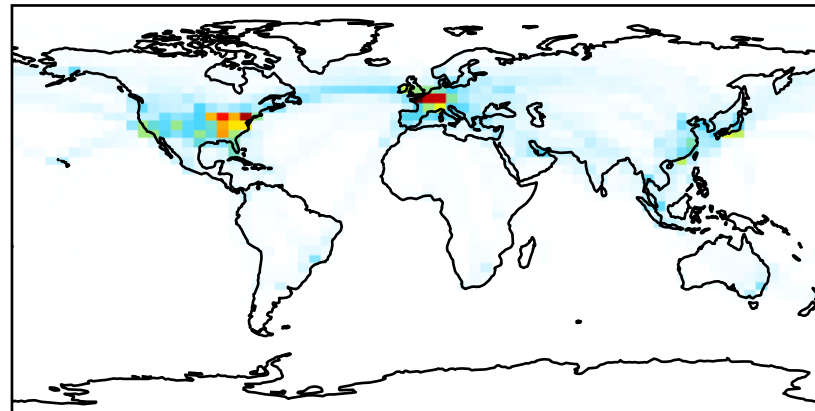


EmisBCPI_Aircraft (Jul2019)

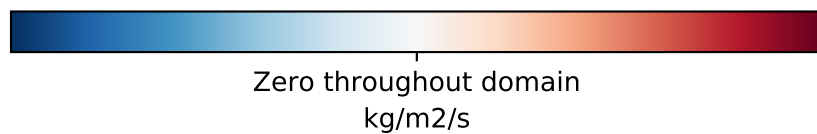
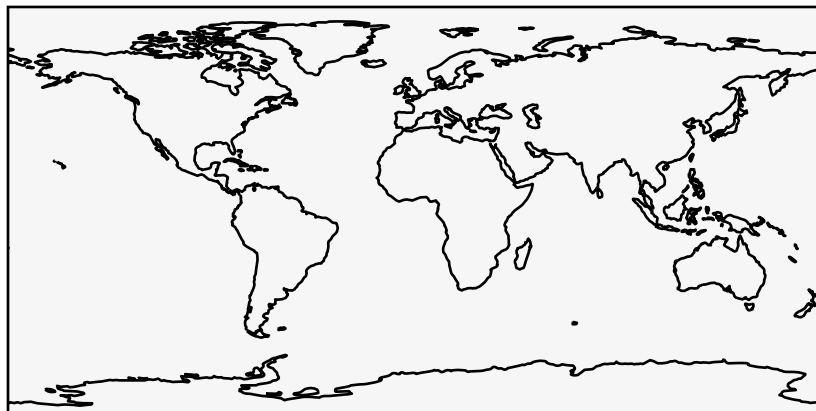
GCC_12.9.0 (Ref)
4.0x5.0



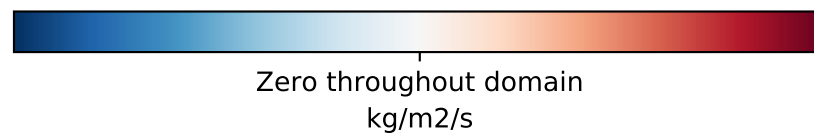
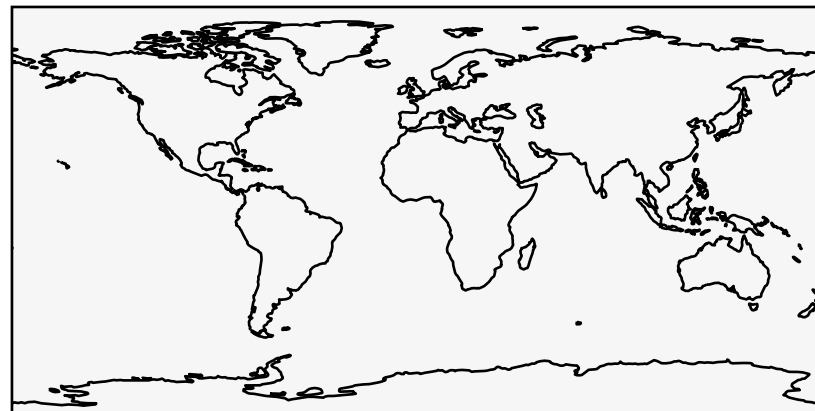
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



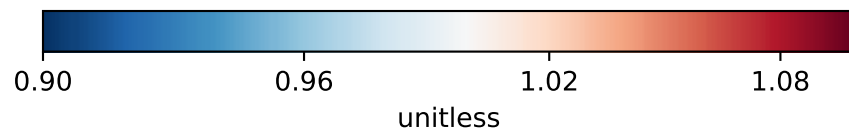
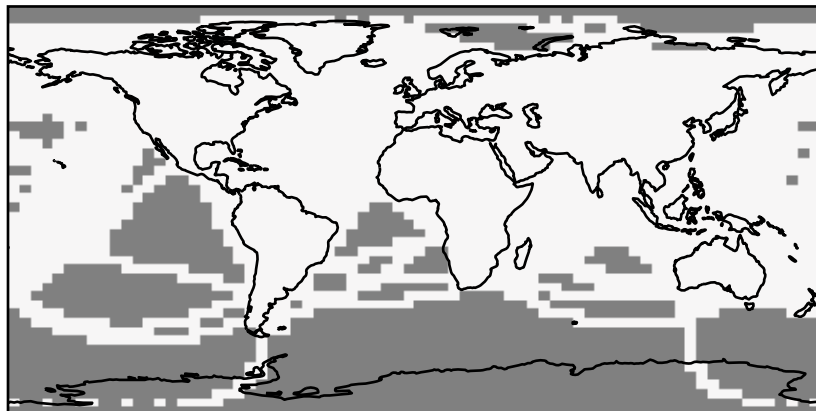
Difference
Dev - Ref, Dynamic Range



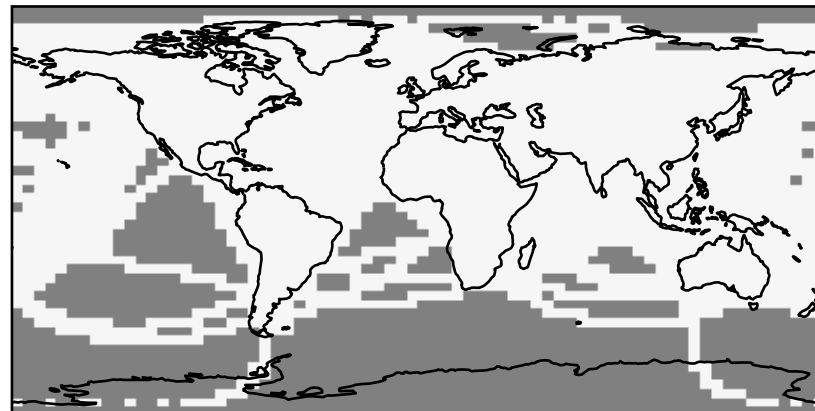
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

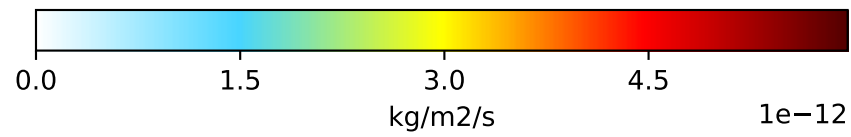
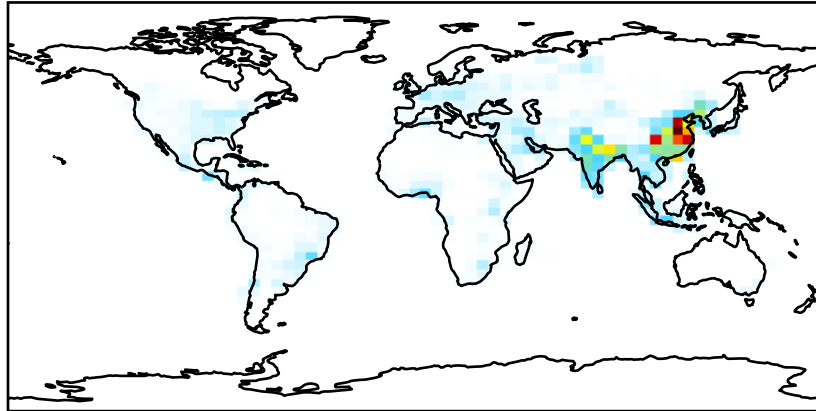


Ratio
Dev/Ref, Fixed Range

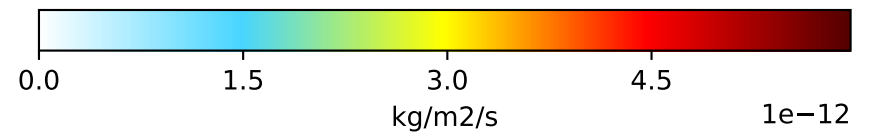
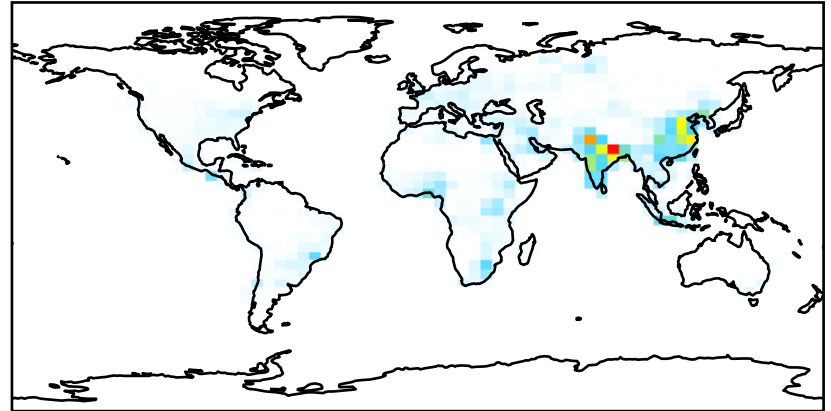


EmisBCPI_Anthro (Jul2019)

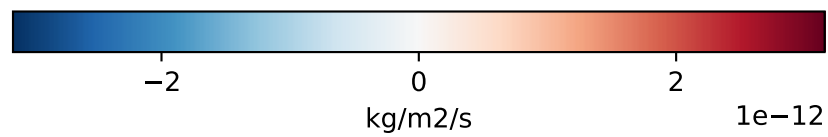
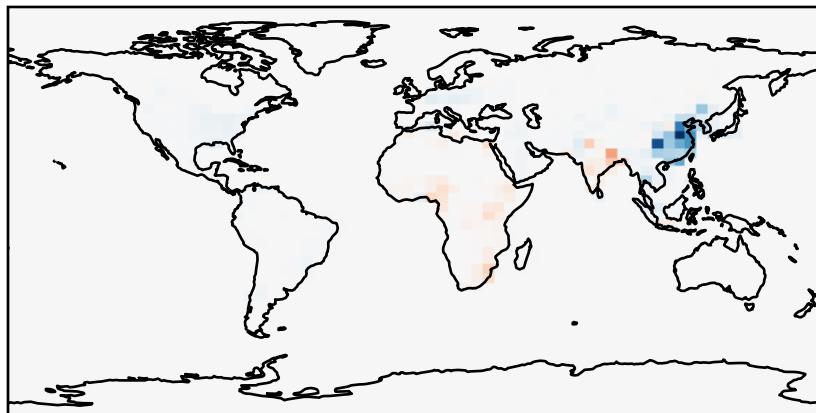
GCC_12.9.0 (Ref)
4.0x5.0



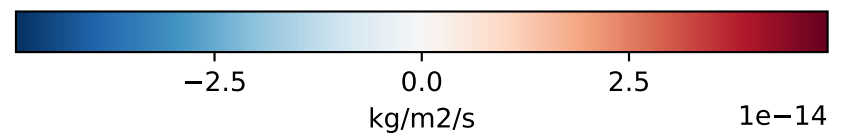
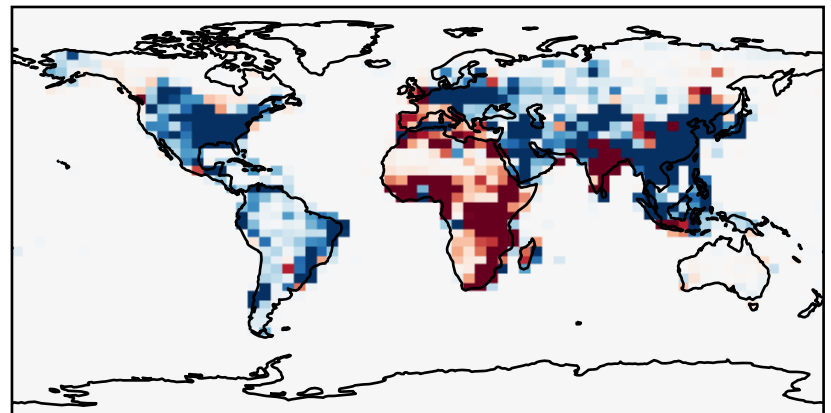
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



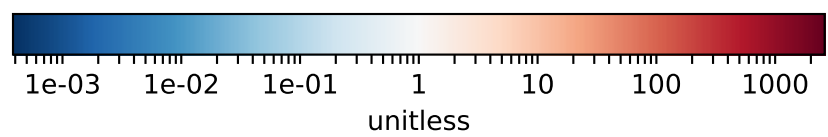
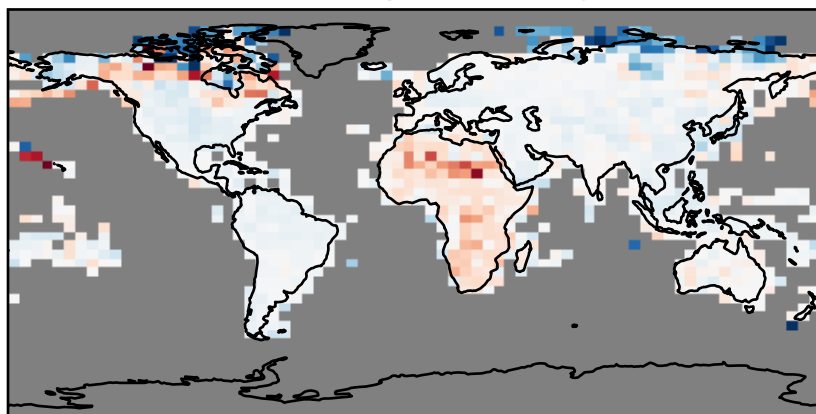
Difference
Dev - Ref, Dynamic Range



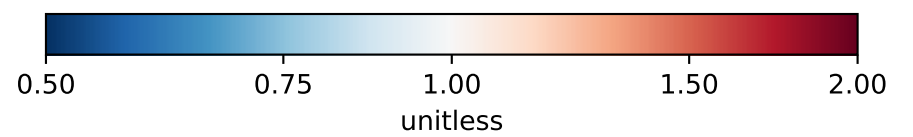
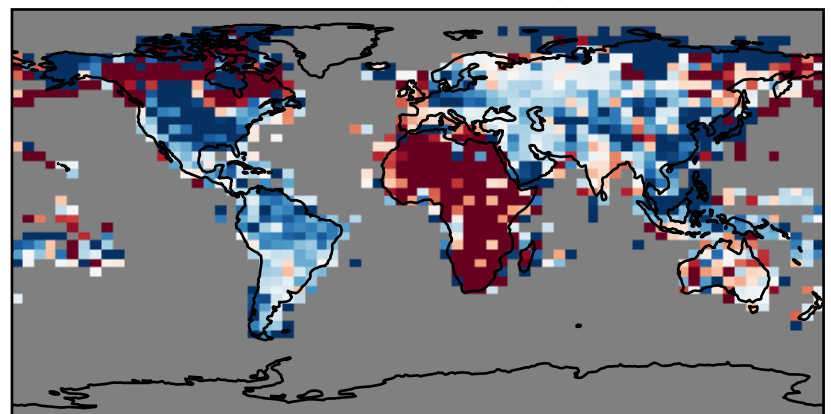
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

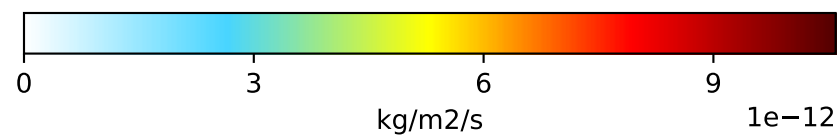
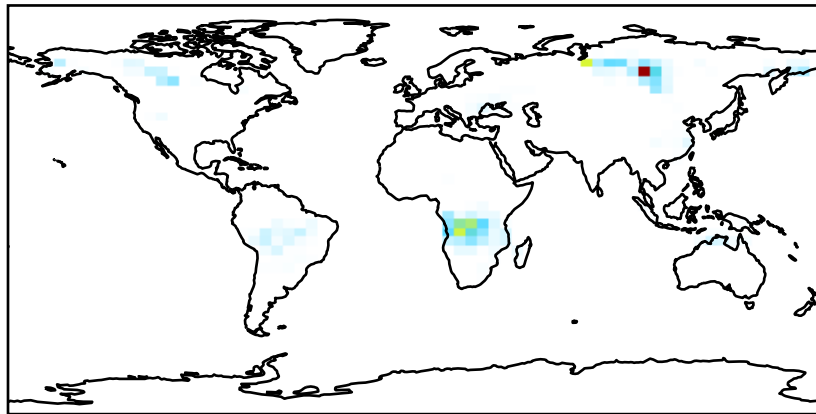


Ratio
Dev/Ref, Fixed Range

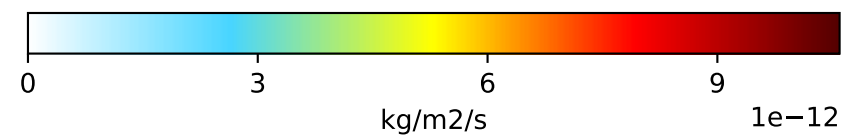
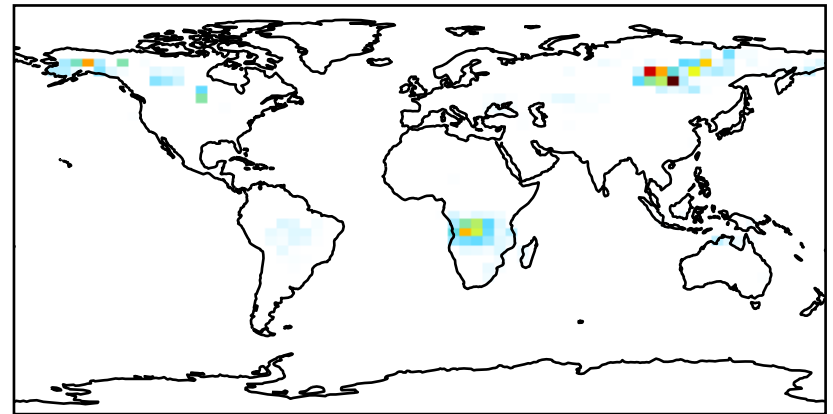


EmisBCPI_BioBurn (Jul2019)

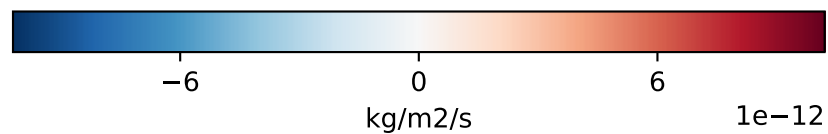
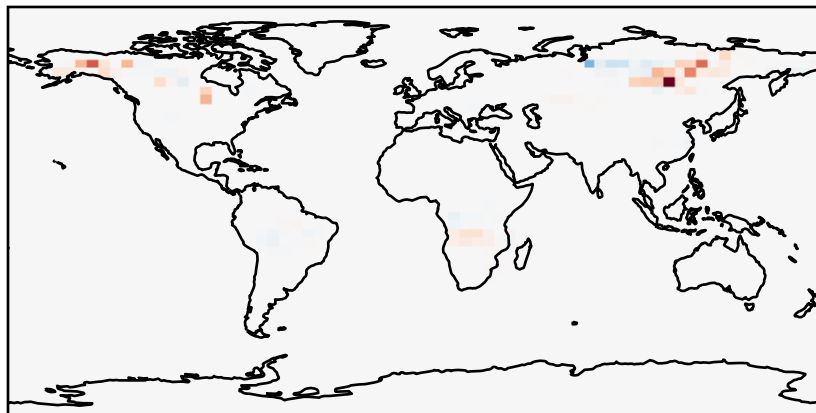
GCC_12.9.0 (Ref)
4.0x5.0



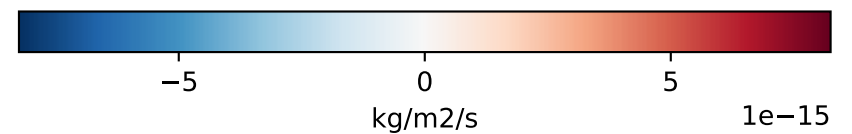
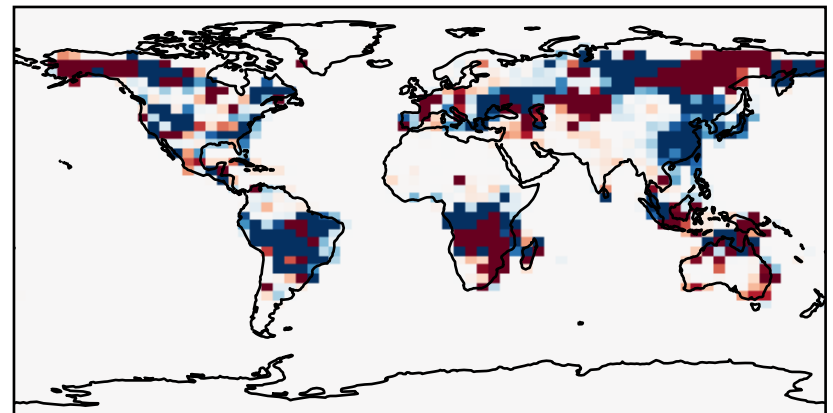
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



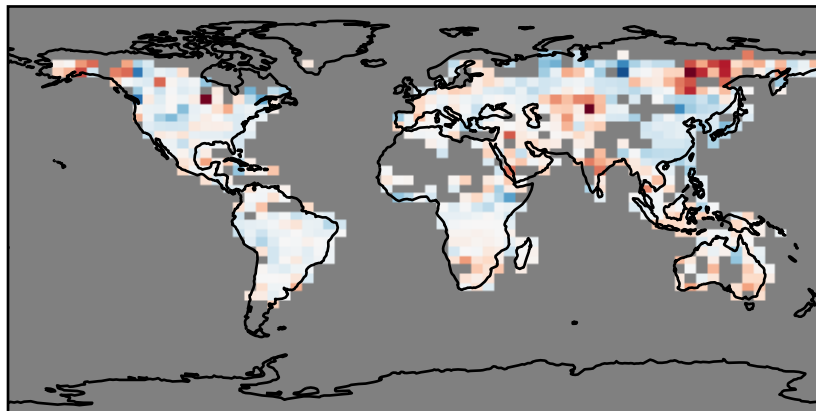
Difference
Dev - Ref, Dynamic Range



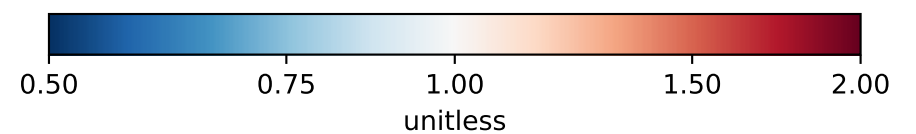
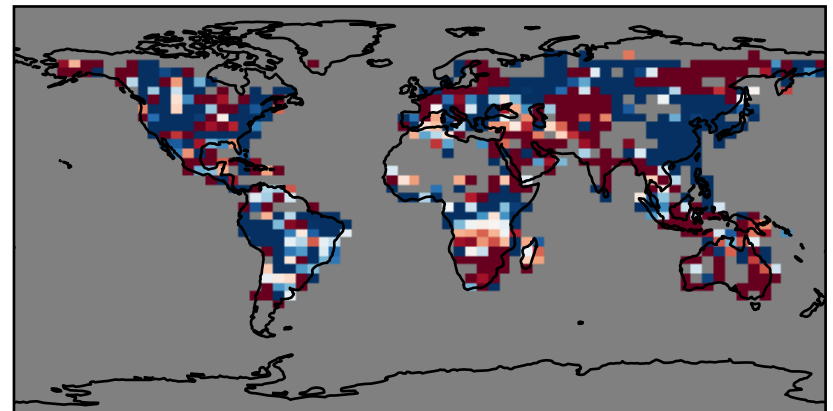
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

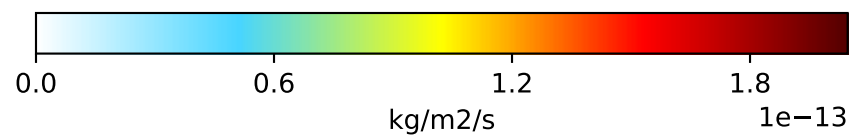
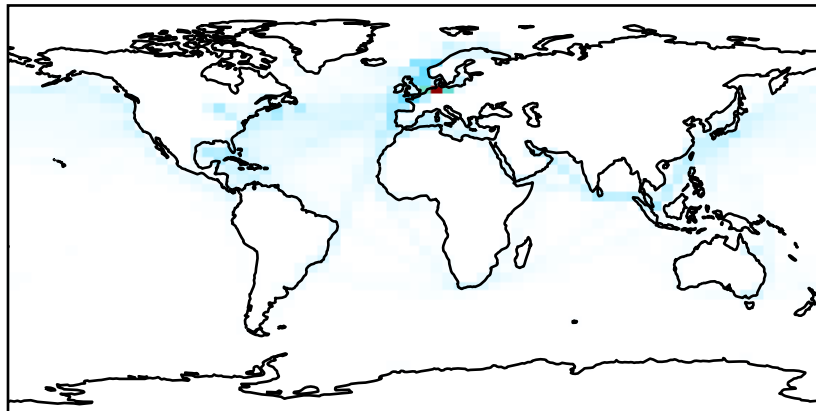


Ratio
Dev/Ref, Fixed Range

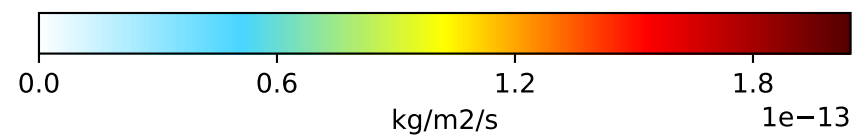
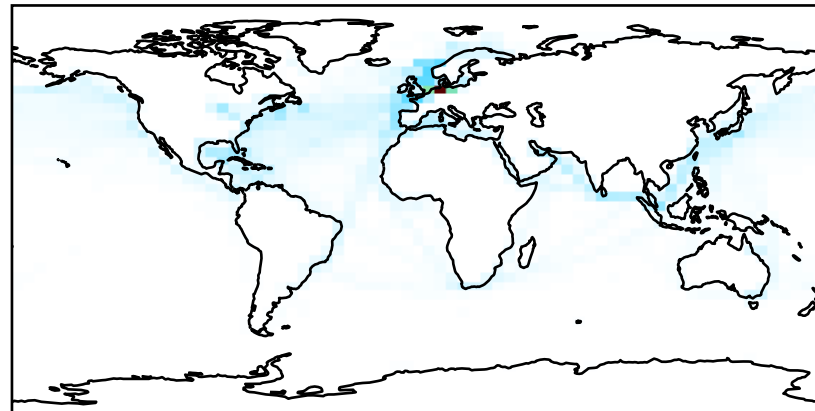


EmisBCPI_Ship (Jul2019)

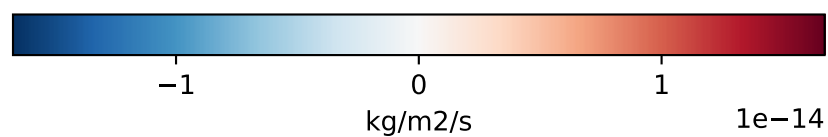
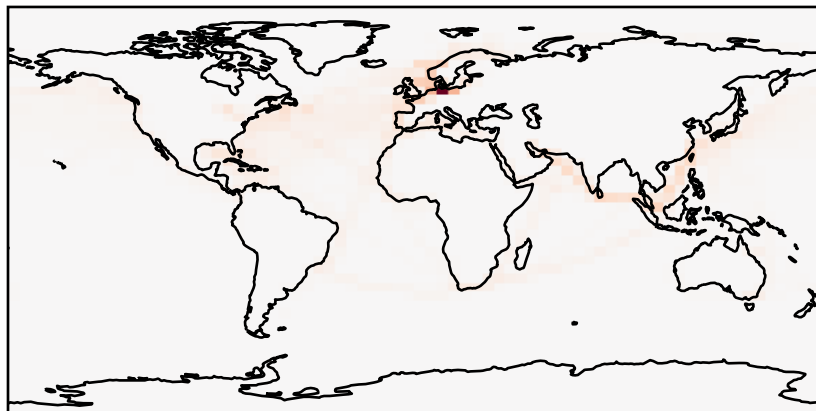
GCC_12.9.0 (Ref)
4.0x5.0



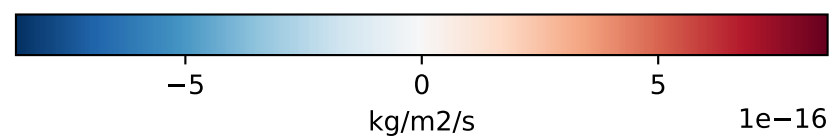
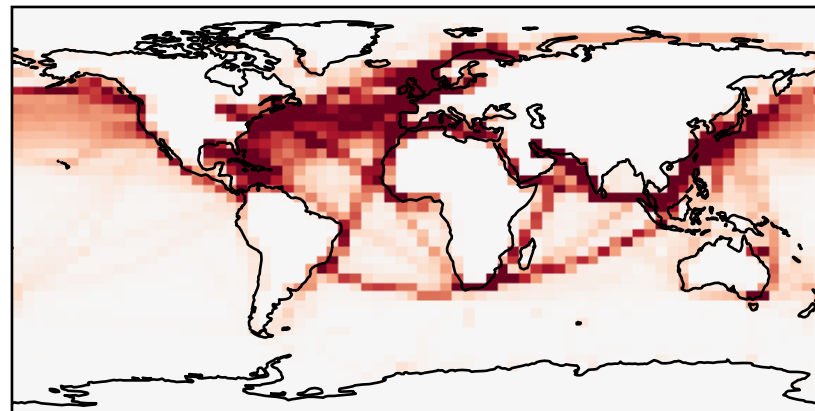
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



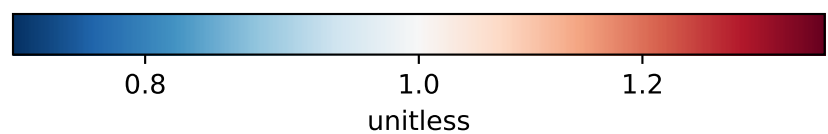
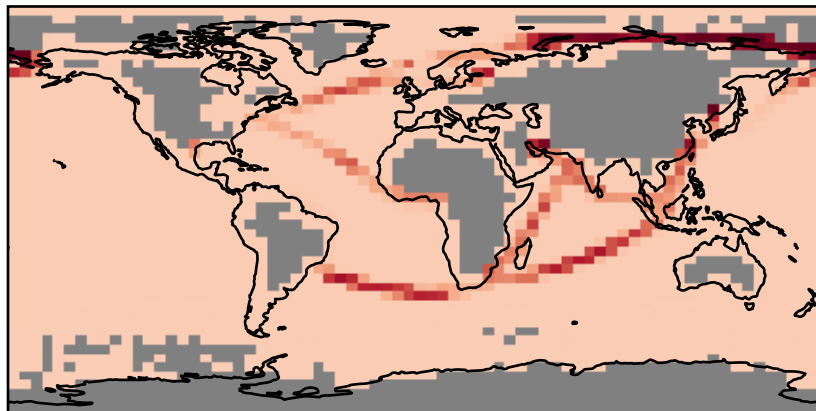
Difference
Dev - Ref, Dynamic Range



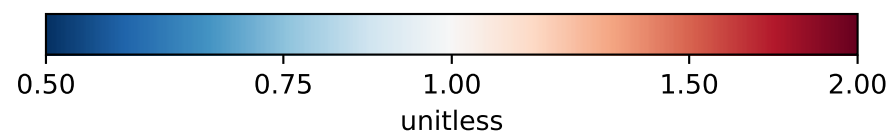
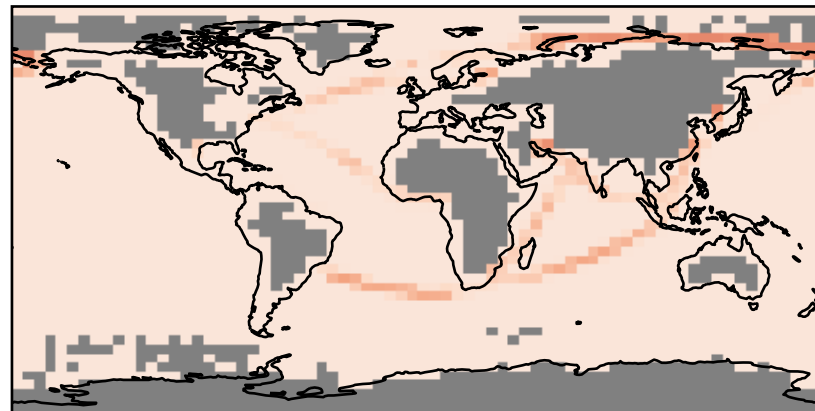
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

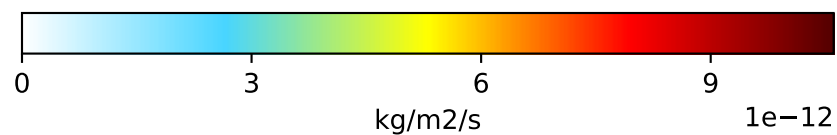
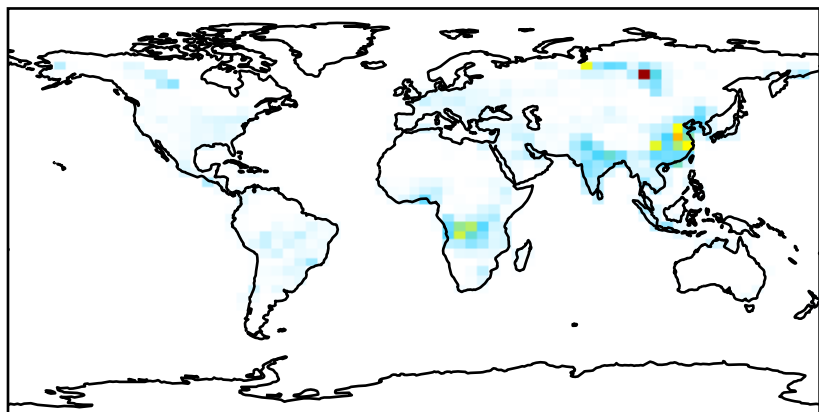


Ratio
Dev/Ref, Fixed Range

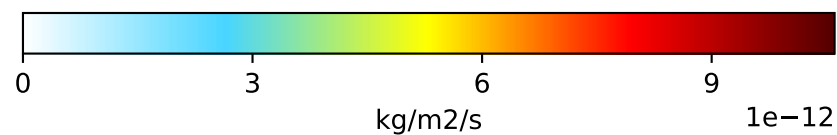
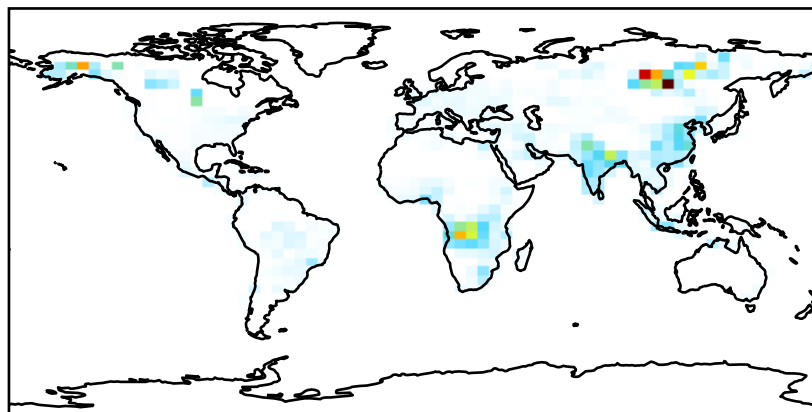


EmisBCPI_Total (Jul2019)

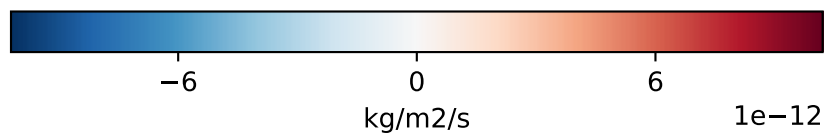
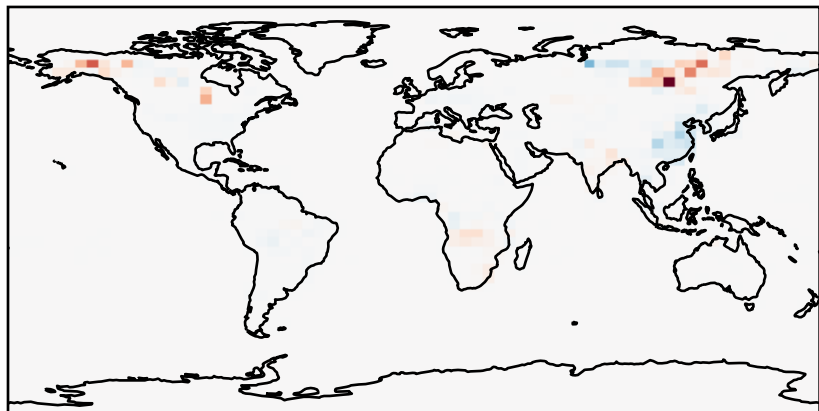
GCC_12.9.0 (Ref)
4.0x5.0



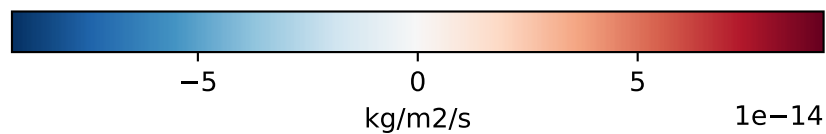
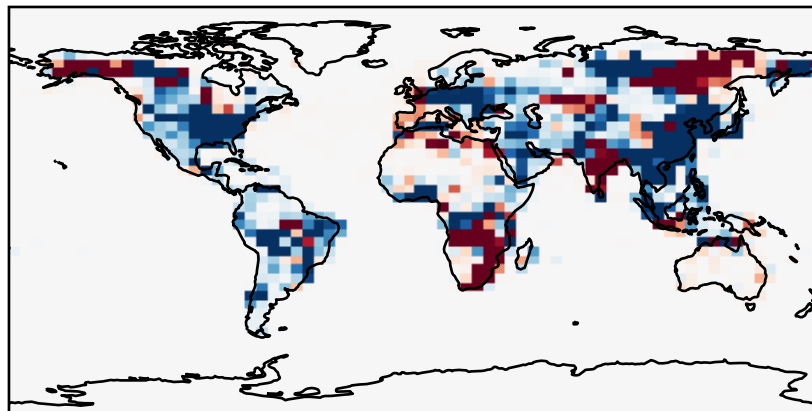
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



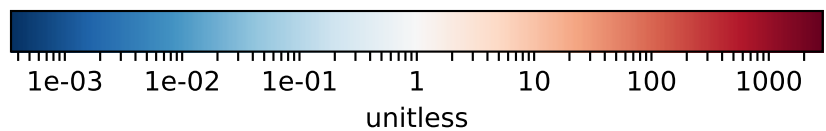
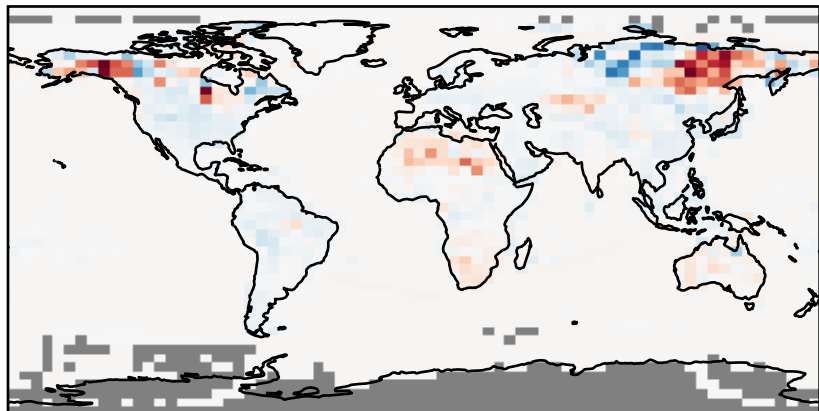
Difference
Dev - Ref, Dynamic Range



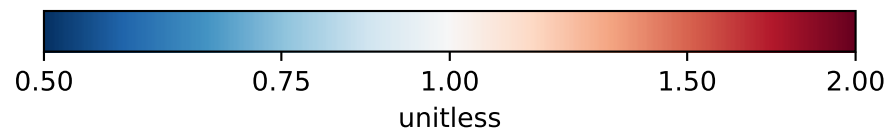
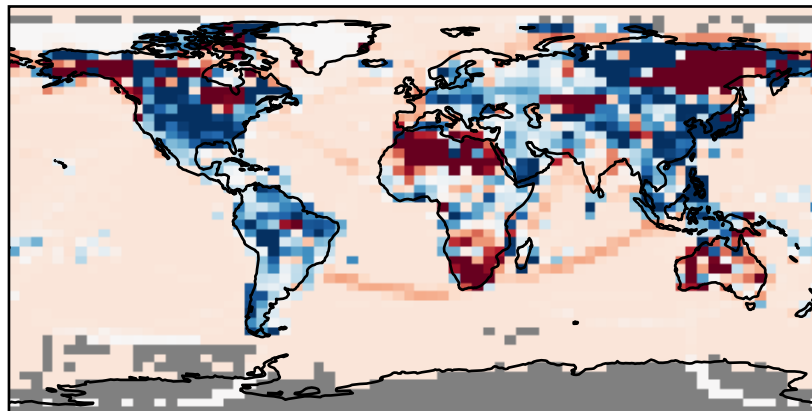
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

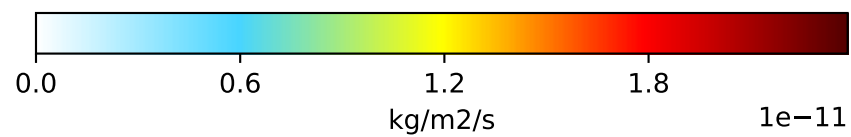
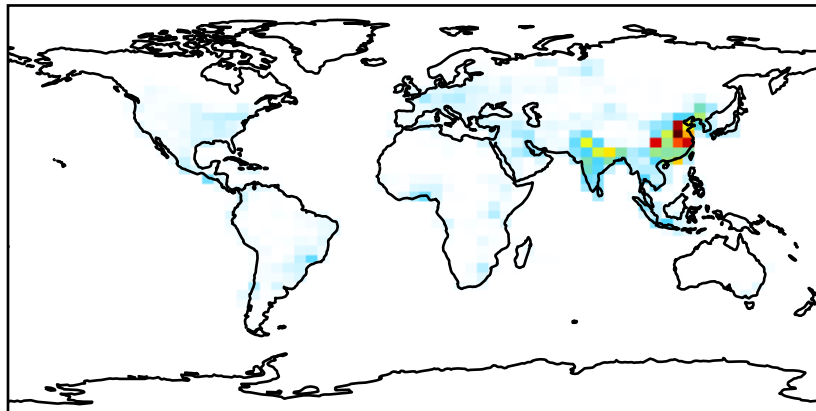


Ratio
Dev/Ref, Fixed Range

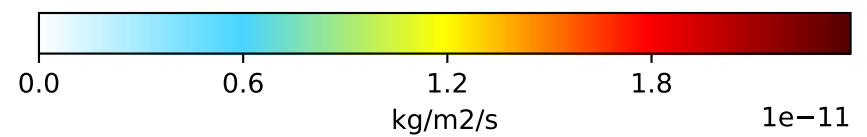
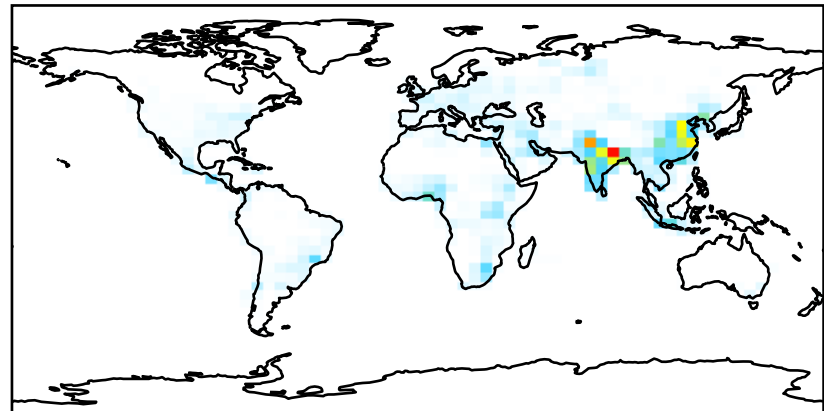


EmisBCPO_Anthro (Jul2019)

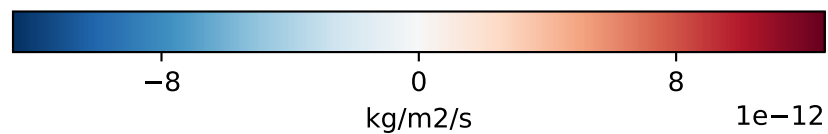
GCC_12.9.0 (Ref)
4.0x5.0



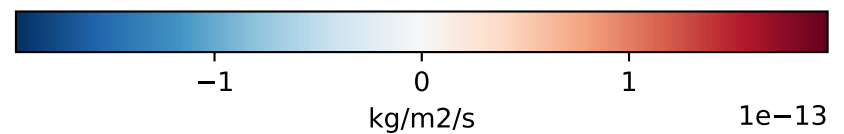
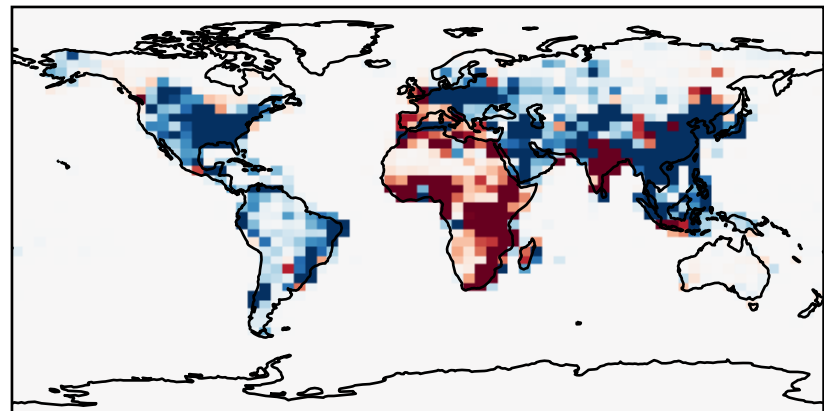
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



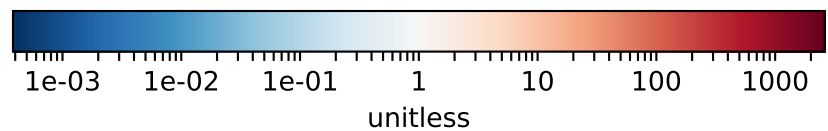
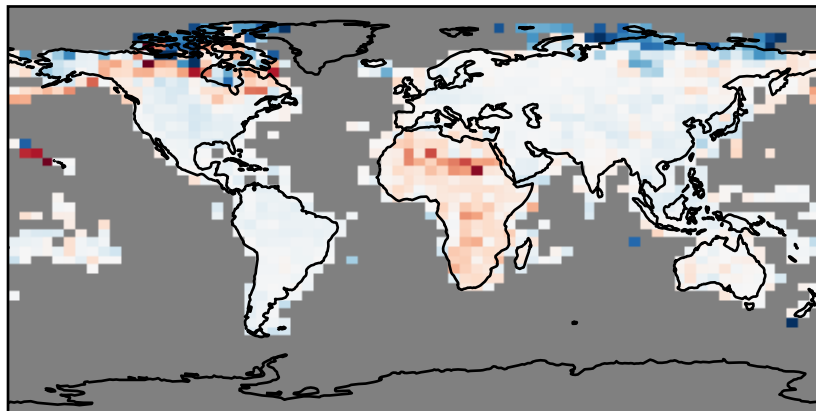
Difference
Dev - Ref, Dynamic Range



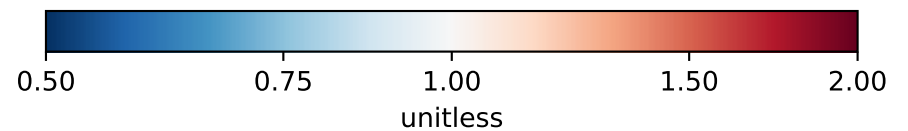
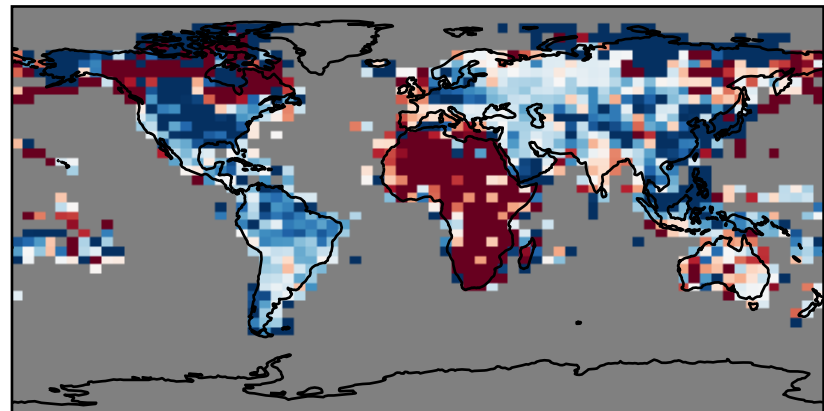
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

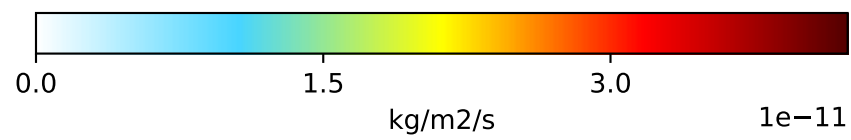
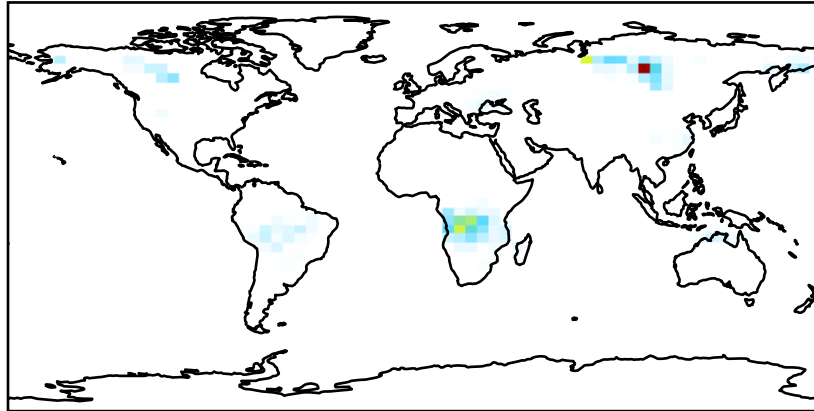


Ratio
Dev/Ref, Fixed Range

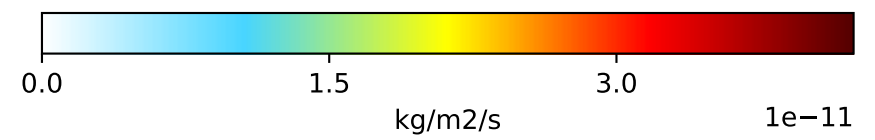
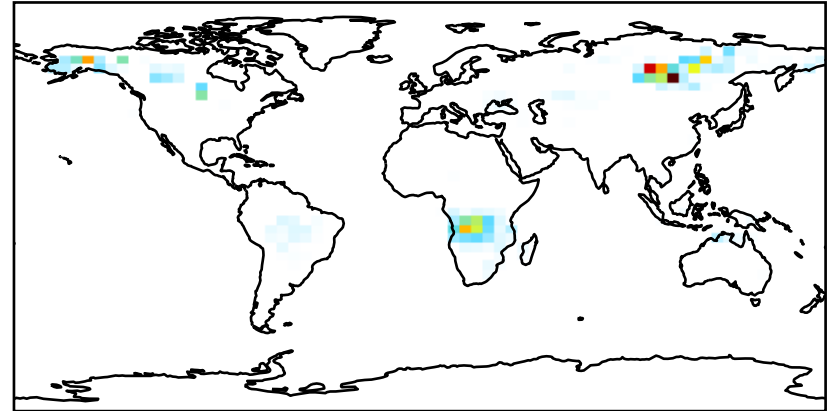


EmisBCPO_BioBurn (Jul2019)

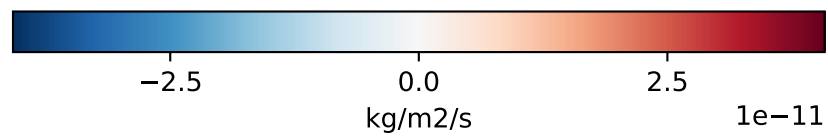
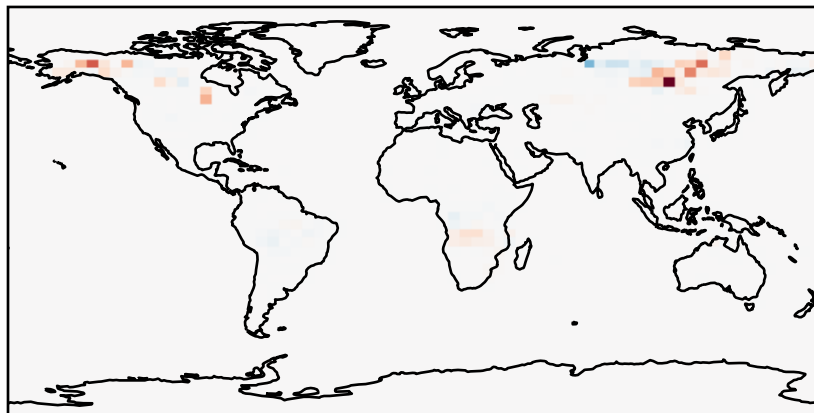
GCC_12.9.0 (Ref)
4.0x5.0



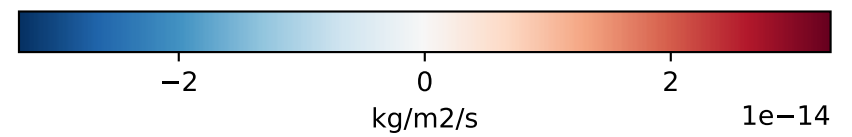
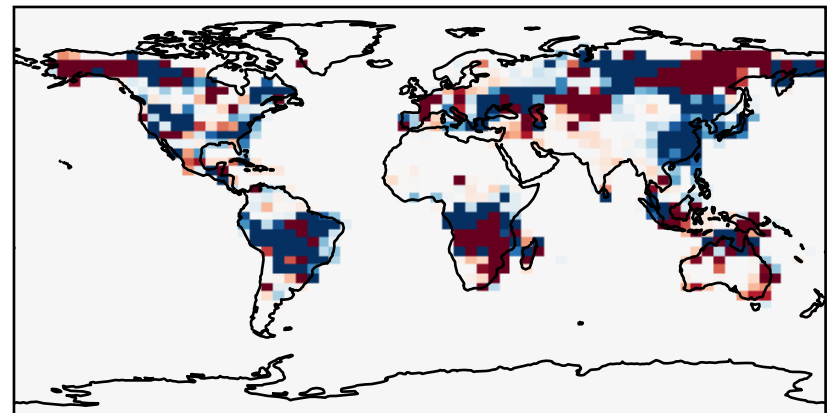
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



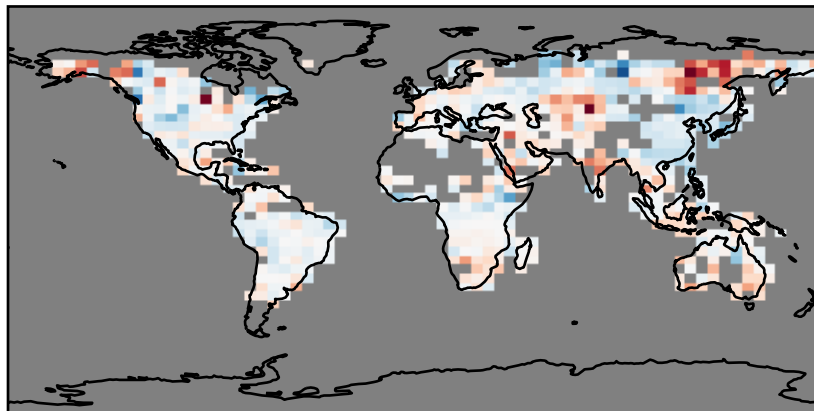
Difference
Dev - Ref, Dynamic Range



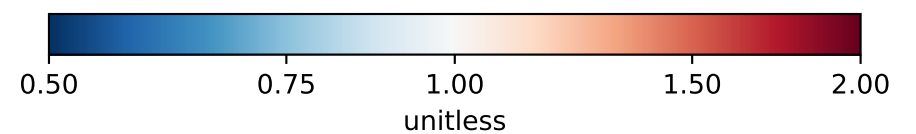
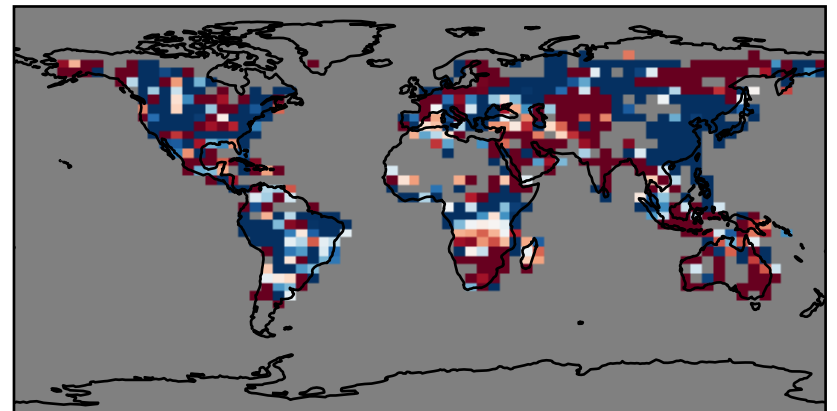
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

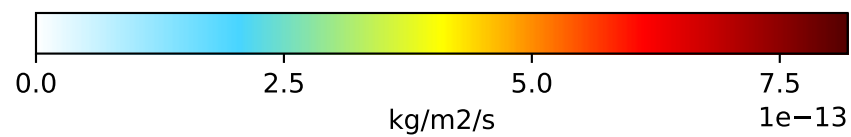
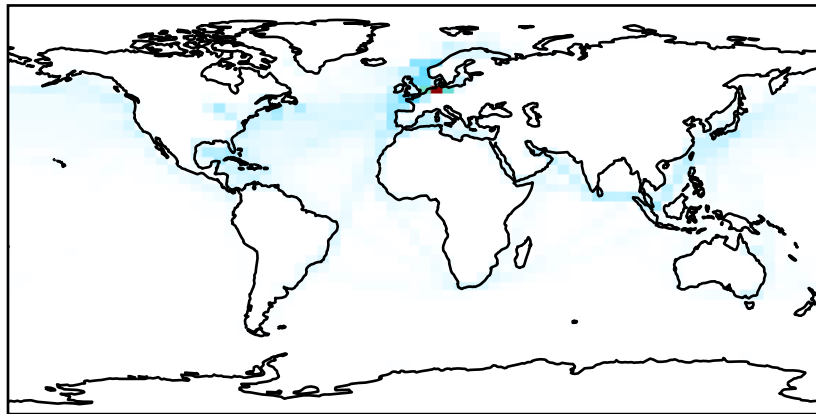


Ratio
Dev/Ref, Fixed Range

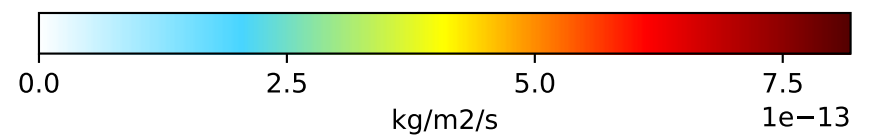
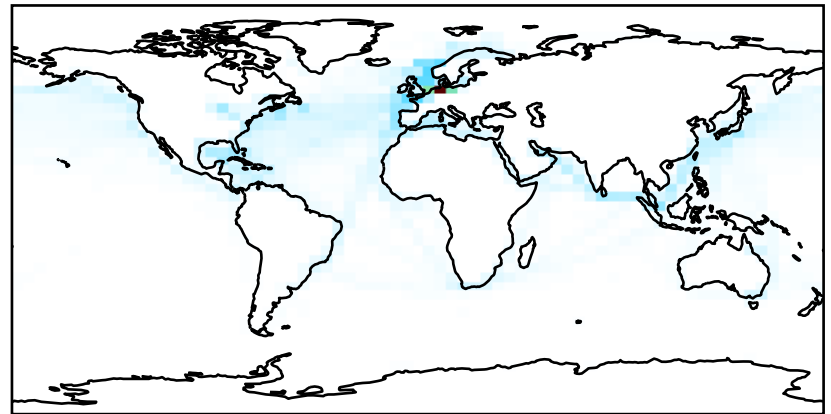


EmisBCPO_Ship (Jul2019)

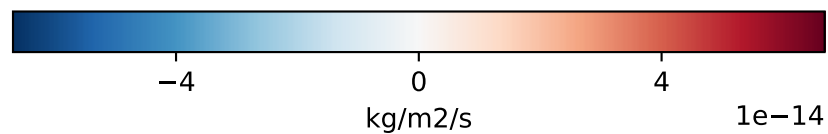
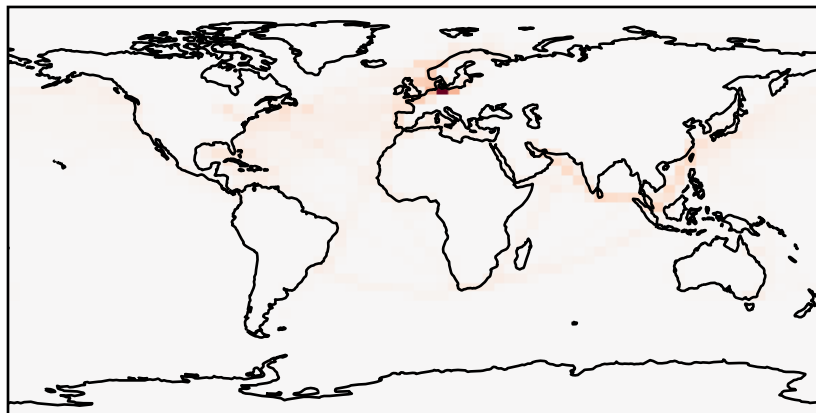
GCC_12.9.0 (Ref)
4.0x5.0



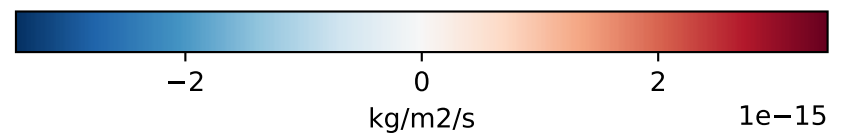
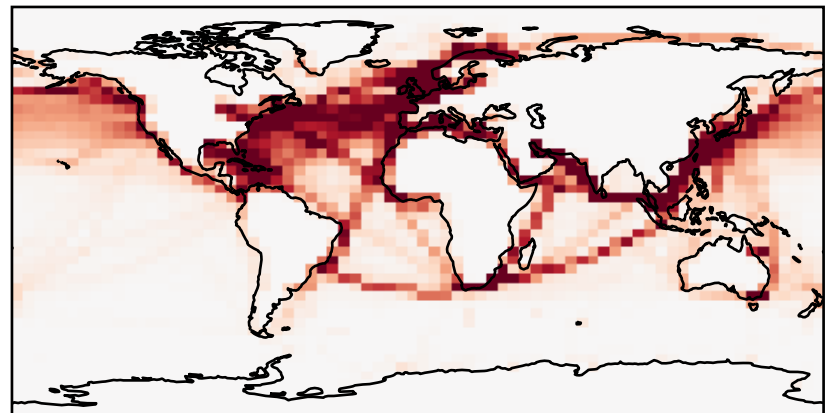
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



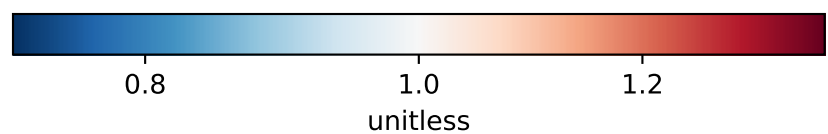
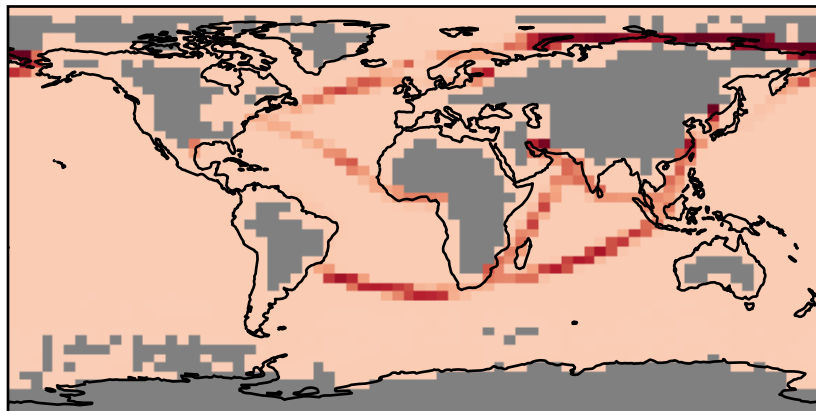
Difference
Dev - Ref, Dynamic Range



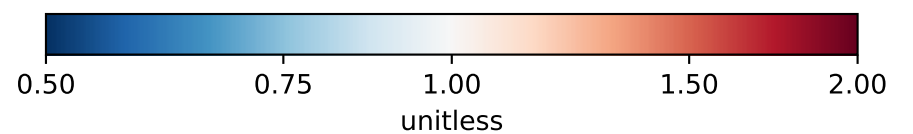
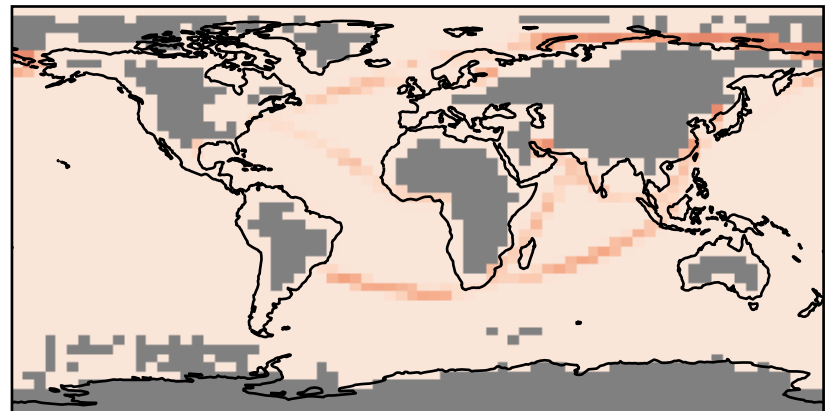
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

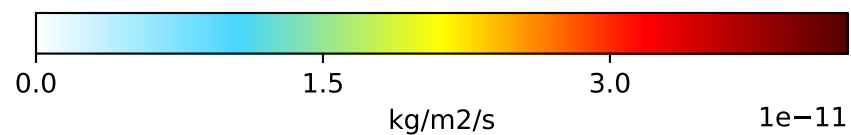
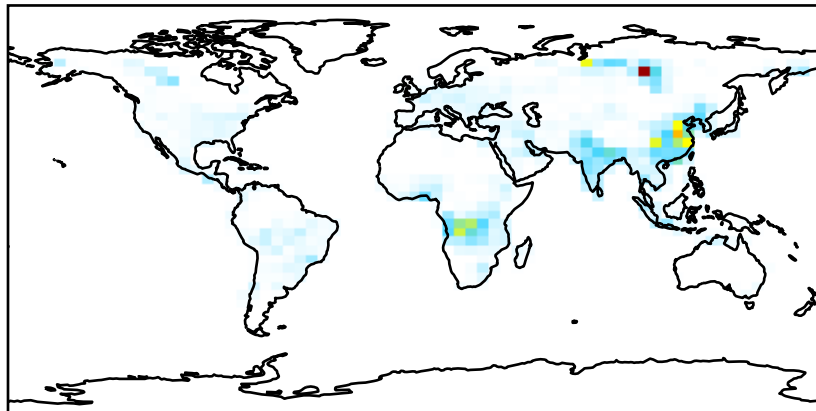


Ratio
Dev/Ref, Fixed Range

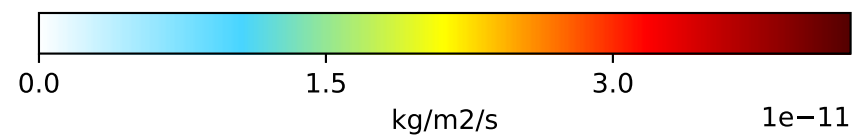
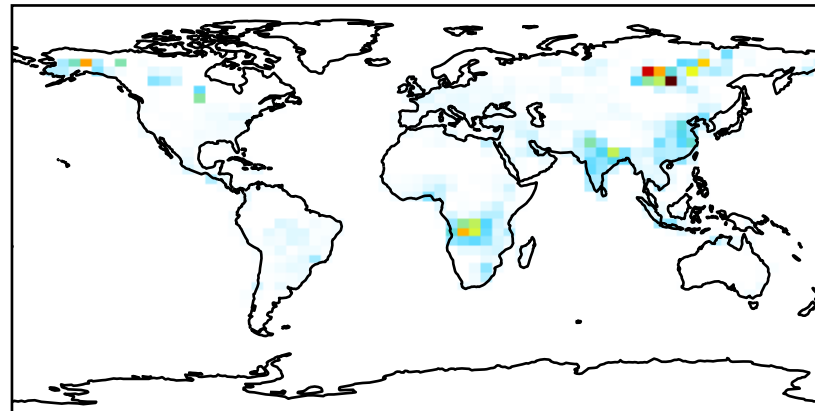


EmisBCPO_Total (Jul2019)

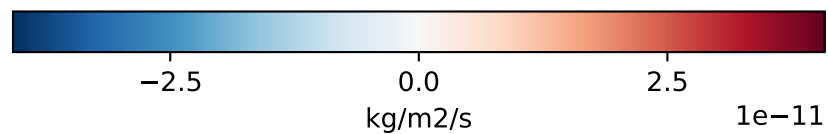
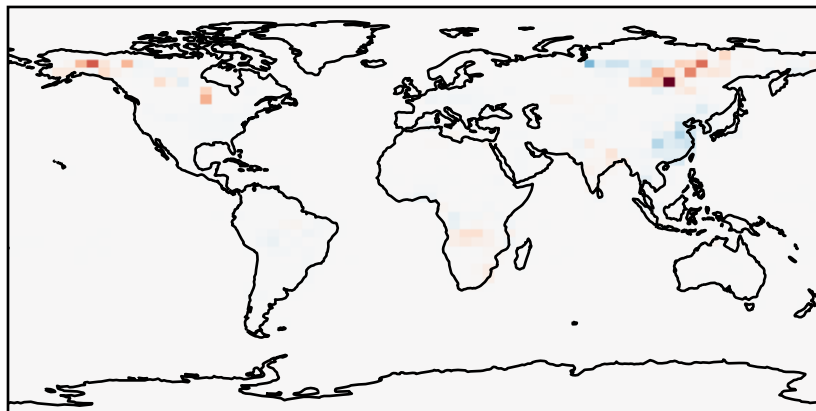
GCC_12.9.0 (Ref)
4.0x5.0



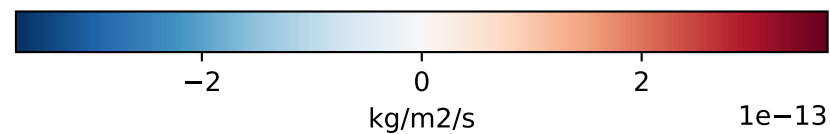
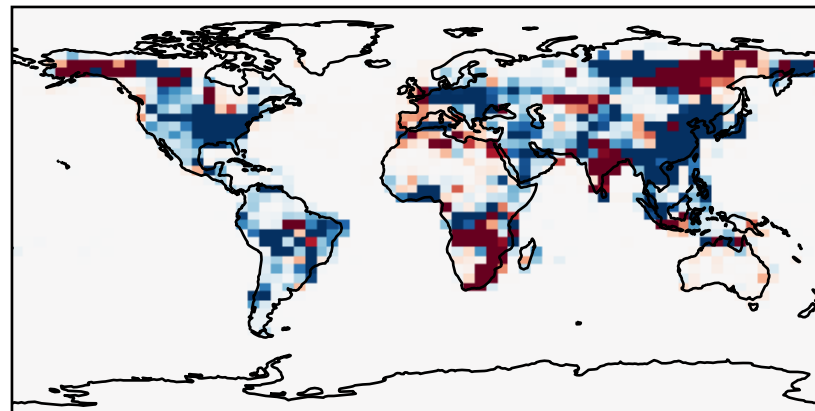
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



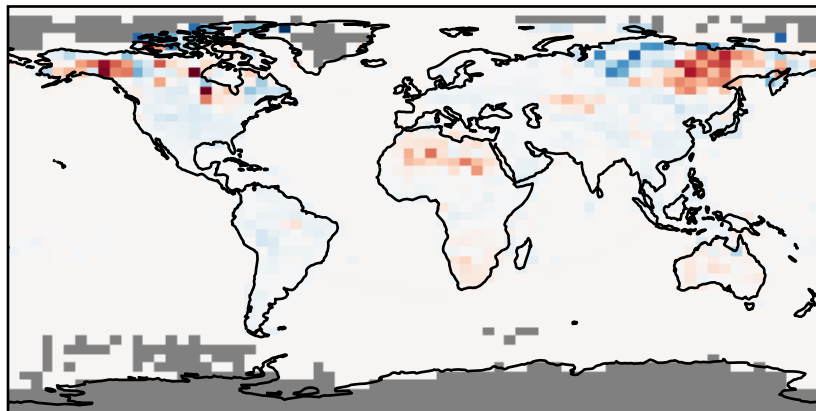
Difference
Dev - Ref, Dynamic Range



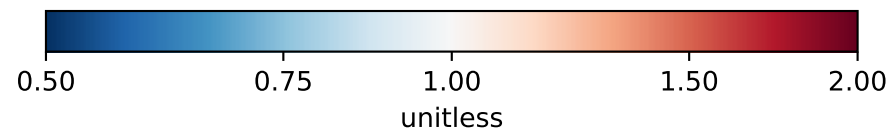
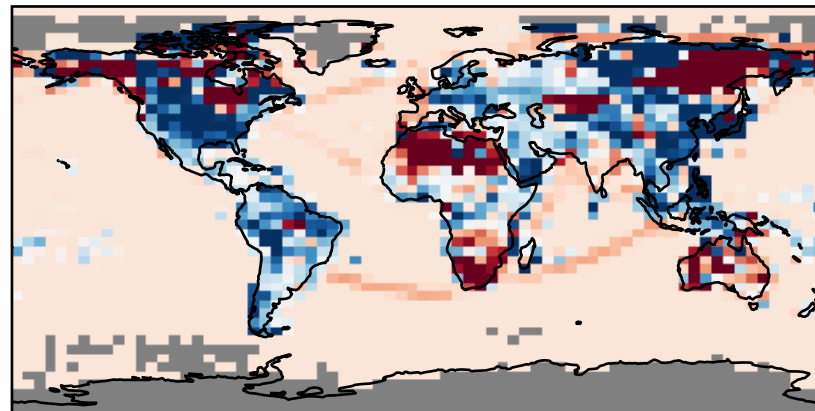
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

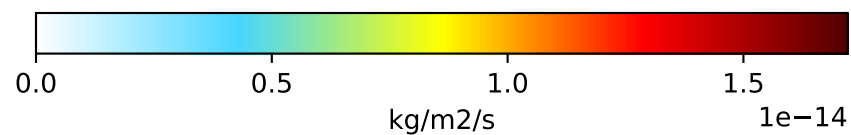
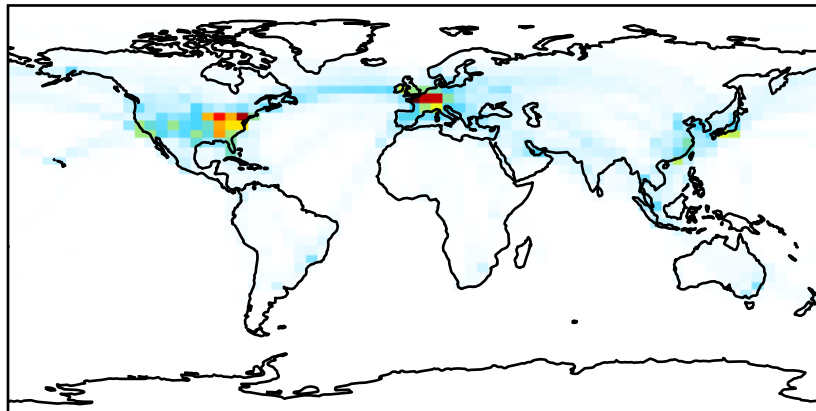


Ratio
Dev/Ref, Fixed Range

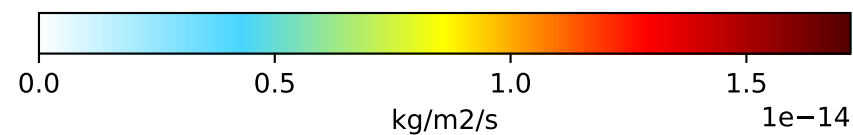
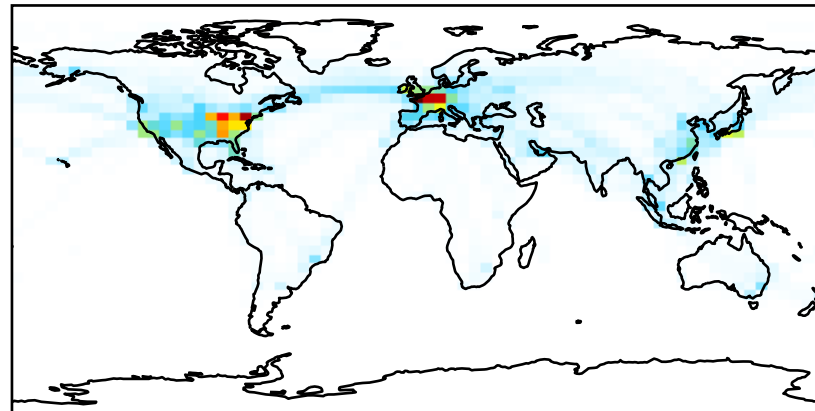


EmisOCPI_Aircraft (Jul2019)

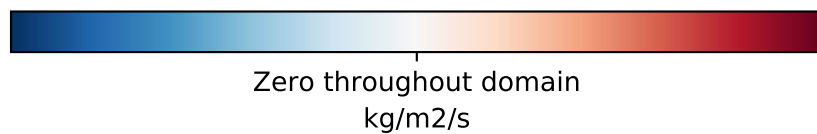
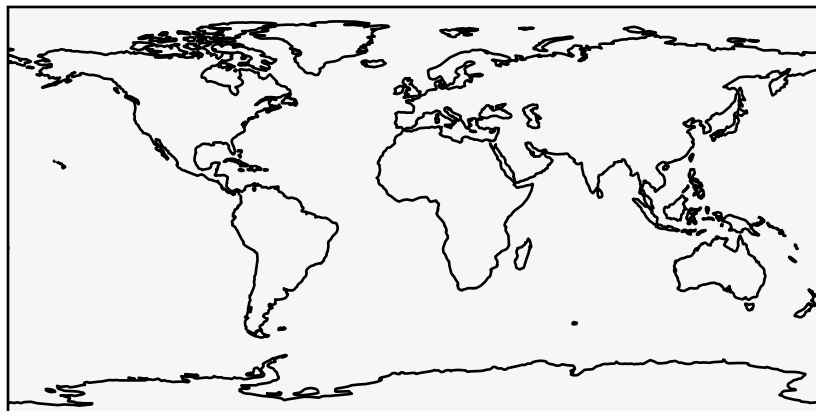
GCC_12.9.0 (Ref)
4.0x5.0



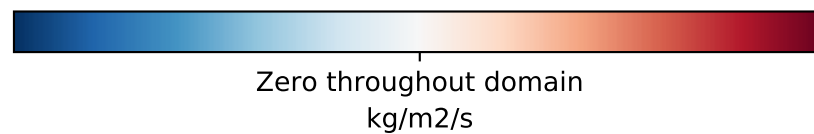
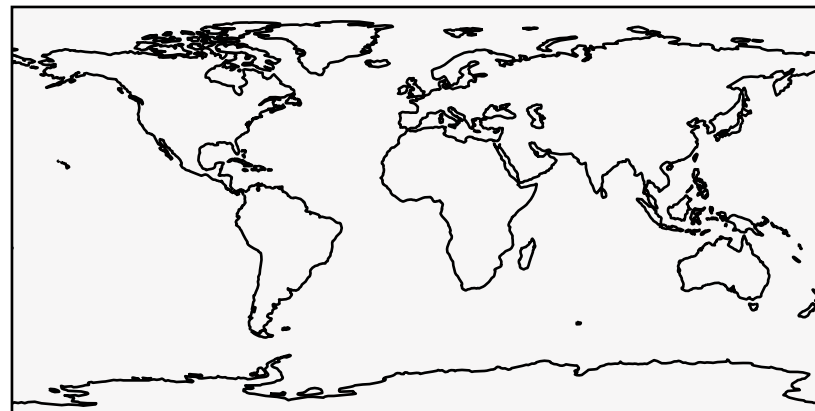
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



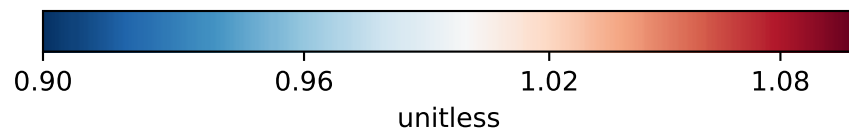
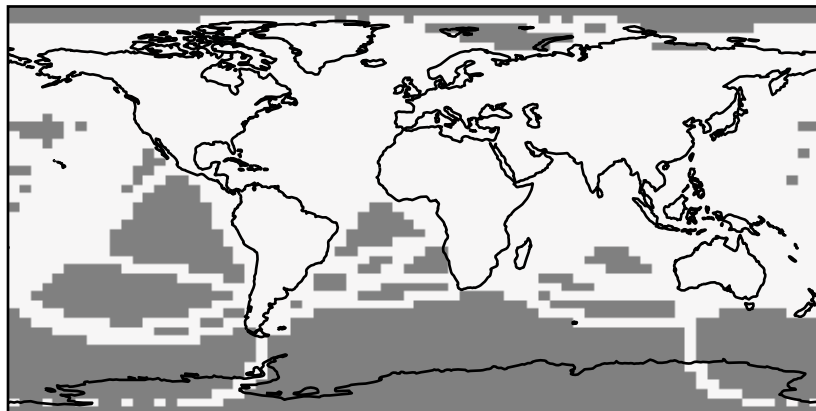
Difference
Dev - Ref, Dynamic Range



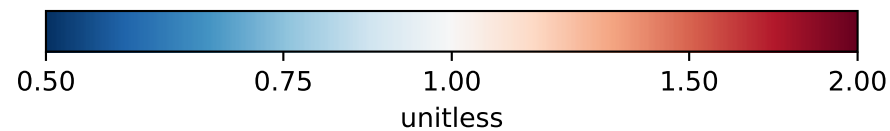
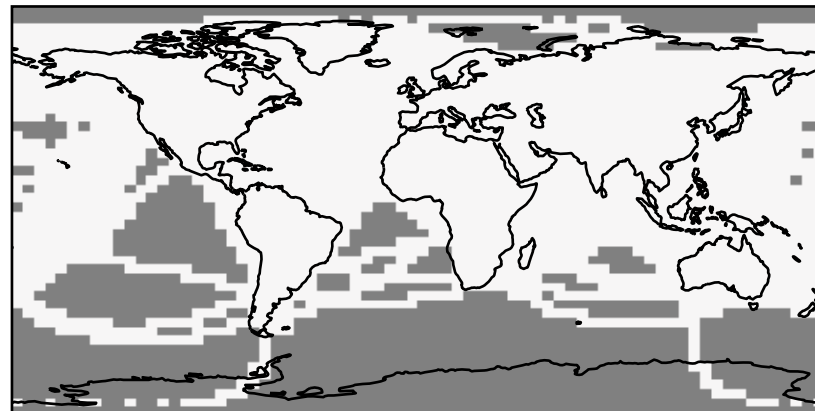
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

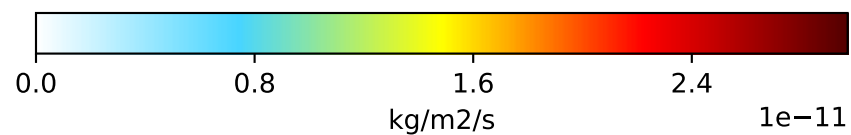
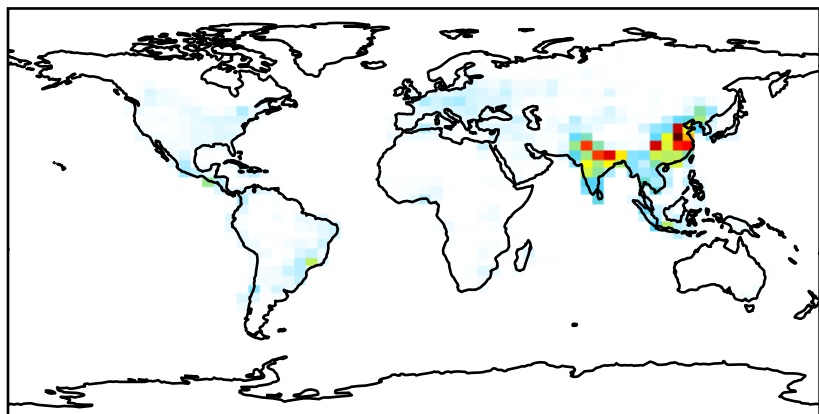


Ratio
Dev/Ref, Fixed Range

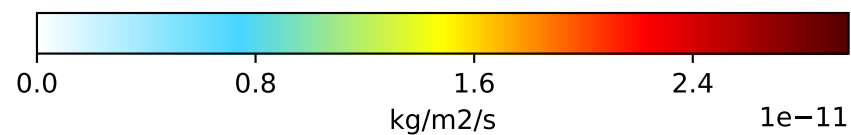
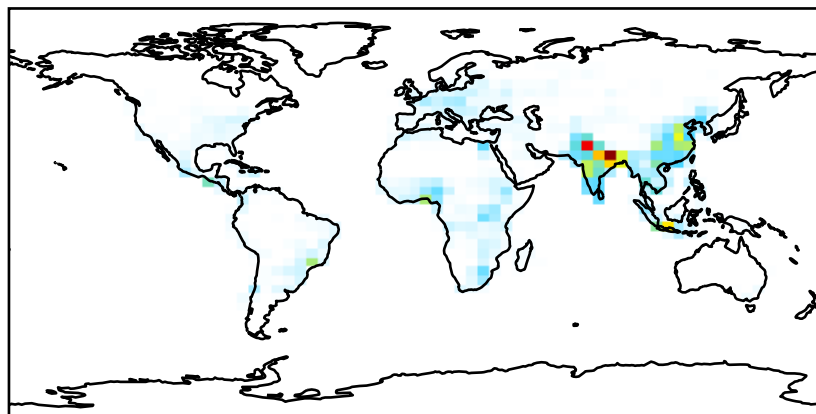


EmisOCPI_Anthro (Jul2019)

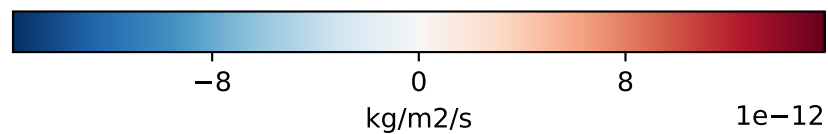
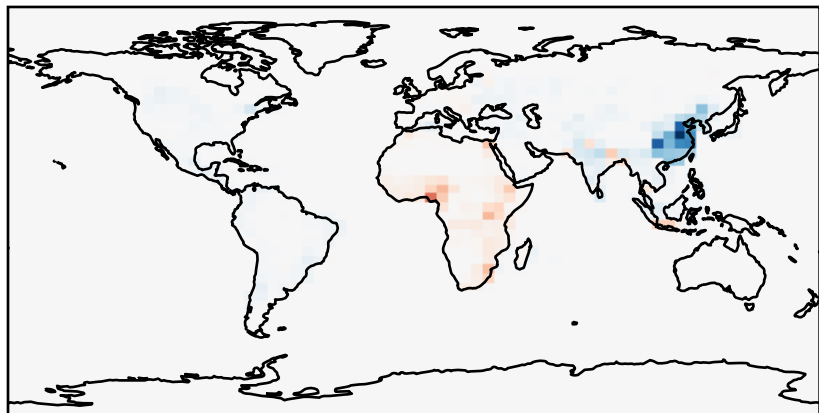
GCC_12.9.0 (Ref)
4.0x5.0



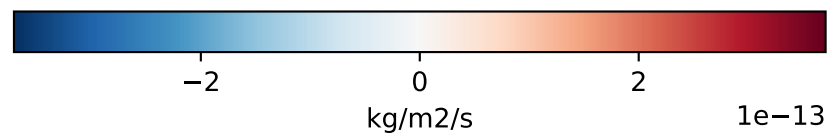
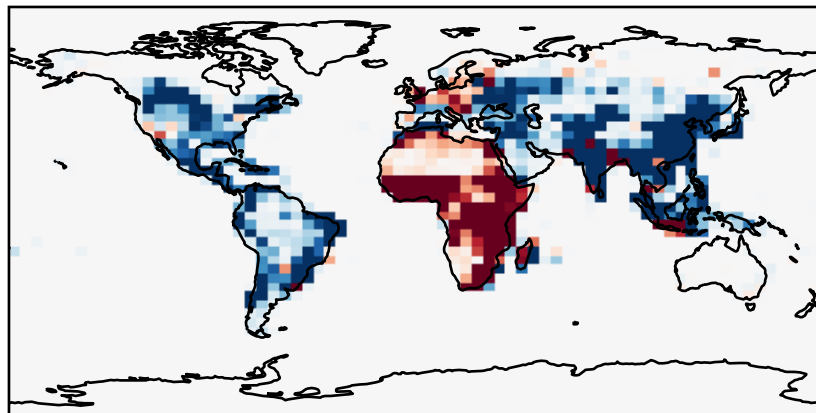
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



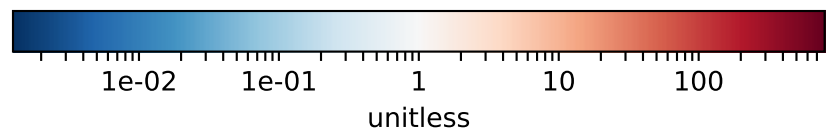
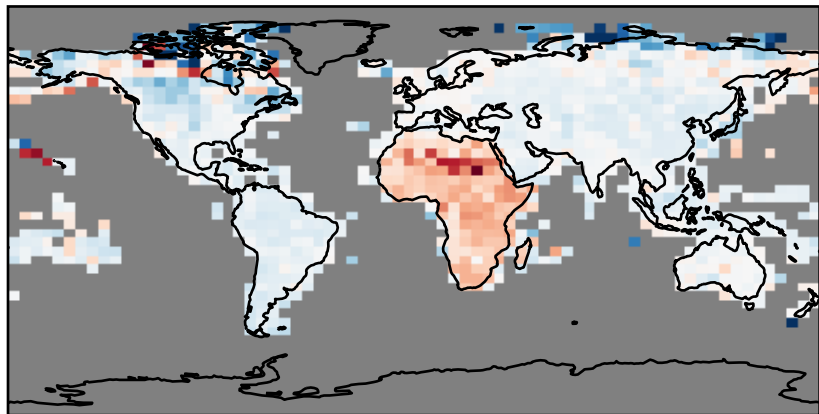
Difference
Dev - Ref, Dynamic Range



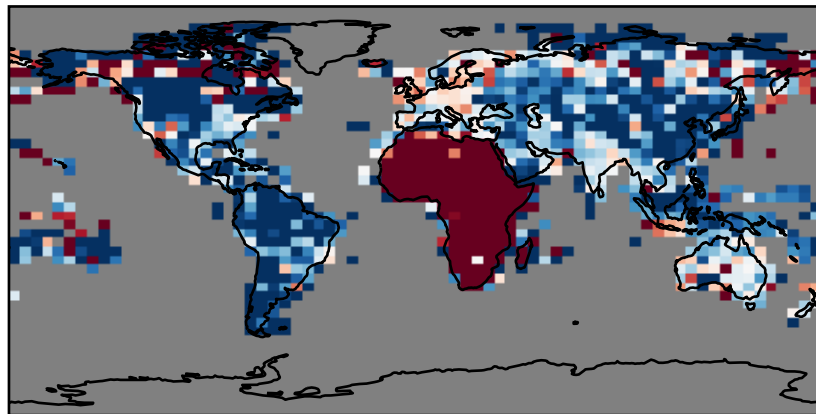
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

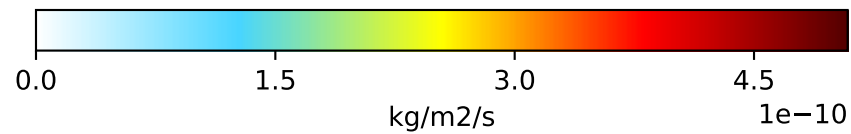
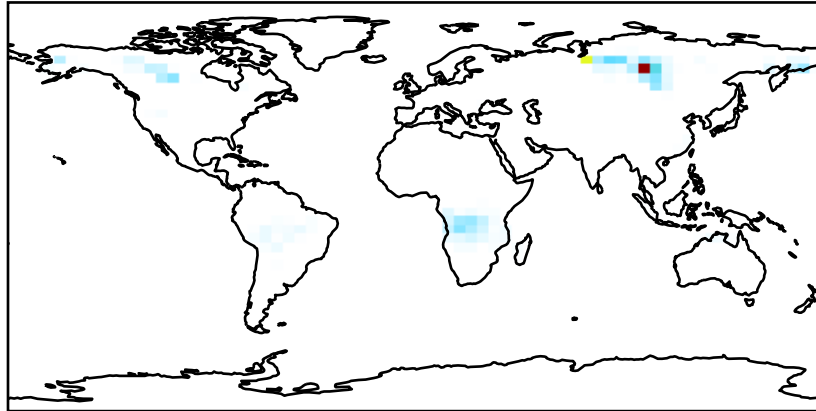


Ratio
Dev/Ref, Fixed Range

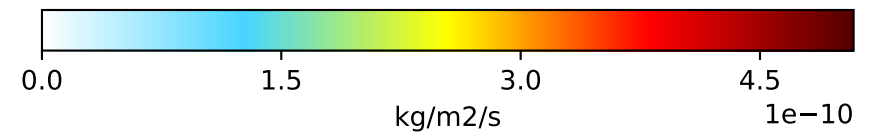
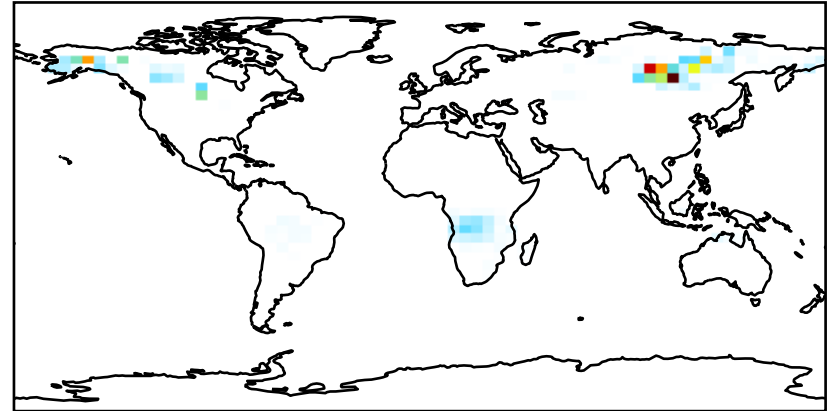


EmisOCPI_BioBurn (Jul2019)

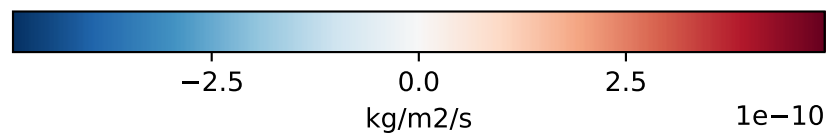
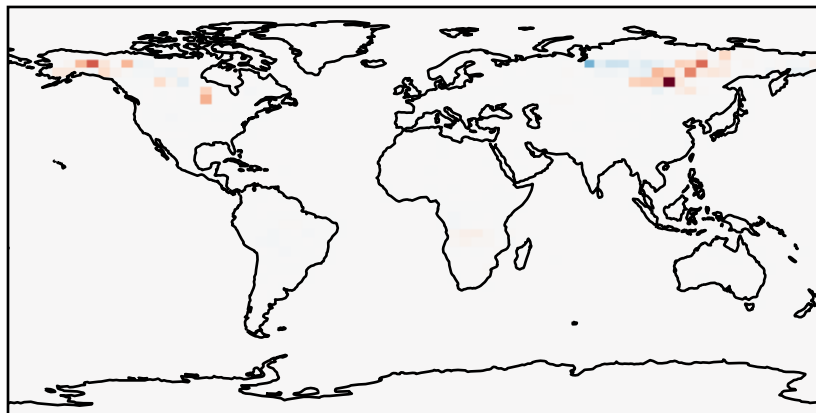
GCC_12.9.0 (Ref)
4.0x5.0



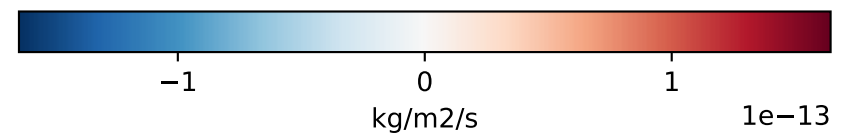
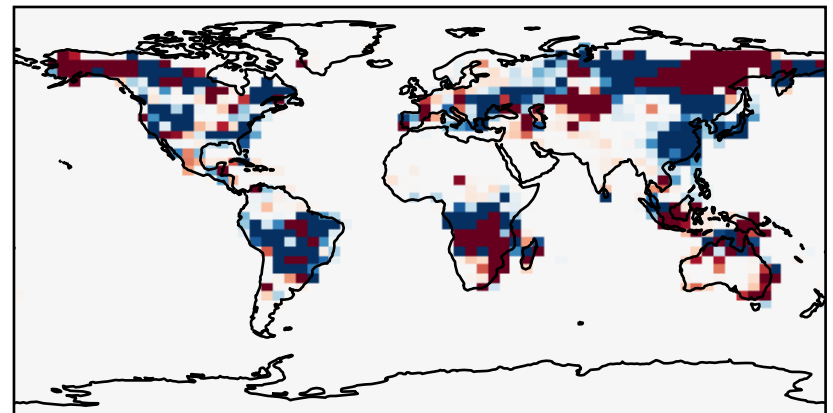
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



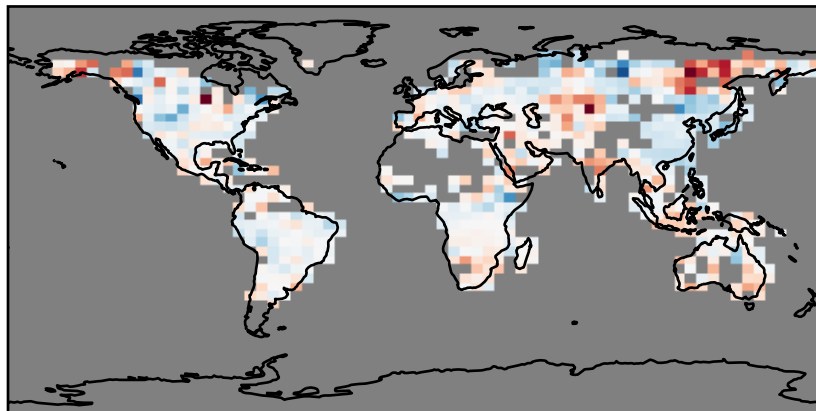
Difference
Dev - Ref, Dynamic Range



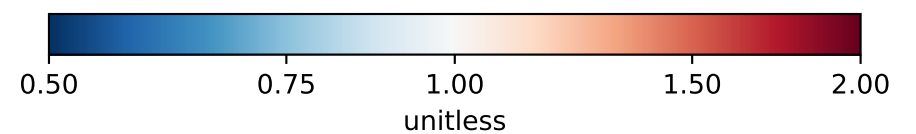
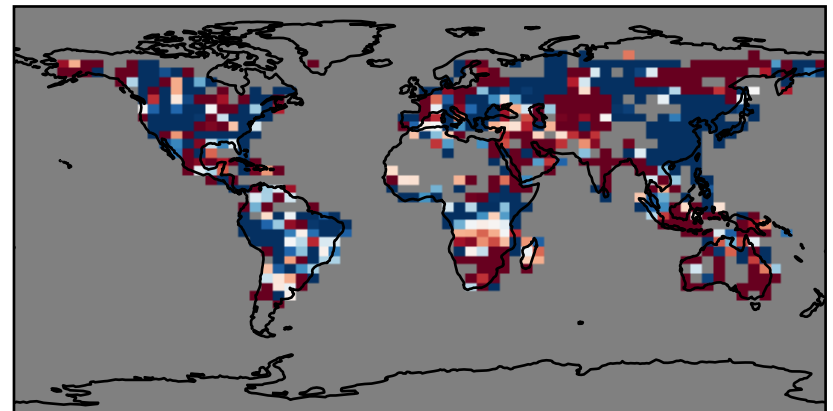
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

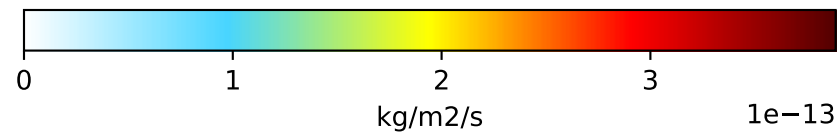
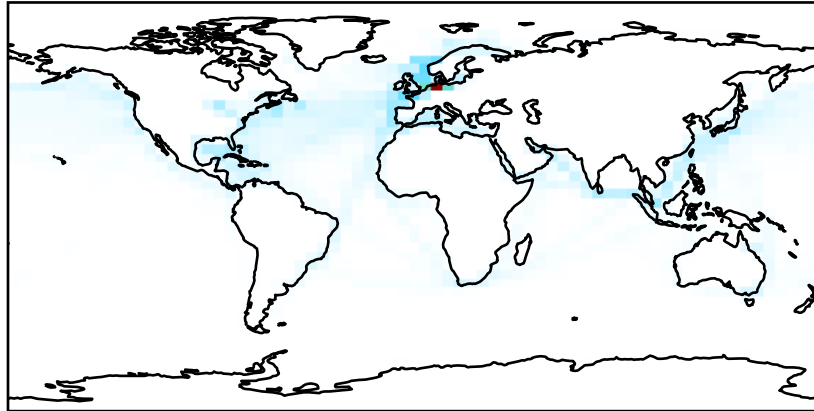


Ratio
Dev/Ref, Fixed Range

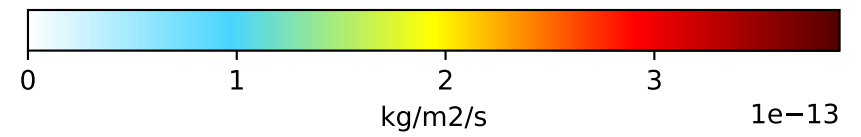
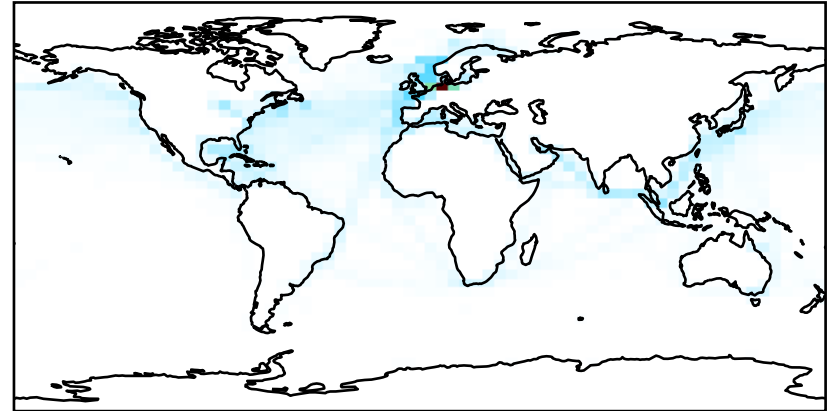


EmisOCPI_Ship (Jul2019)

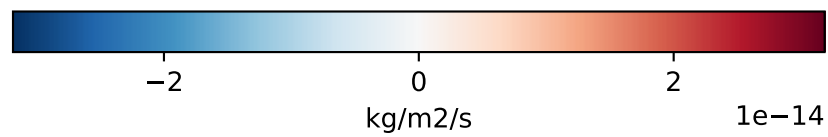
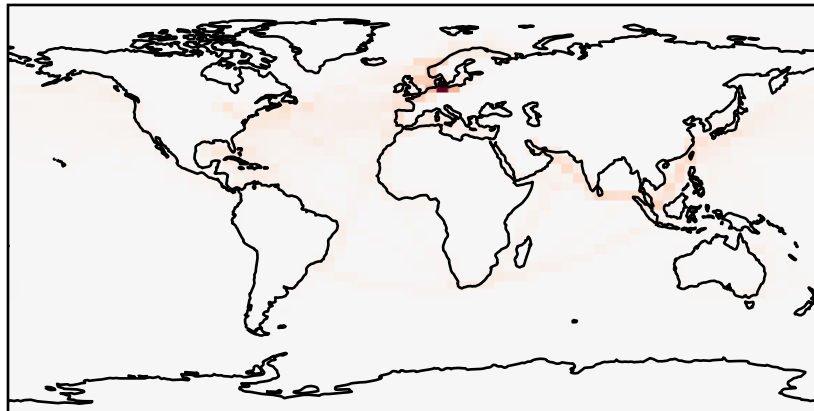
GCC_12.9.0 (Ref)
4.0x5.0



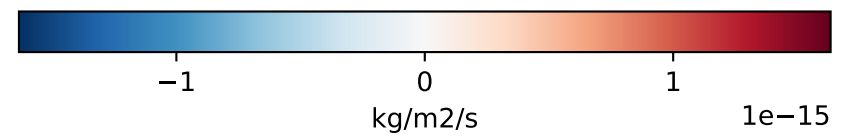
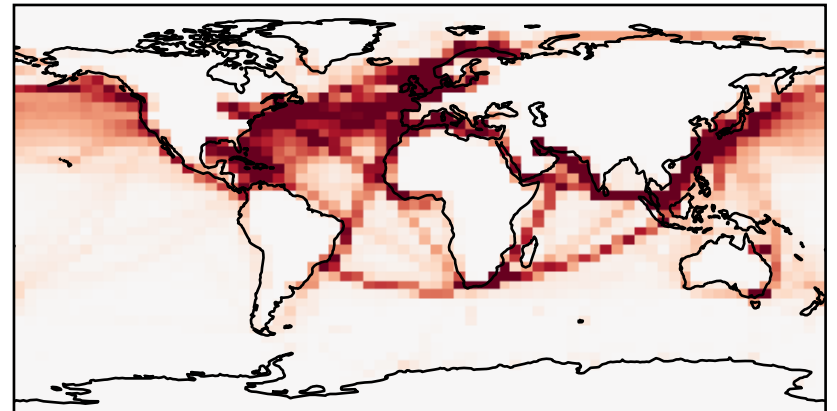
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



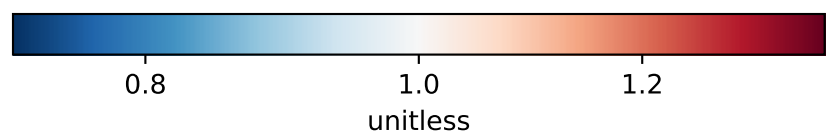
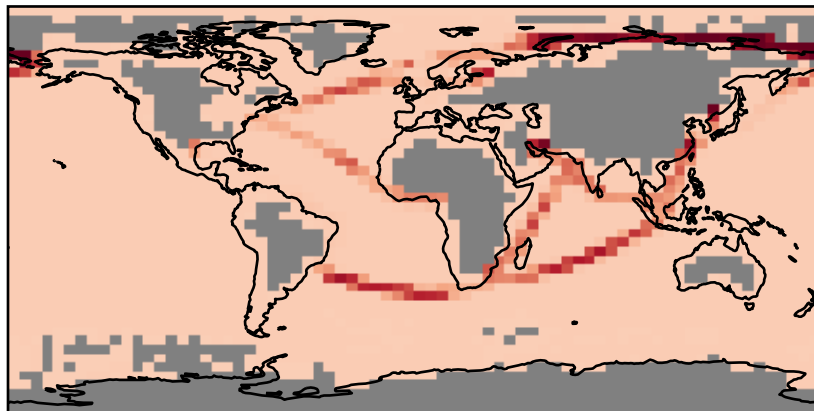
Difference
Dev - Ref, Dynamic Range



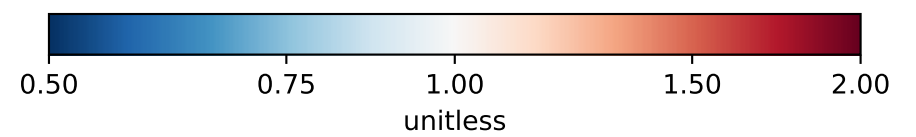
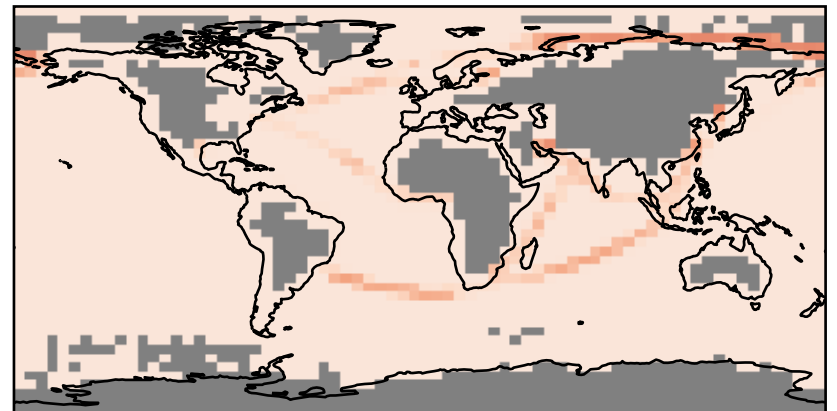
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

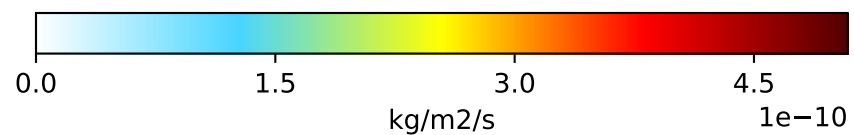
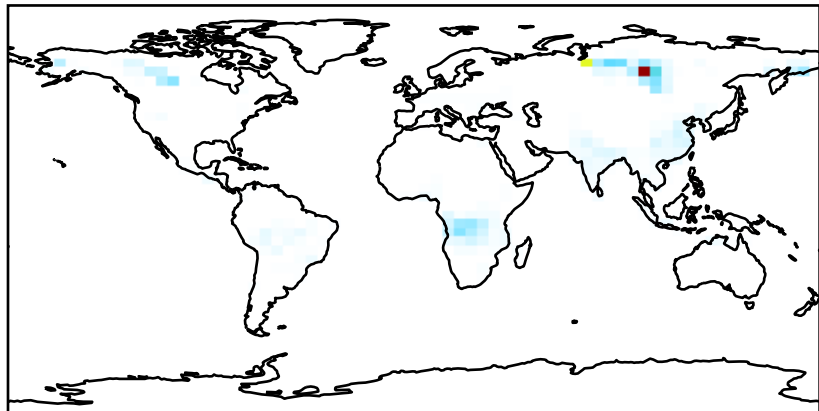


Ratio
Dev/Ref, Fixed Range

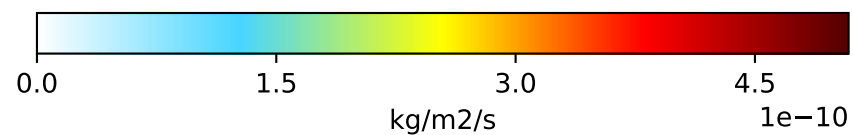
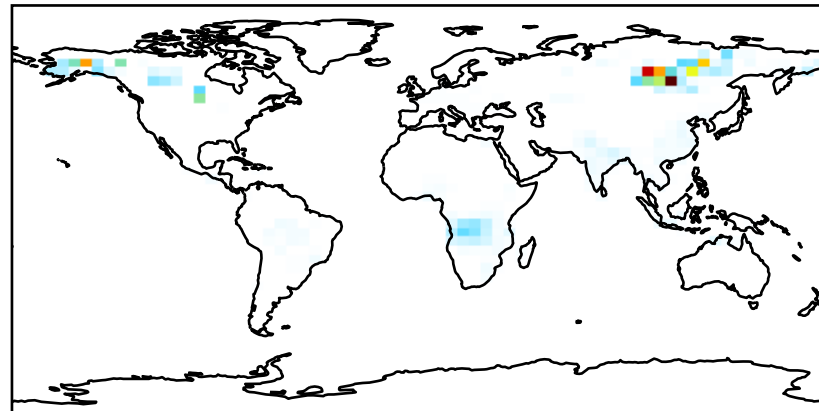


EmisOCPI_Total (Jul2019)

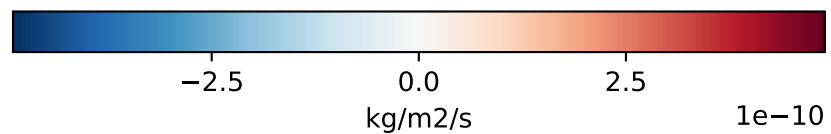
GCC_12.9.0 (Ref)
4.0x5.0



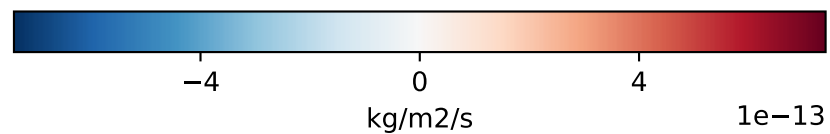
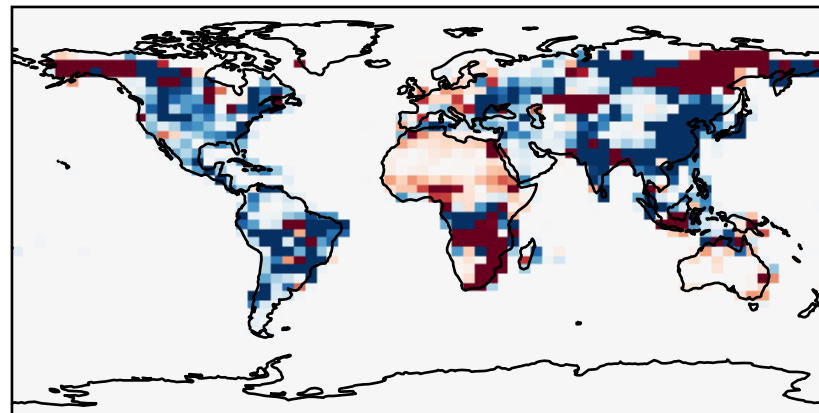
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



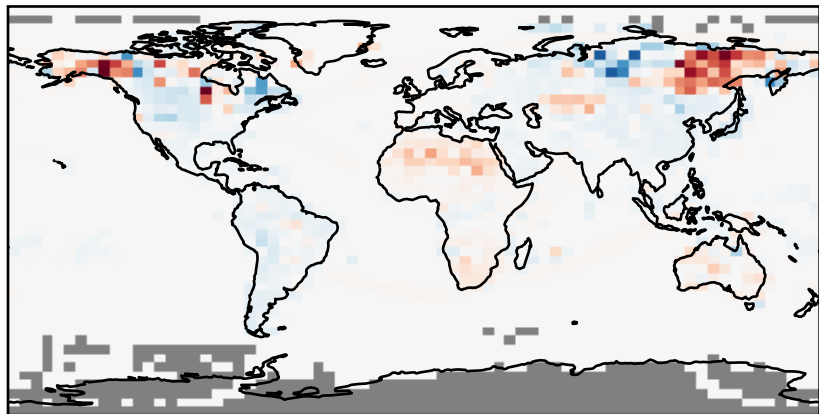
Difference
Dev - Ref, Dynamic Range



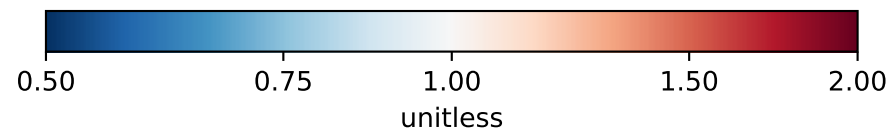
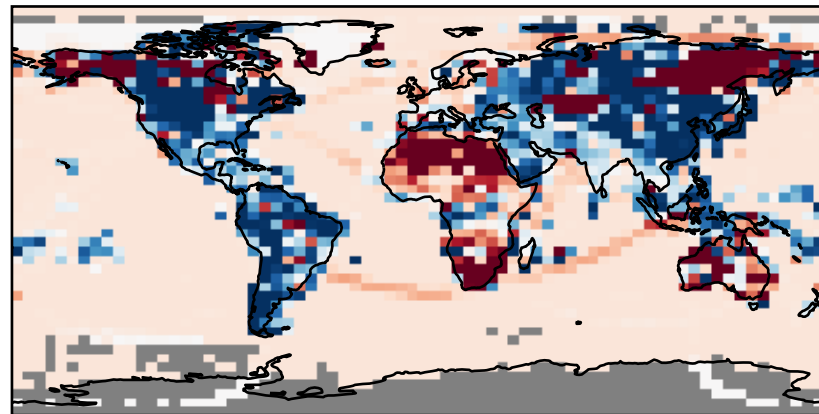
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

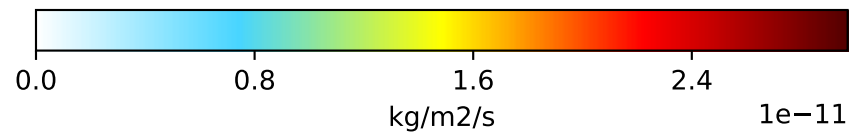
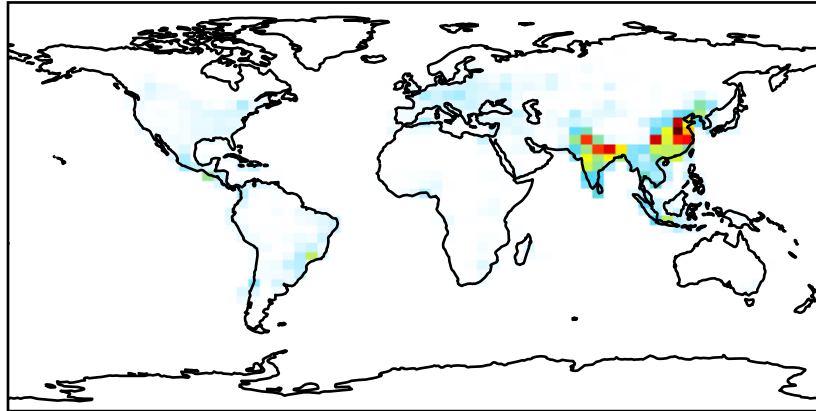


Ratio
Dev/Ref, Fixed Range

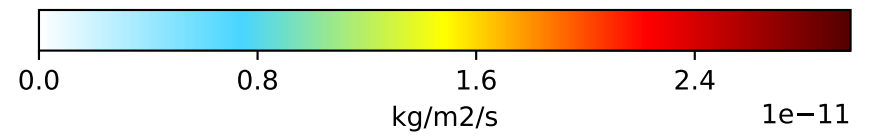
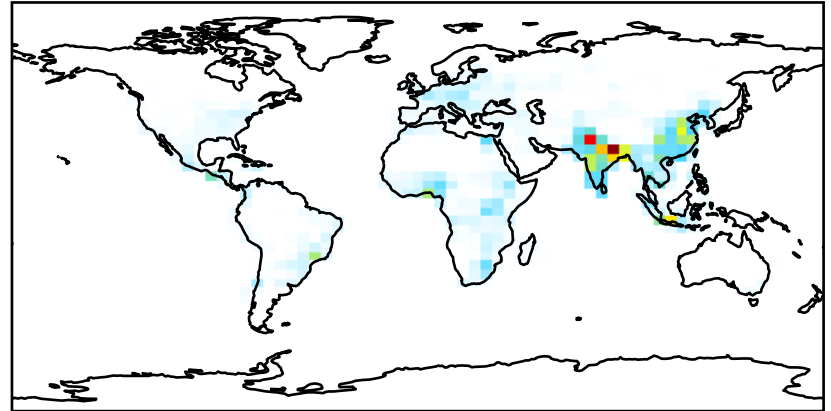


EmisOCPO_Anthro (Jul2019)

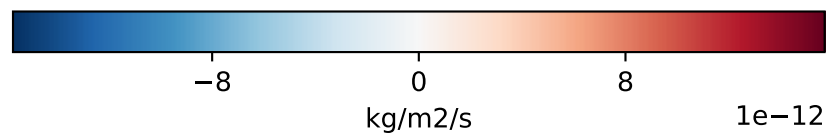
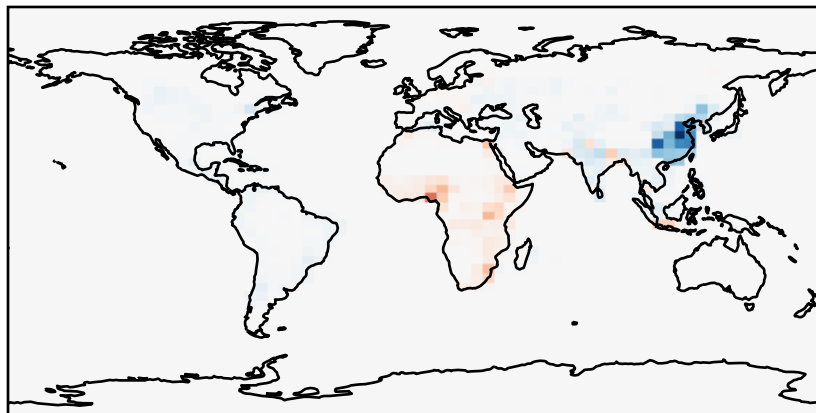
GCC_12.9.0 (Ref)
4.0x5.0



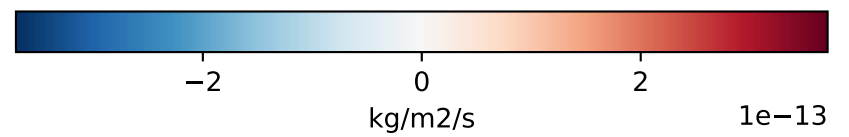
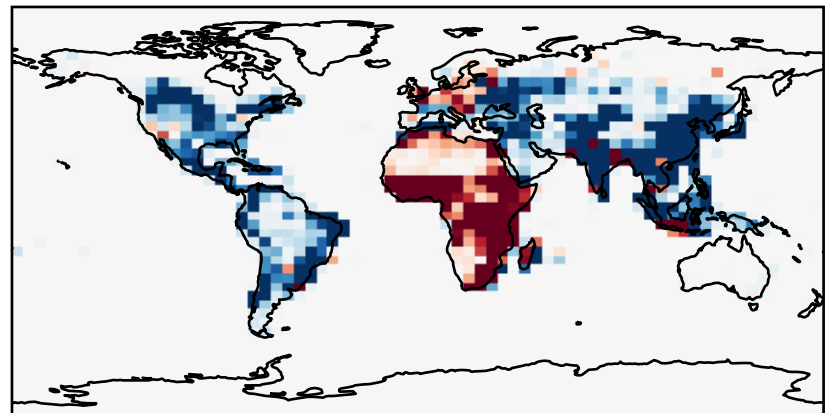
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



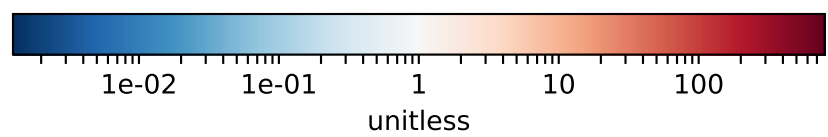
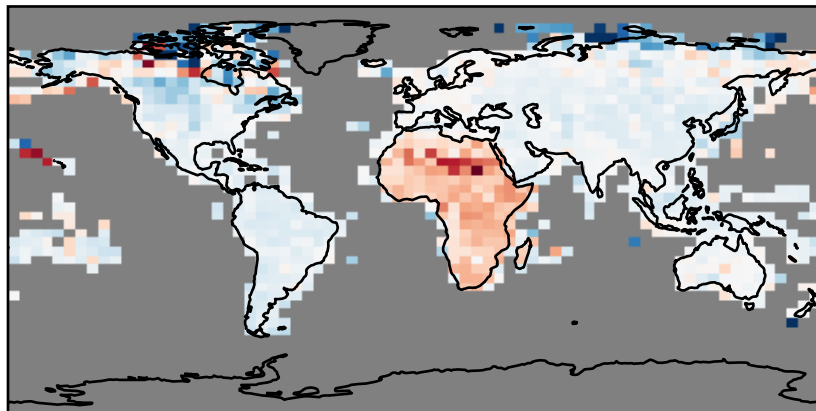
Difference
Dev - Ref, Dynamic Range



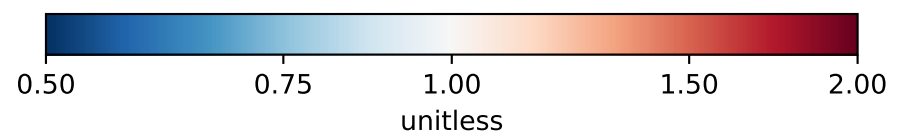
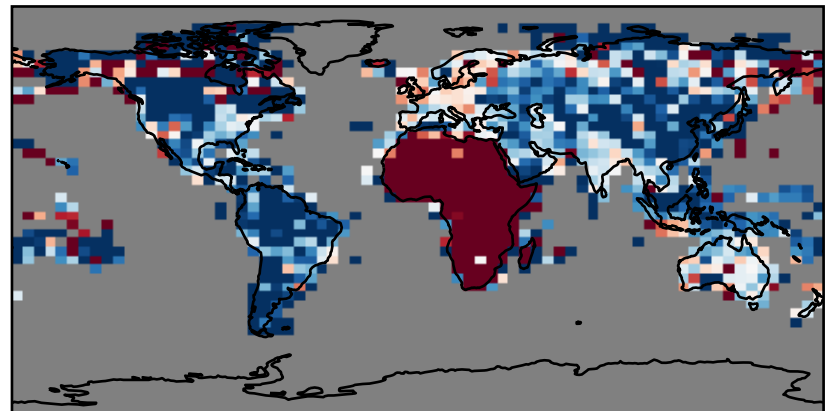
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

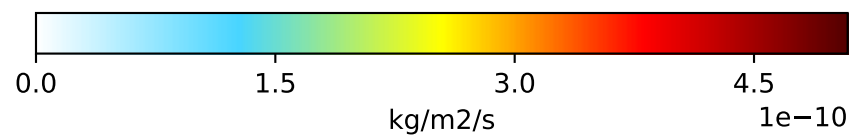
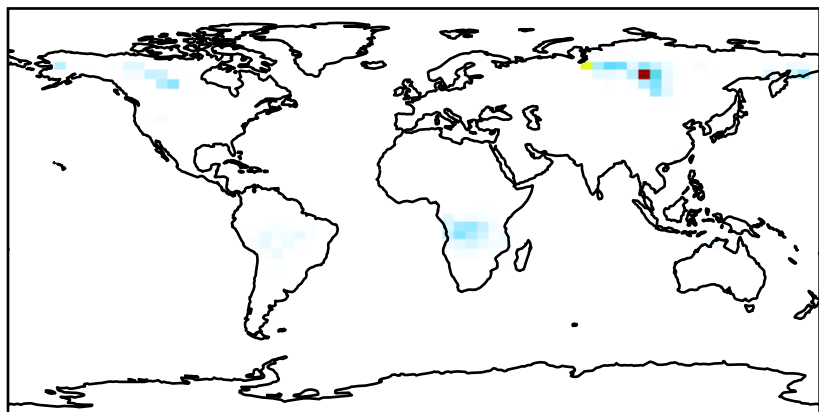


Ratio
Dev/Ref, Fixed Range

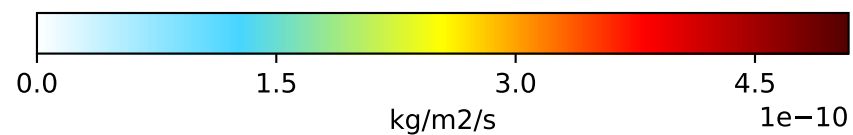
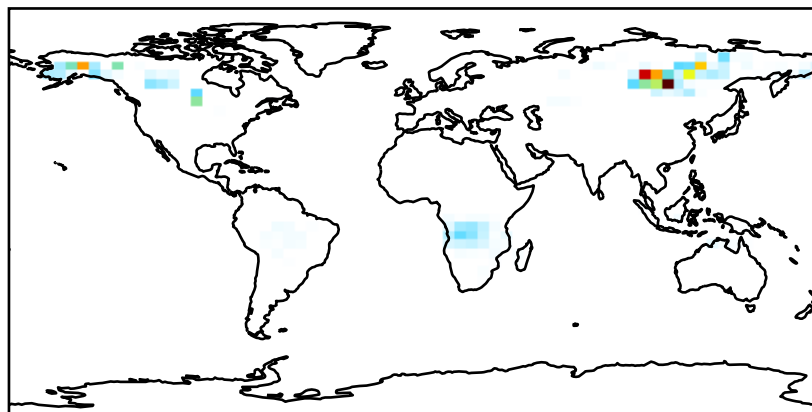


EmisOCPO_BioBurn (Jul2019)

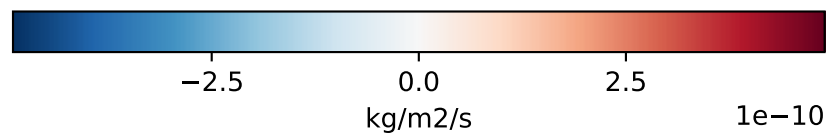
GCC_12.9.0 (Ref)
4.0x5.0



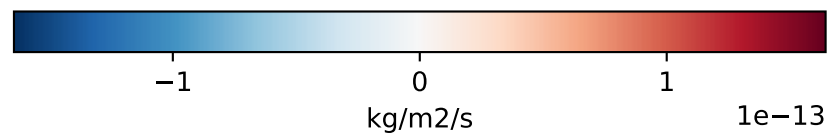
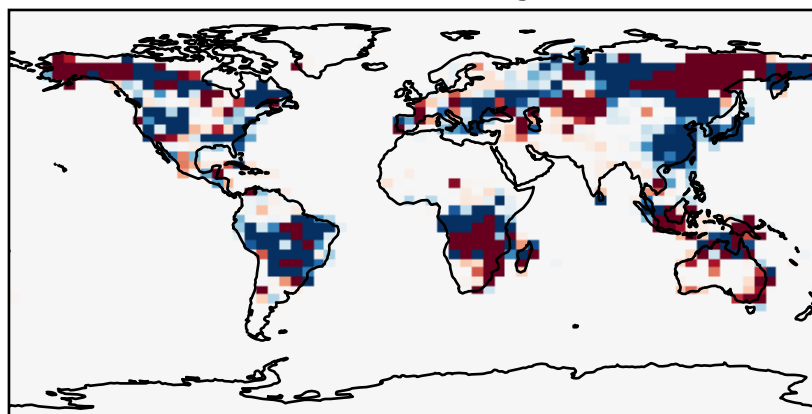
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



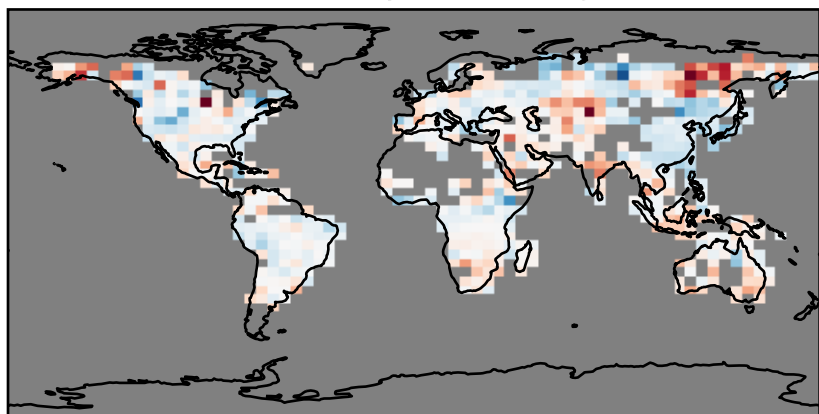
Difference
Dev - Ref, Dynamic Range



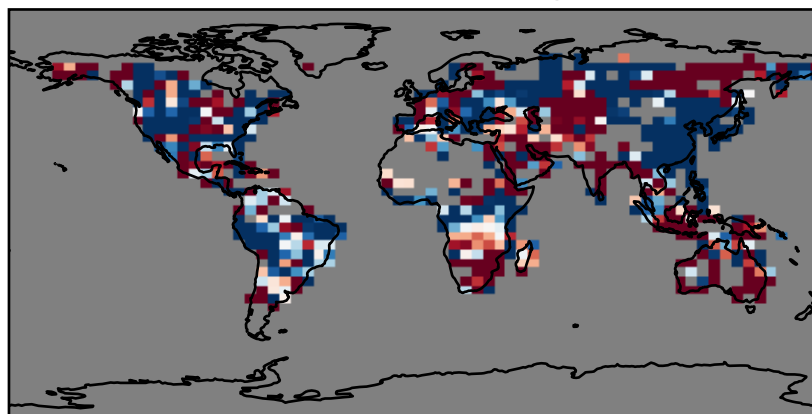
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

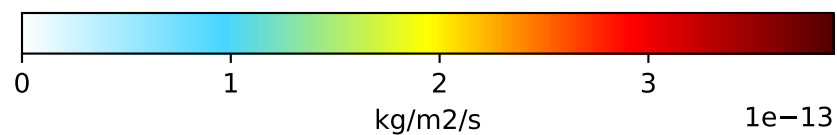
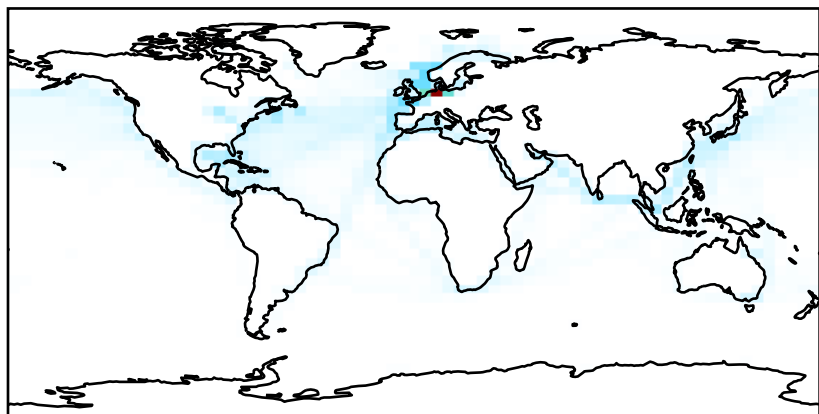


Ratio
Dev/Ref, Fixed Range

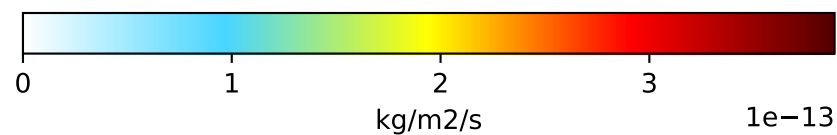
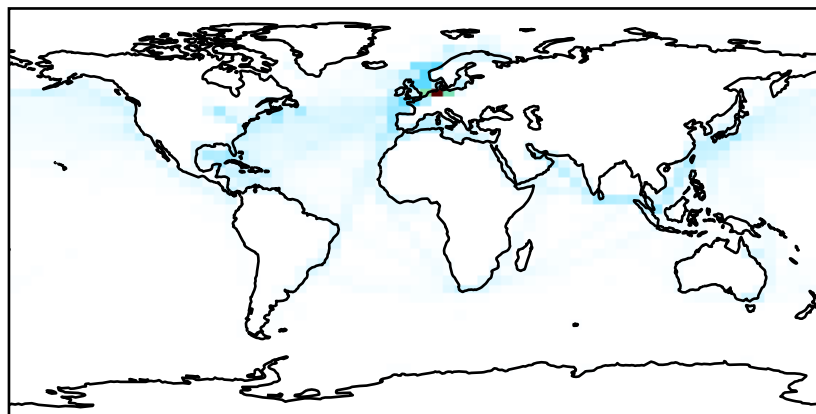


EmisOCPO_Ship (Jul2019)

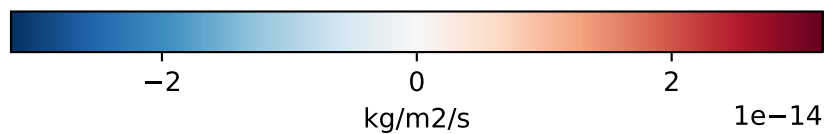
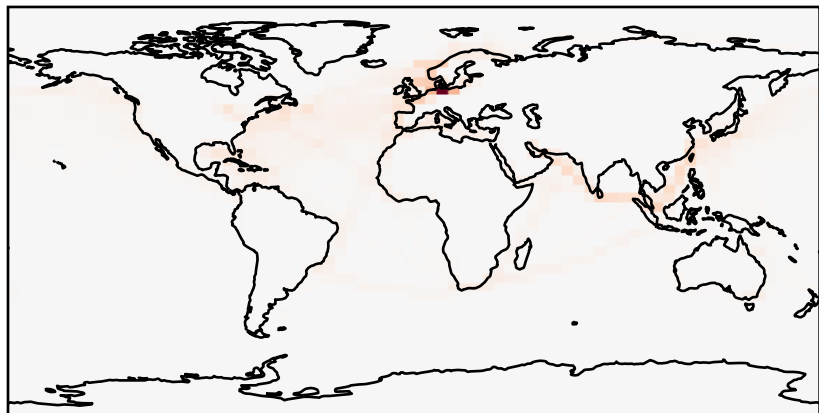
GCC_12.9.0 (Ref)
4.0x5.0



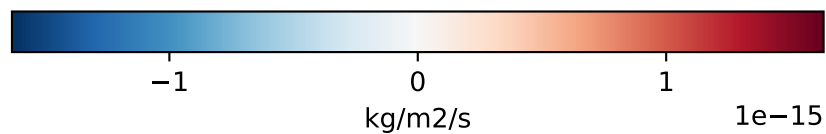
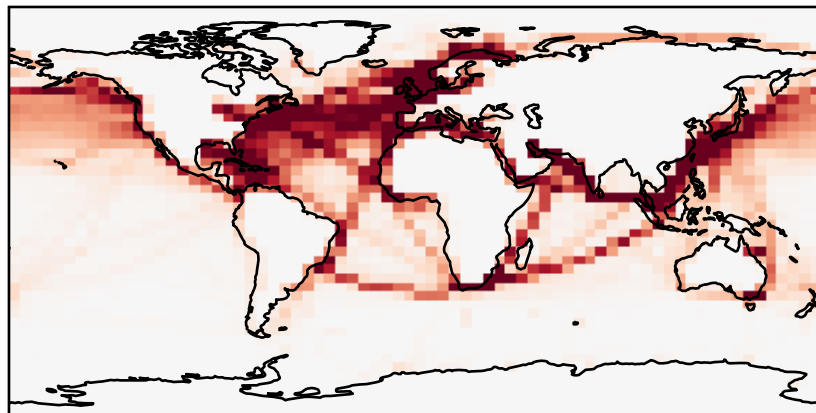
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



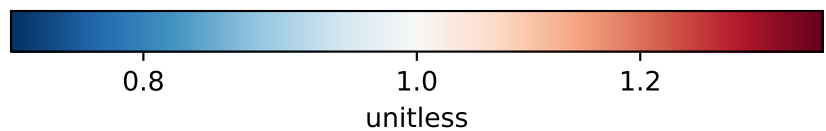
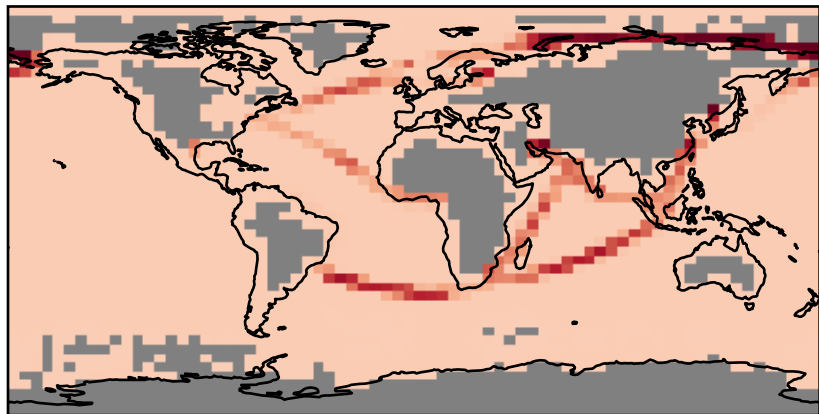
Difference
Dev - Ref, Dynamic Range



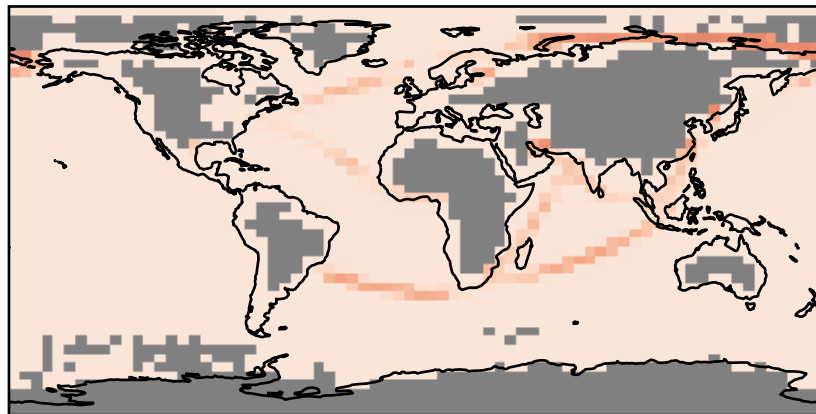
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

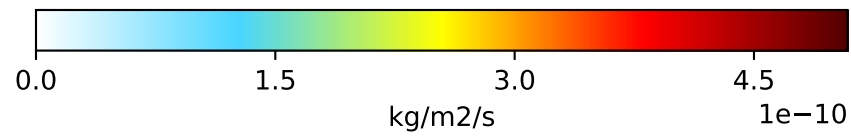
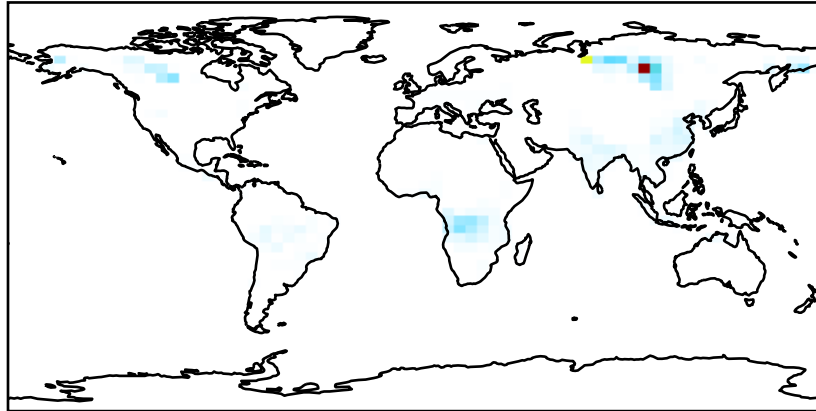


Ratio
Dev/Ref, Fixed Range

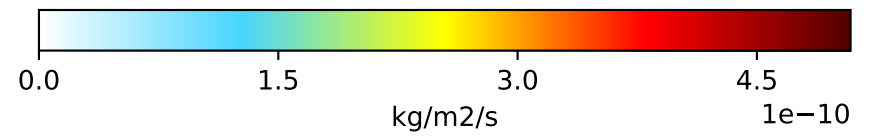
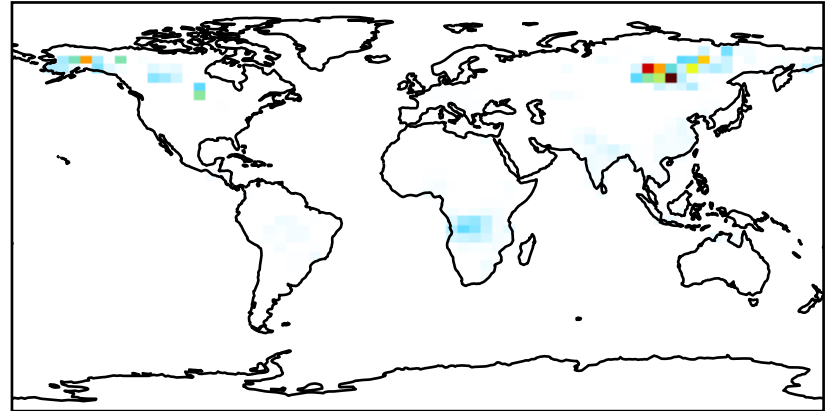


EmisOCPO_Total (Jul2019)

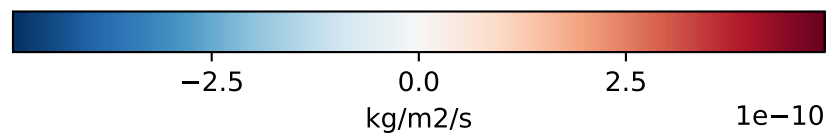
GCC_12.9.0 (Ref)
4.0x5.0



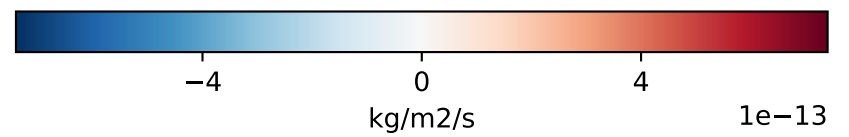
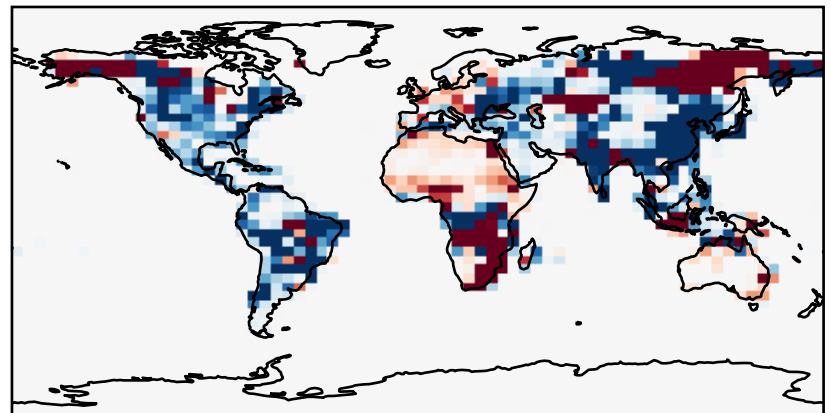
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



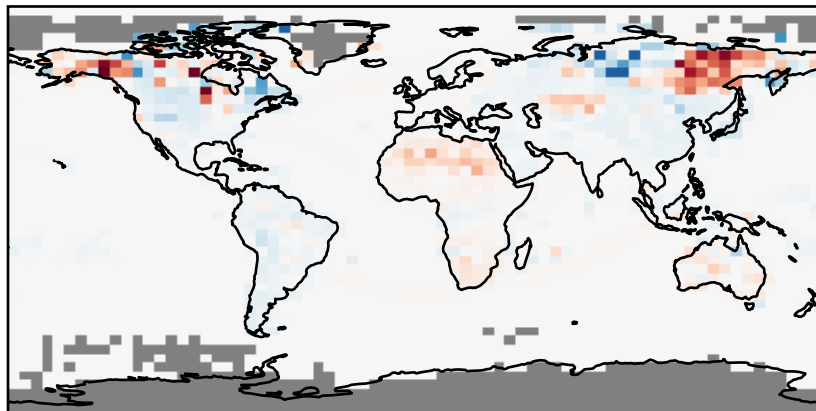
Difference
Dev - Ref, Dynamic Range



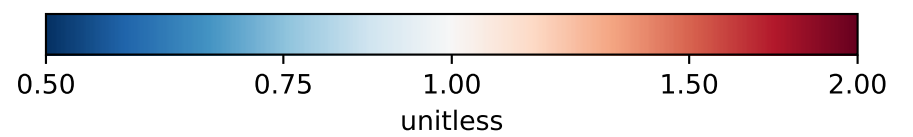
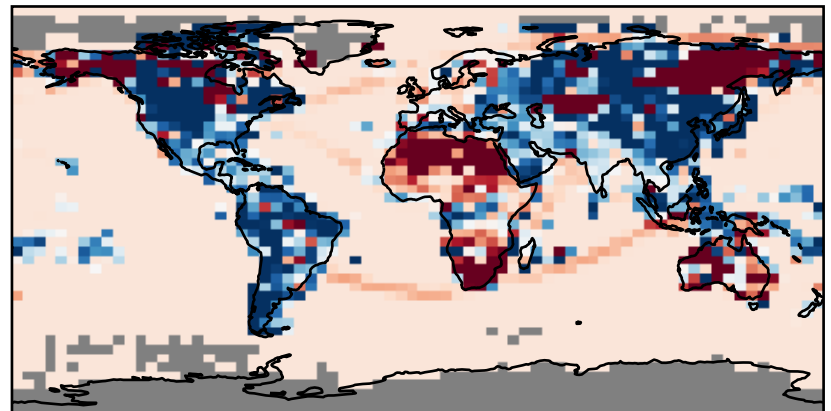
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

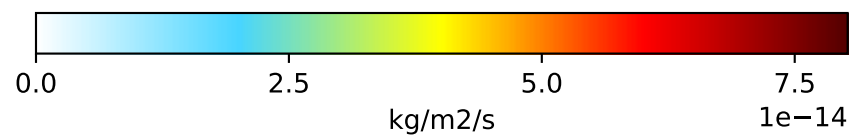
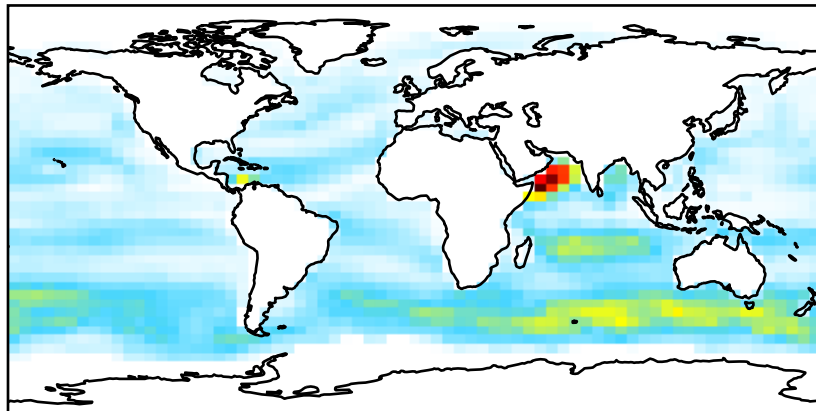


Ratio
Dev/Ref, Fixed Range

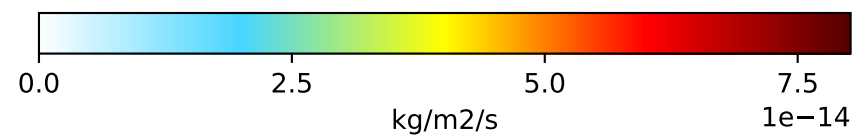
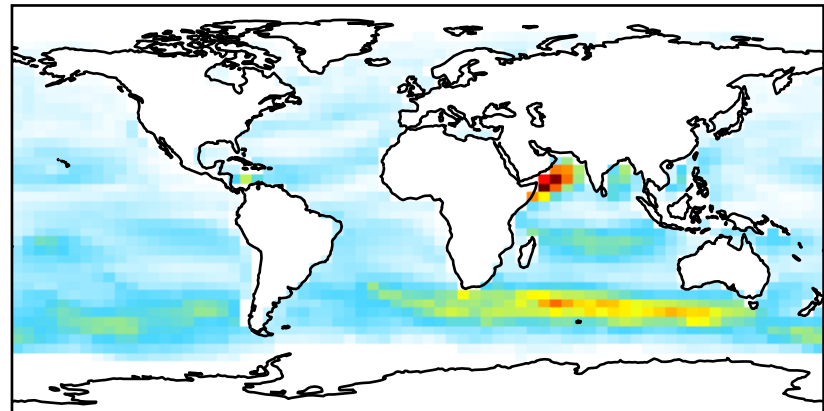


EmisBrSALA_Natural (Jul2019)

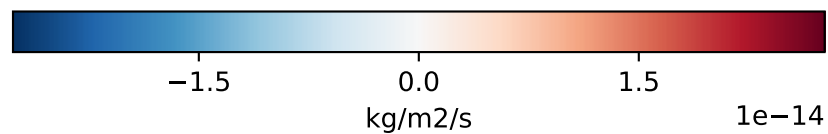
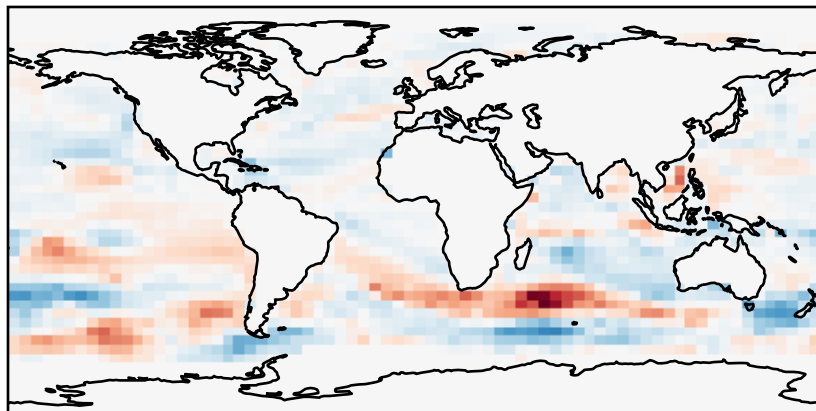
GCC_12.9.0 (Ref)
4.0x5.0



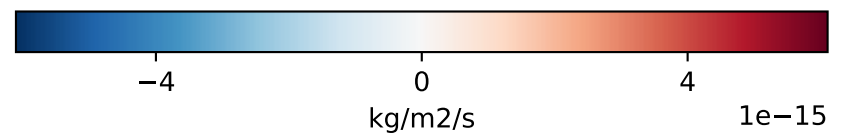
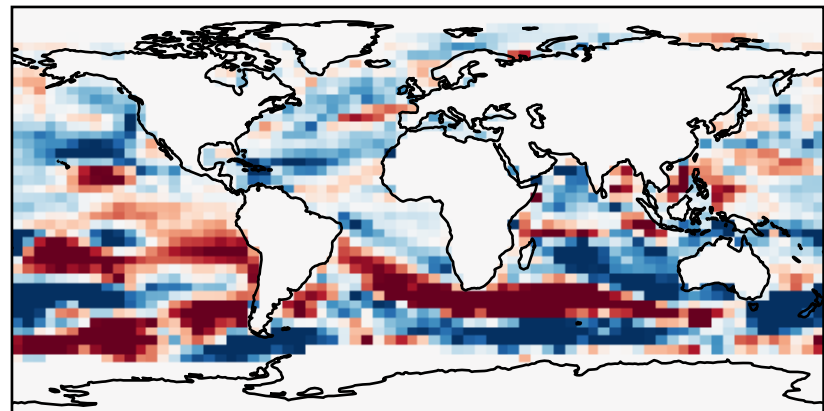
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



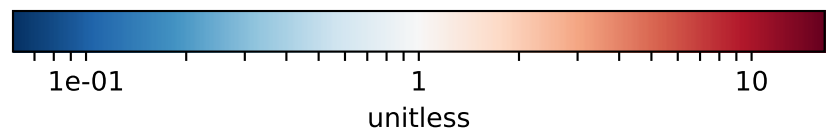
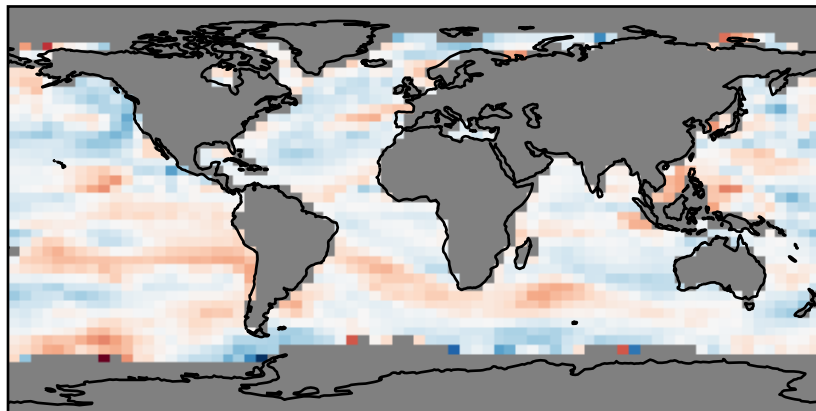
Difference
Dev - Ref, Dynamic Range



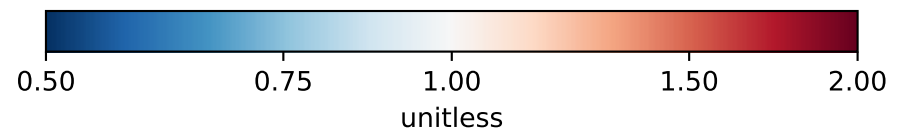
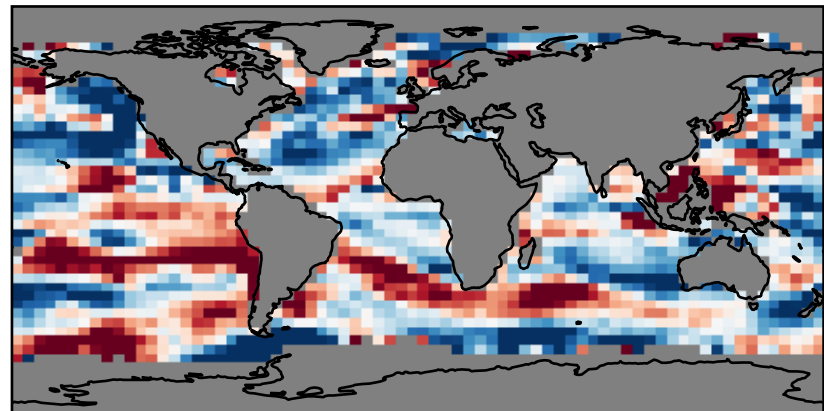
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

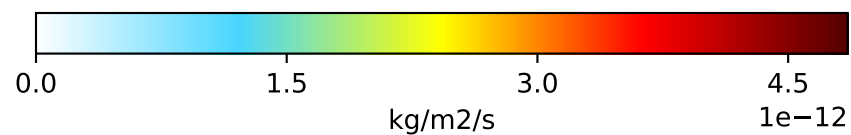
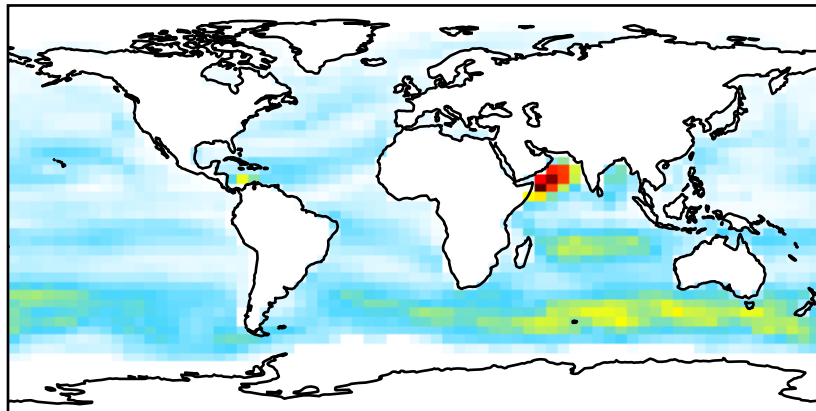


Ratio
Dev/Ref, Fixed Range

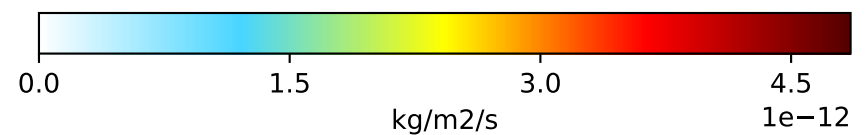
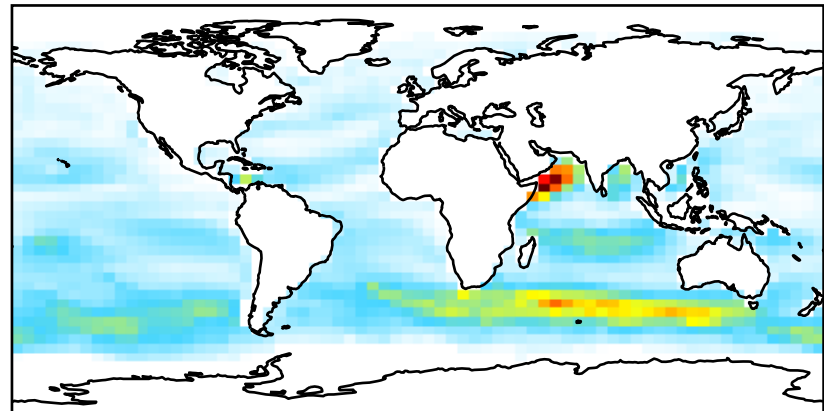


EmisBrSALC_Natural (Jul2019)

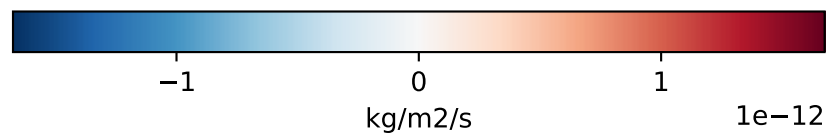
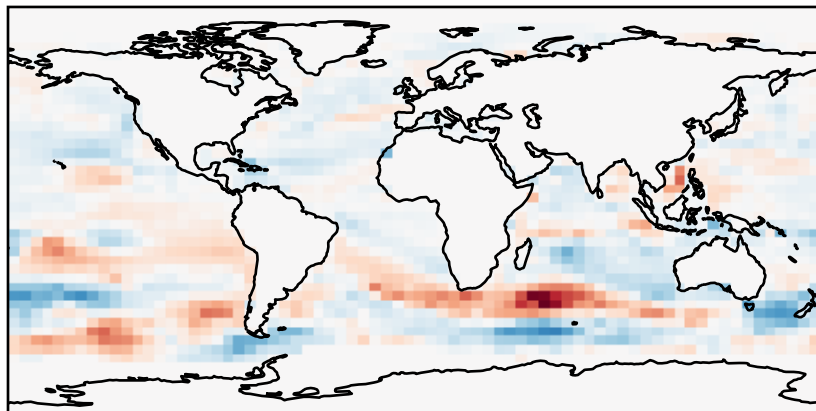
GCC_12.9.0 (Ref)
4.0x5.0



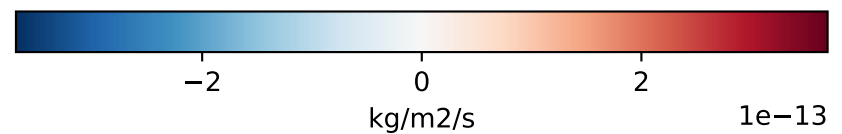
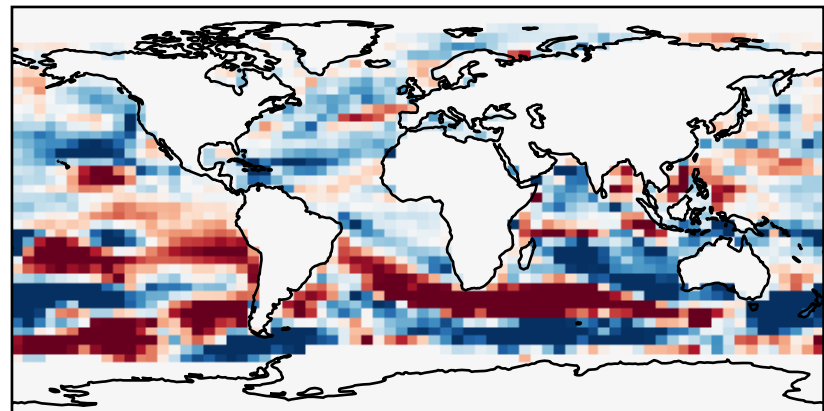
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



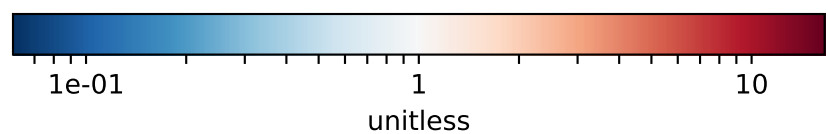
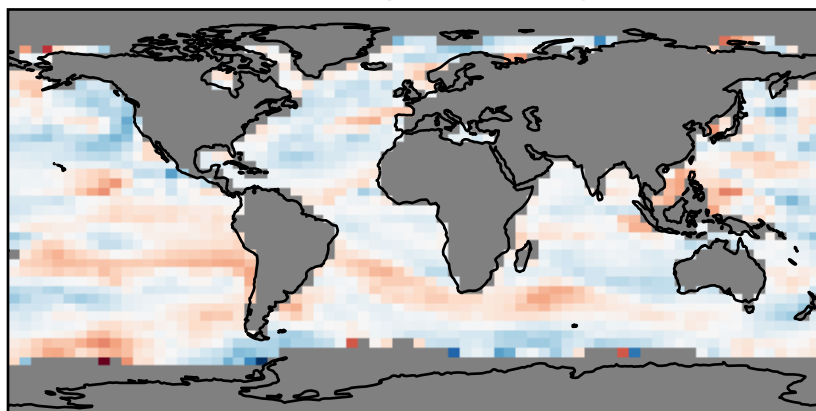
Difference
Dev - Ref, Dynamic Range



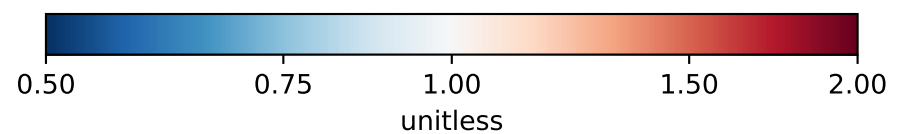
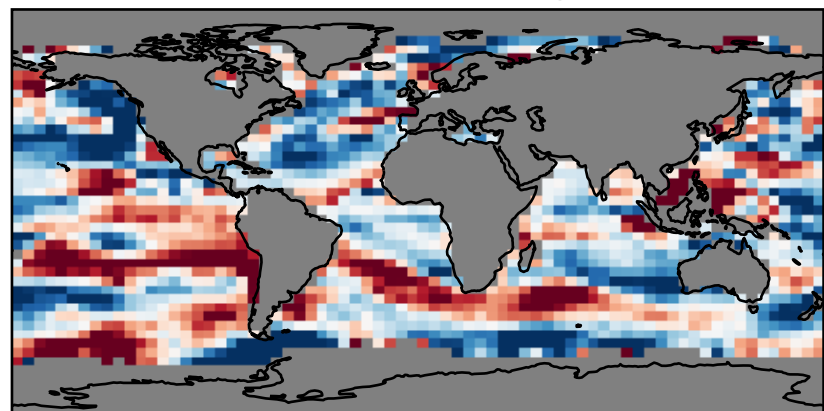
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

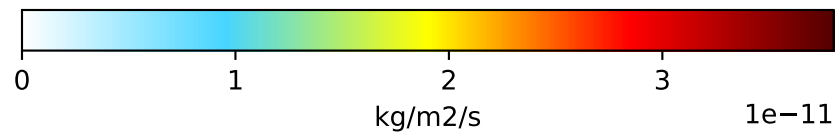
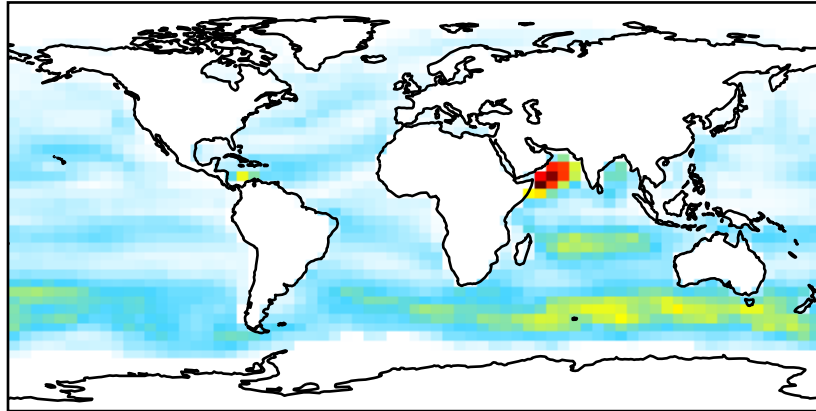


Ratio
Dev/Ref, Fixed Range

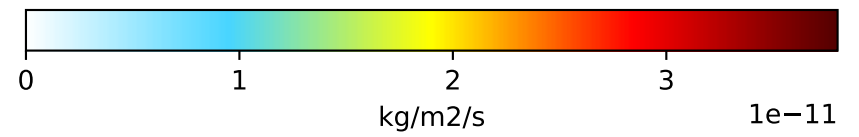
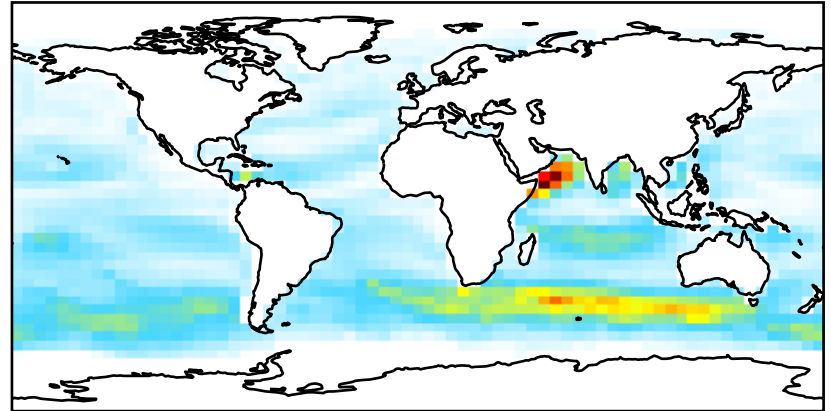


EmisSALA_Natural (Jul2019)

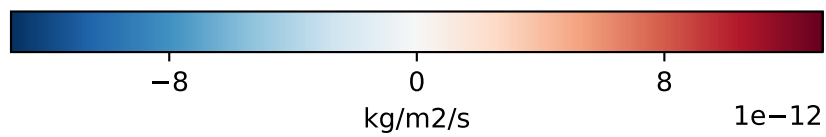
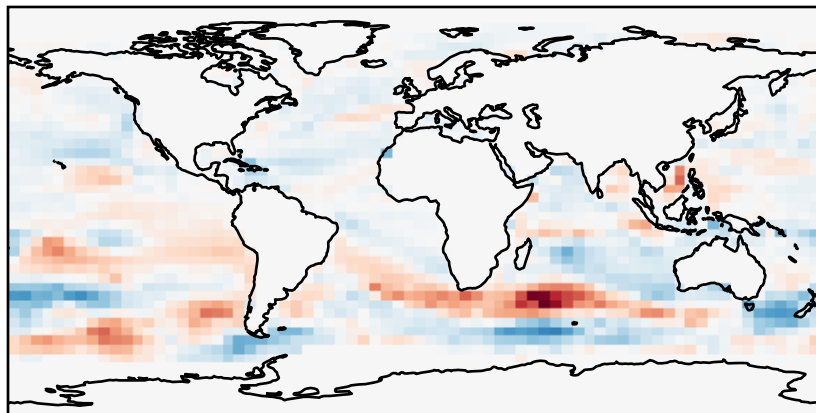
GCC_12.9.0 (Ref)
4.0x5.0



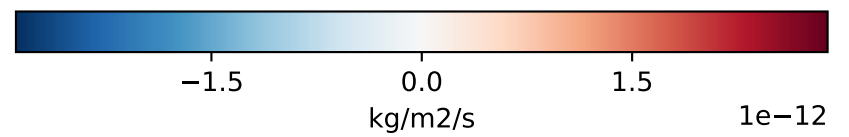
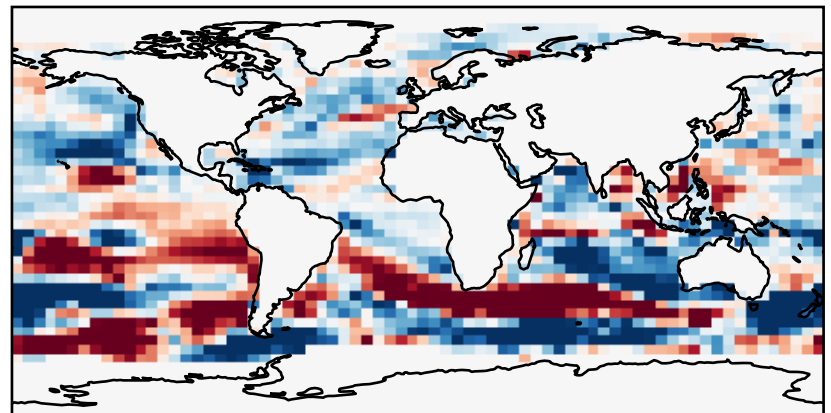
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



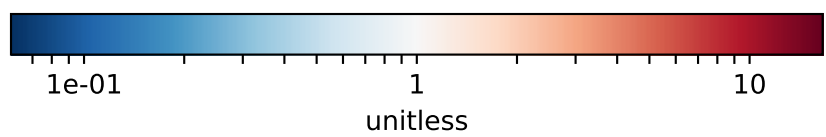
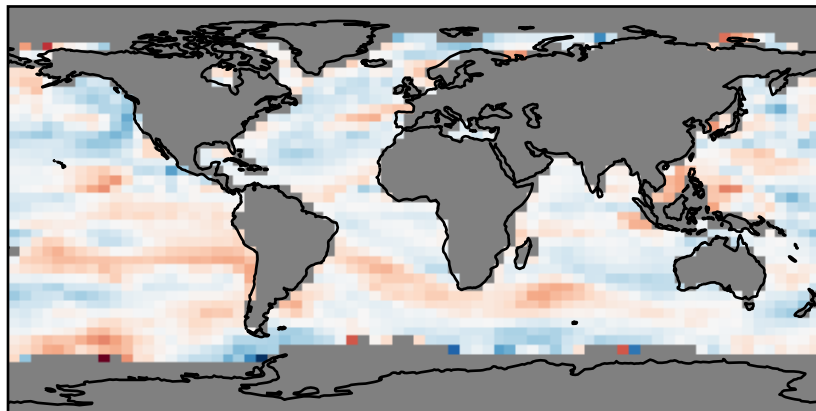
Difference
Dev - Ref, Dynamic Range



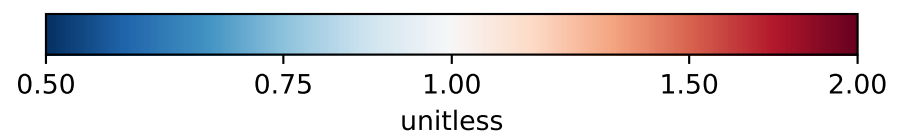
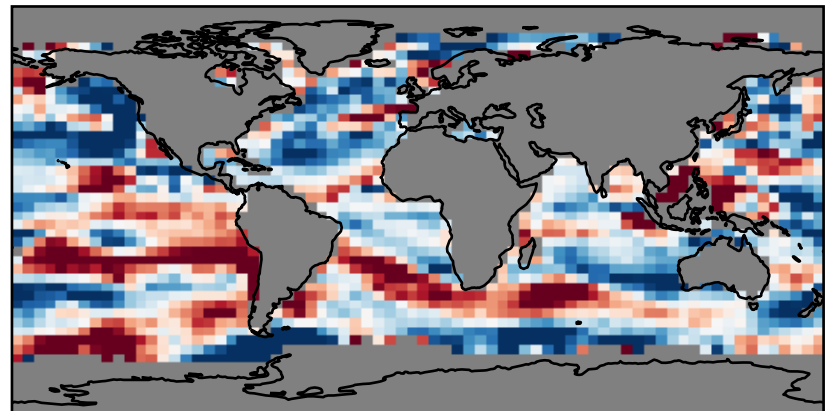
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

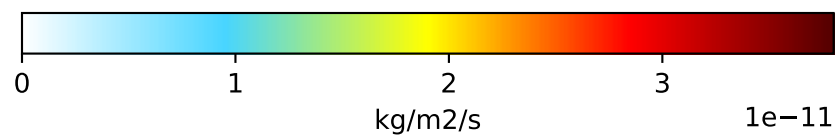
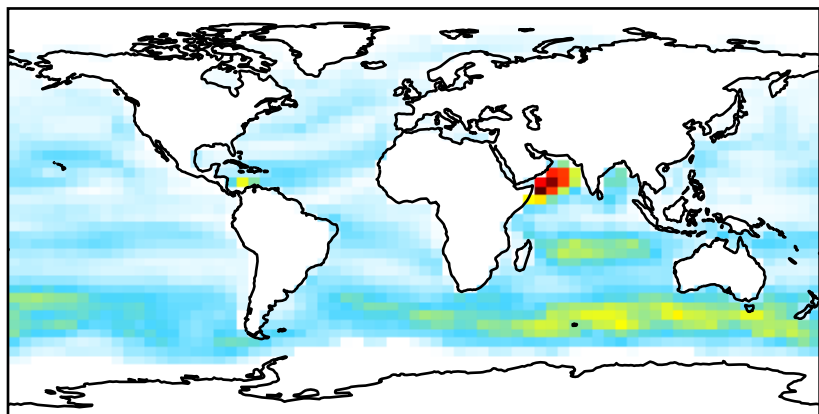


Ratio
Dev/Ref, Fixed Range

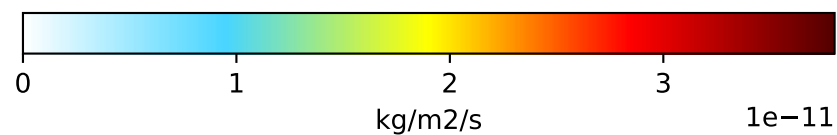
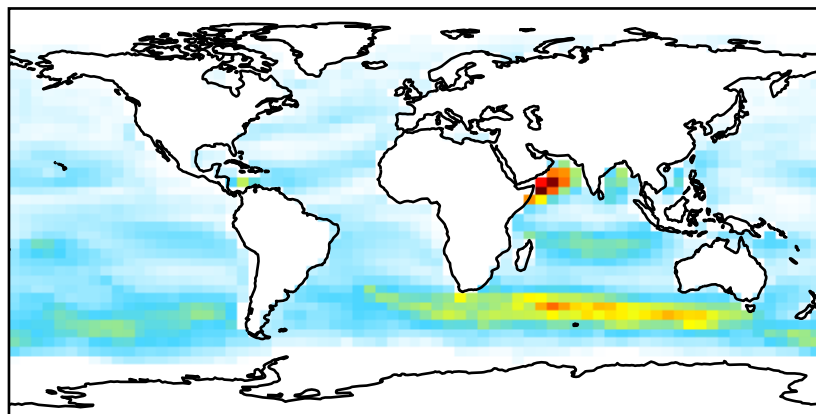


EmisSALAAL_Natural (Jul2019)

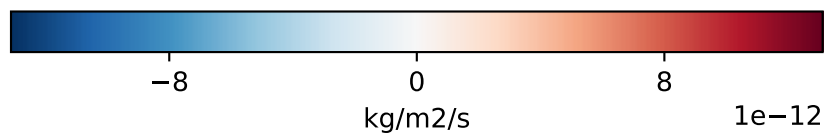
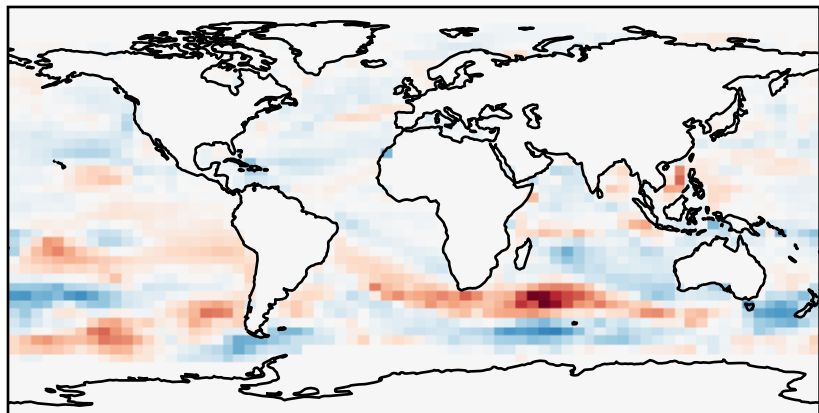
GCC_12.9.0 (Ref)
4.0x5.0



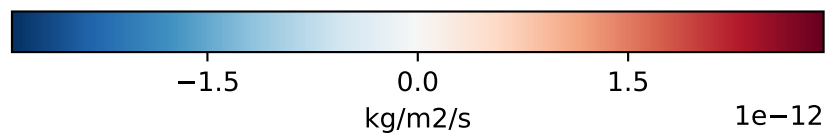
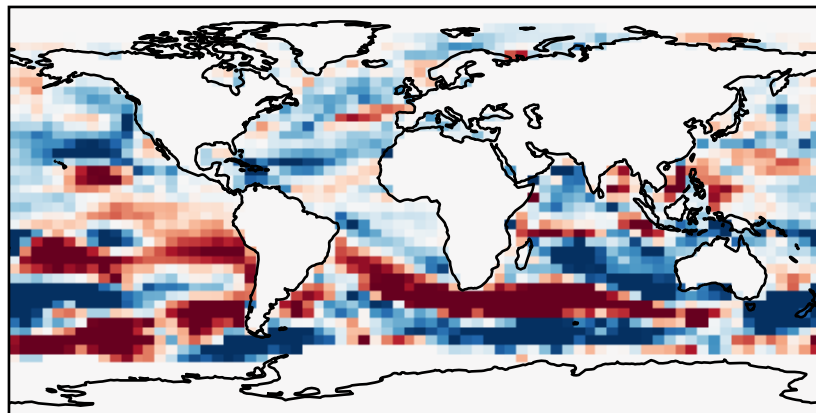
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



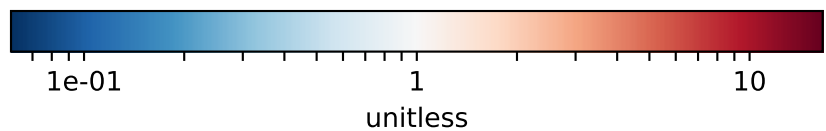
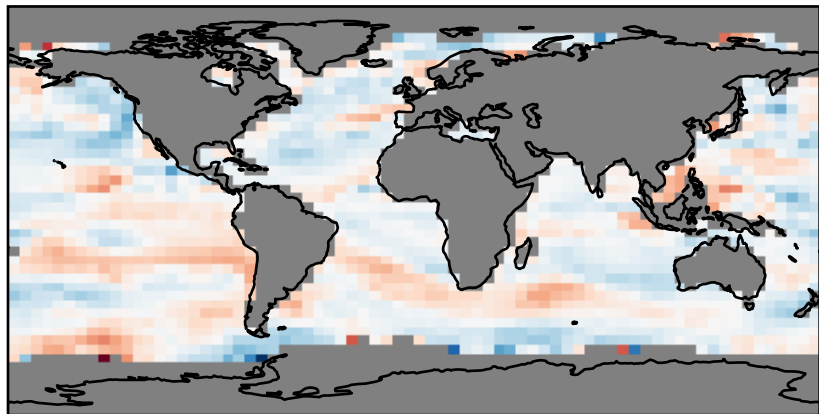
Difference
Dev - Ref, Dynamic Range



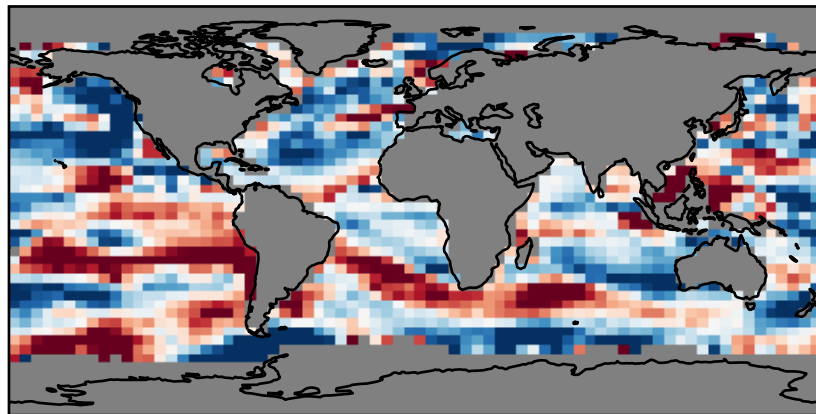
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

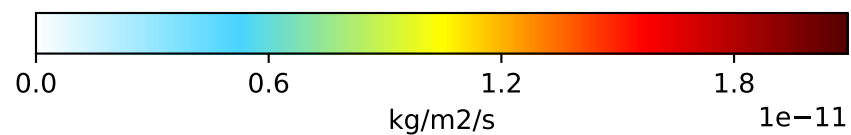
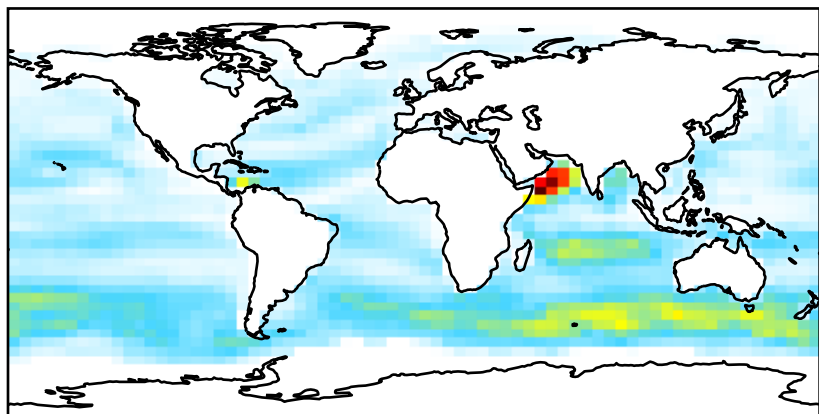


Ratio
Dev/Ref, Fixed Range

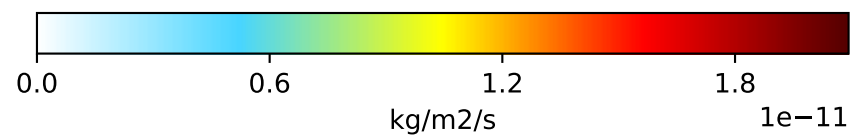
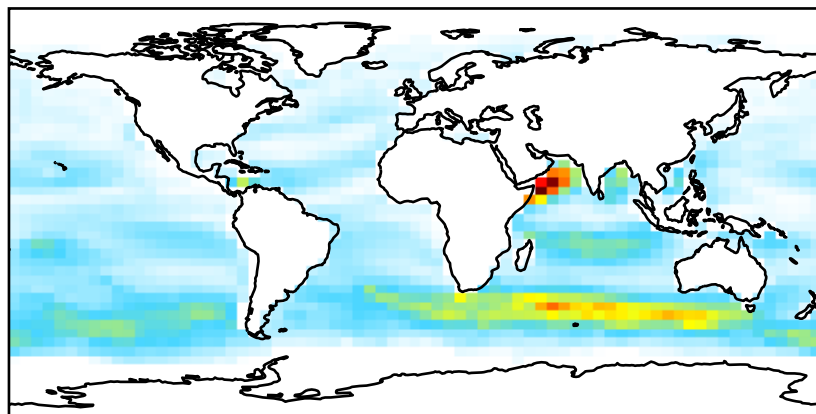


EmisSALACL_Natural (Jul2019)

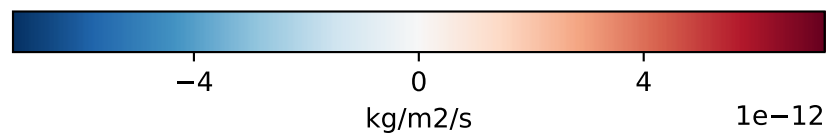
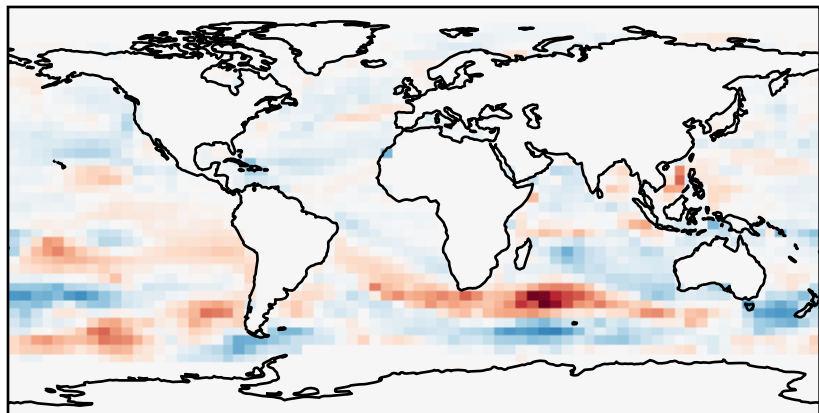
GCC_12.9.0 (Ref)
4.0x5.0



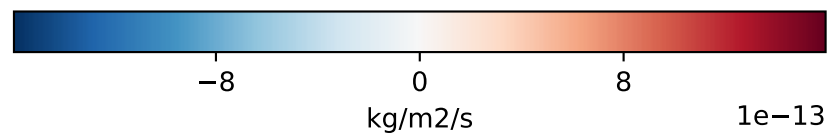
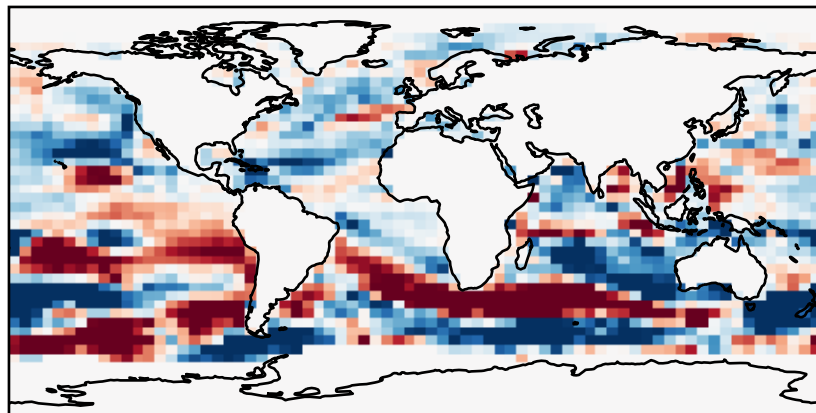
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



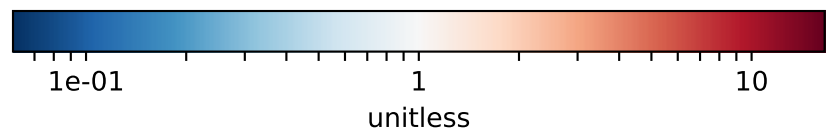
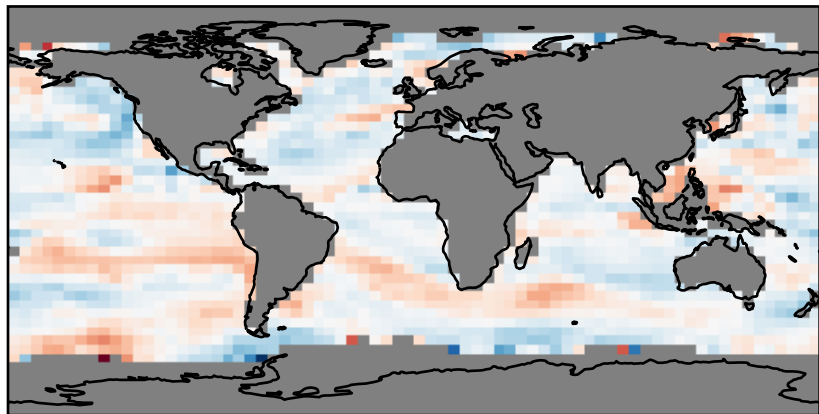
Difference
Dev - Ref, Dynamic Range



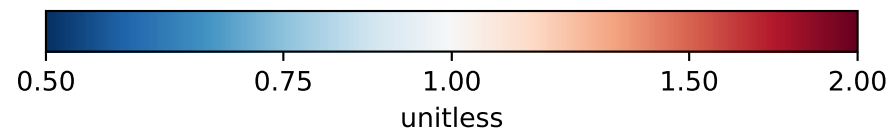
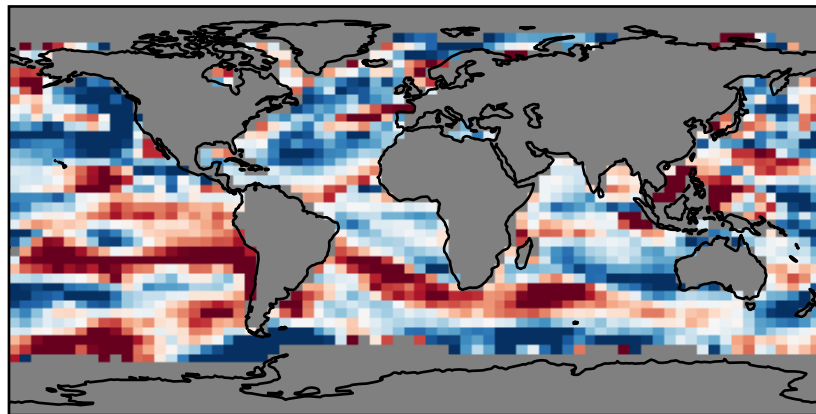
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

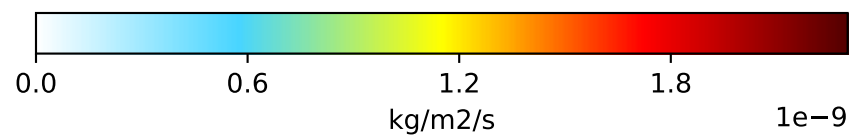
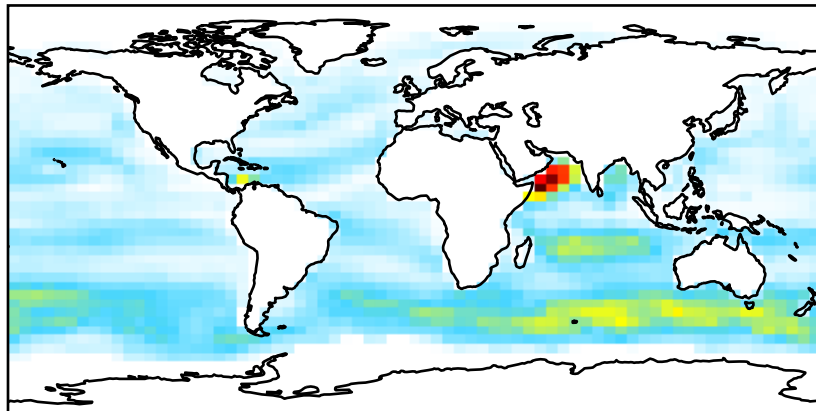


Ratio
Dev/Ref, Fixed Range

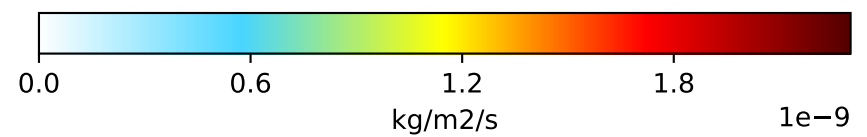
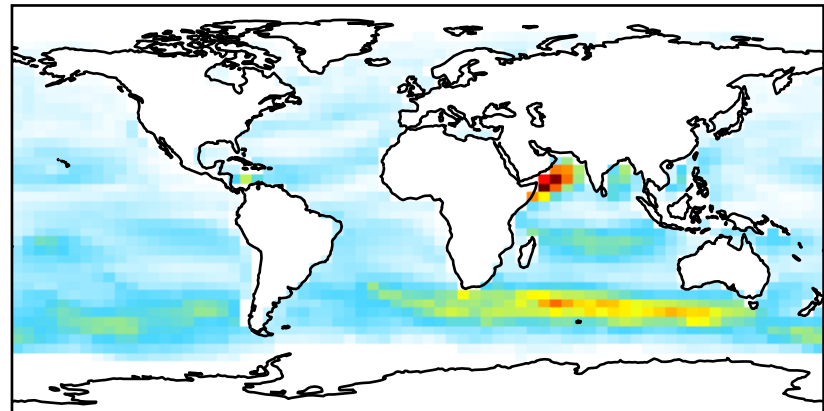


EmisSALC_Natural (Jul2019)

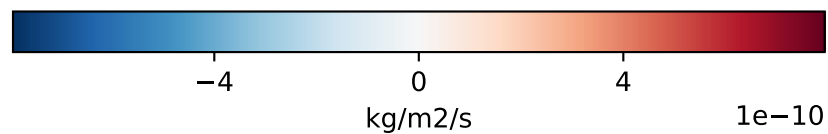
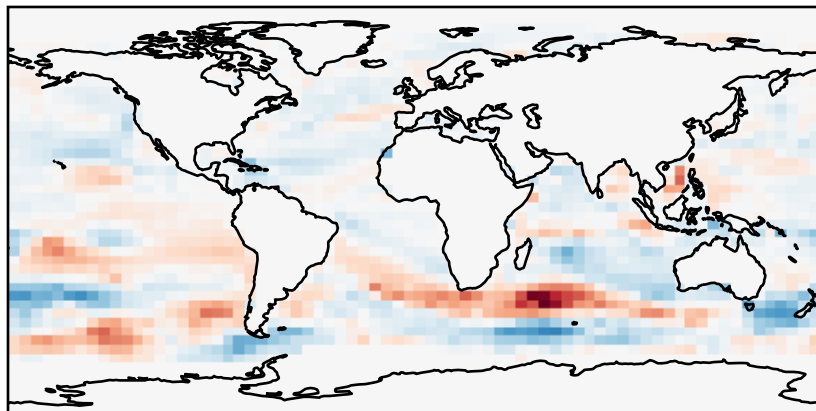
GCC_12.9.0 (Ref)
4.0x5.0



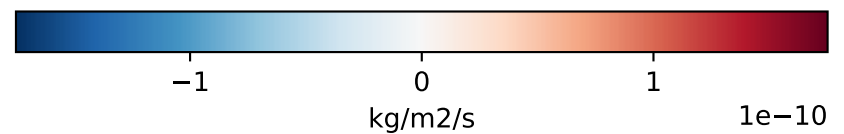
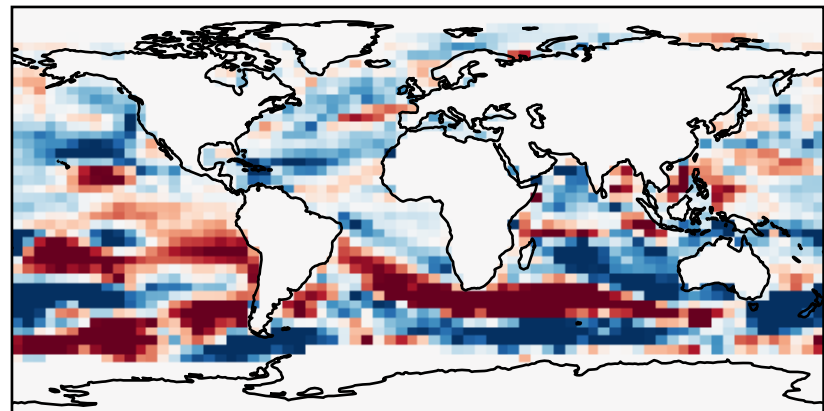
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



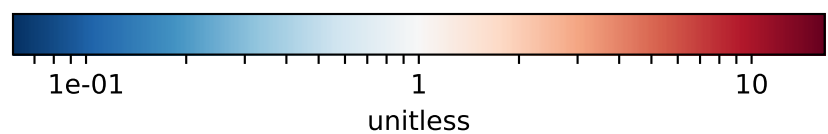
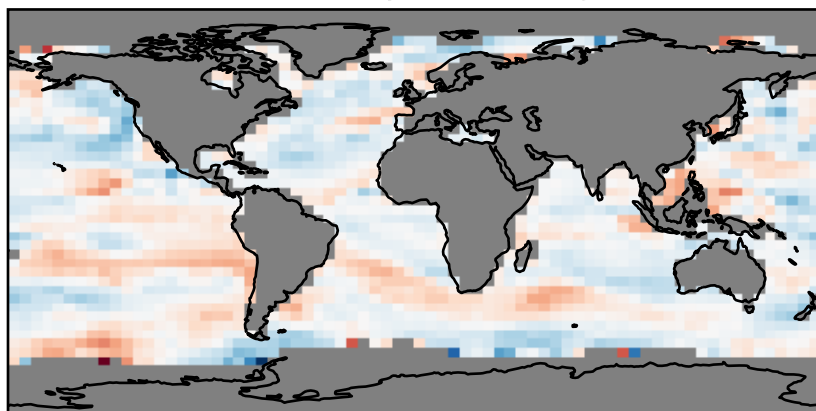
Difference
Dev - Ref, Dynamic Range



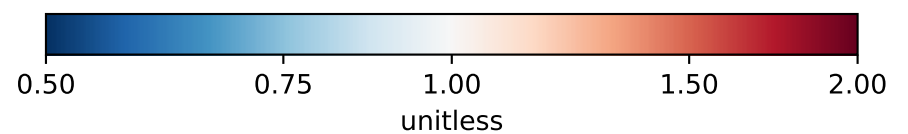
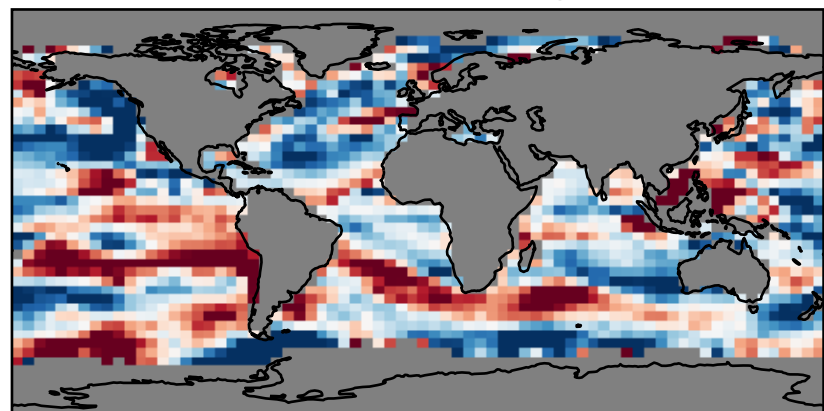
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

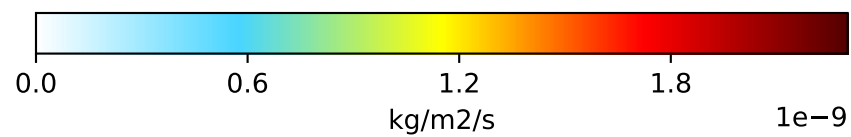
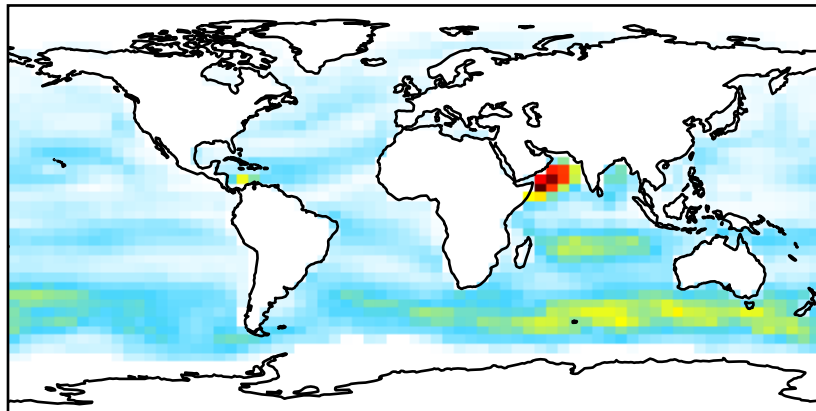


Ratio
Dev/Ref, Fixed Range

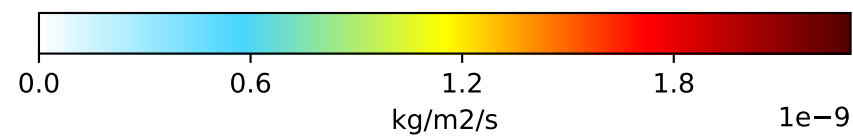
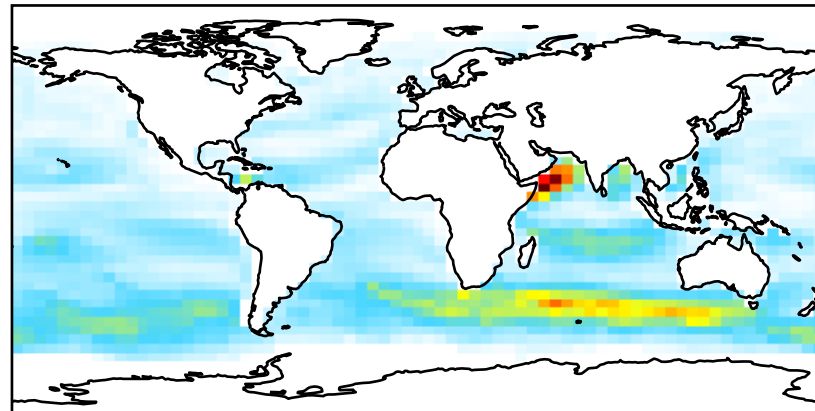


EmisSALCAL_Natural (Jul2019)

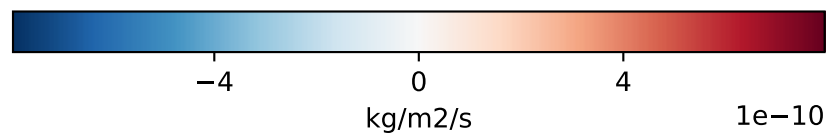
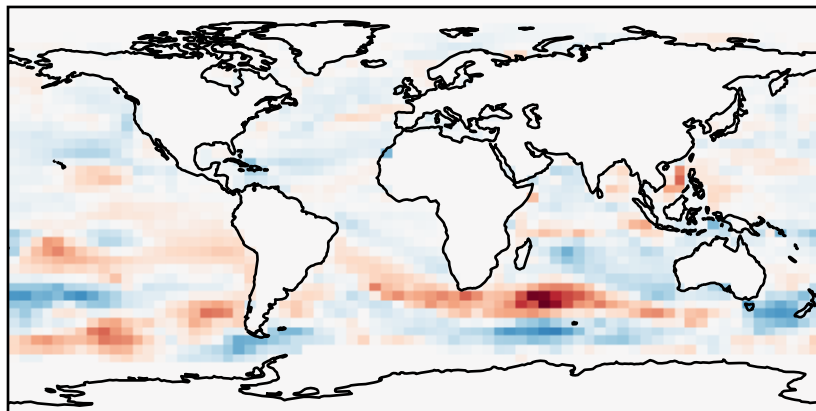
GCC_12.9.0 (Ref)
4.0x5.0



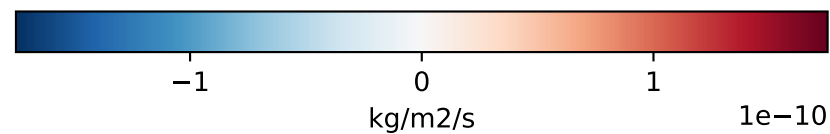
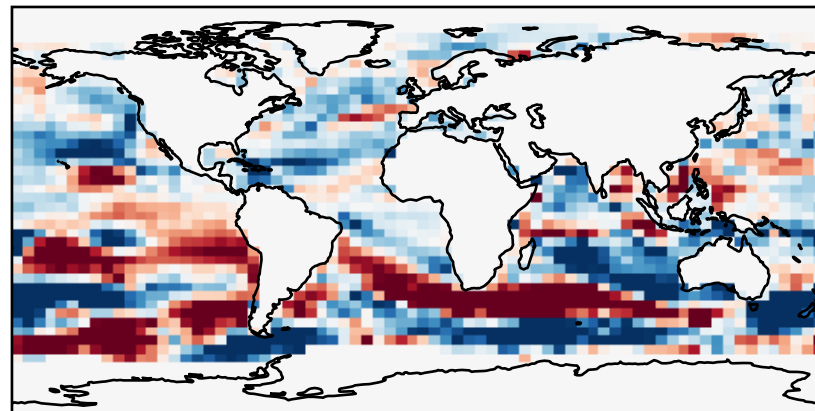
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



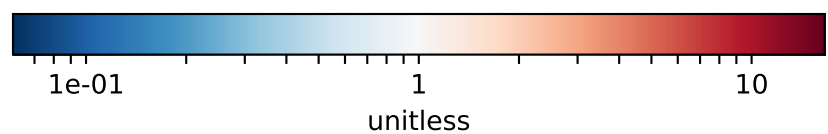
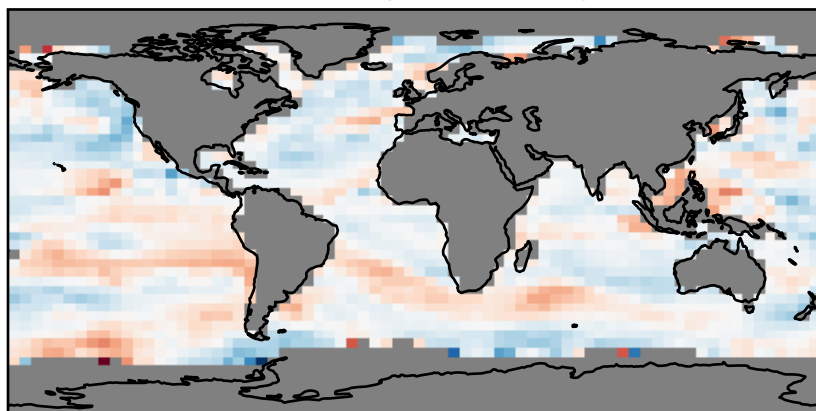
Difference
Dev - Ref, Dynamic Range



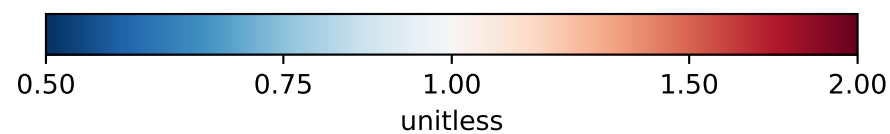
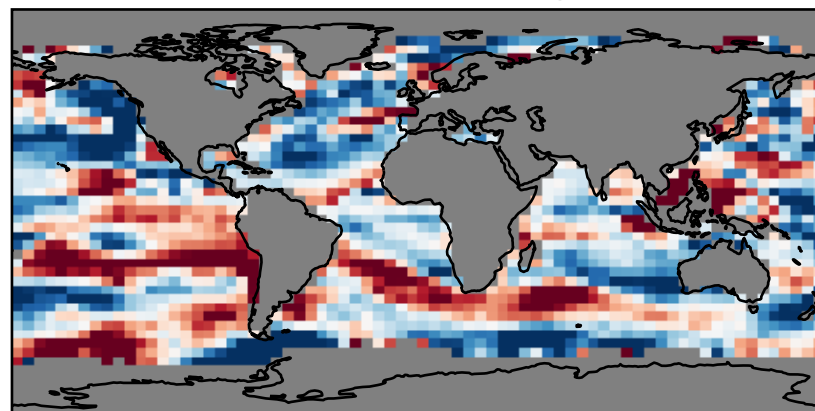
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

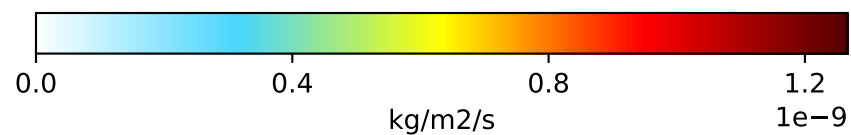
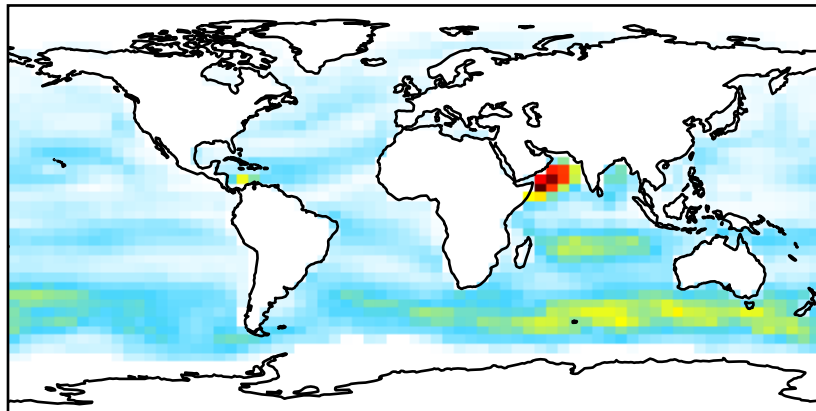


Ratio
Dev/Ref, Fixed Range

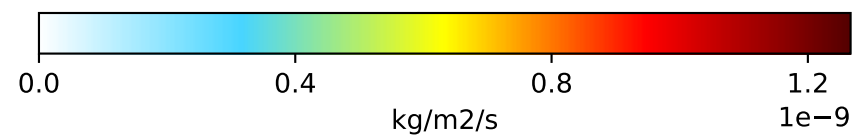
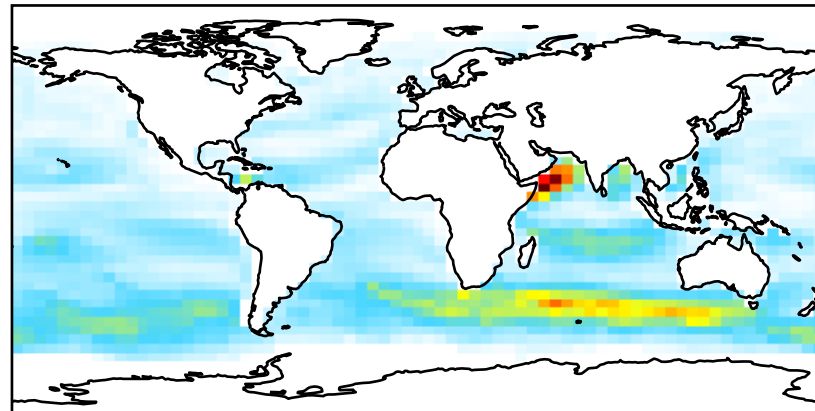


EmisSALCCL_Natural (Jul2019)

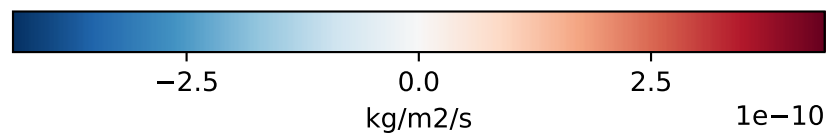
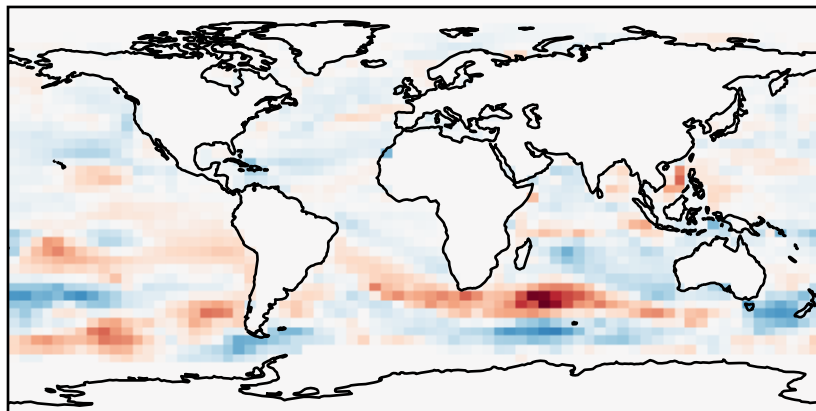
GCC_12.9.0 (Ref)
4.0x5.0



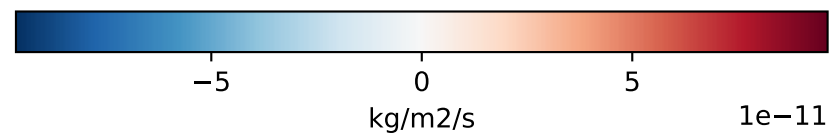
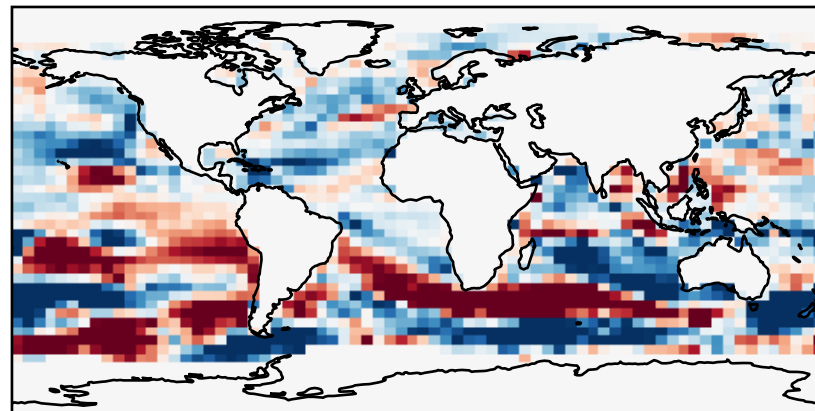
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



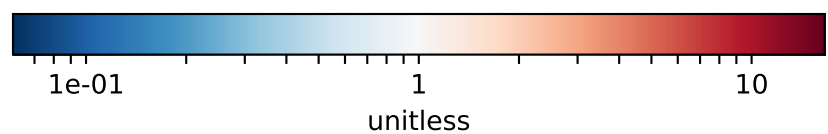
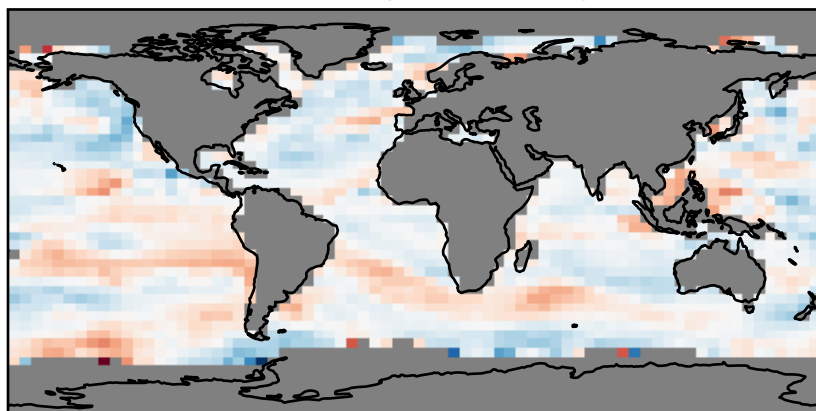
Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range



Ratio
Dev/Ref, Fixed Range

