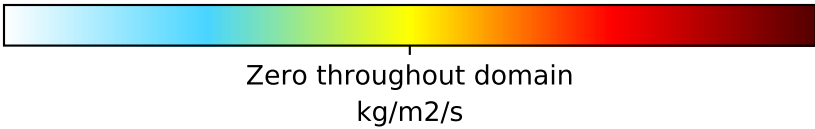
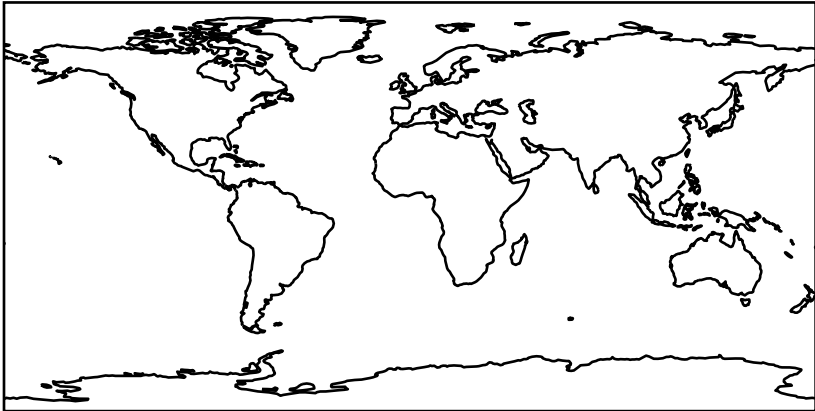
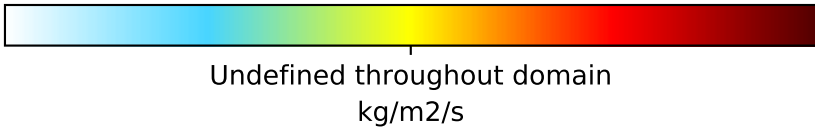
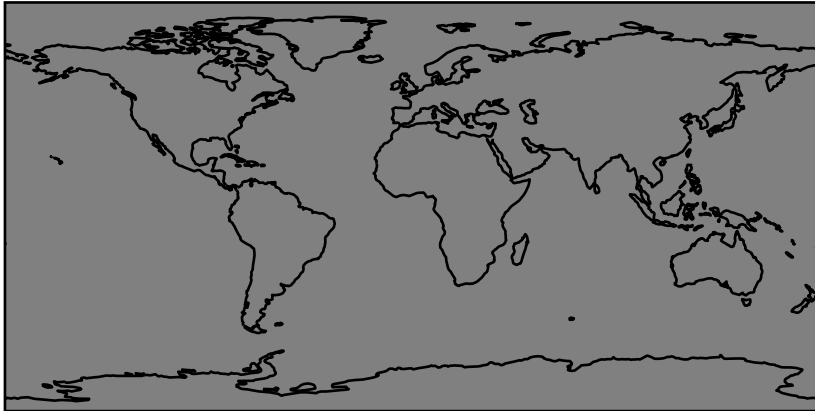


# EmisBr2\_Natural (Jan2019)

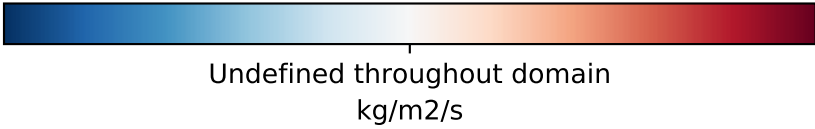
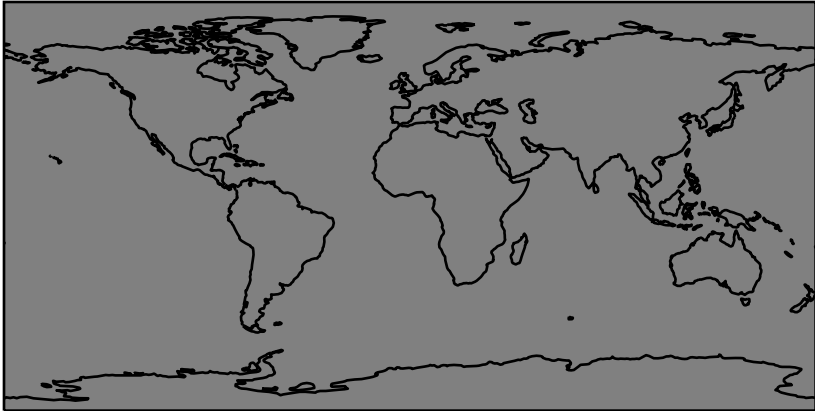
GCC\_12.9.0 (Ref)  
4.0x5.0



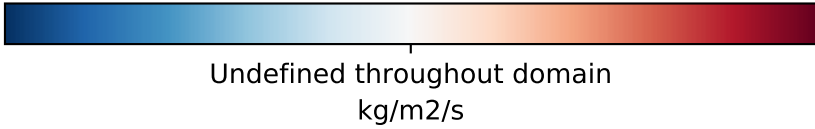
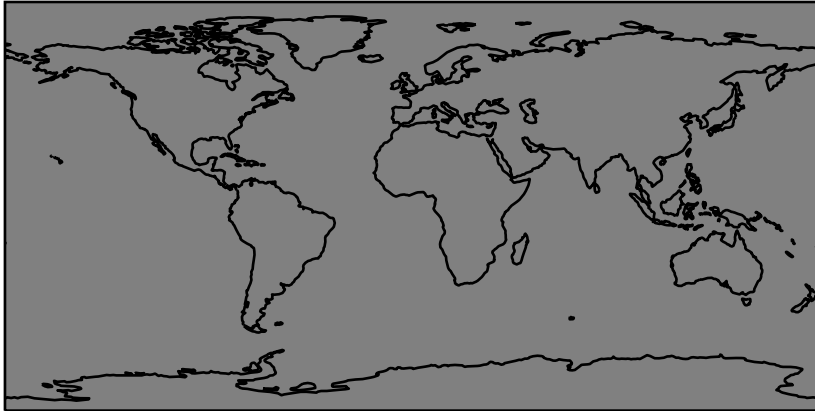
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



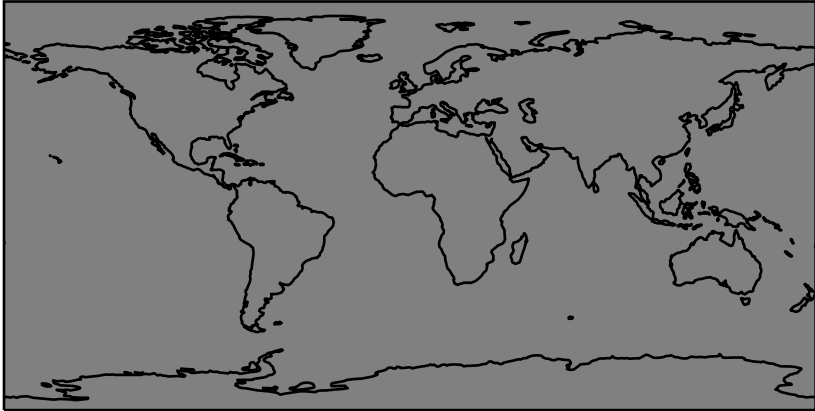
Difference  
Dev - Ref, Dynamic Range



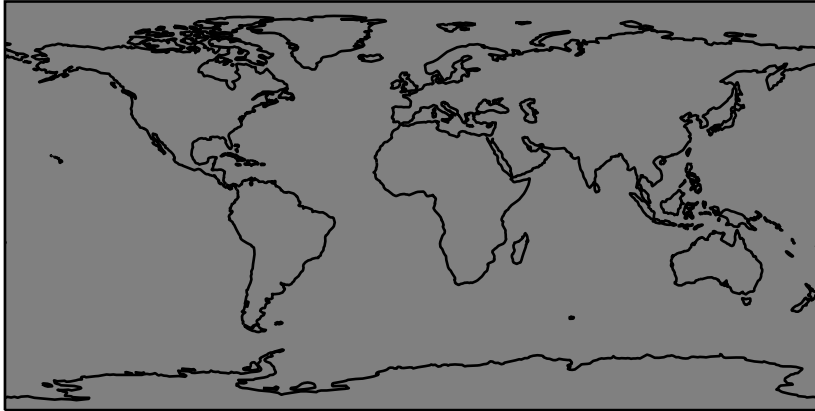
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

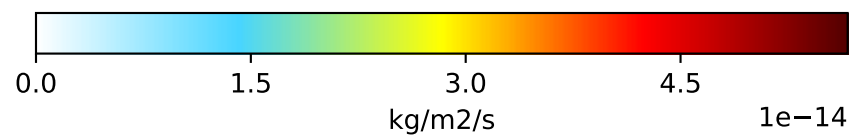
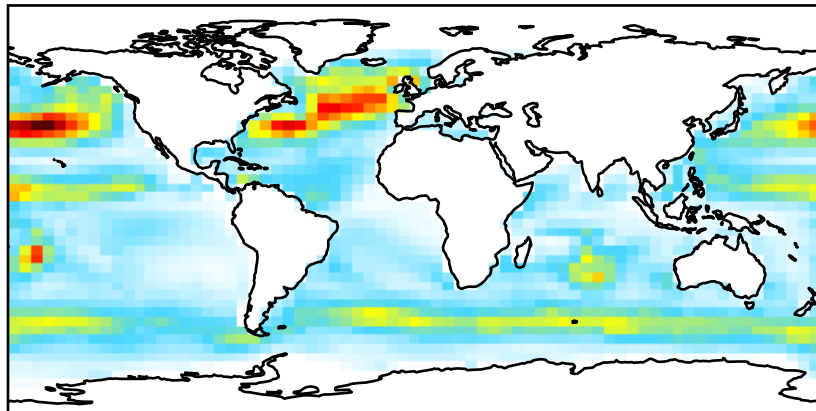


Ratio  
Dev/Ref, Fixed Range

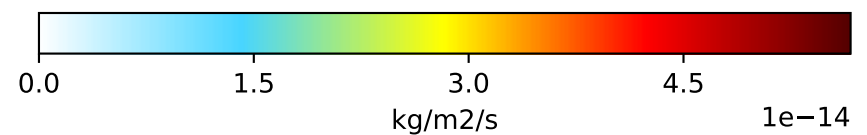
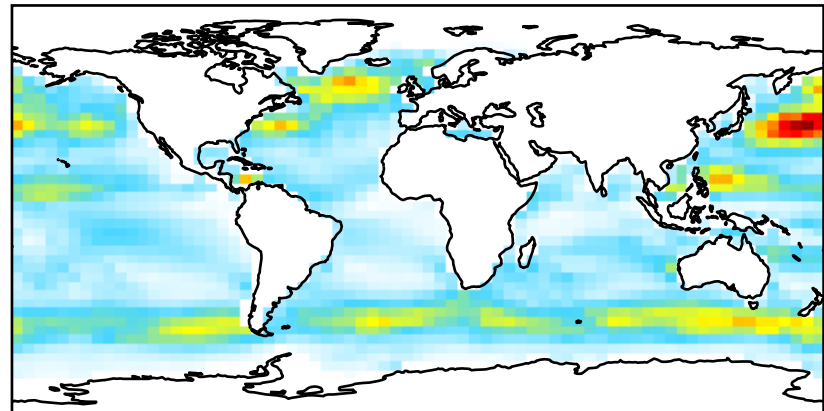


# EmisBrSALA\_Natural (Jan2019)

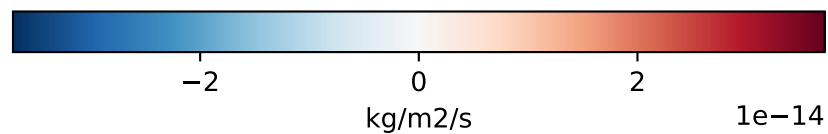
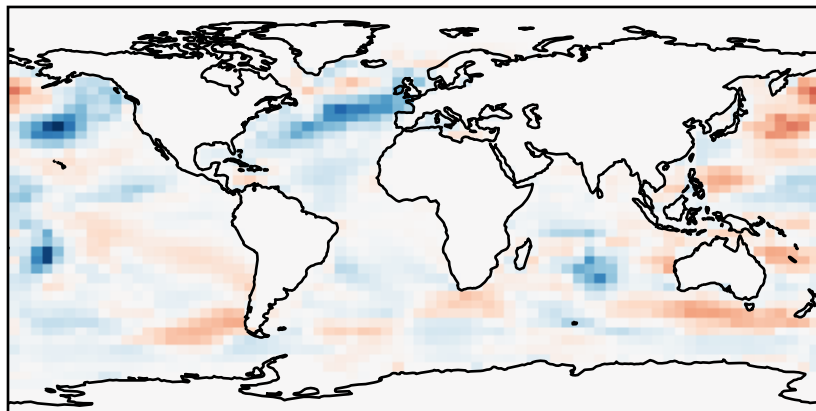
GCC\_12.9.0 (Ref)  
4.0x5.0



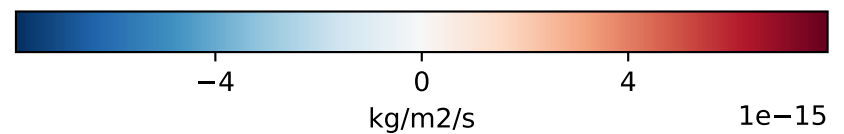
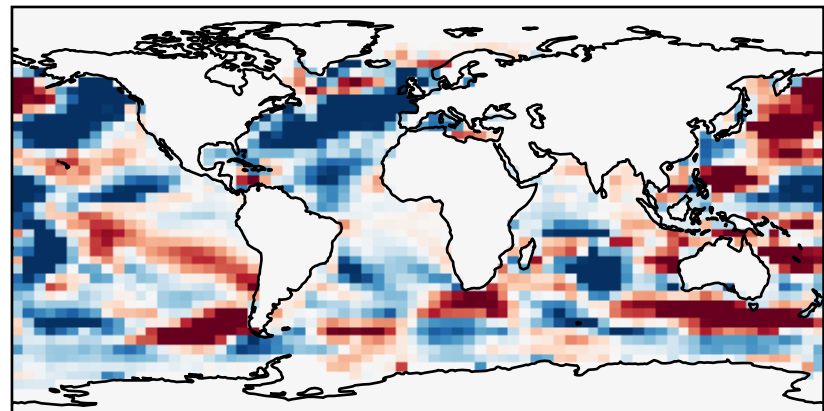
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



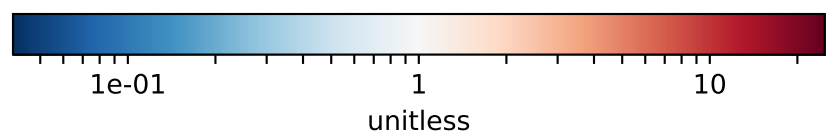
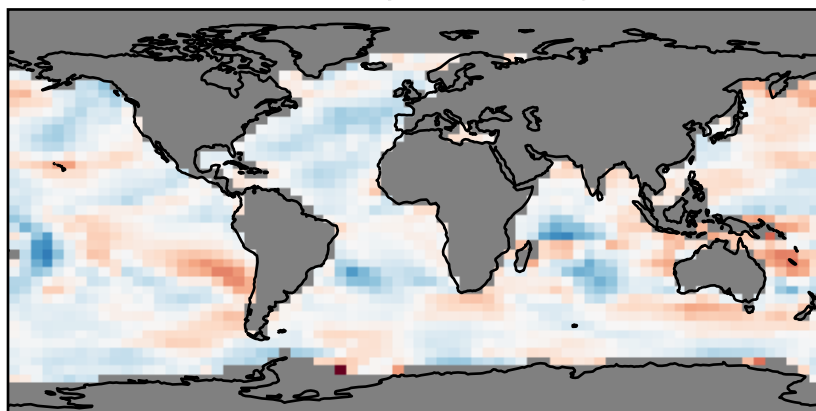
Difference  
Dev - Ref, Dynamic Range



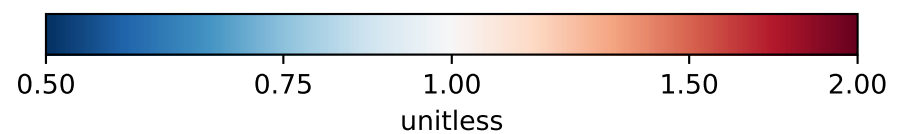
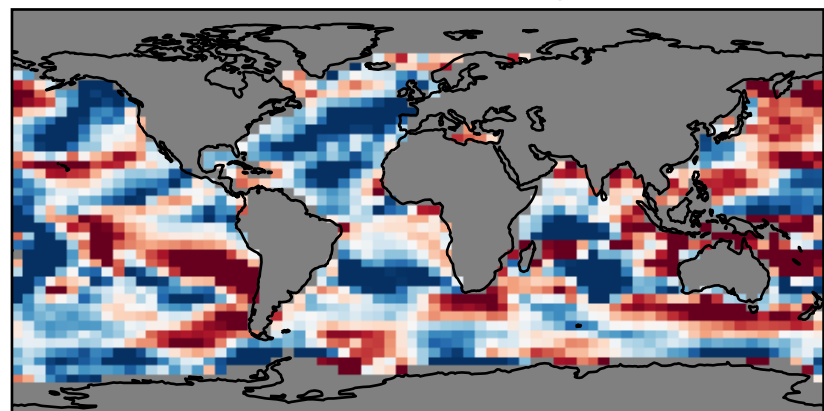
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

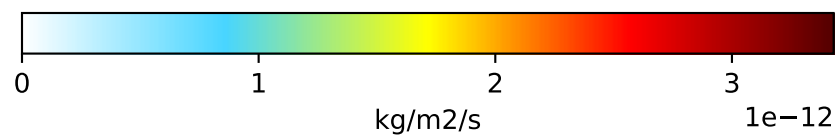
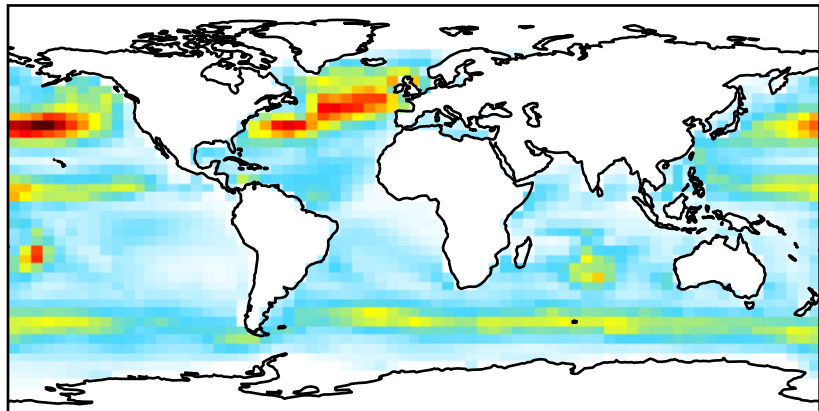


Ratio  
Dev/Ref, Fixed Range

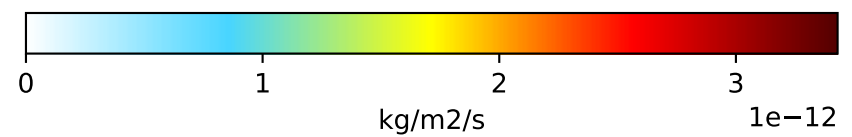
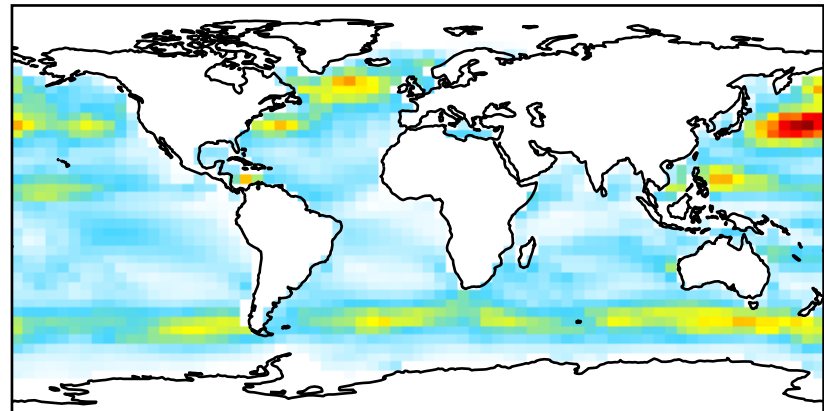


# EmisBrSALC\_Natural (Jan2019)

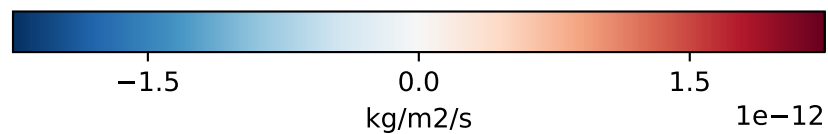
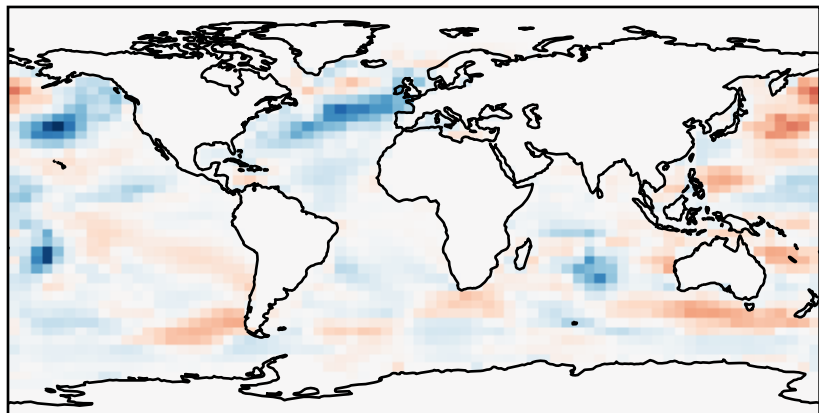
GCC\_12.9.0 (Ref)  
4.0x5.0



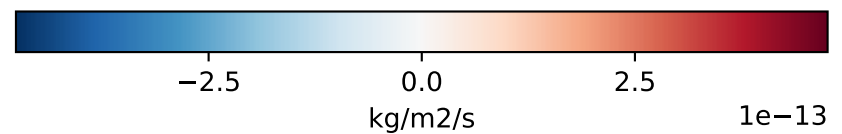
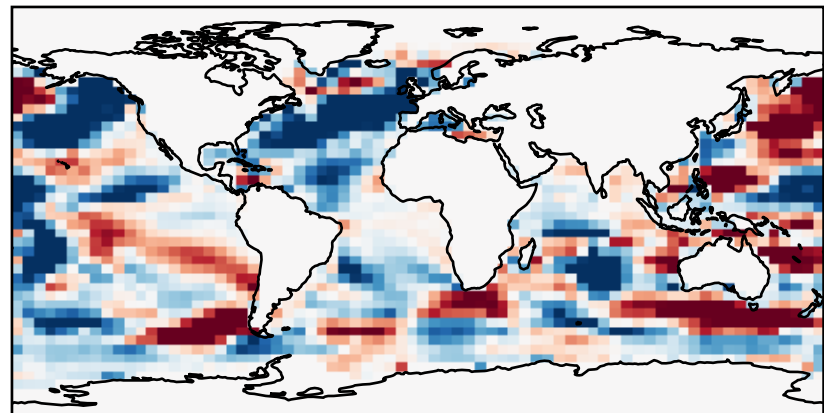
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



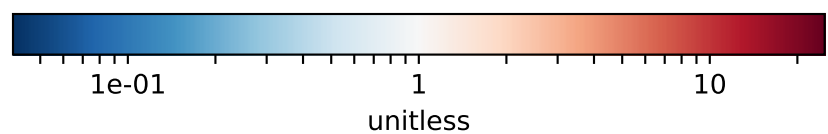
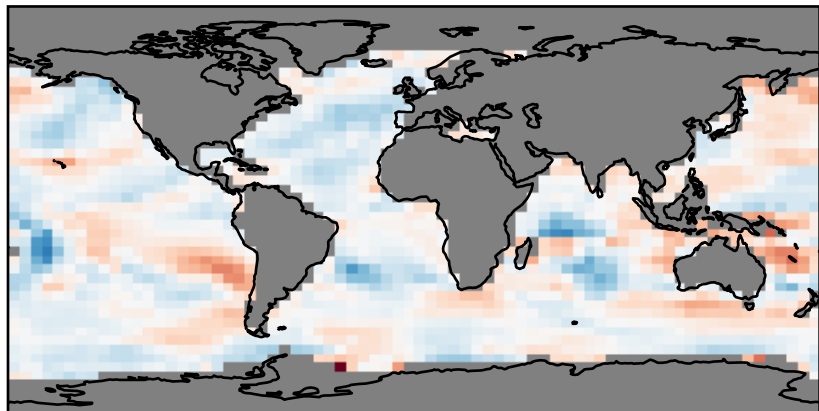
Difference  
Dev - Ref, Dynamic Range



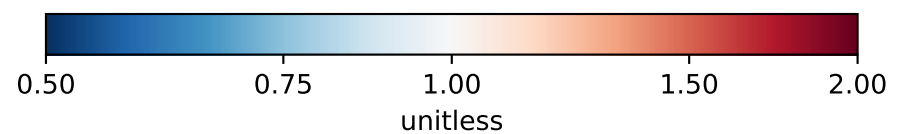
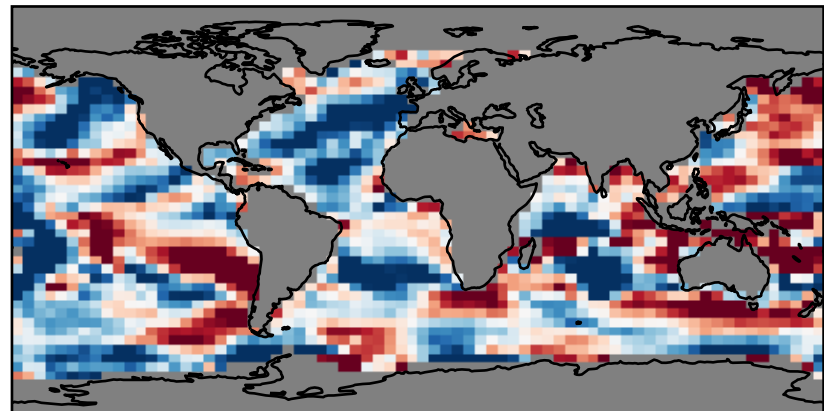
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

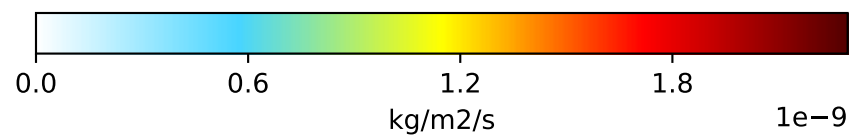
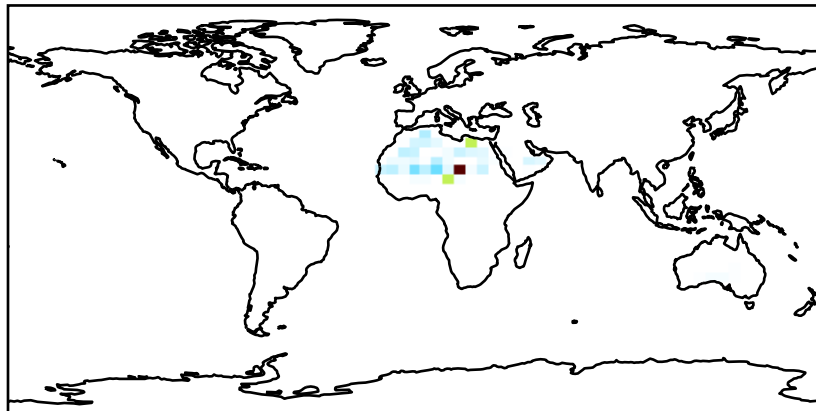


Ratio  
Dev/Ref, Fixed Range

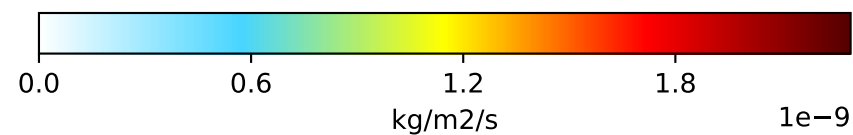
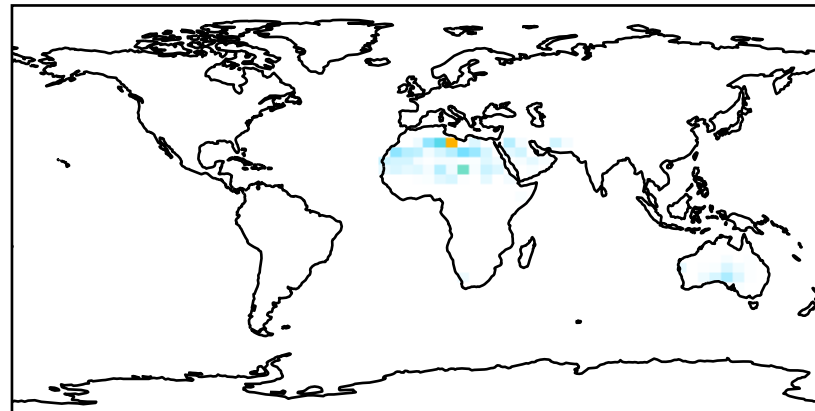


# EmisDST1\_Natural (Jan2019)

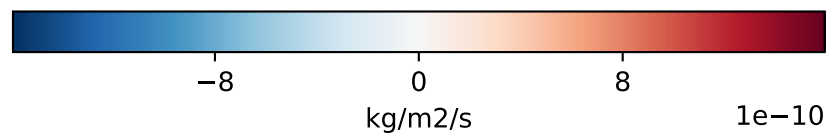
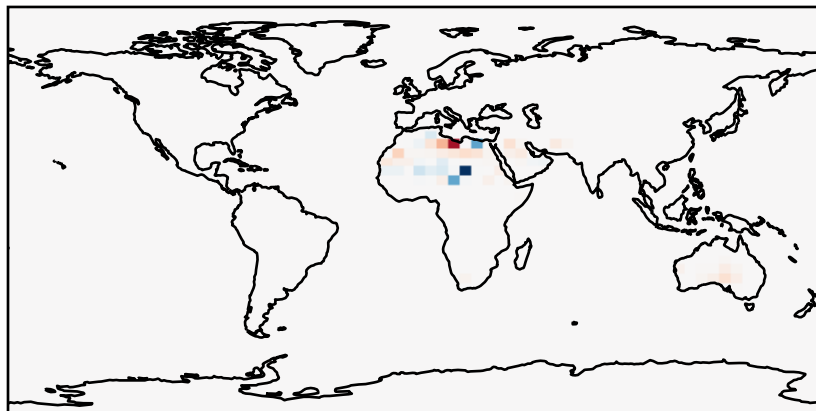
GCC\_12.9.0 (Ref)  
4.0x5.0



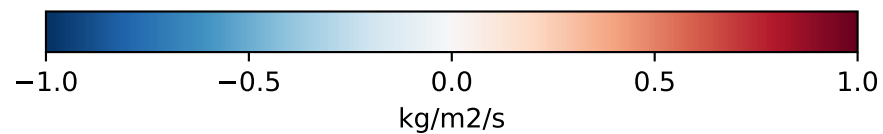
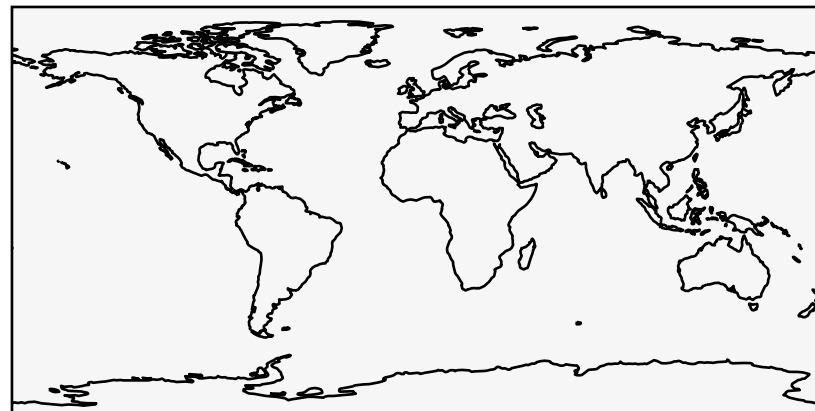
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



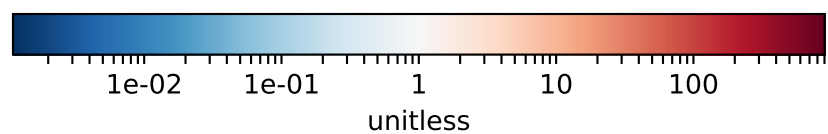
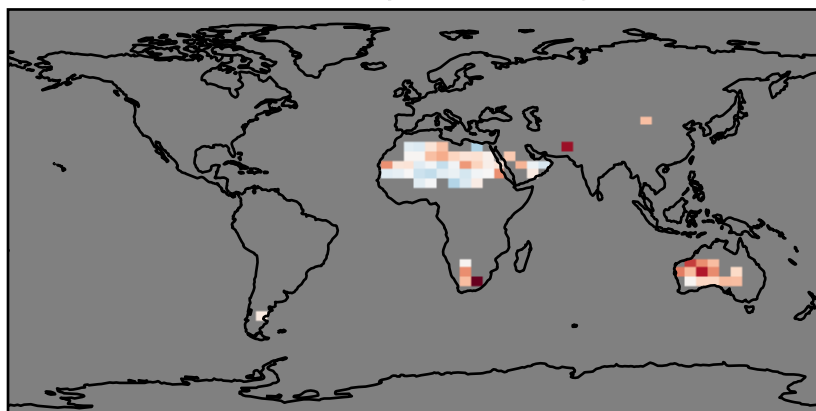
Difference  
Dev - Ref, Dynamic Range



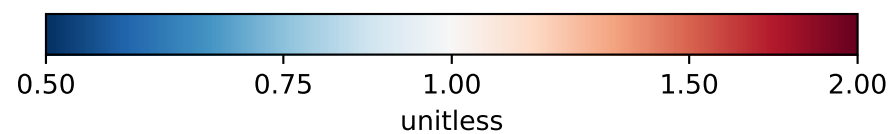
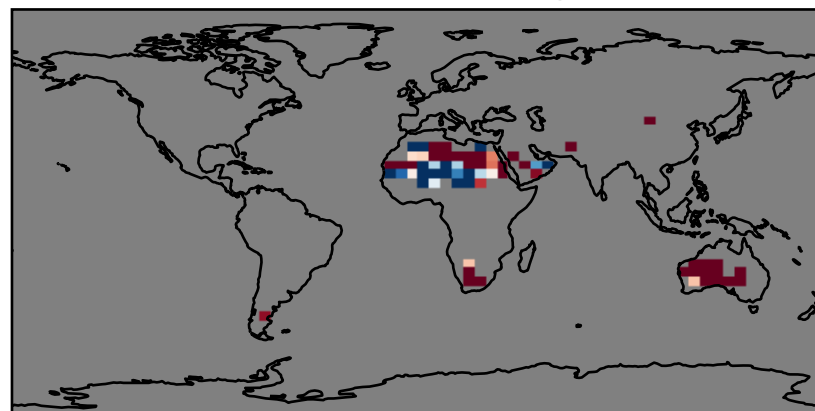
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

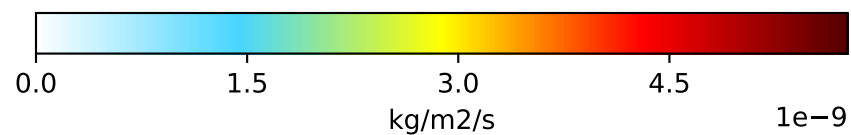
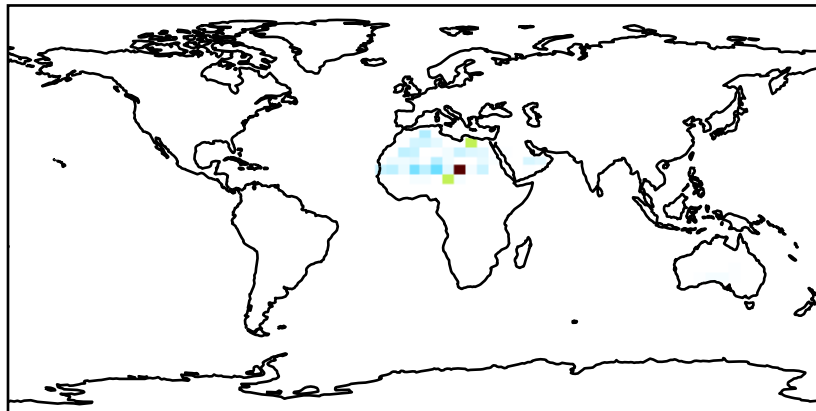


Ratio  
Dev/Ref, Fixed Range

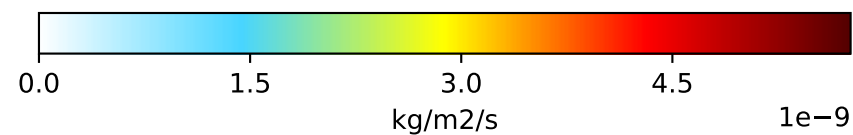
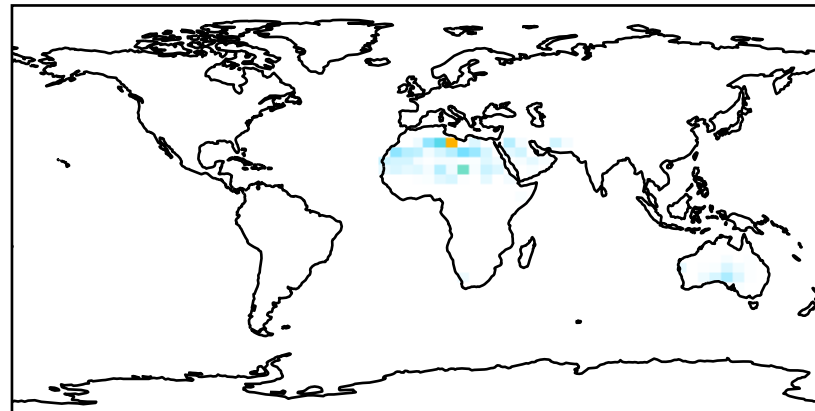


# EmisDST2\_Natural (Jan2019)

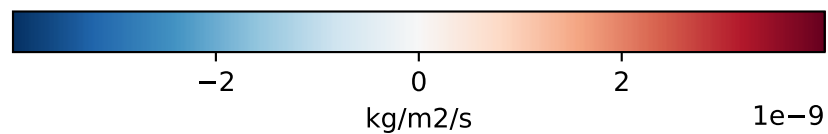
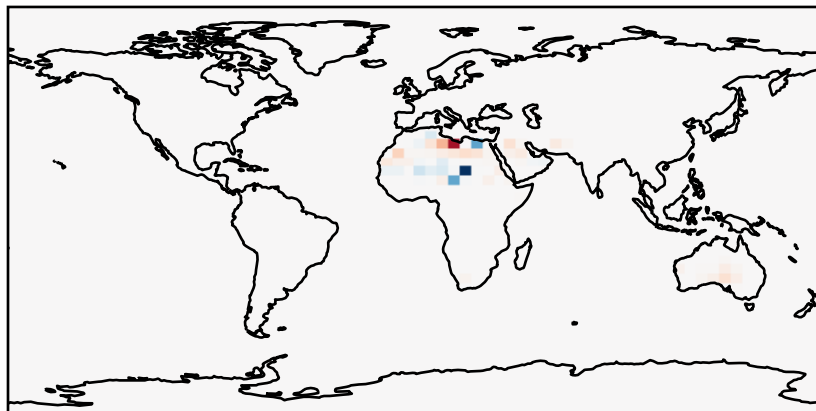
GCC\_12.9.0 (Ref)  
4.0x5.0



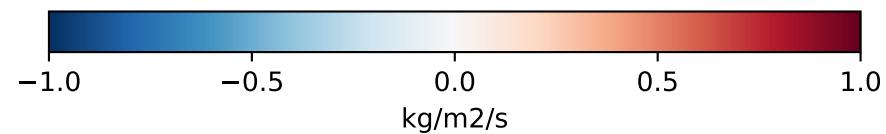
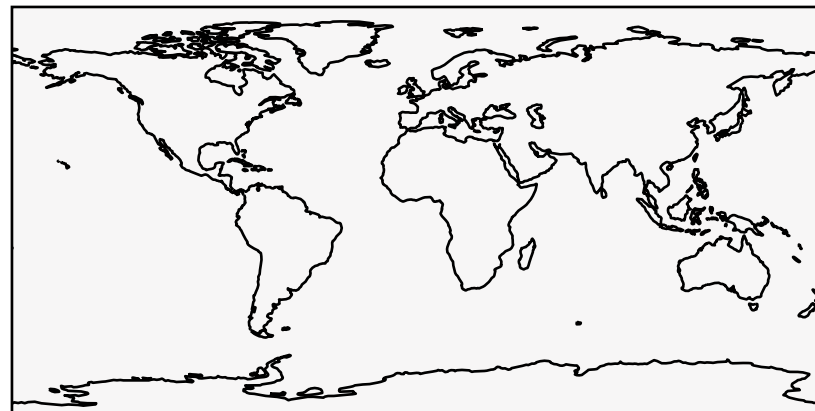
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



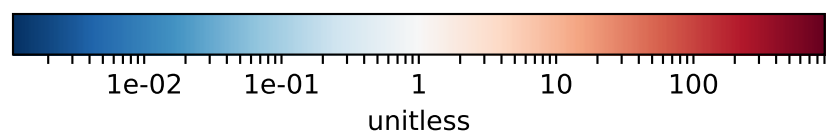
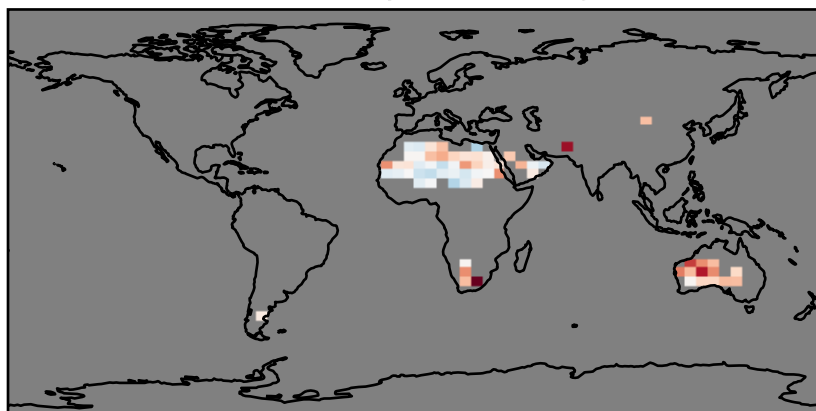
Difference  
Dev - Ref, Dynamic Range



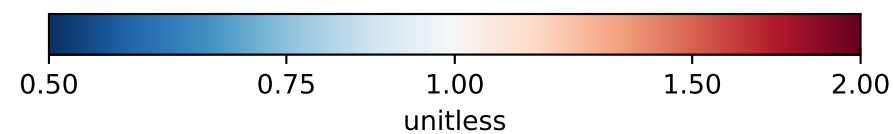
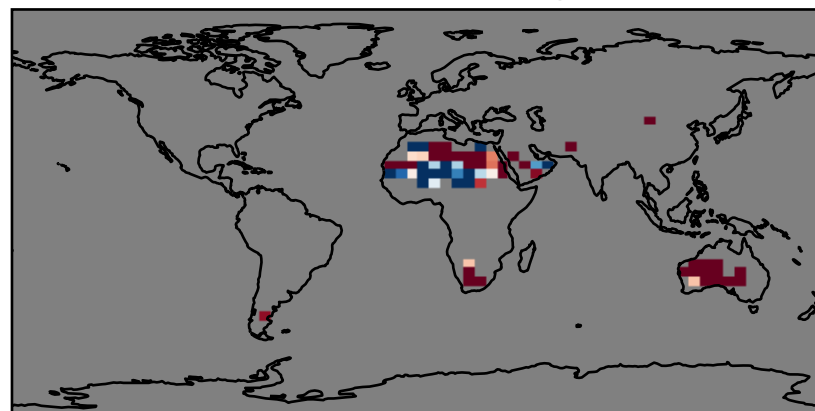
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



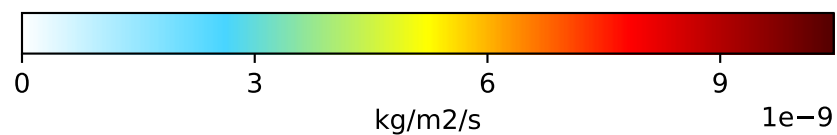
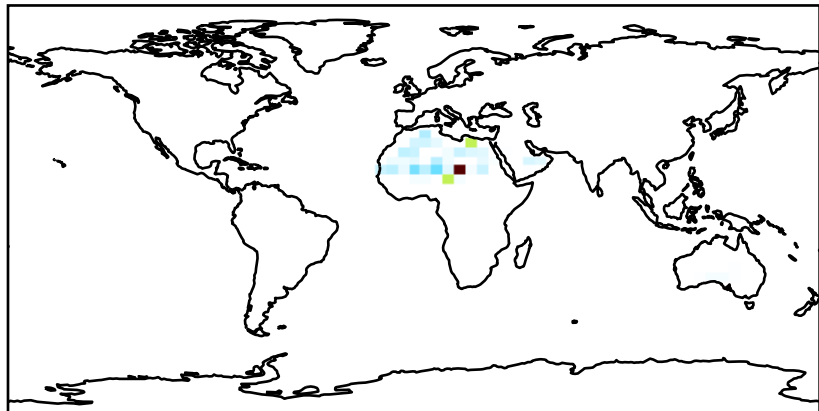
Ratio  
Dev/Ref, Fixed Range



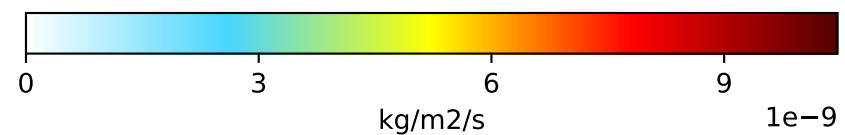
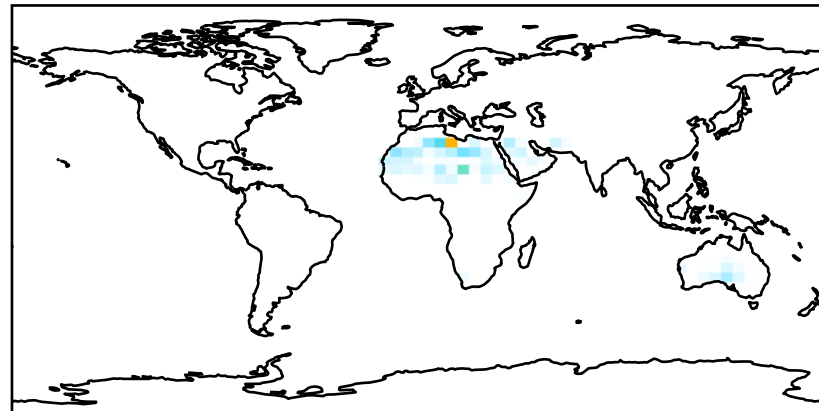


# EmisDST3\_Natural (Jan2019)

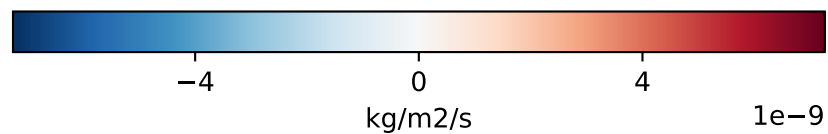
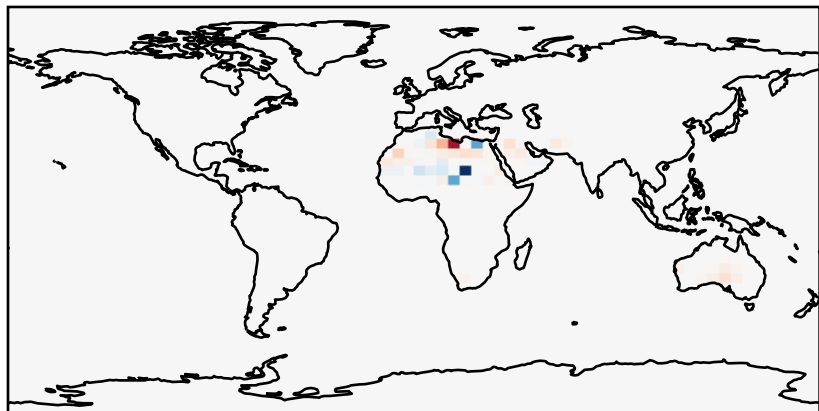
GCC\_12.9.0 (Ref)  
4.0x5.0



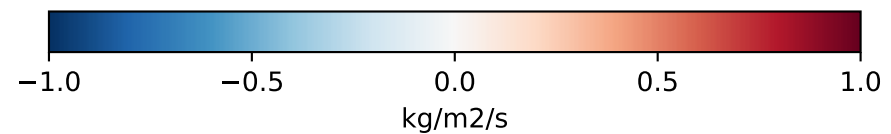
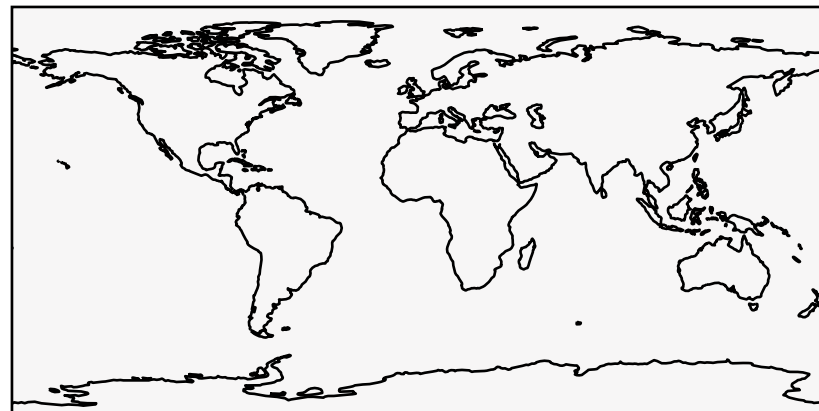
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



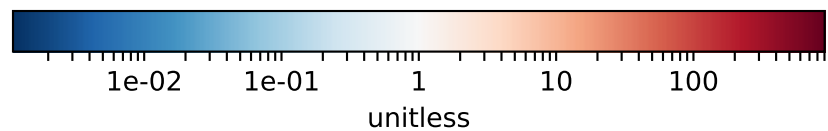
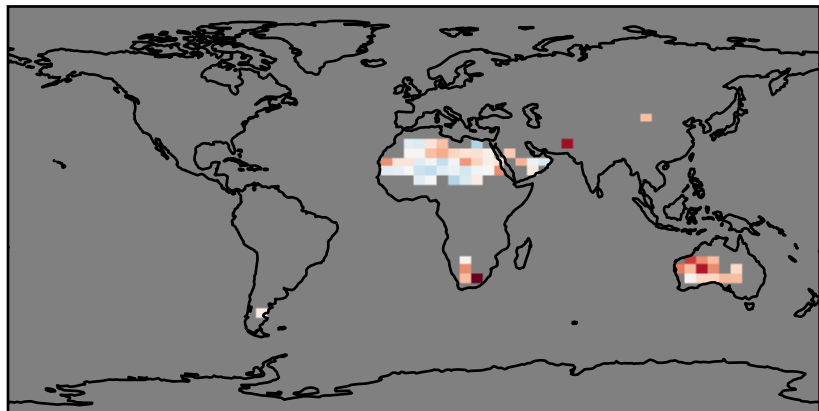
Difference  
Dev - Ref, Dynamic Range



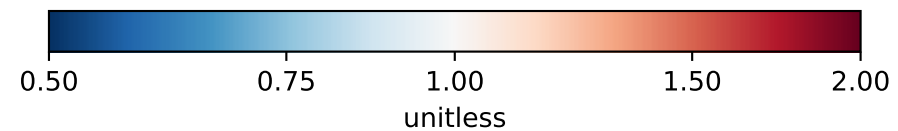
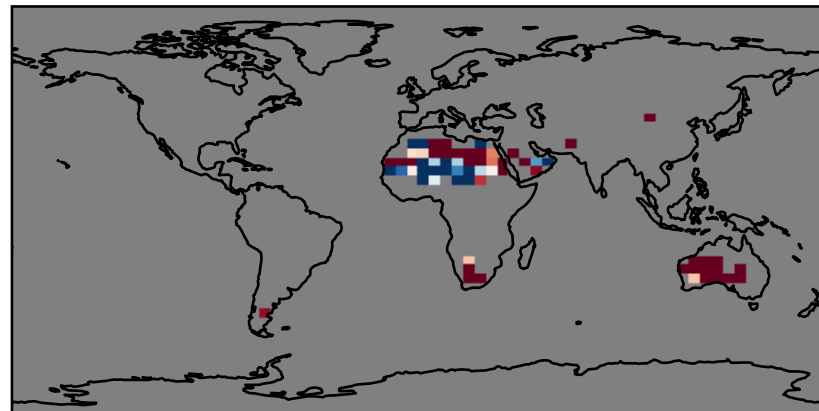
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

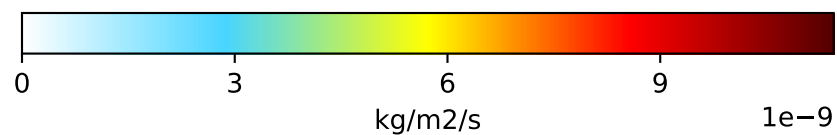
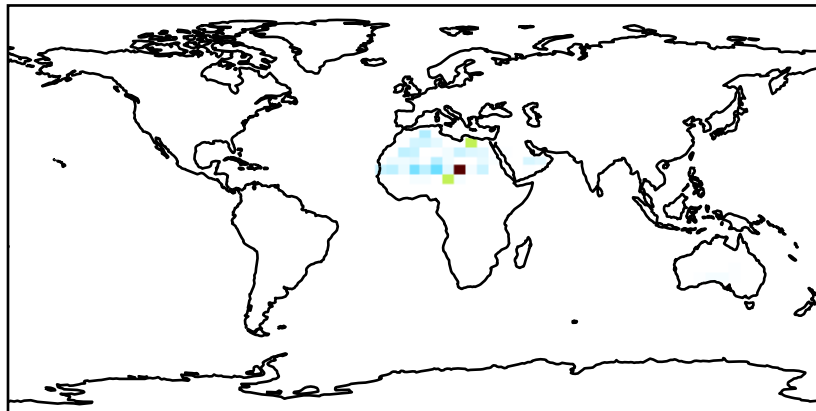


Ratio  
Dev/Ref, Fixed Range

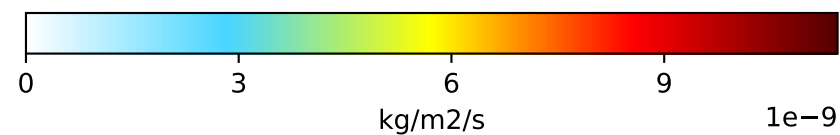
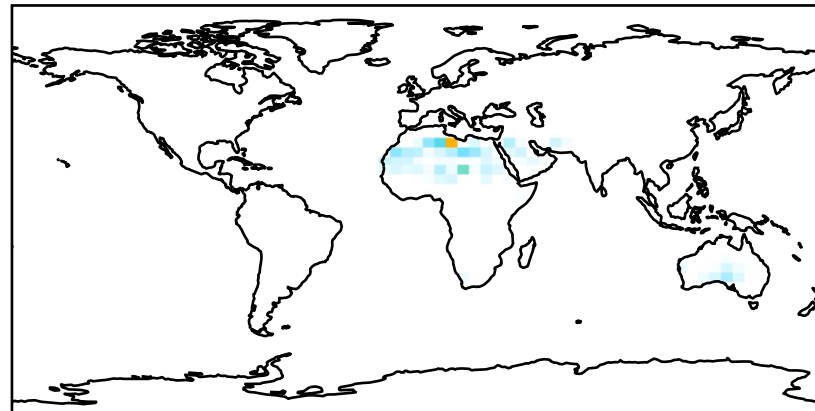


# EmisDST4\_Natural (Jan2019)

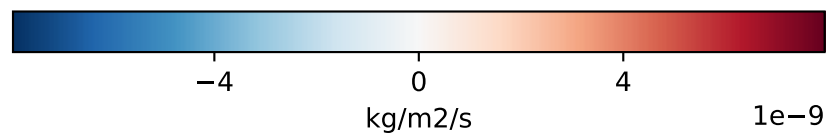
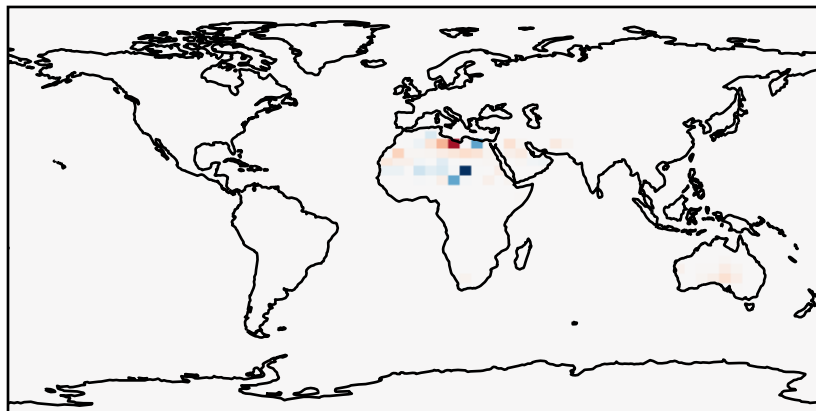
GCC\_12.9.0 (Ref)  
4.0x5.0



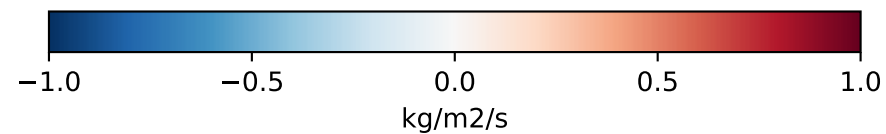
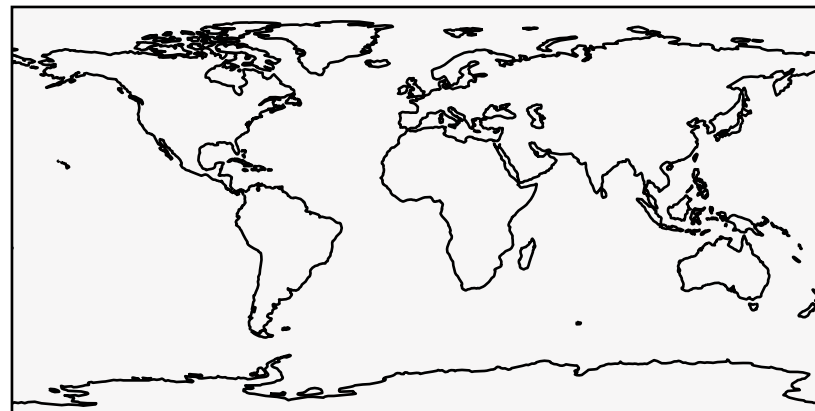
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



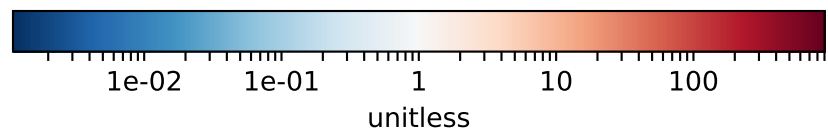
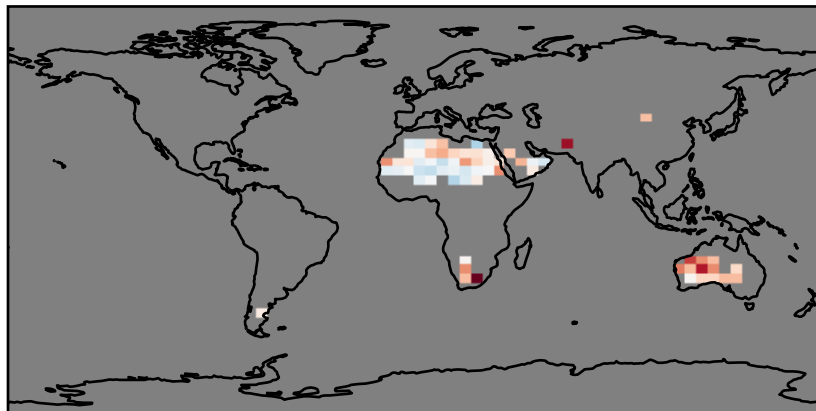
Difference  
Dev - Ref, Dynamic Range



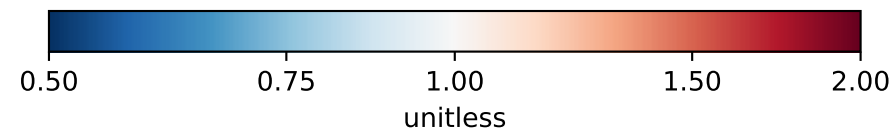
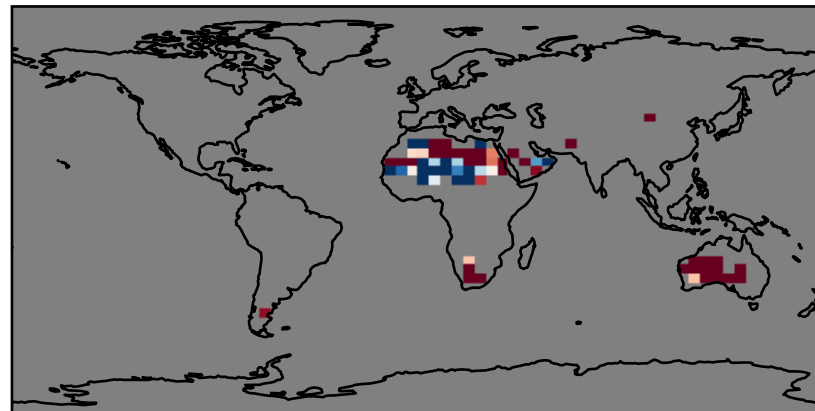
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

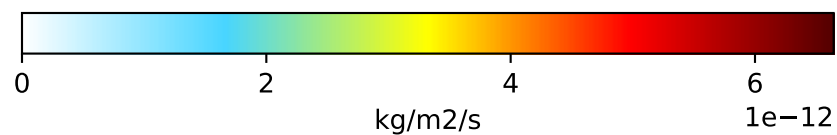
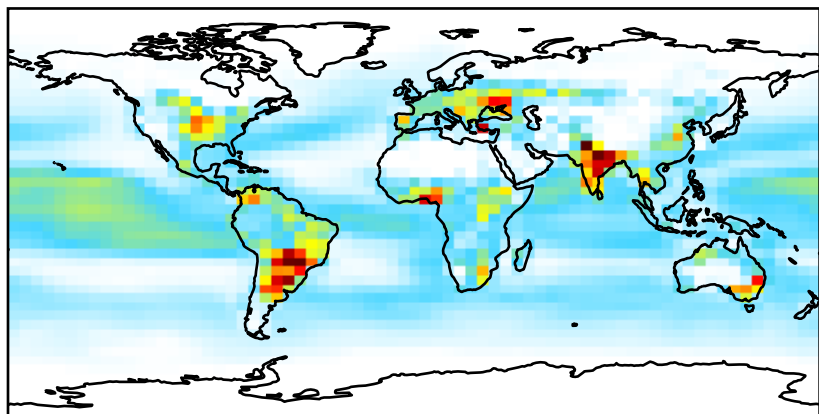


Ratio  
Dev/Ref, Fixed Range

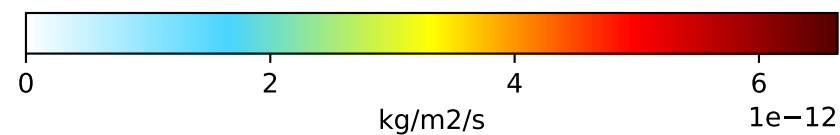
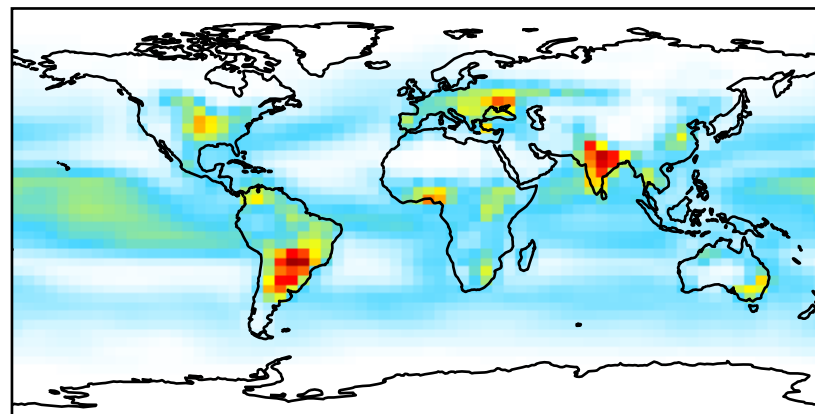


# EmisNH3\_Natural (Jan2019)

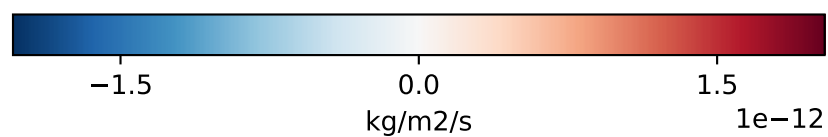
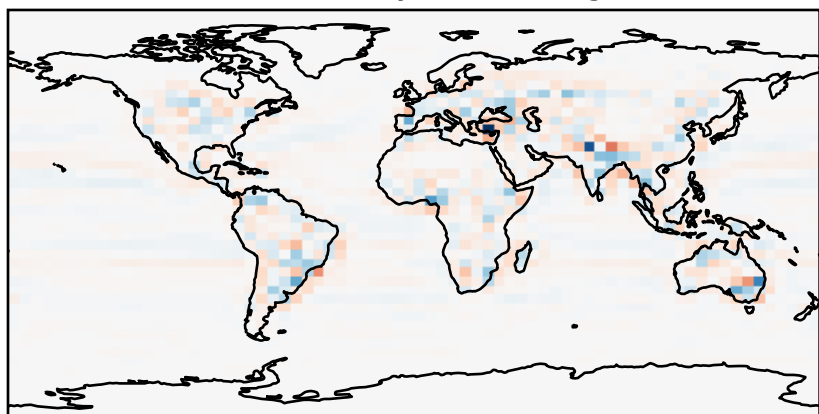
GCC\_12.9.0 (Ref)  
4.0x5.0



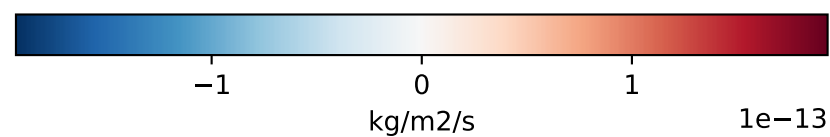
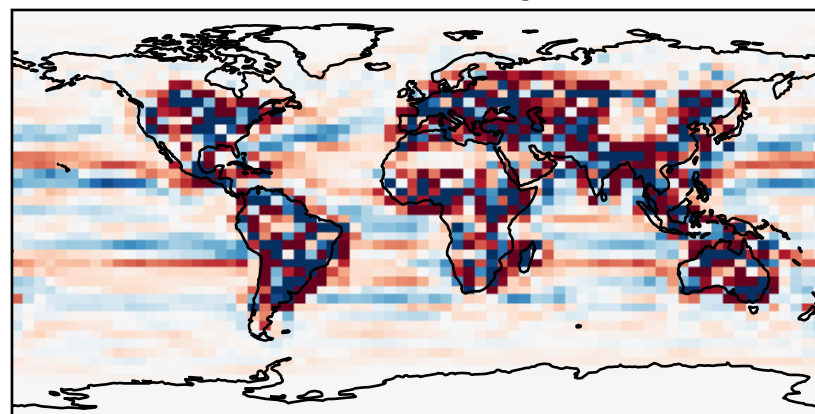
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



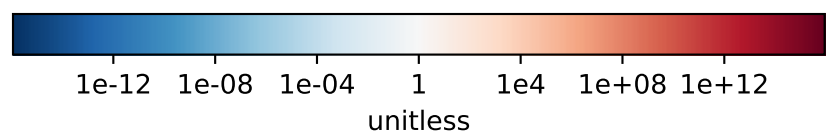
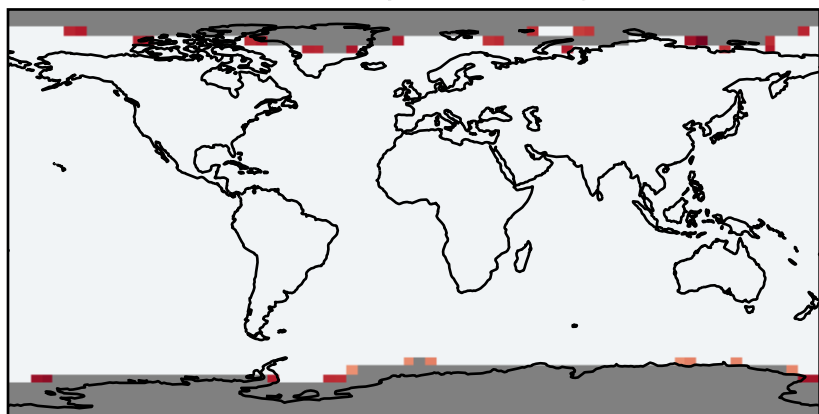
Difference  
Dev - Ref, Dynamic Range



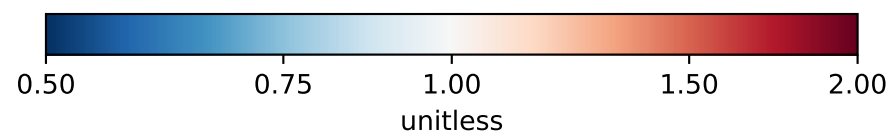
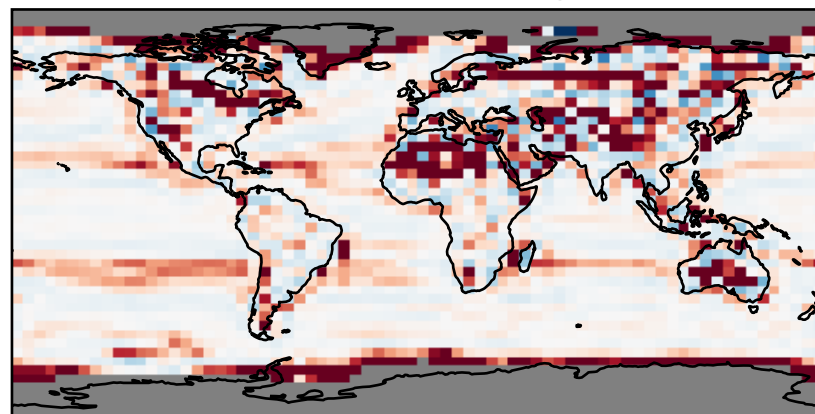
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



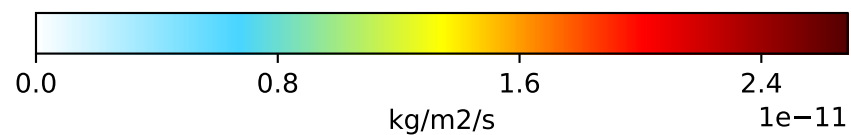
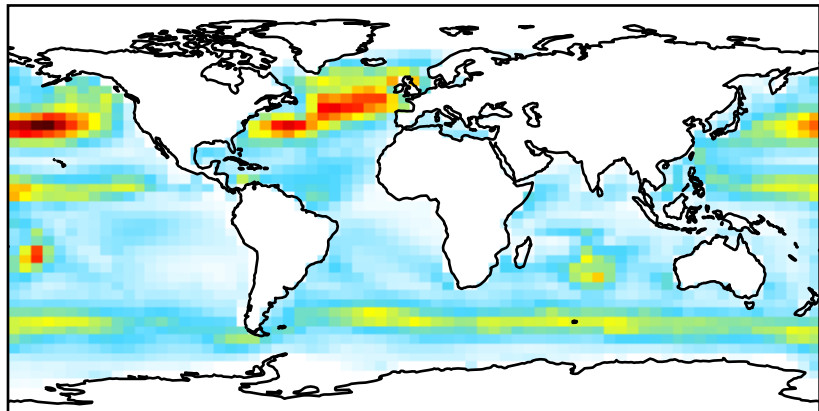
Ratio  
Dev/Ref, Fixed Range



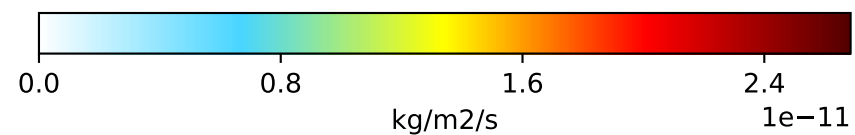
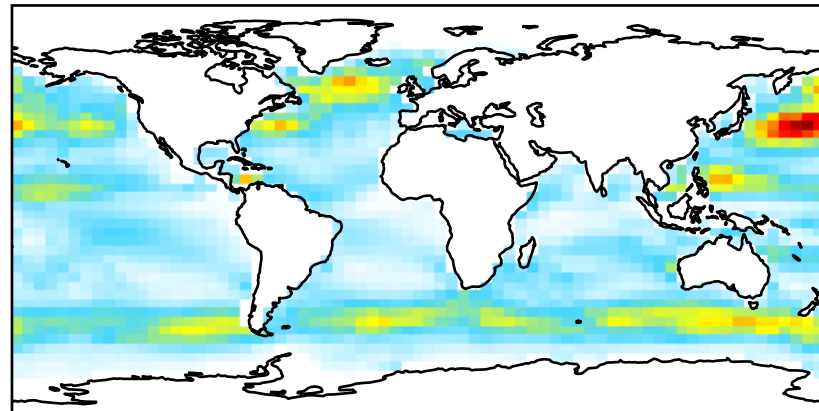


# EmisSALA\_Natural (Jan2019)

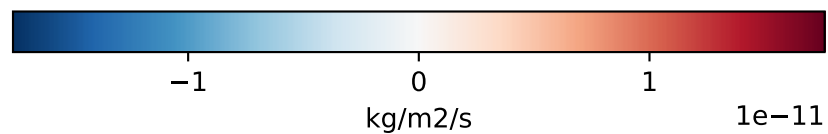
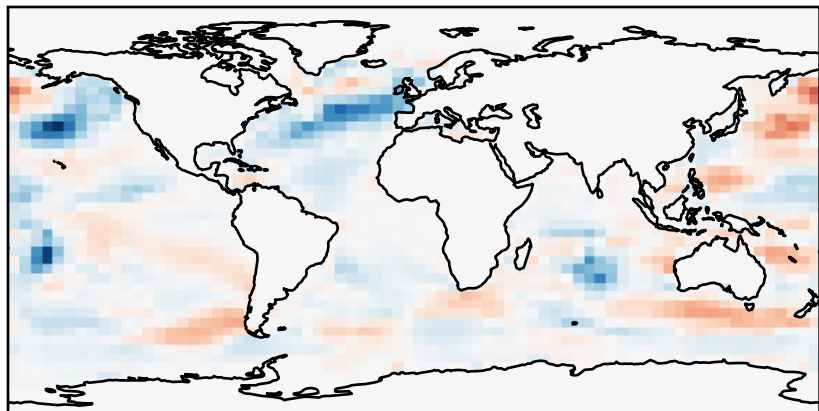
GCC\_12.9.0 (Ref)  
4.0x5.0



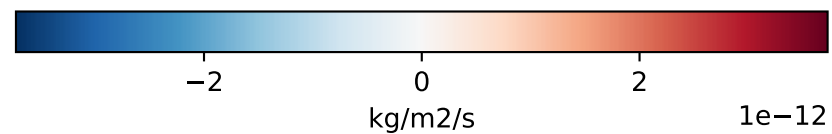
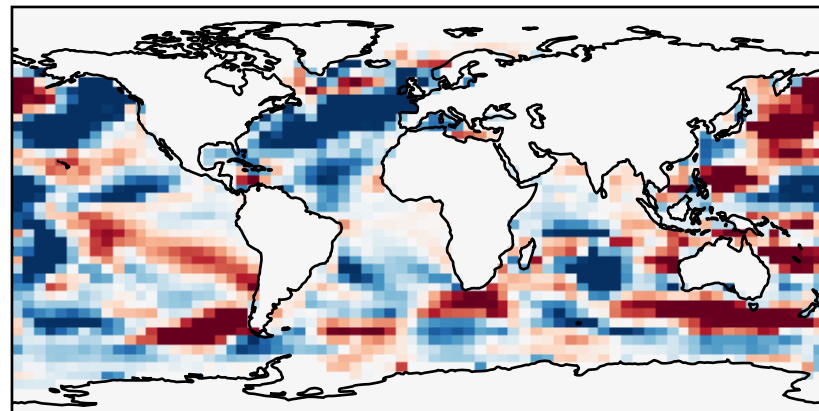
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



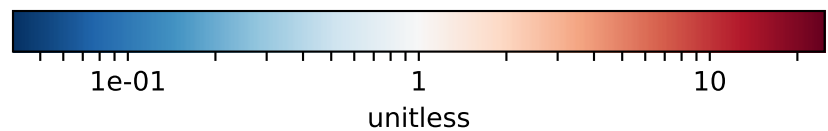
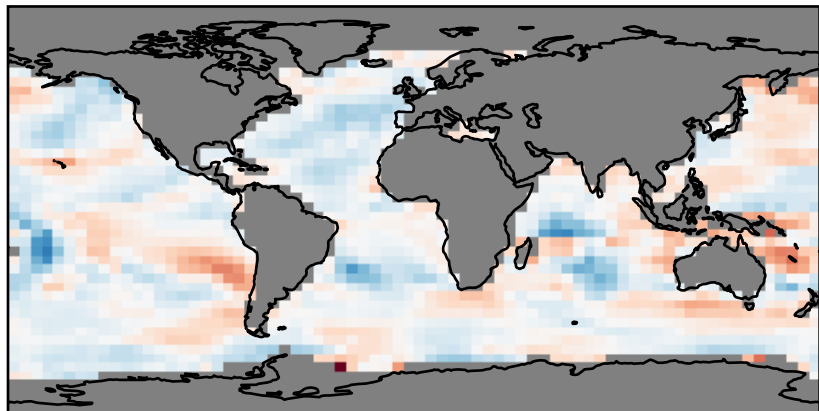
Difference  
Dev - Ref, Dynamic Range



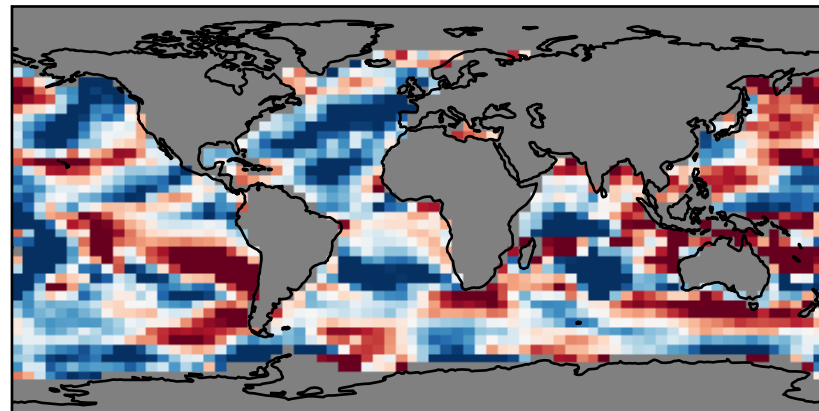
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

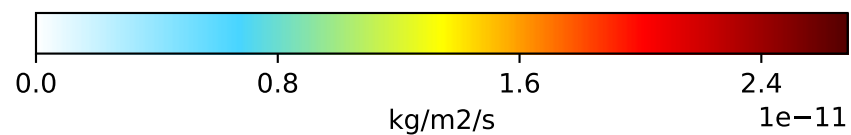
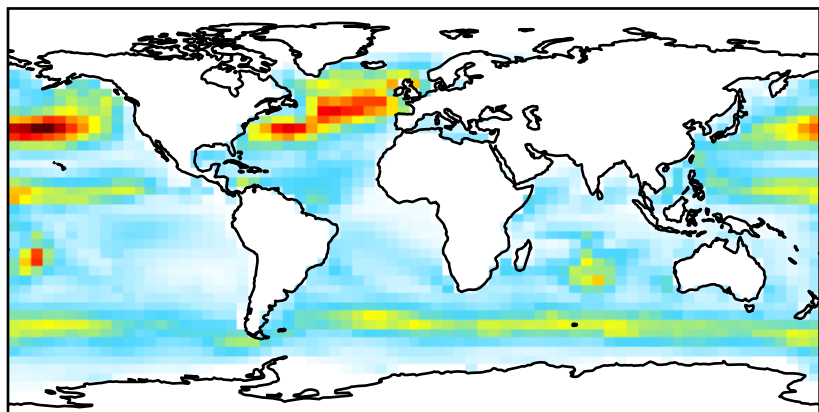


Ratio  
Dev/Ref, Fixed Range

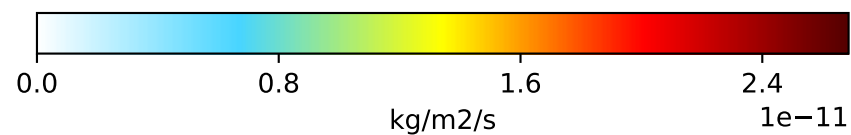
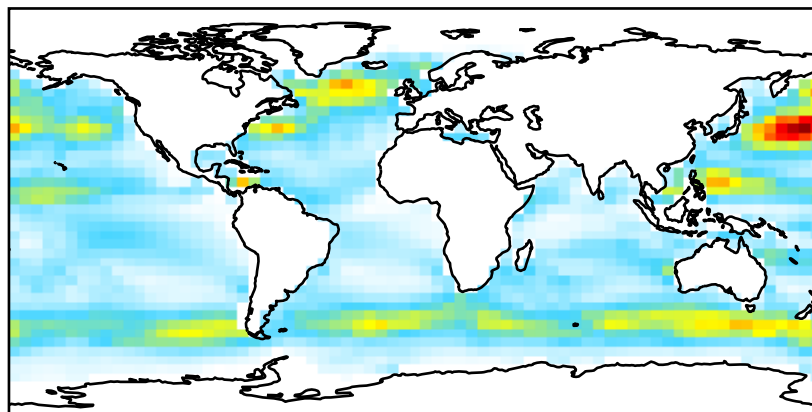


# EmisSALAAL\_Natural (Jan2019)

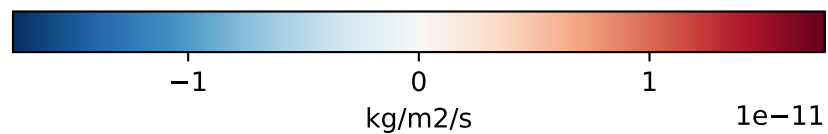
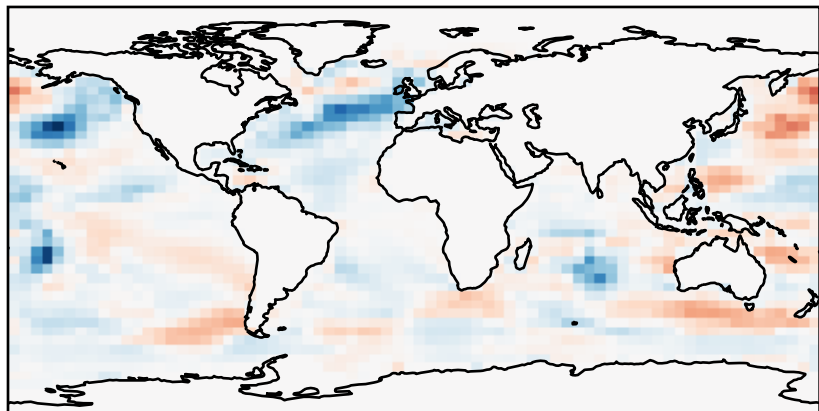
GCC\_12.9.0 (Ref)  
4.0x5.0



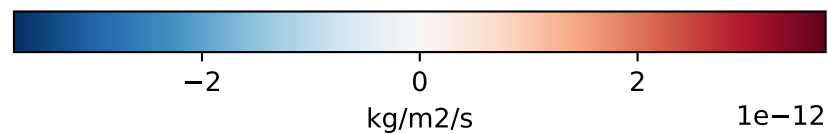
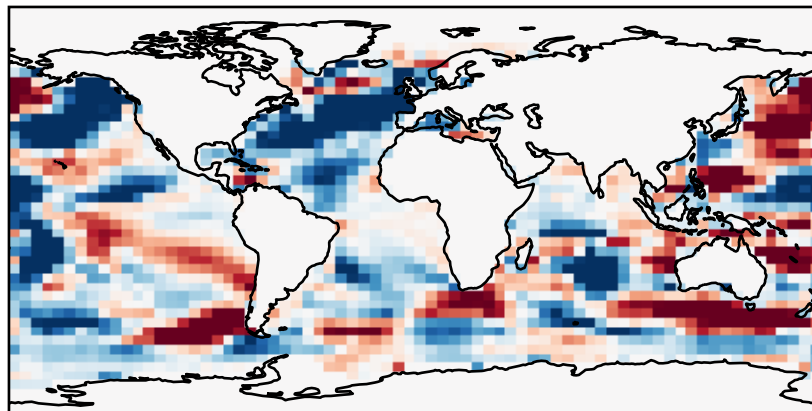
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



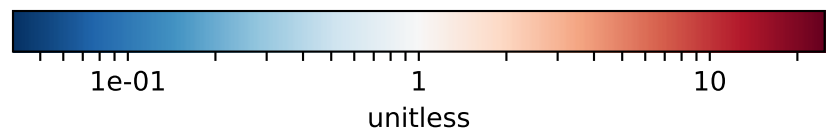
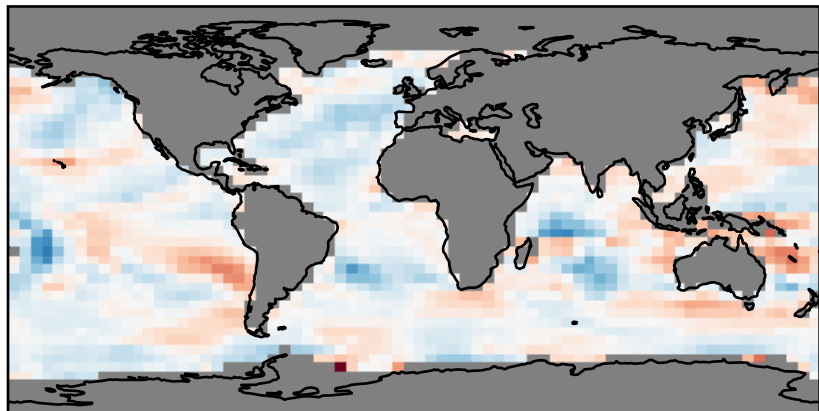
Difference  
Dev - Ref, Dynamic Range



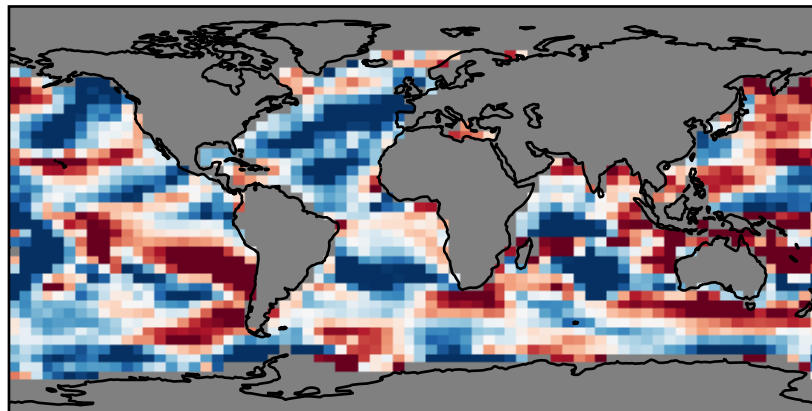
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

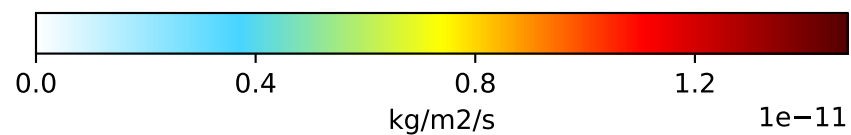
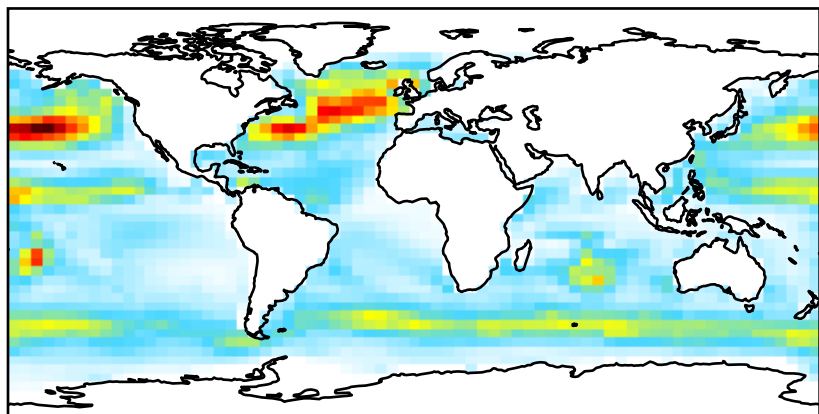


Ratio  
Dev/Ref, Fixed Range

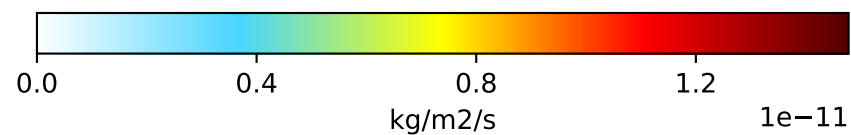
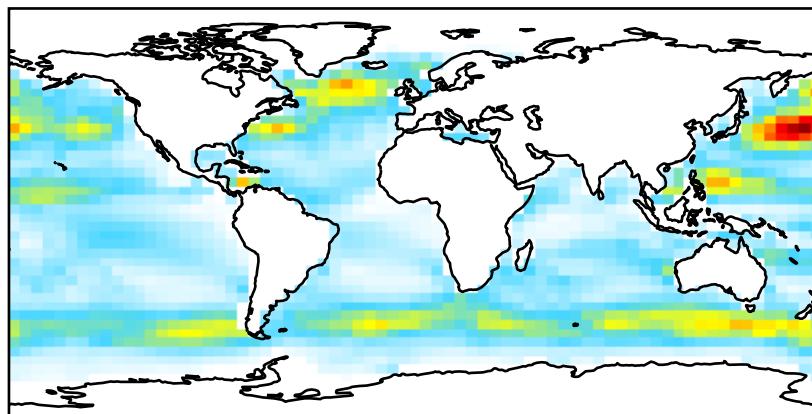


# EmisSALACL\_Natural (Jan2019)

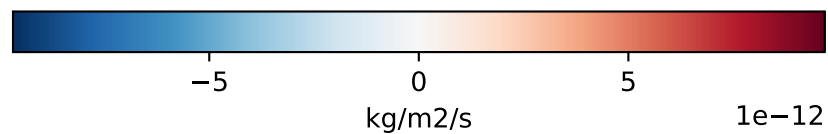
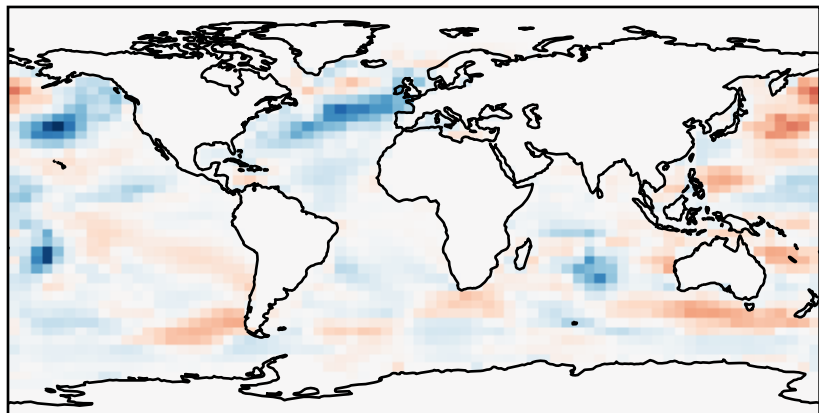
GCC\_12.9.0 (Ref)  
4.0x5.0



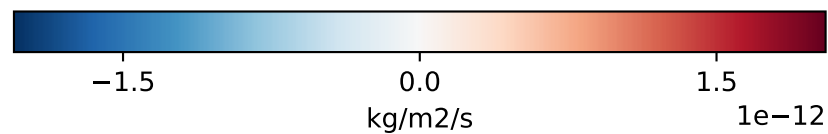
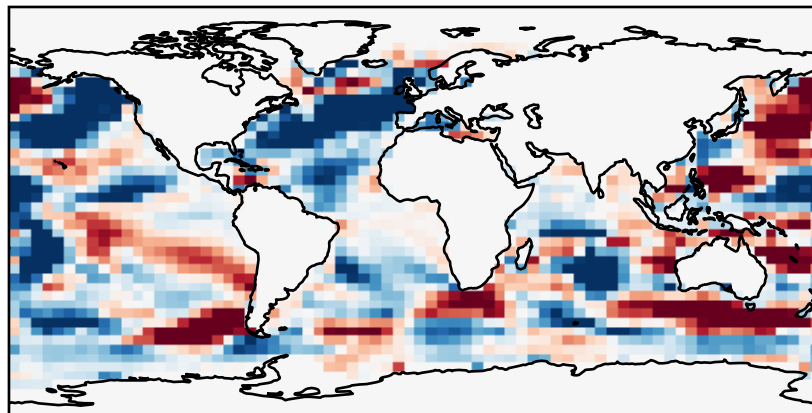
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



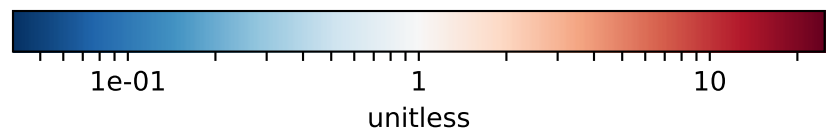
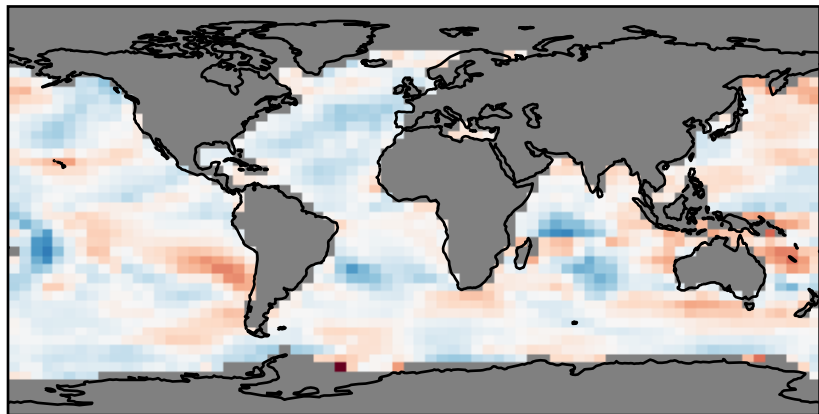
Difference  
Dev - Ref, Dynamic Range



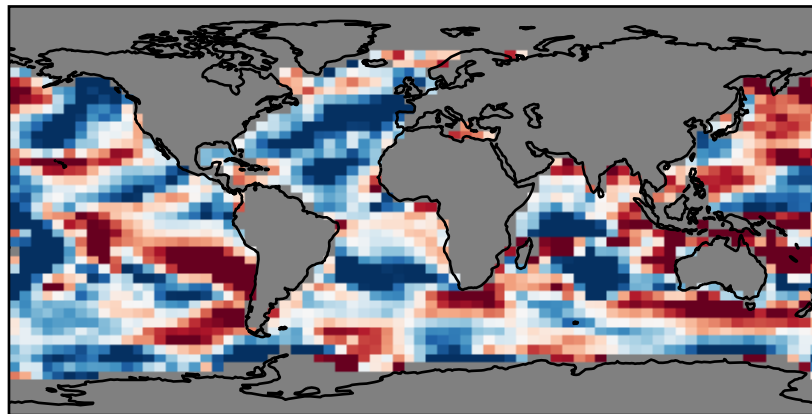
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

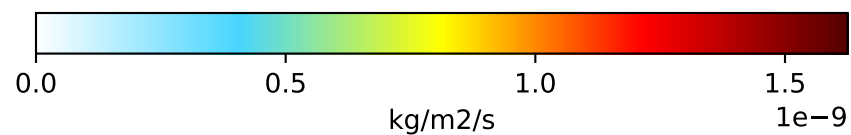
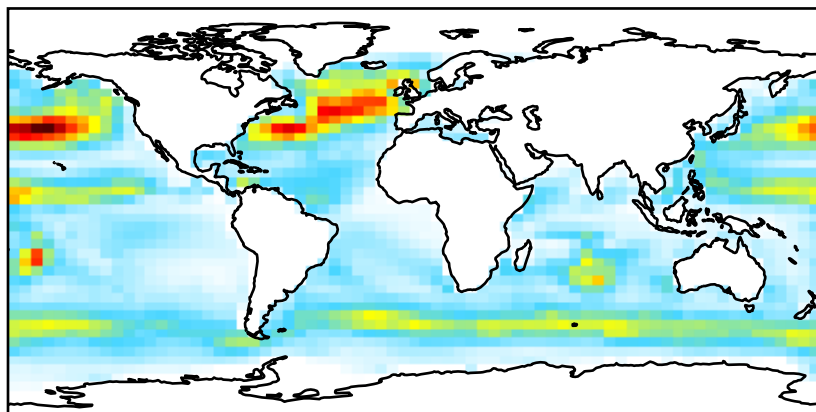


Ratio  
Dev/Ref, Fixed Range

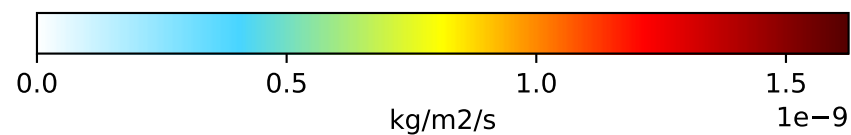
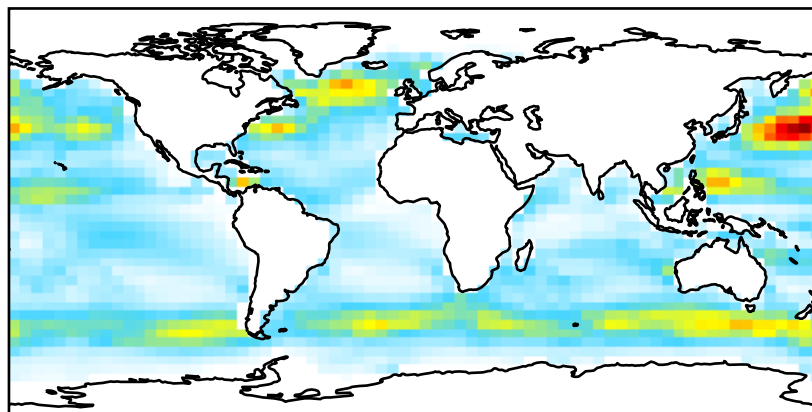


# EmisSALC\_Natural (Jan2019)

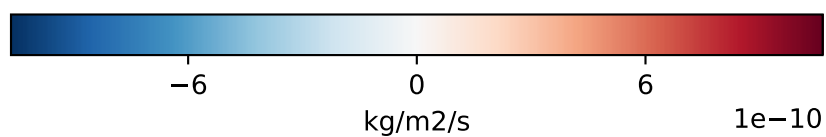
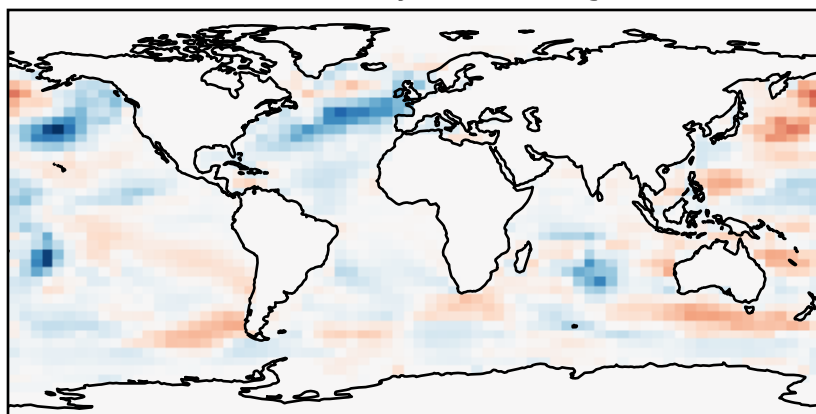
GCC\_12.9.0 (Ref)  
4.0x5.0



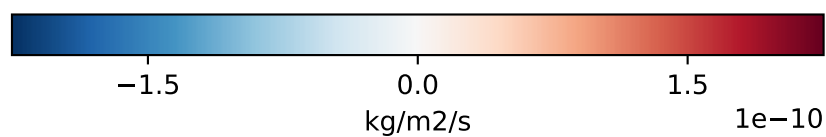
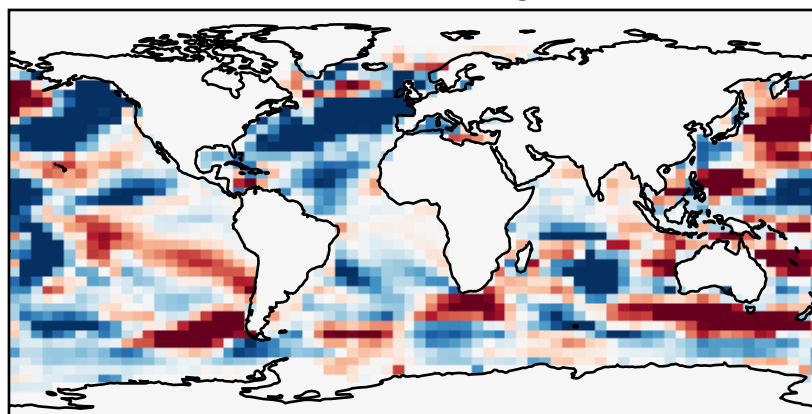
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



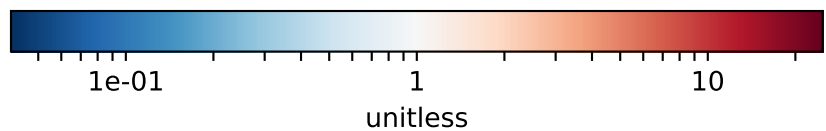
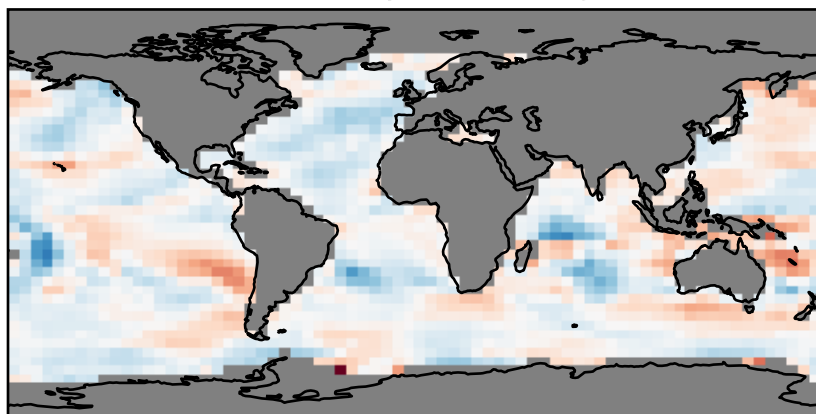
Difference  
Dev - Ref, Dynamic Range



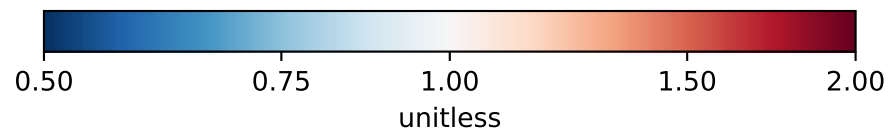
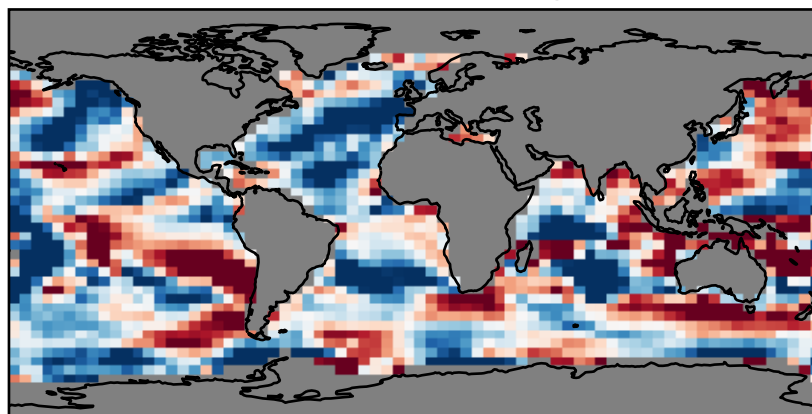
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



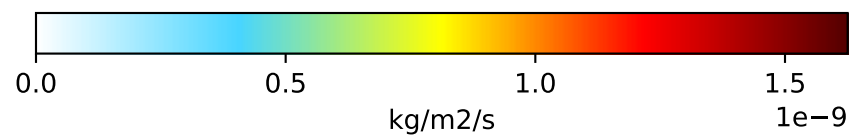
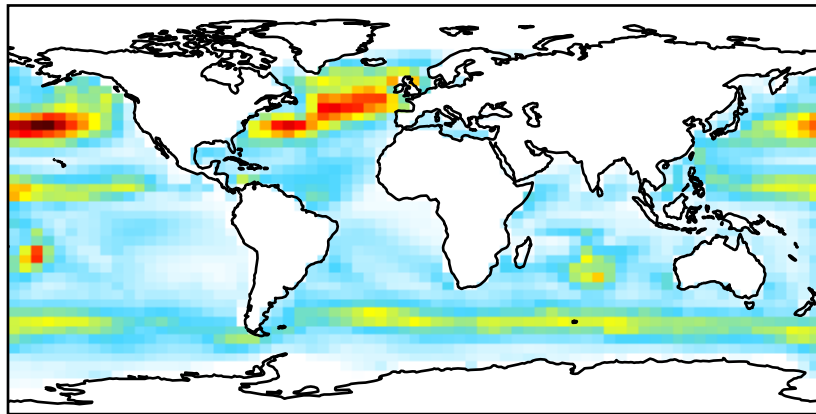
Ratio  
Dev/Ref, Fixed Range



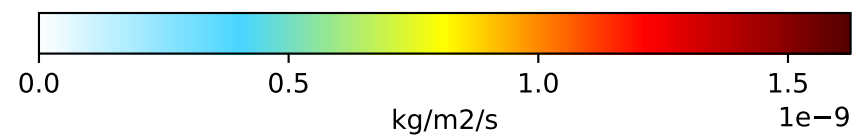
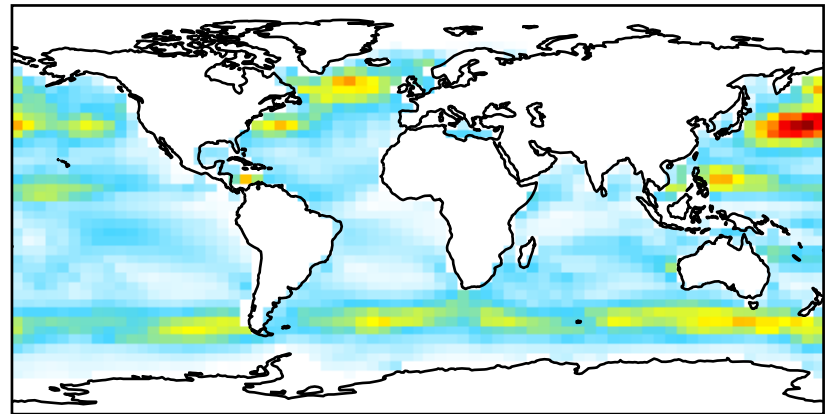


# EmisSALCAL\_Natural (Jan2019)

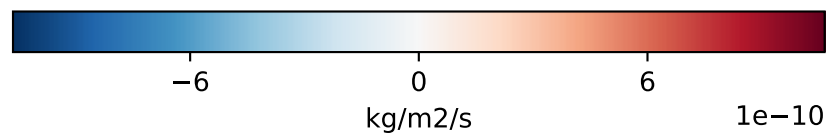
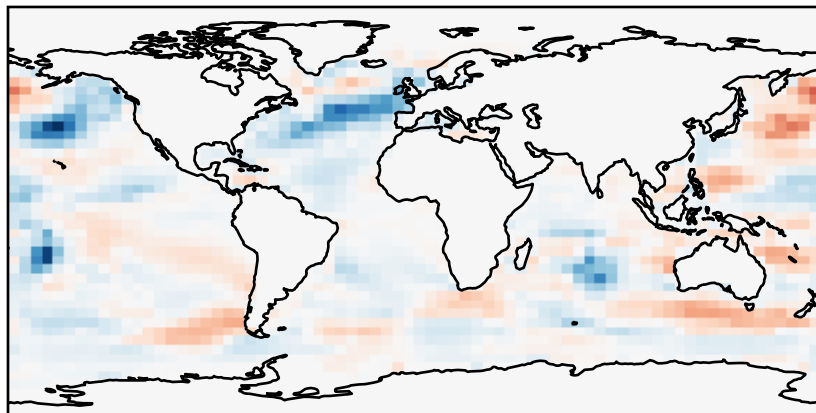
GCC\_12.9.0 (Ref)  
4.0x5.0



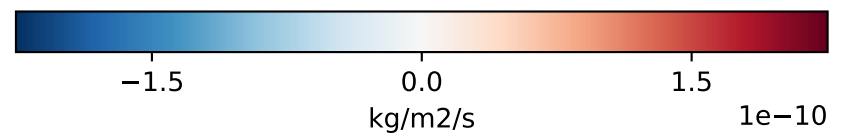
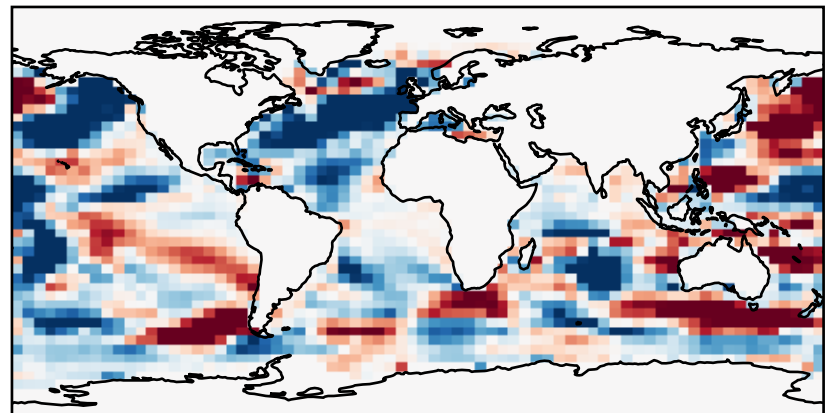
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



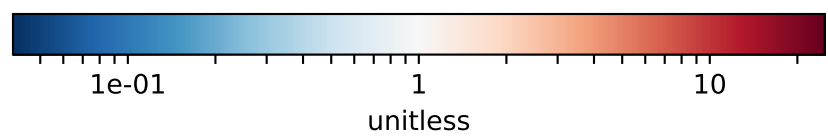
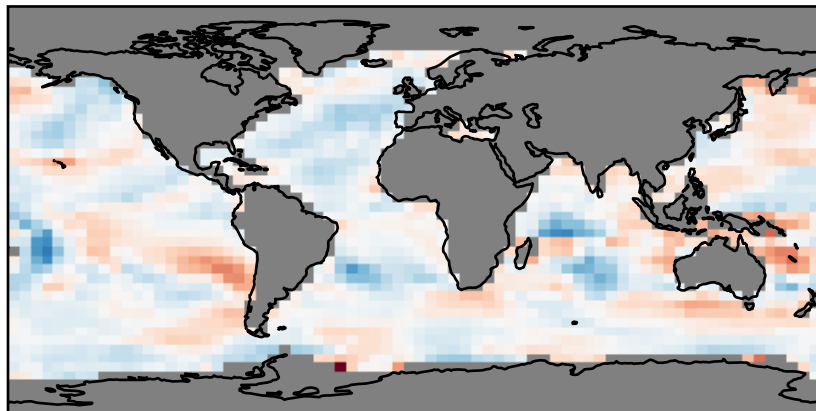
Difference  
Dev - Ref, Dynamic Range



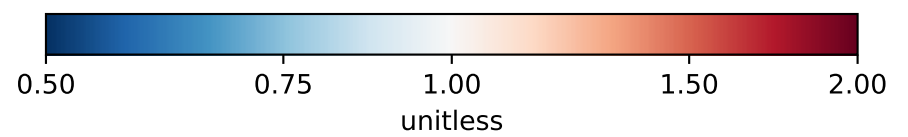
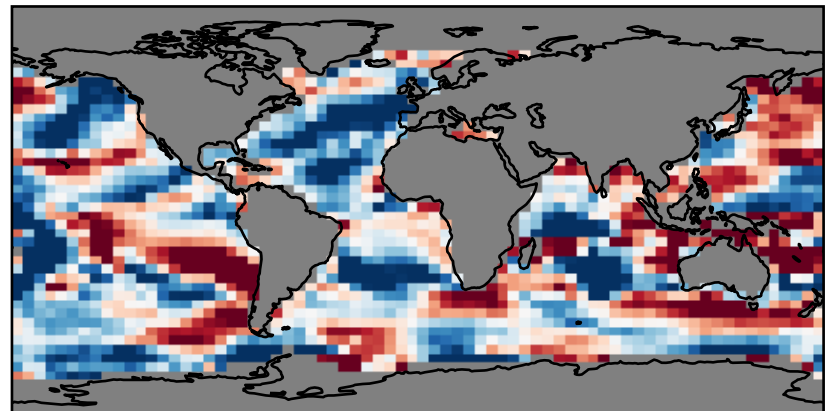
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range





# EmisSALCCL\_Natural (Jan2019)

