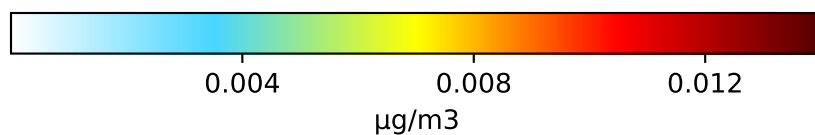
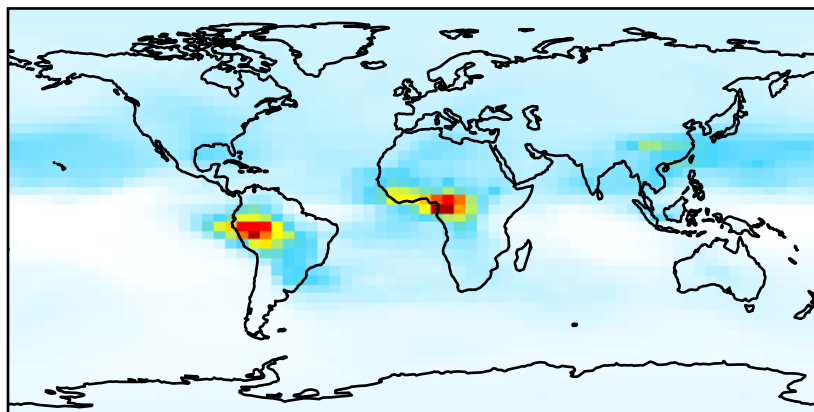
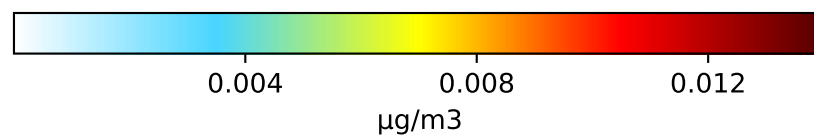
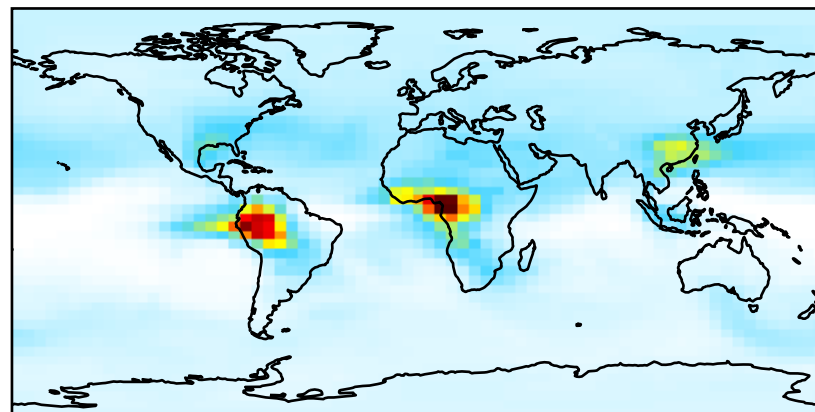


# SpeciesConc\_TSOA0 (Apr2019)

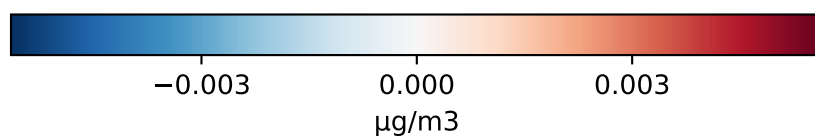
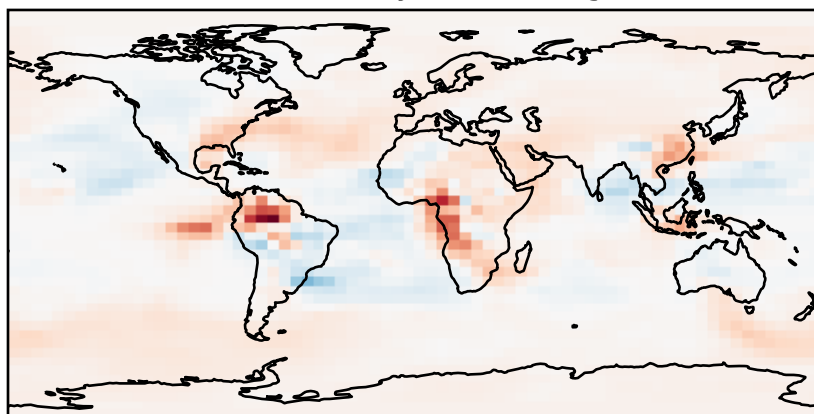
GCC\_12.9.0 (Ref)  
4.0x5.0



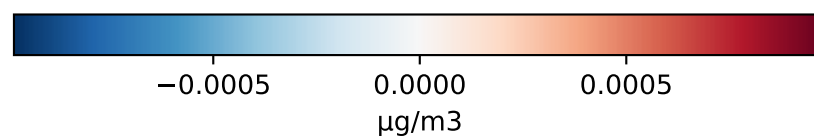
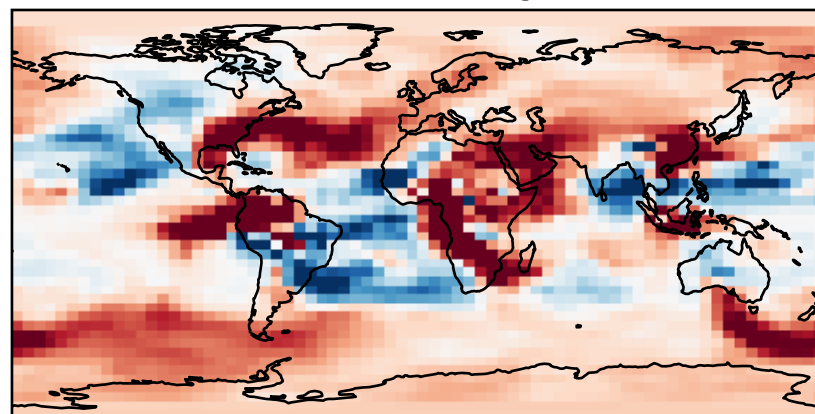
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



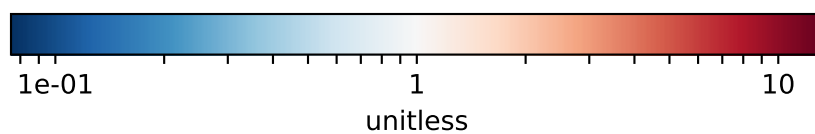
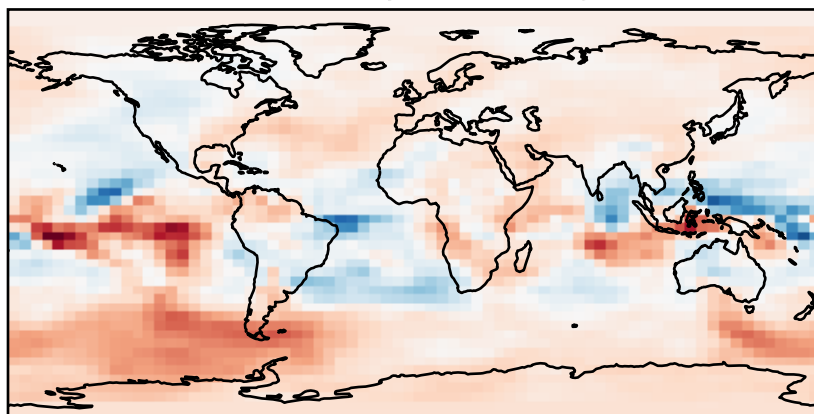
Difference  
Dev - Ref, Dynamic Range



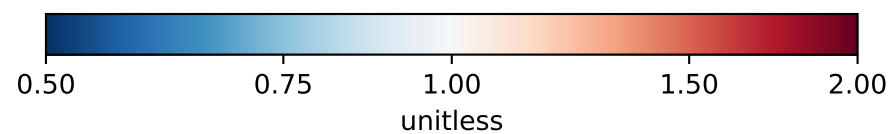
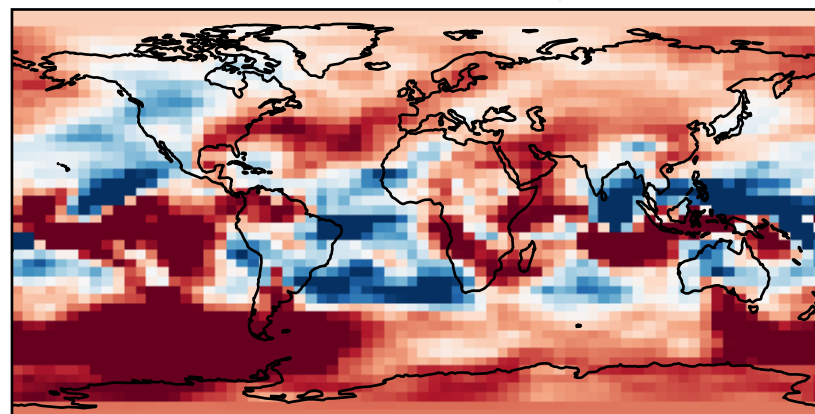
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

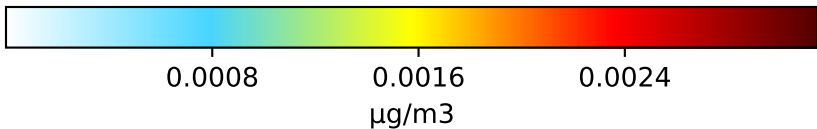
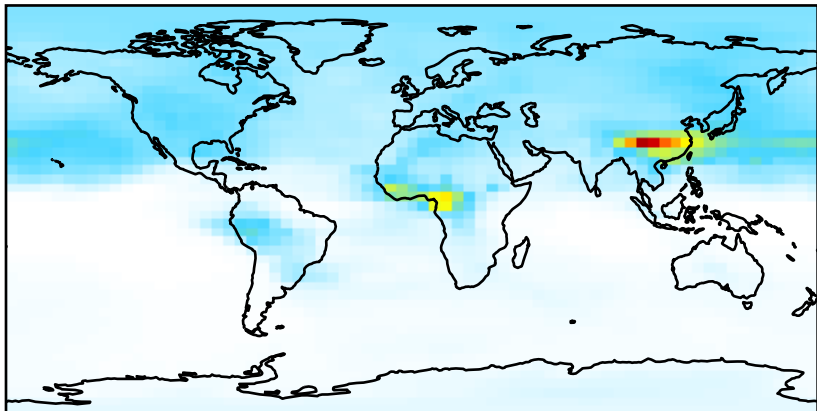


Ratio  
Dev/Ref, Fixed Range

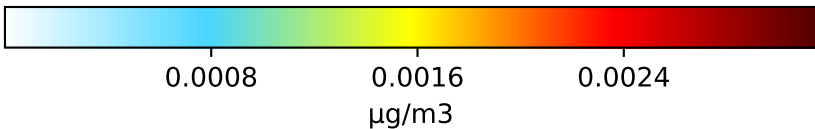
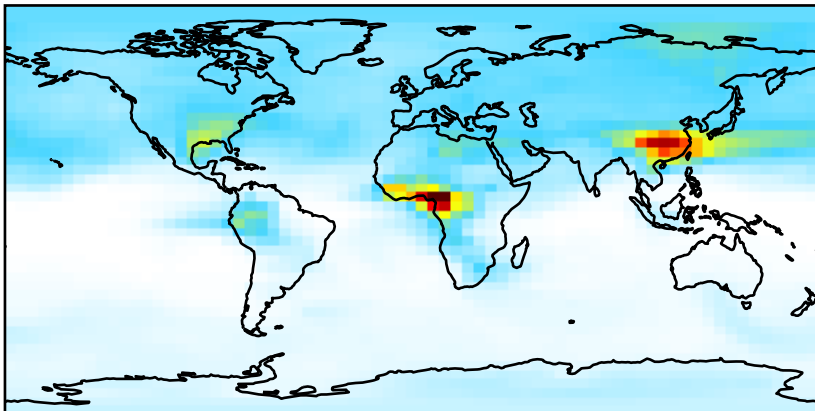


# SpeciesConc\_TSOA1 (Apr2019)

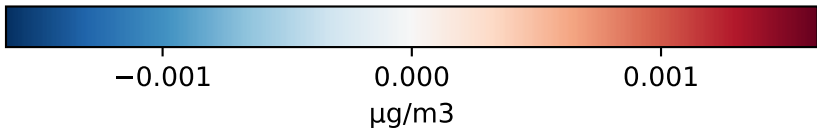
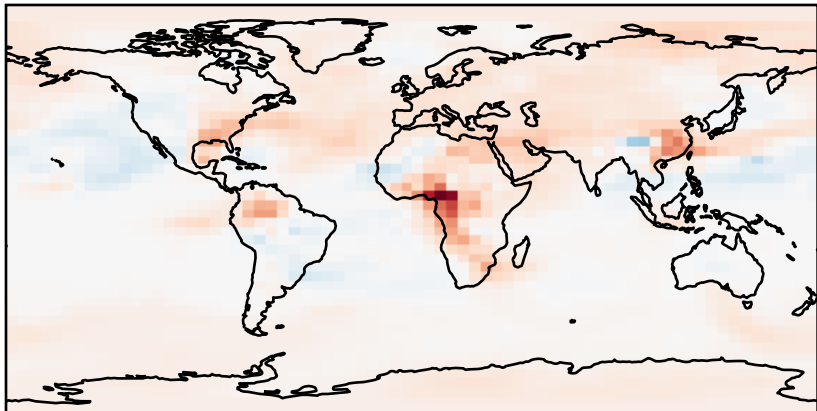
GCC\_12.9.0 (Ref)  
4.0x5.0



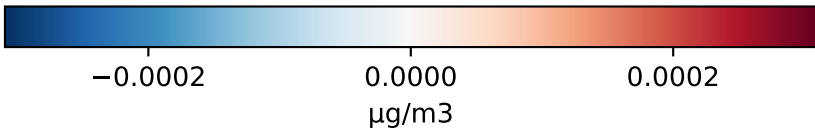
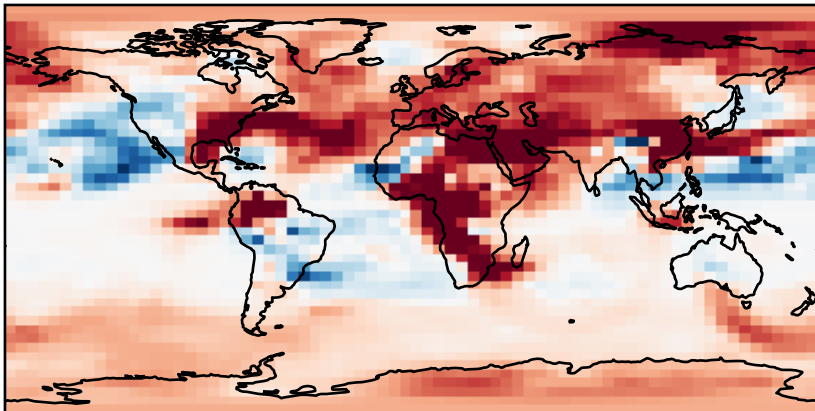
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



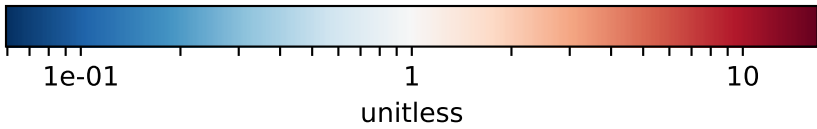
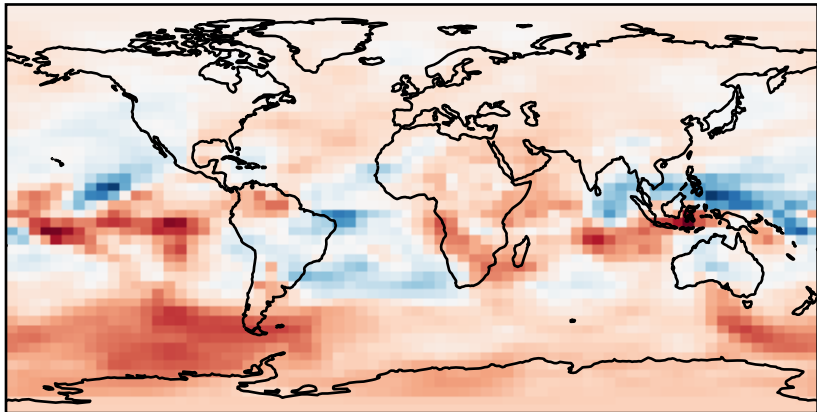
Difference  
Dev - Ref, Dynamic Range



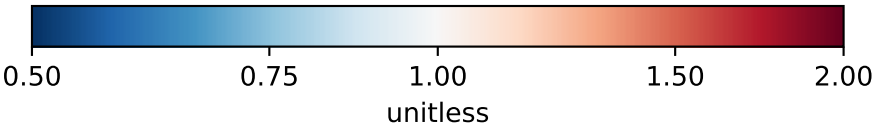
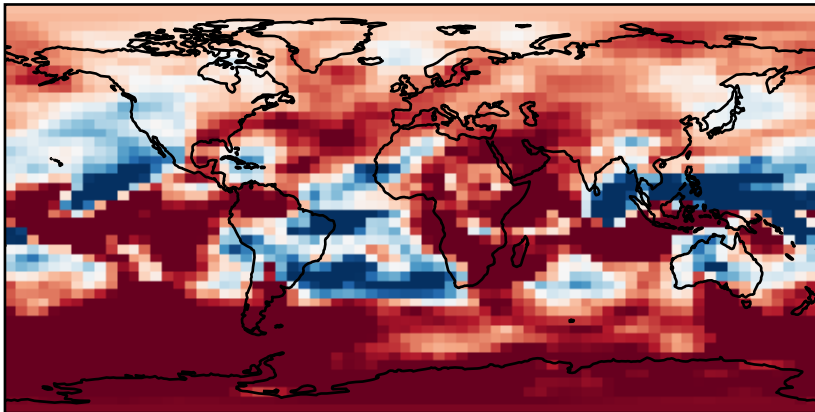
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

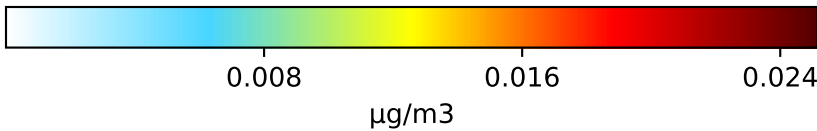
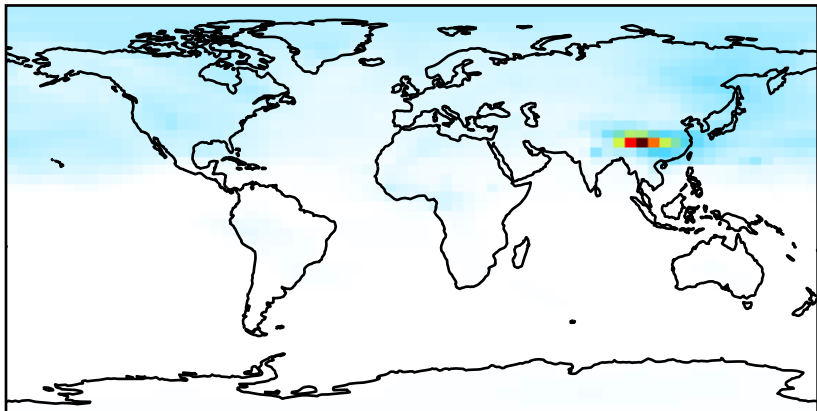


Ratio  
Dev/Ref, Fixed Range

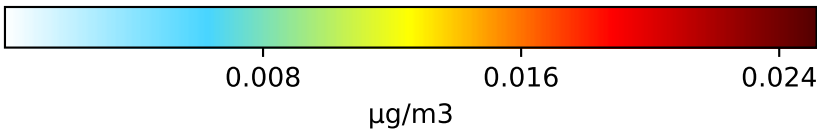
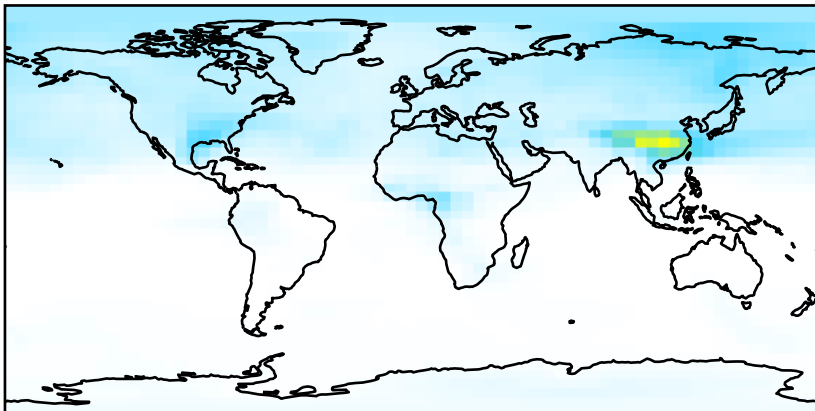


# SpeciesConc\_TSOA2 (Apr2019)

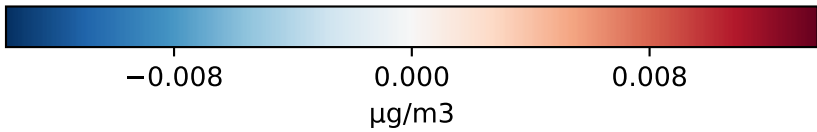
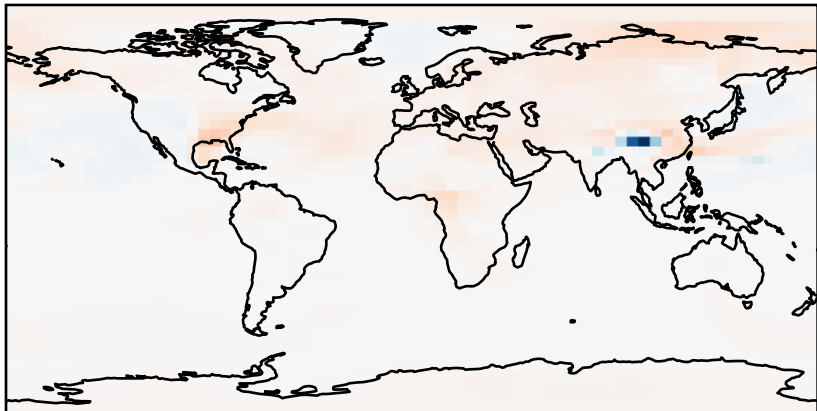
GCC\_12.9.0 (Ref)  
4.0x5.0



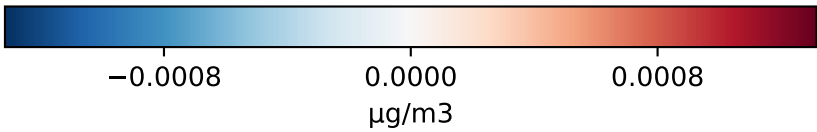
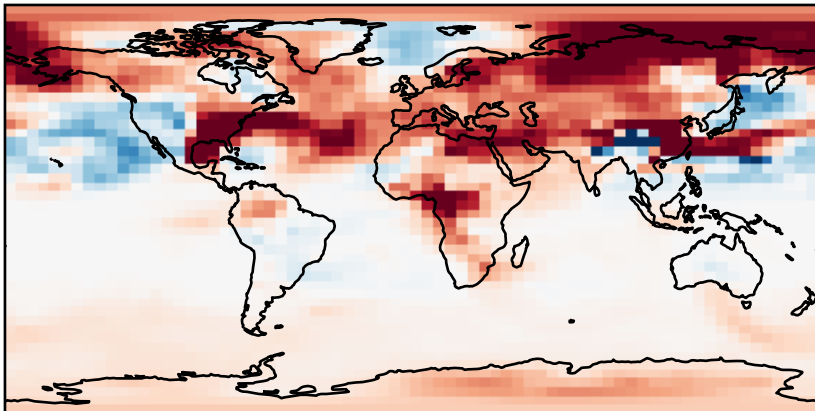
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



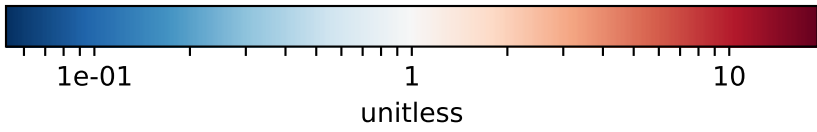
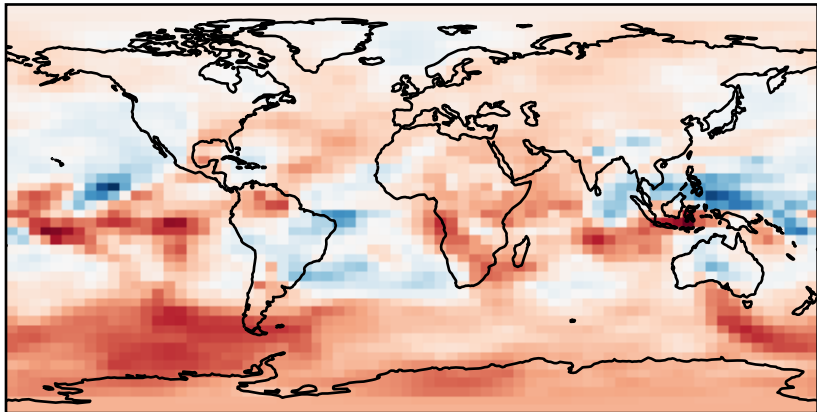
Difference  
Dev - Ref, Dynamic Range



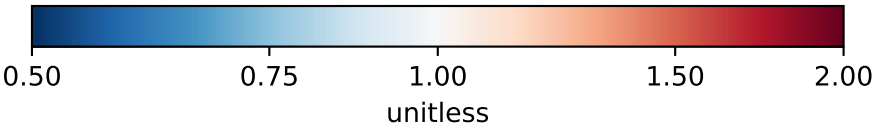
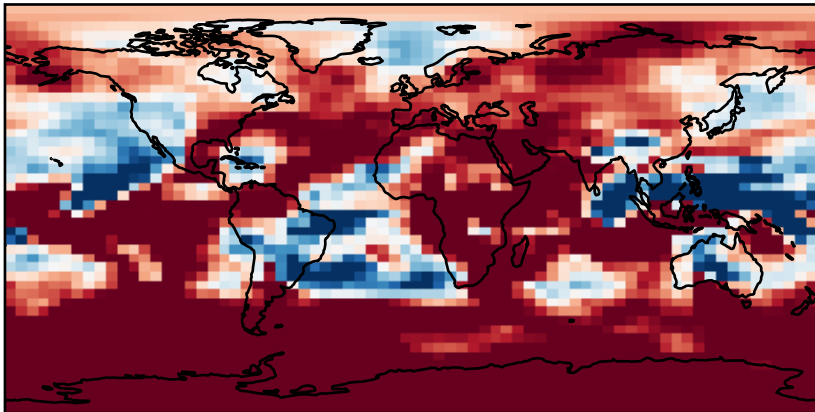
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

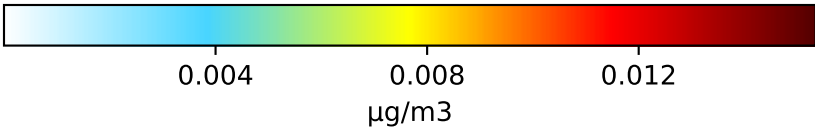


Ratio  
Dev/Ref, Fixed Range

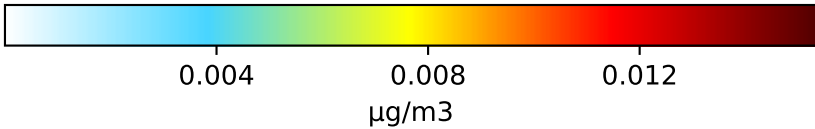


# SpeciesConc\_TSOA3 (Apr2019)

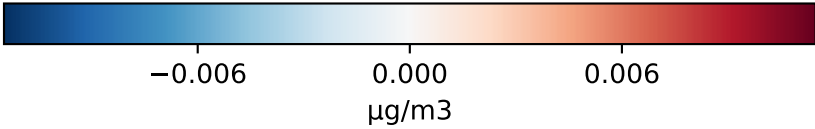
GCC\_12.9.0 (Ref)  
4.0x5.0



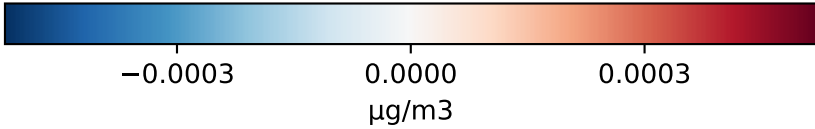
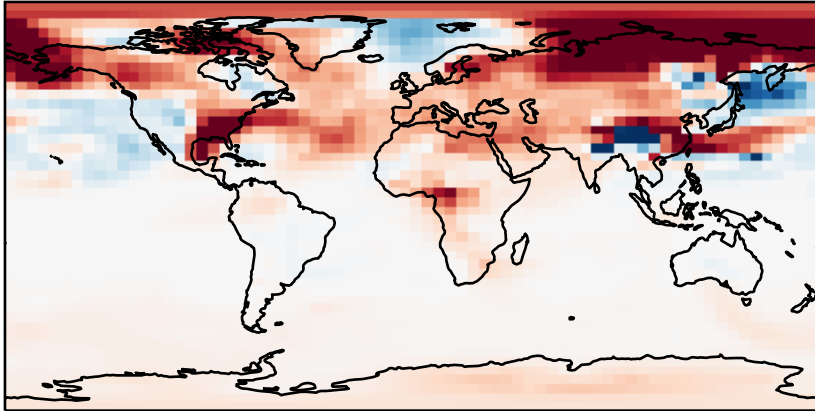
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



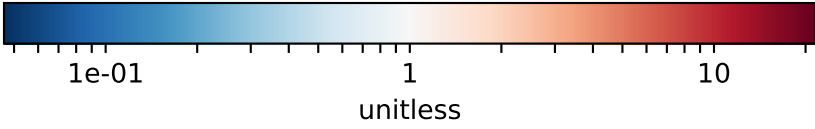
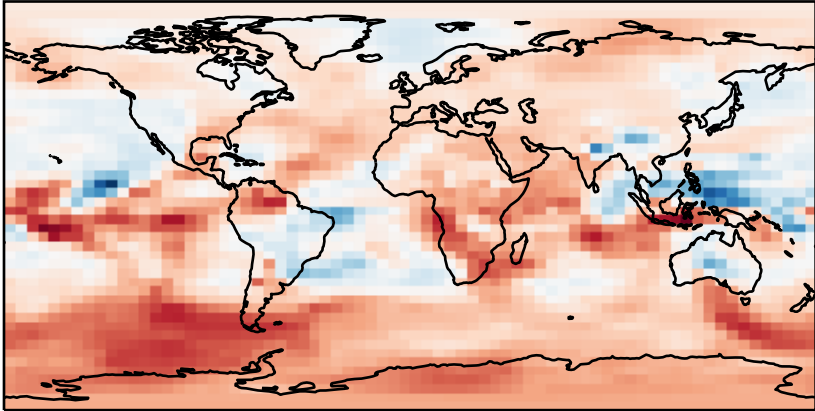
Difference  
Dev - Ref, Dynamic Range



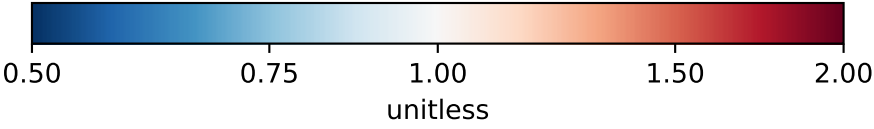
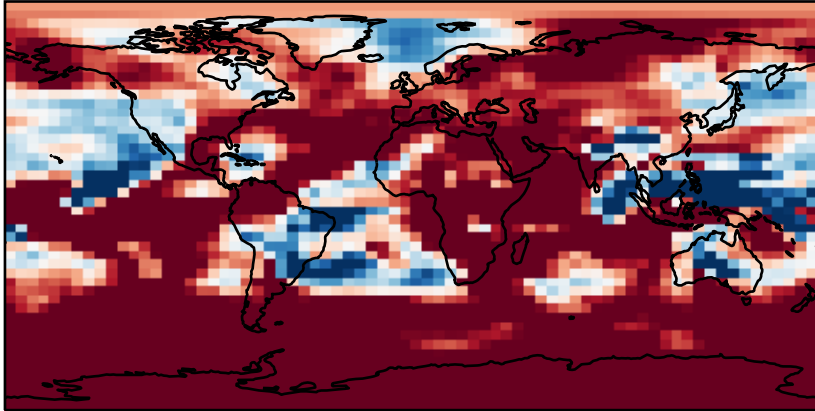
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



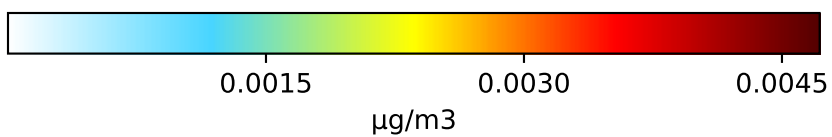
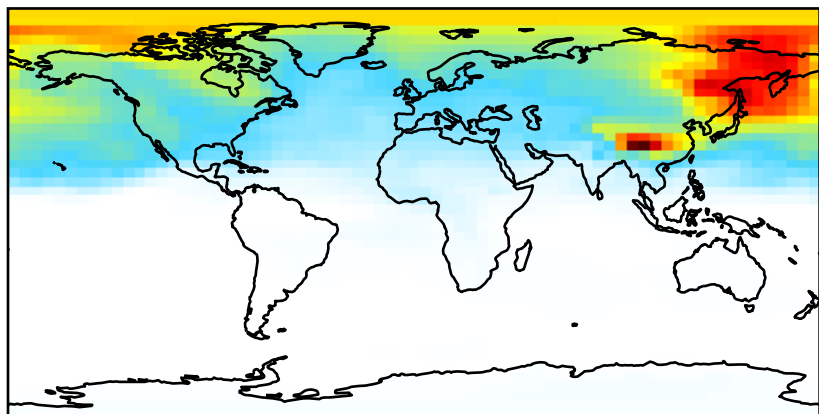
Ratio  
Dev/Ref, Fixed Range



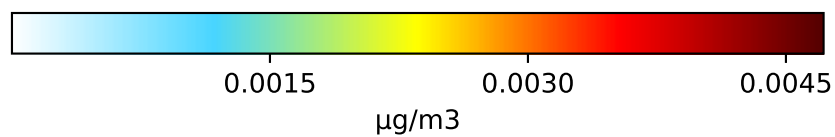
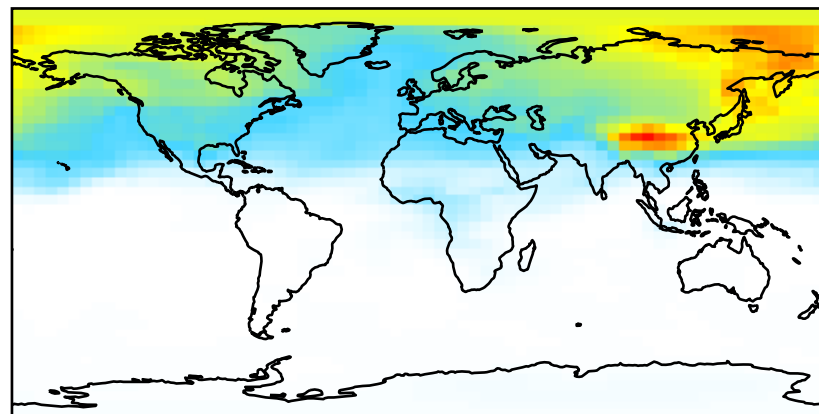


# SpeciesConc\_ASOA1 (Apr2019)

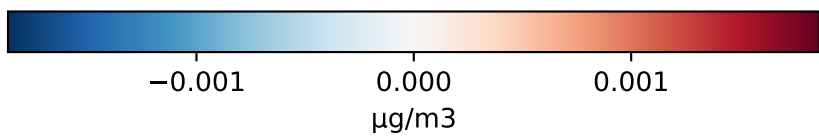
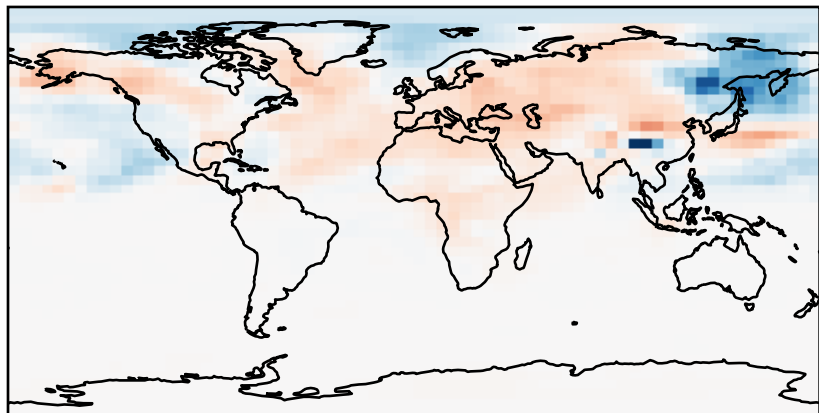
GCC\_12.9.0 (Ref)  
4.0x5.0



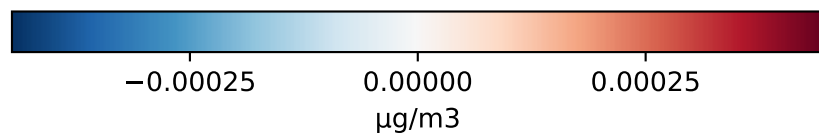
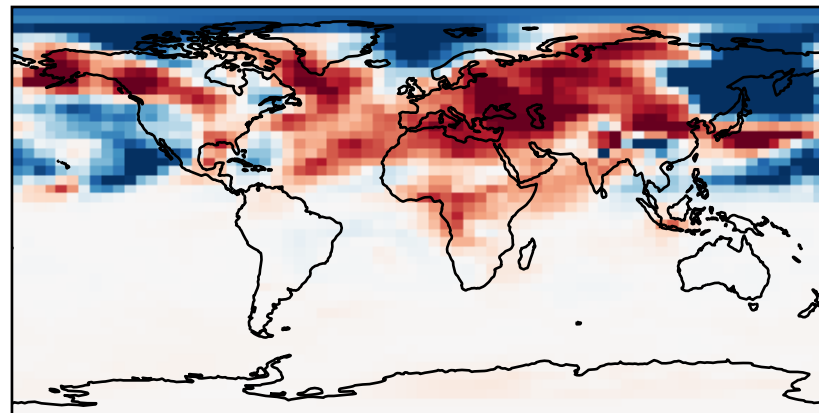
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



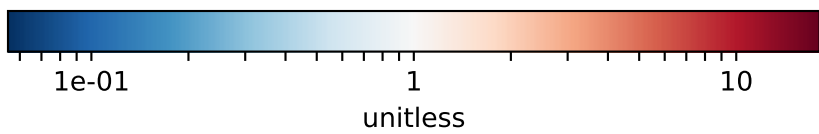
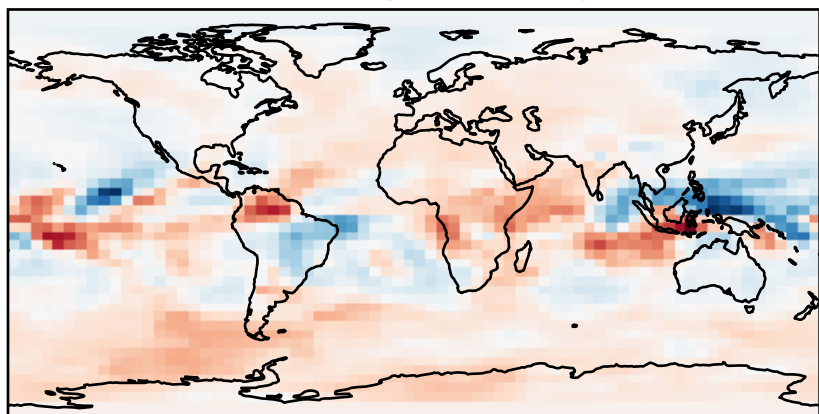
Difference  
Dev - Ref, Dynamic Range



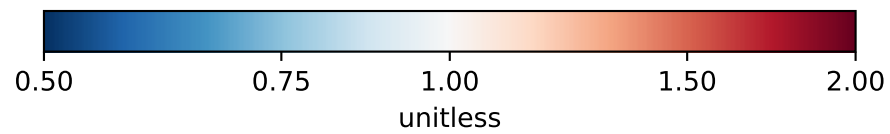
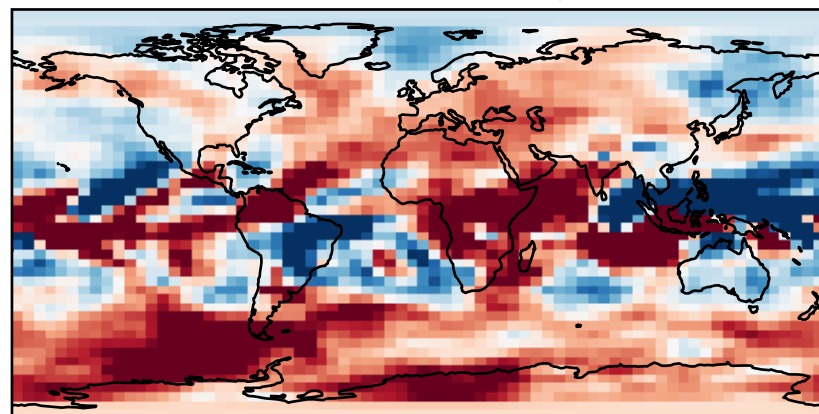
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

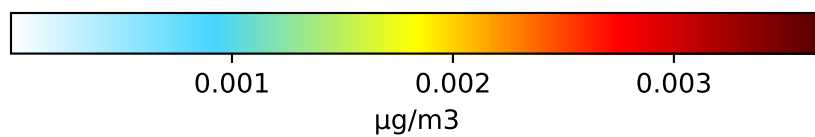
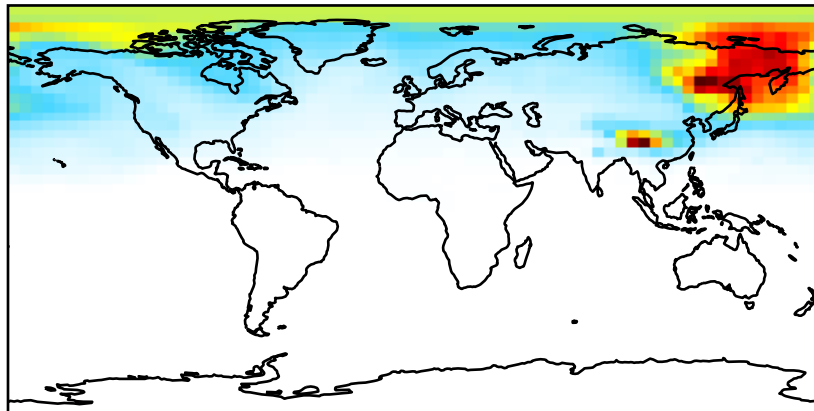


Ratio  
Dev/Ref, Fixed Range

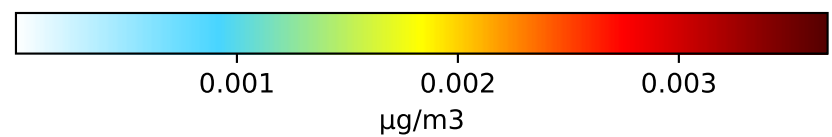
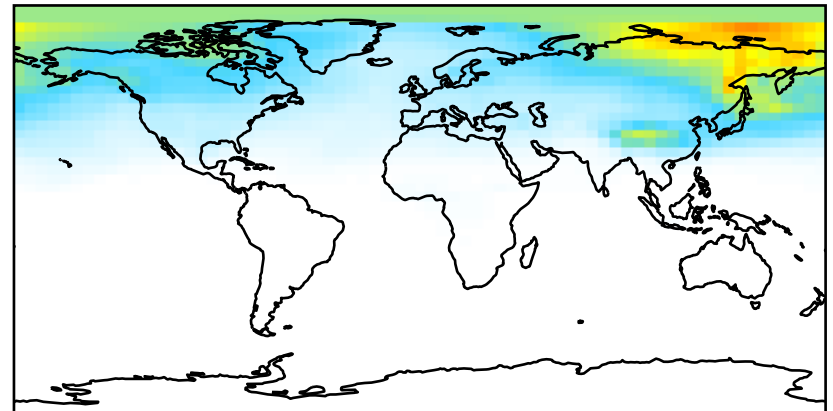


# SpeciesConc\_ASOA2 (Apr2019)

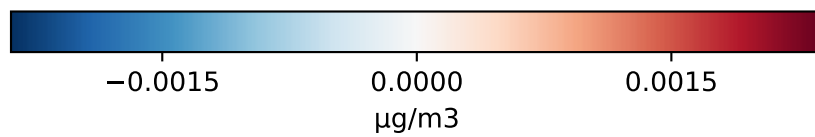
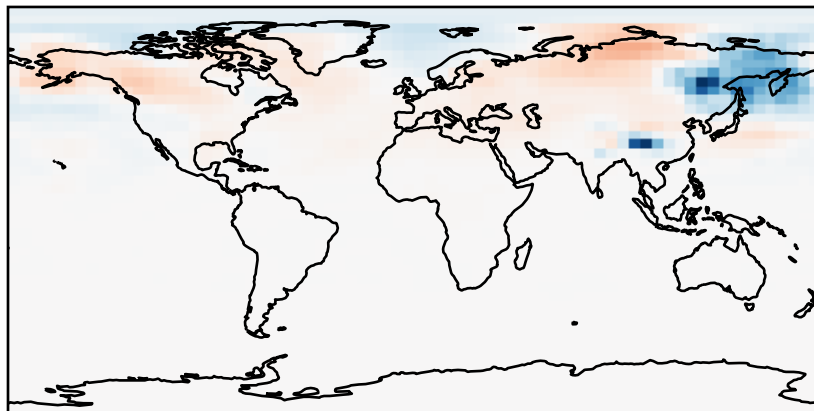
GCC\_12.9.0 (Ref)  
4.0x5.0



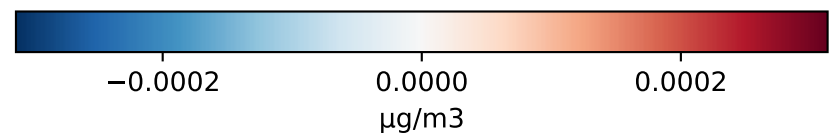
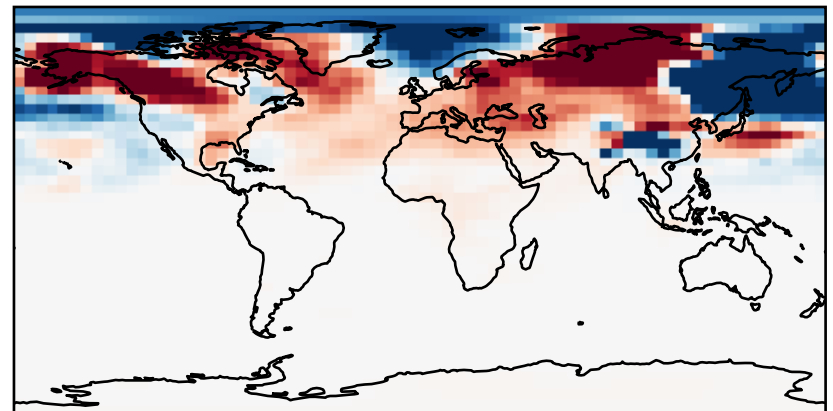
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



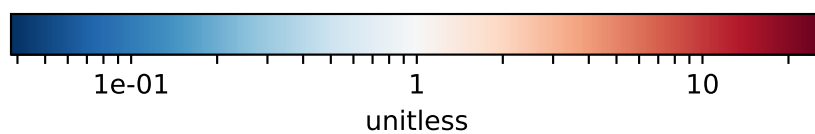
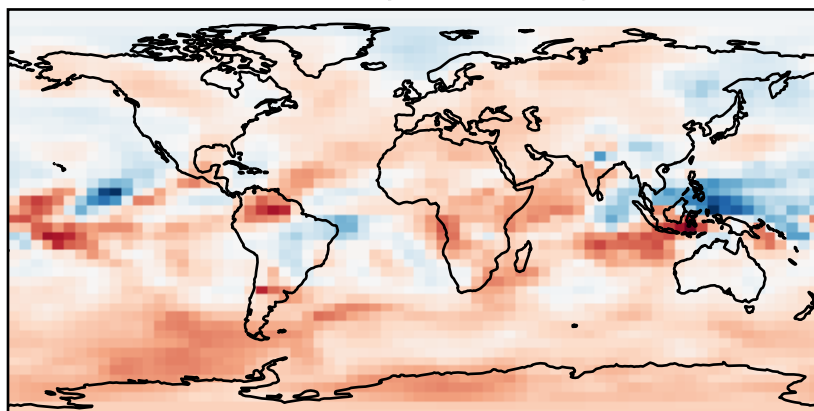
Difference  
Dev - Ref, Dynamic Range



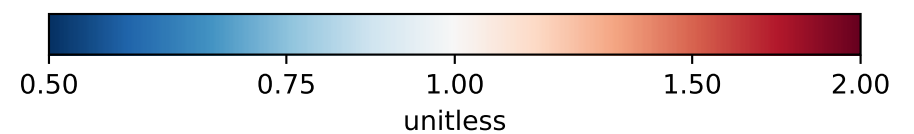
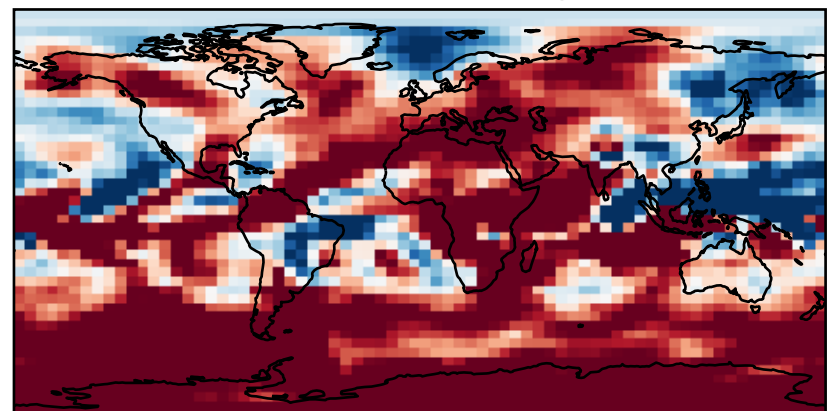
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

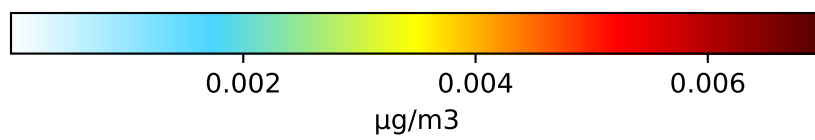
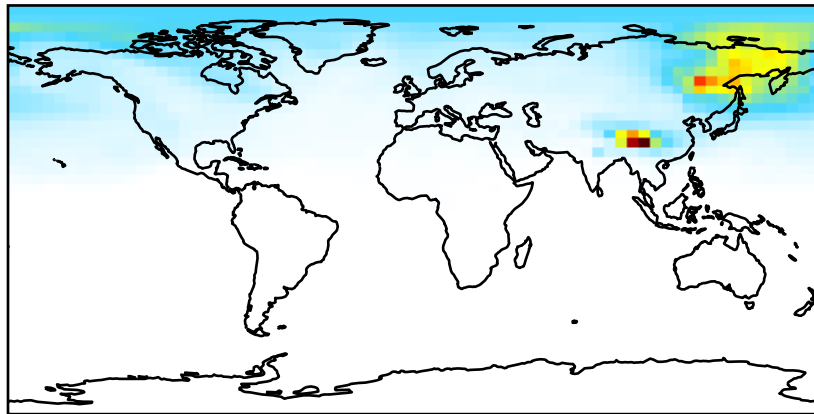


Ratio  
Dev/Ref, Fixed Range

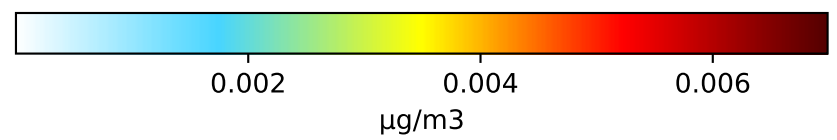
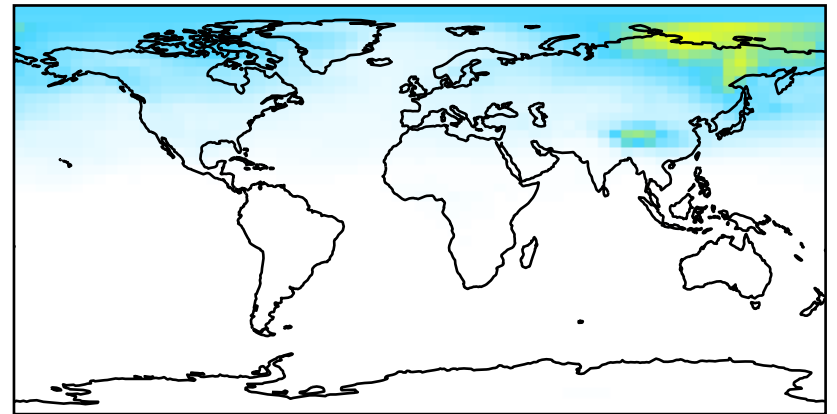


# SpeciesConc\_ASOA3 (Apr2019)

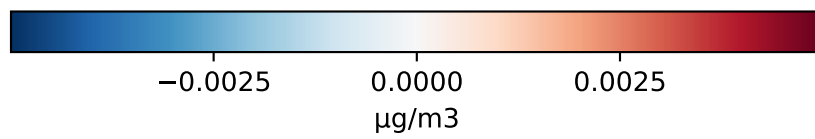
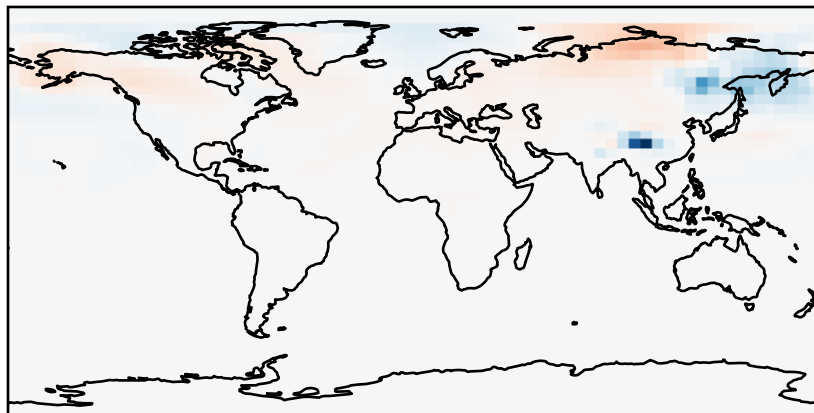
GCC\_12.9.0 (Ref)  
4.0x5.0



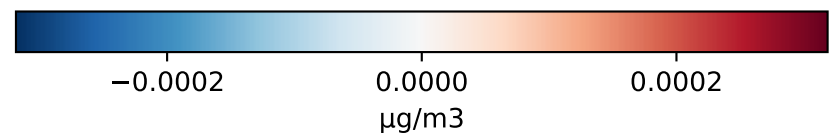
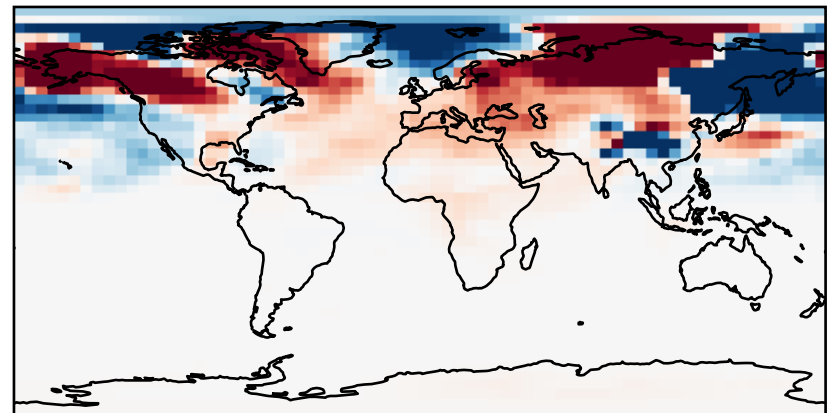
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



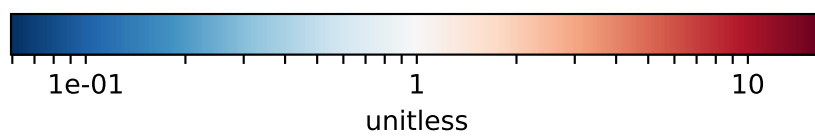
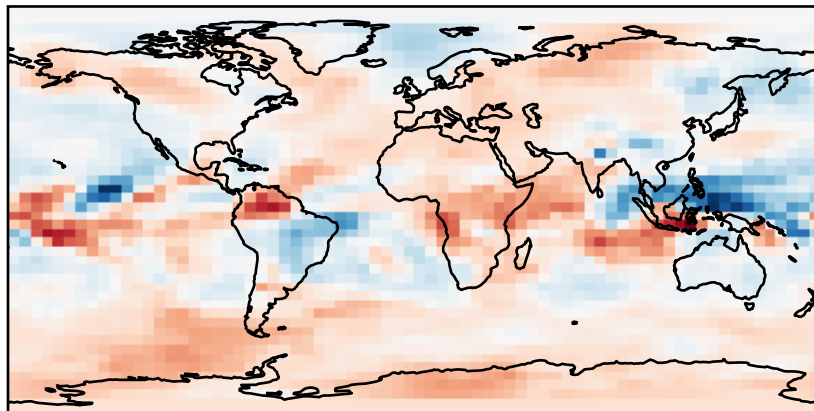
Difference  
Dev - Ref, Dynamic Range



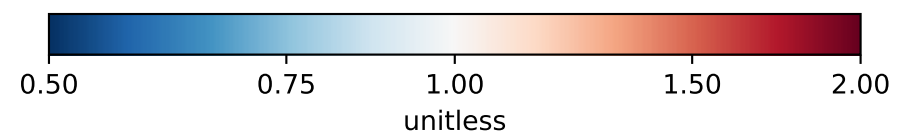
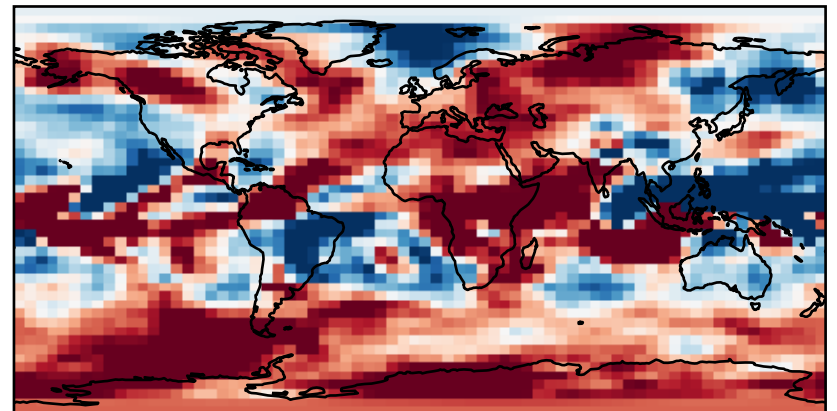
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

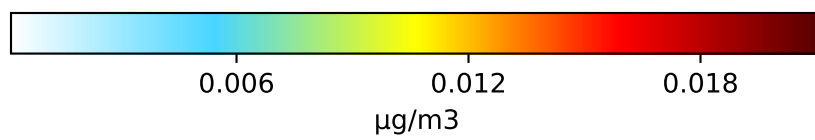
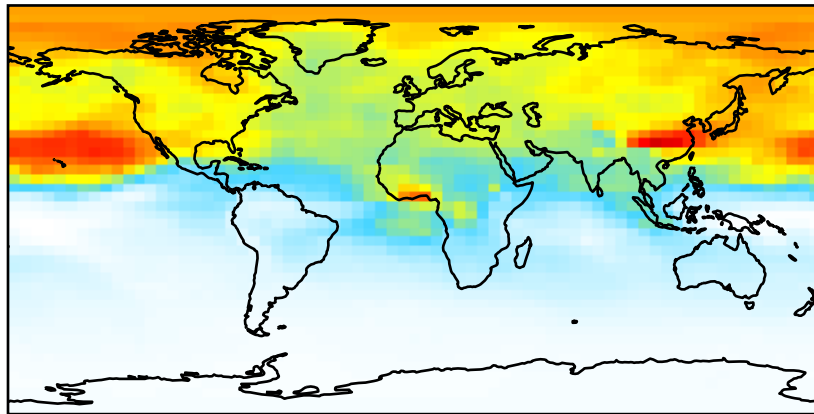


Ratio  
Dev/Ref, Fixed Range

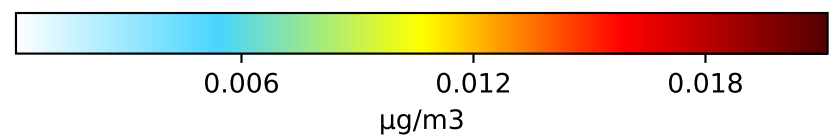
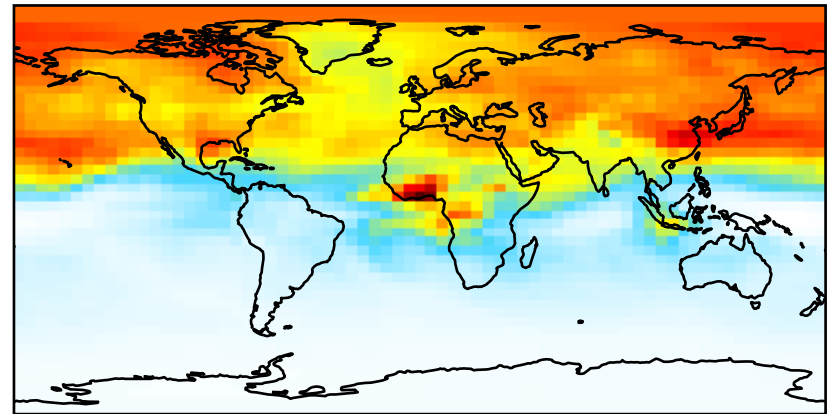


# SpeciesConc\_ASOAN (Apr2019)

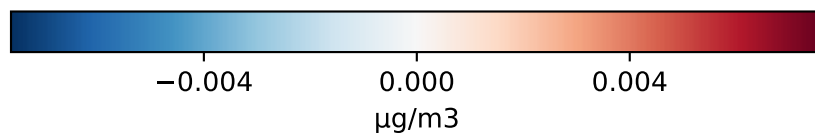
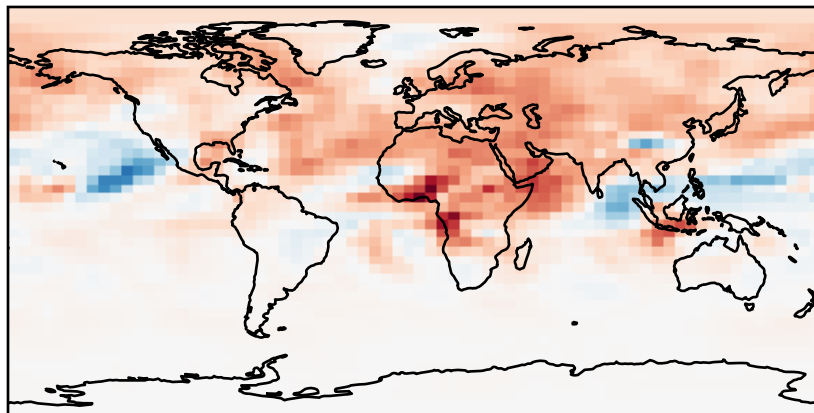
GCC\_12.9.0 (Ref)  
4.0x5.0



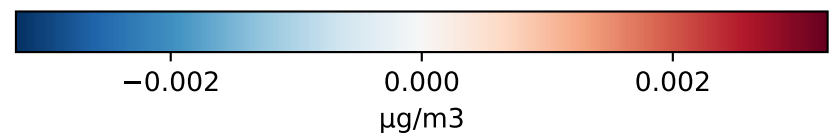
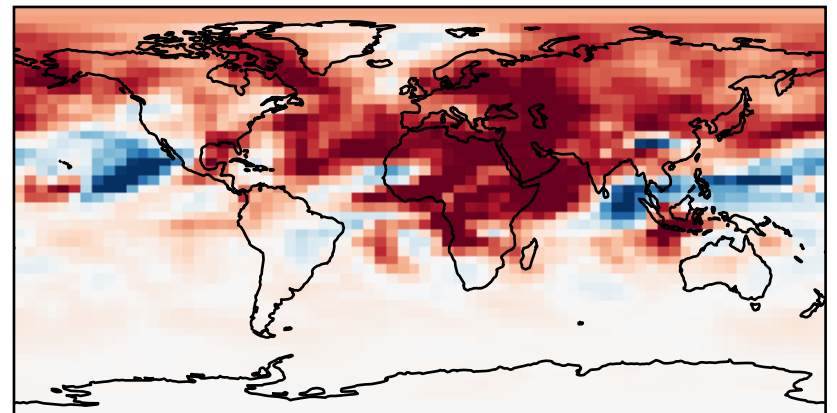
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



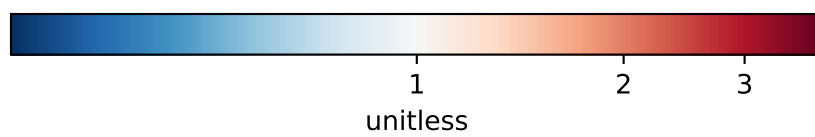
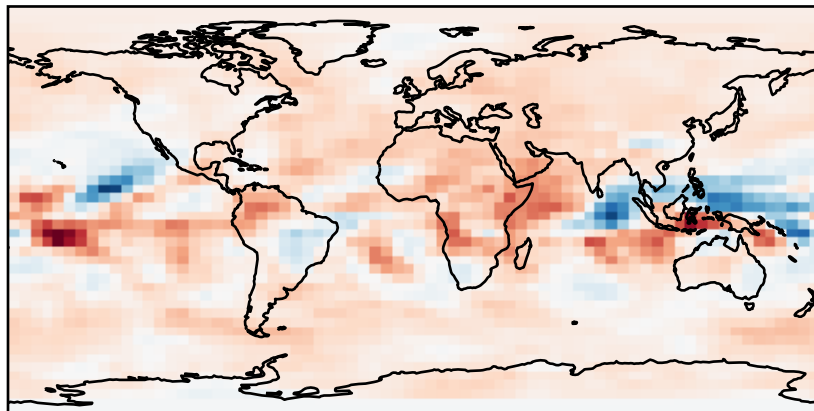
Difference  
Dev - Ref, Dynamic Range



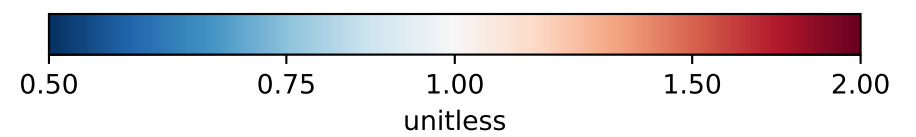
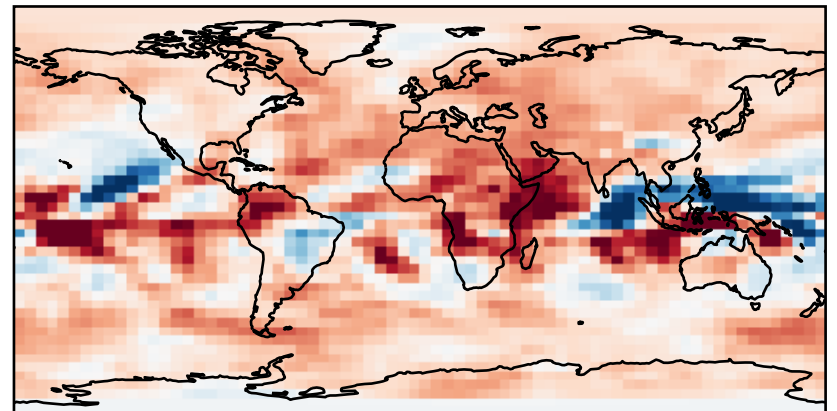
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



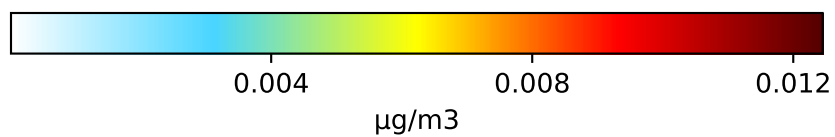
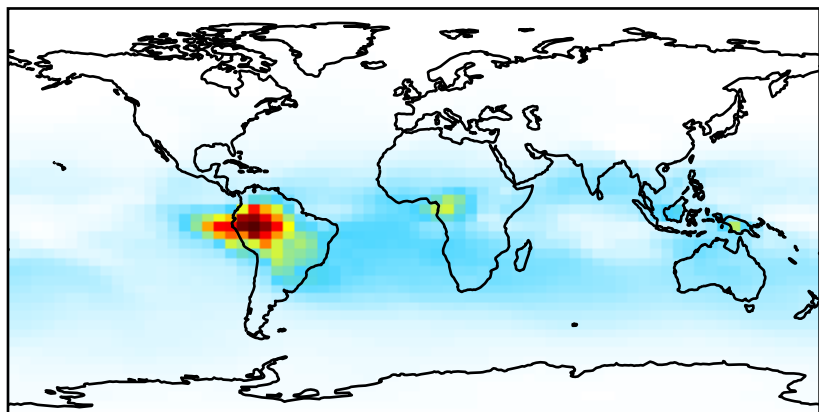
Ratio  
Dev/Ref, Fixed Range



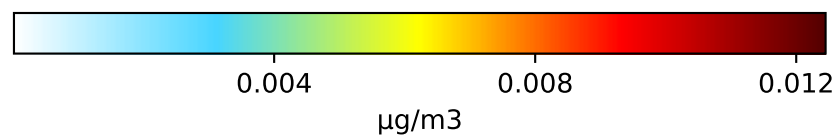
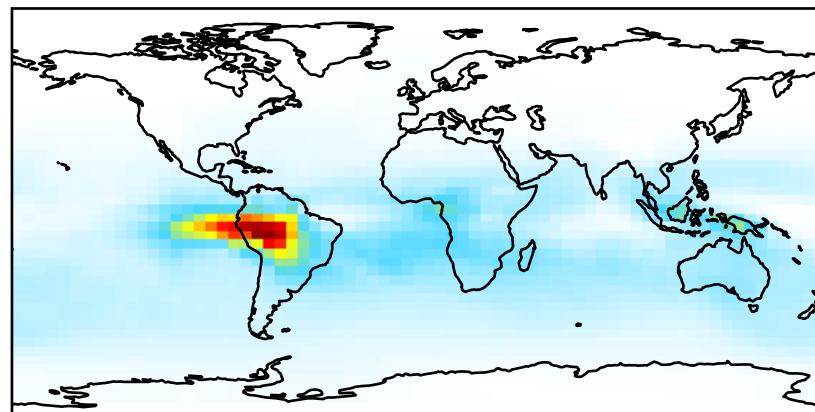


# SpeciesConc\_TSOG0 (Apr2019)

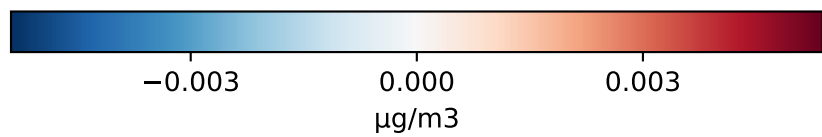
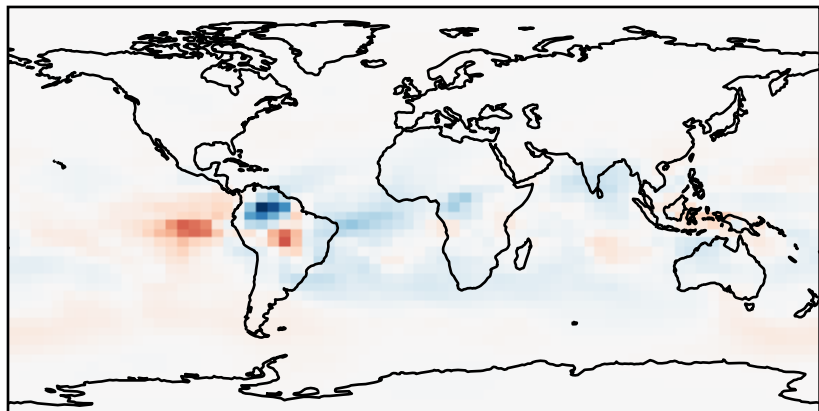
GCC\_12.9.0 (Ref)  
4.0x5.0



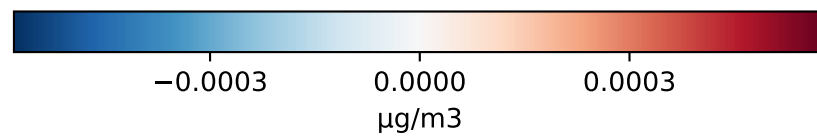
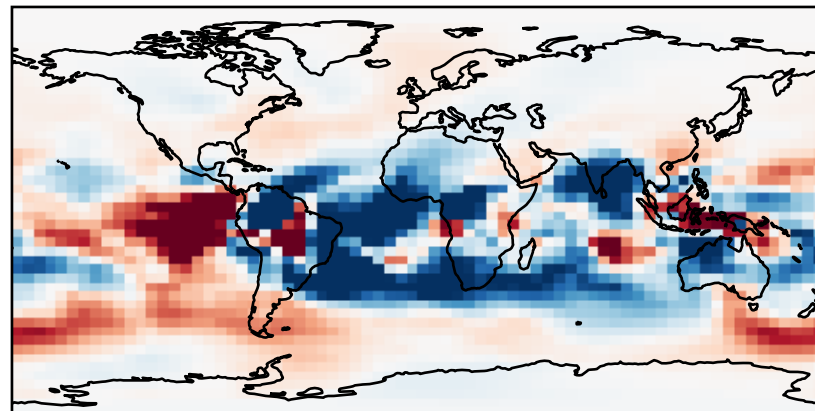
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



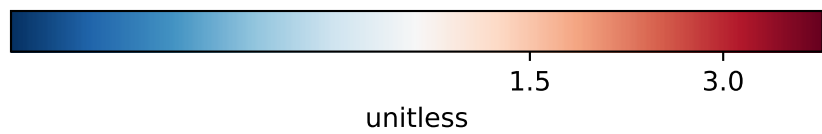
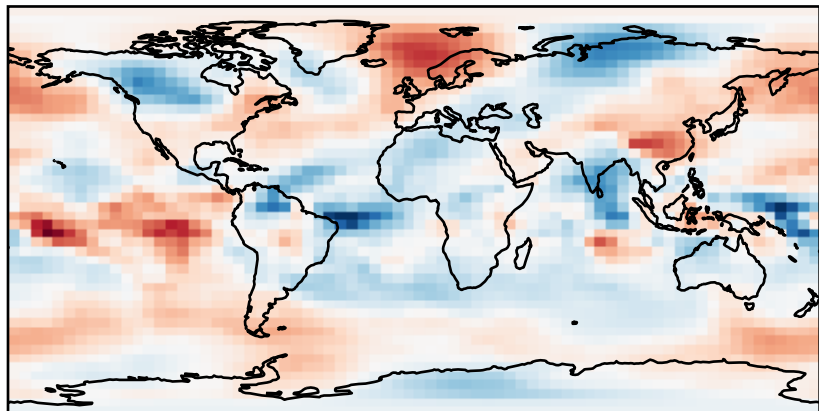
Difference  
Dev - Ref, Dynamic Range



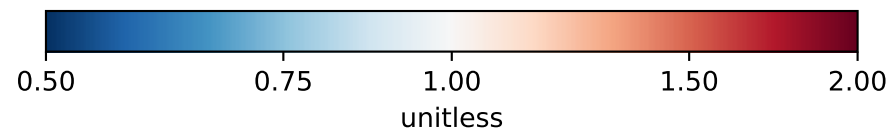
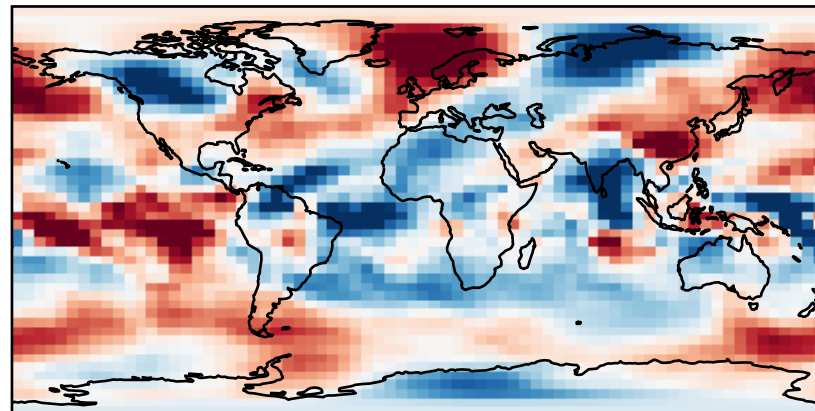
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

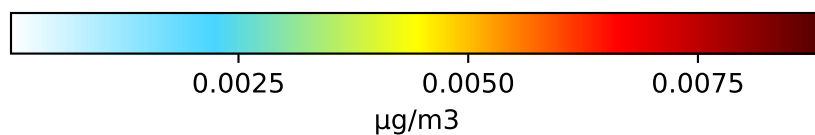
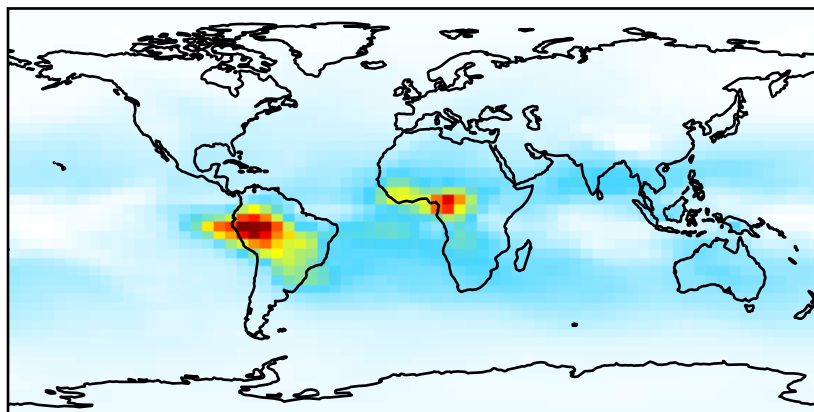


Ratio  
Dev/Ref, Fixed Range

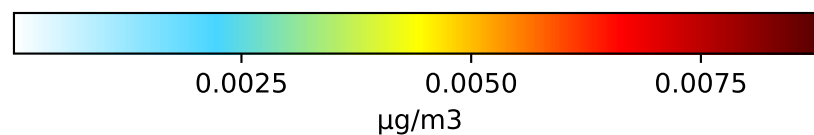
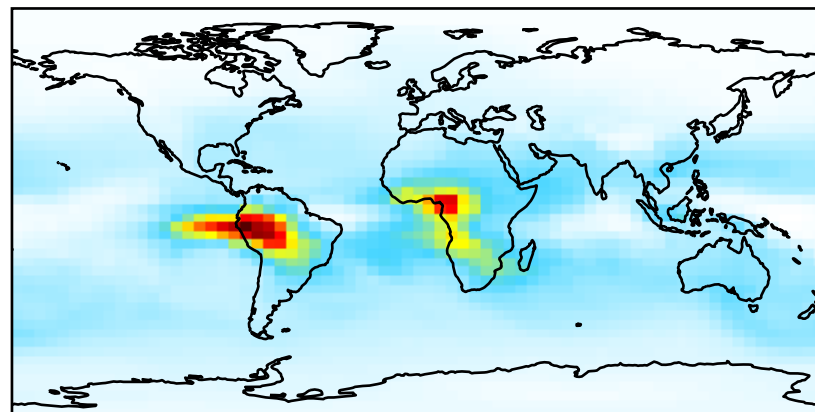


# SpeciesConc\_TSOG1 (Apr2019)

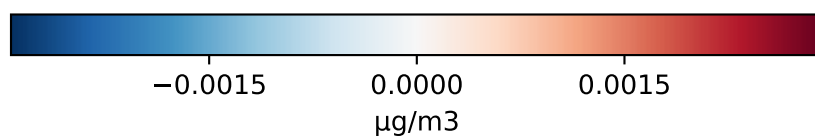
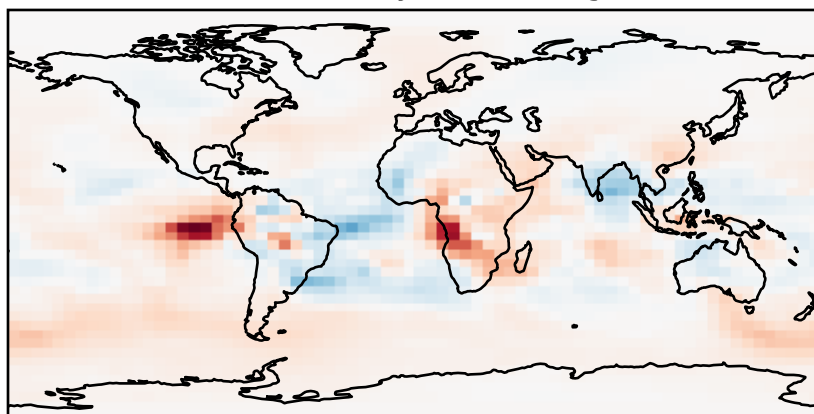
GCC\_12.9.0 (Ref)  
4.0x5.0



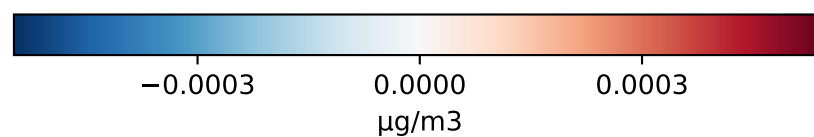
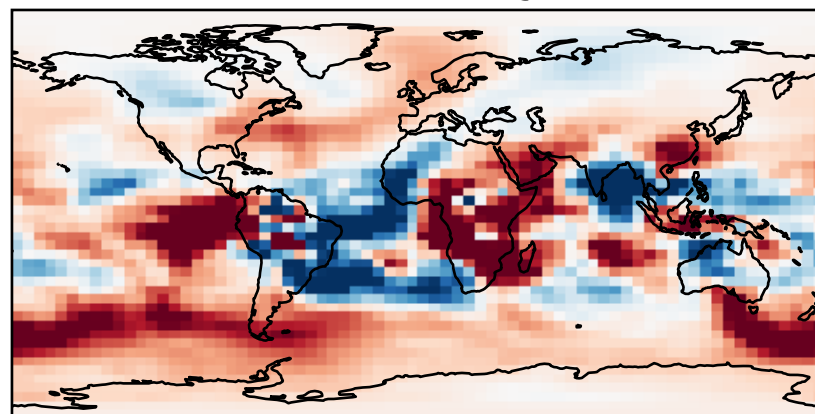
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



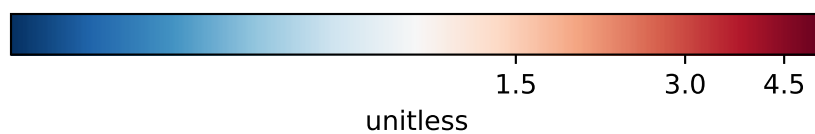
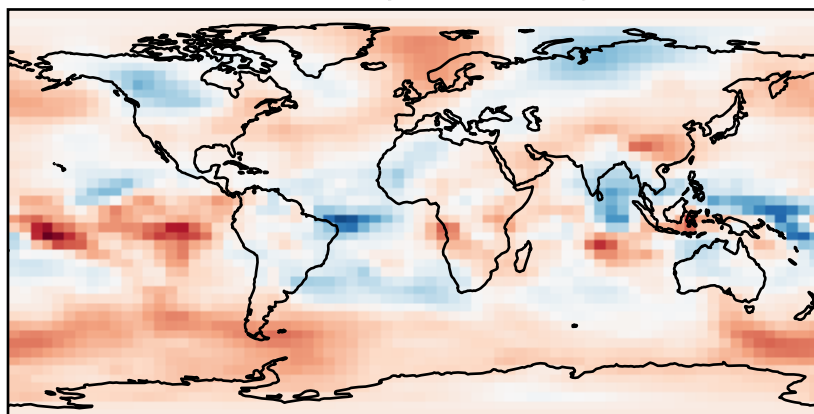
Difference  
Dev - Ref, Dynamic Range



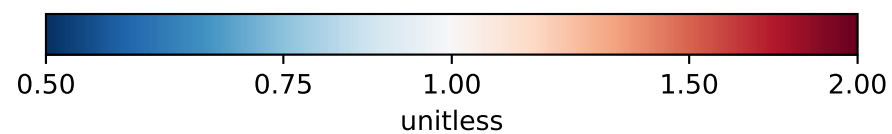
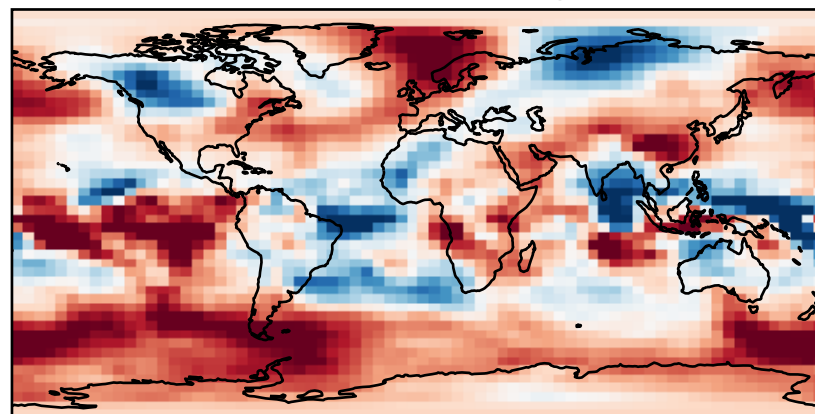
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

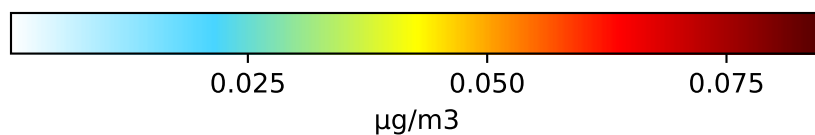
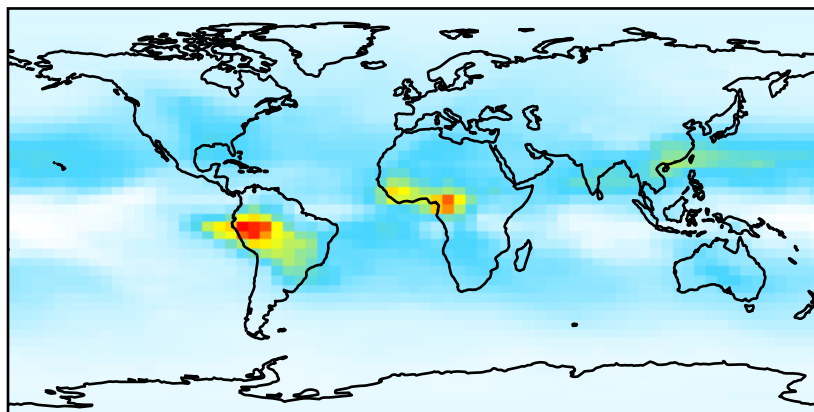


Ratio  
Dev/Ref, Fixed Range

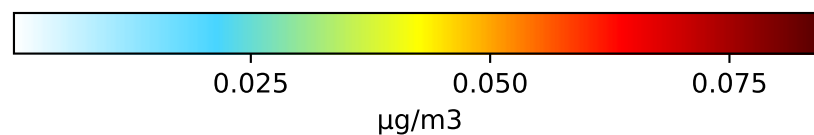
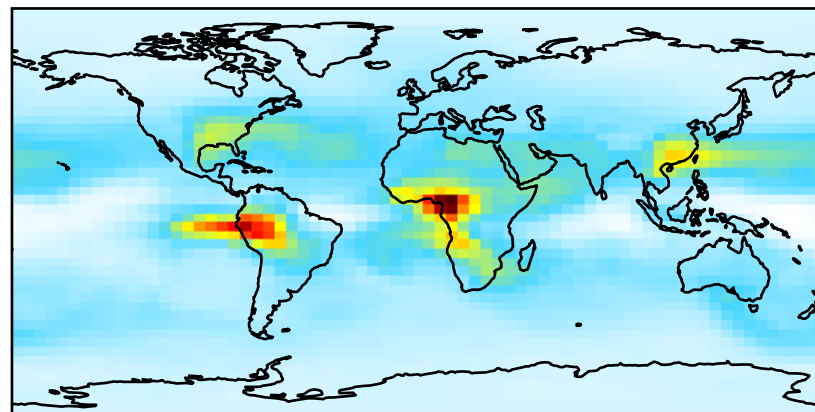


# SpeciesConc\_TSOG2 (Apr2019)

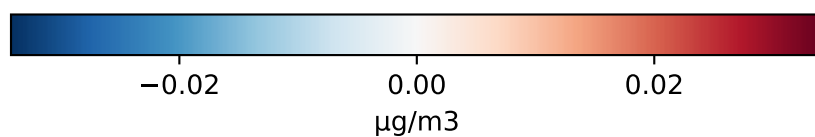
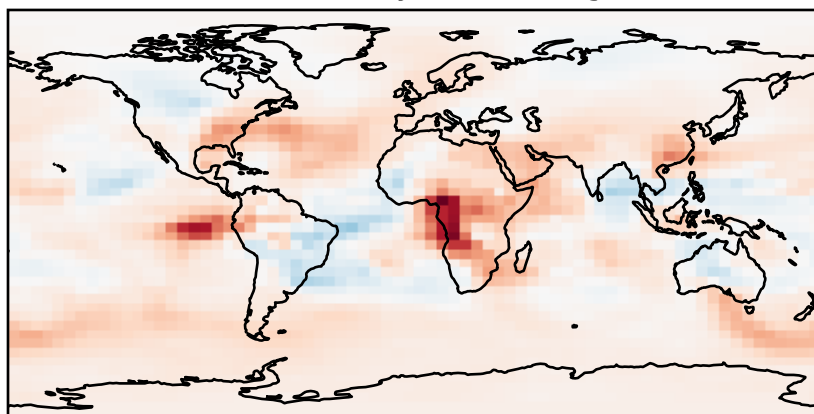
GCC\_12.9.0 (Ref)  
4.0x5.0



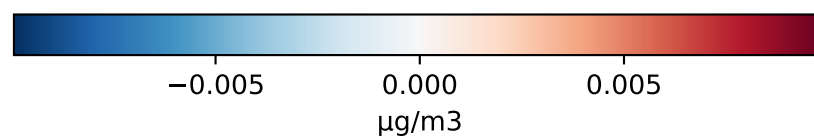
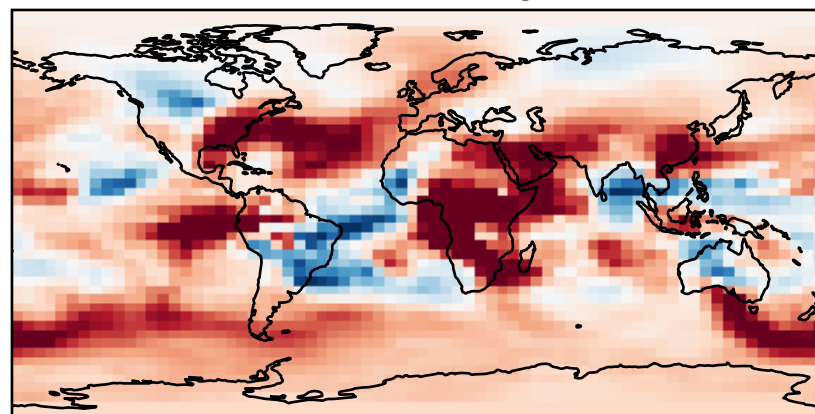
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



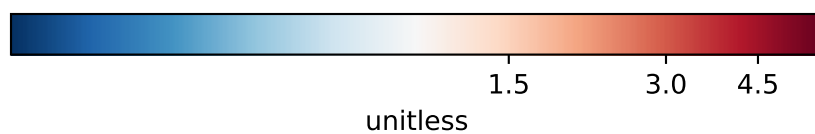
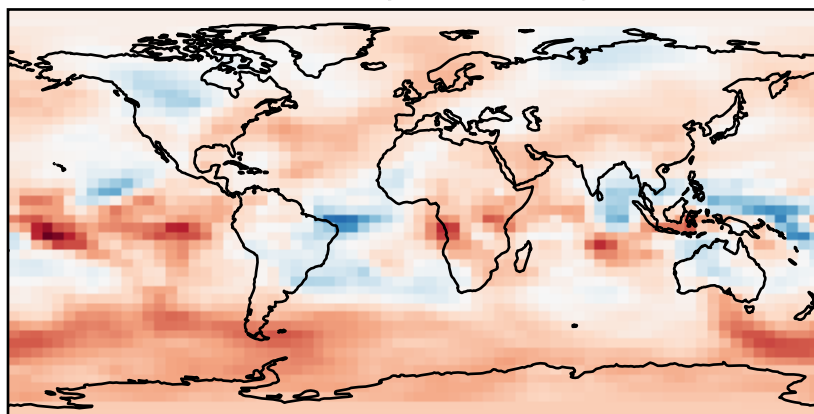
Difference  
Dev - Ref, Dynamic Range



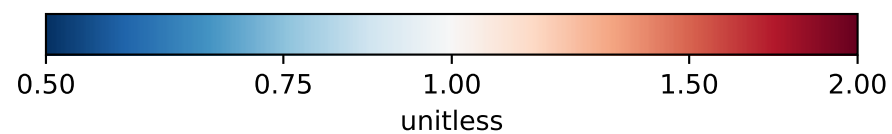
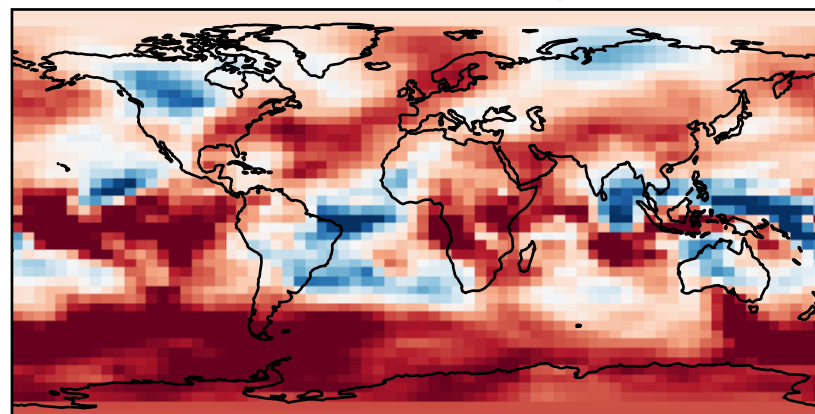
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

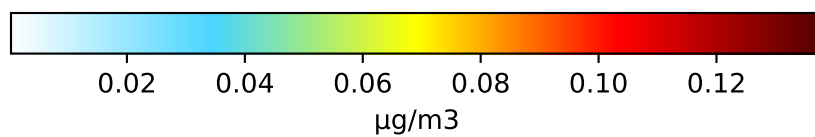
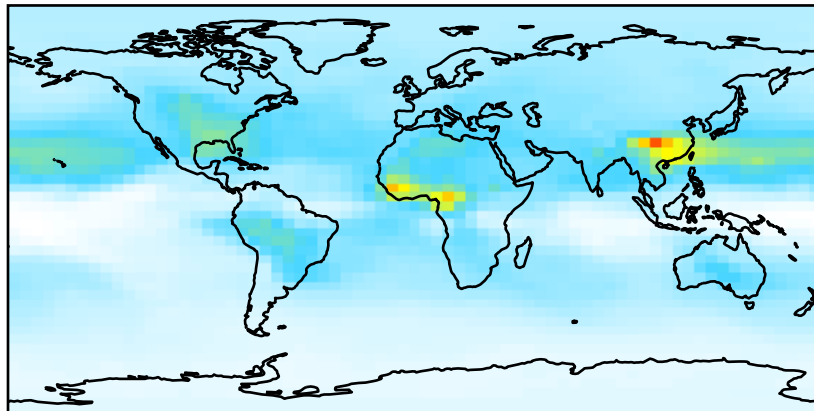


Ratio  
Dev/Ref, Fixed Range

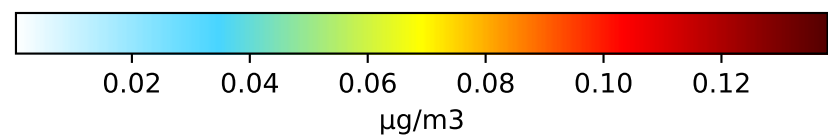
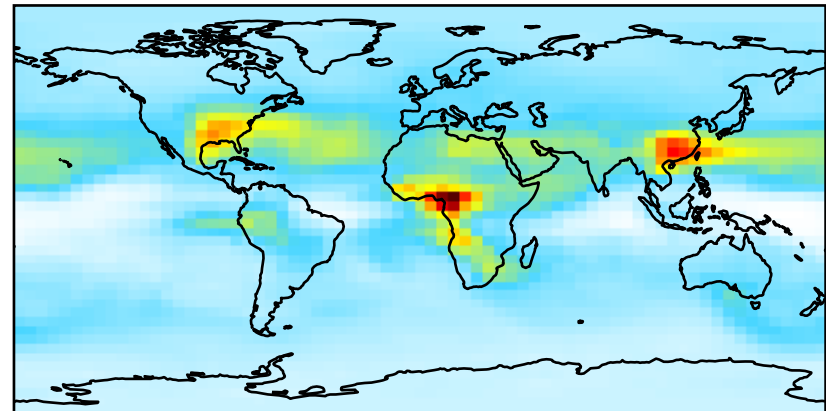


# SpeciesConc\_TSOG3 (Apr2019)

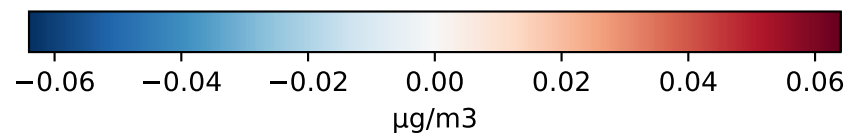
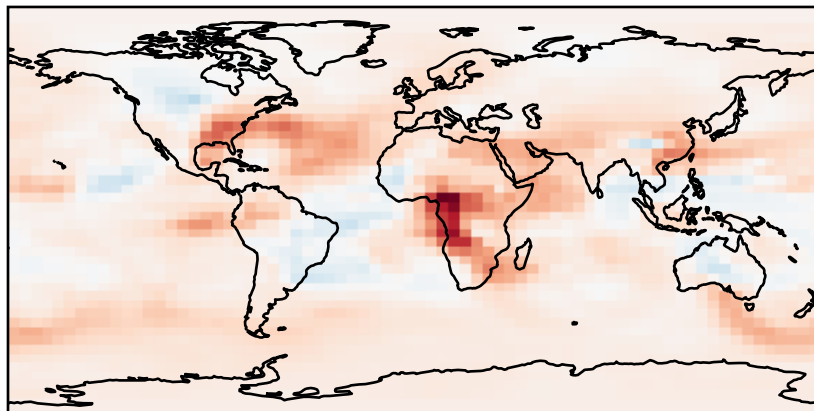
GCC\_12.9.0 (Ref)  
4.0x5.0



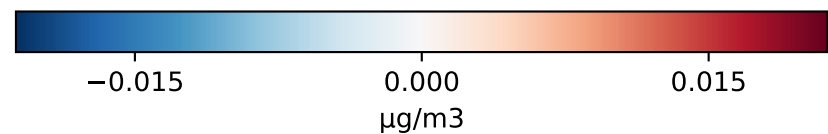
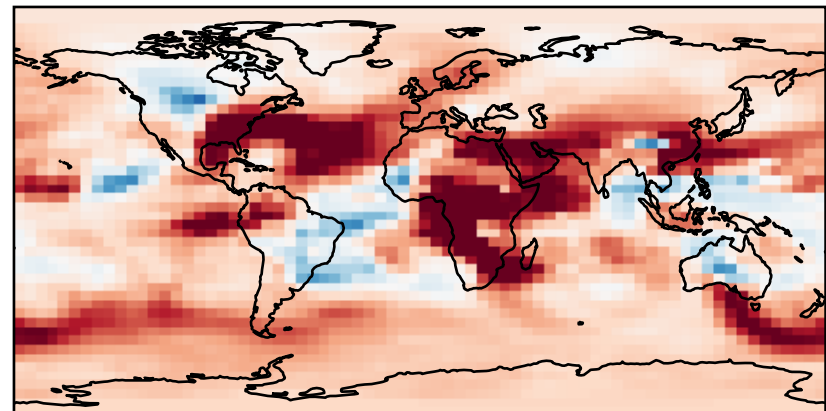
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



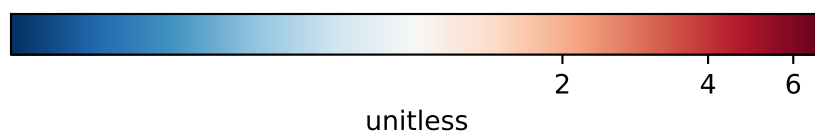
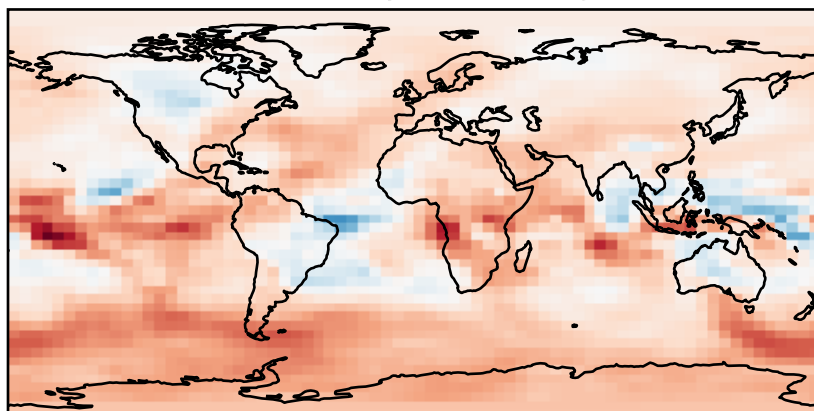
Difference  
Dev - Ref, Dynamic Range



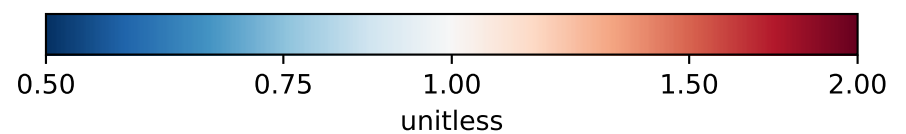
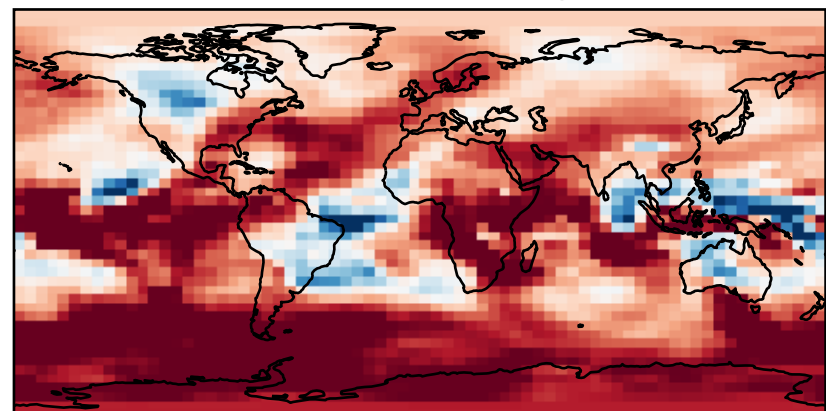
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



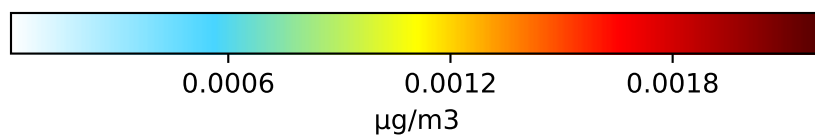
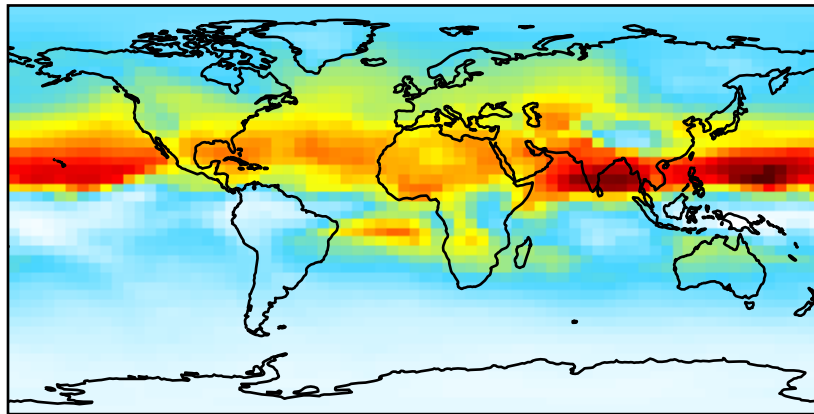
Ratio  
Dev/Ref, Fixed Range



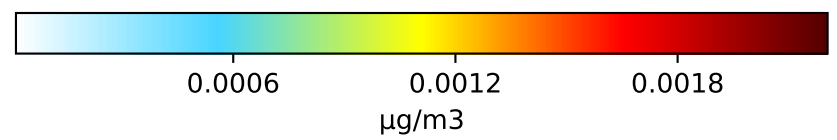
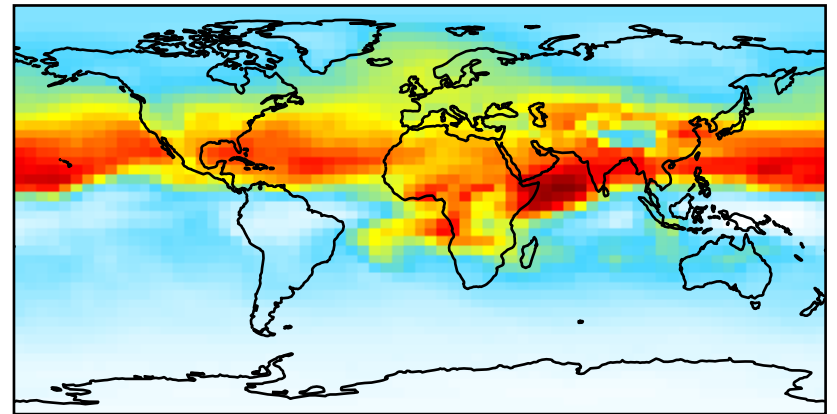


# SpeciesConc\_ASOG1 (Apr2019)

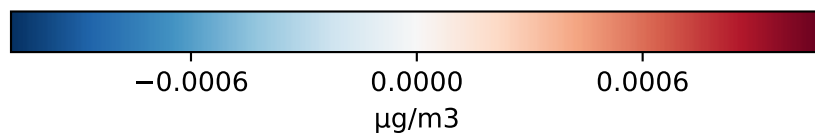
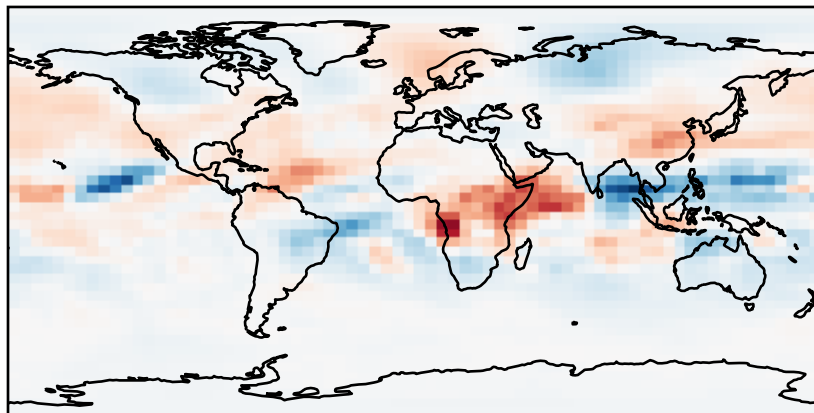
GCC\_12.9.0 (Ref)  
4.0x5.0



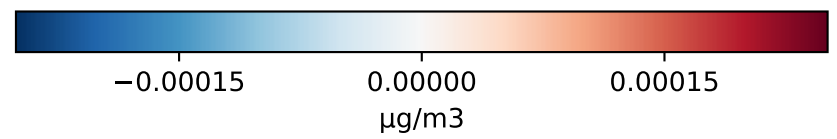
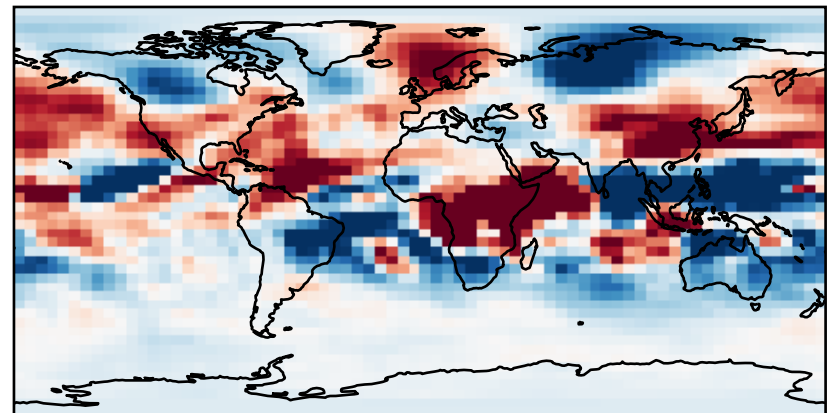
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



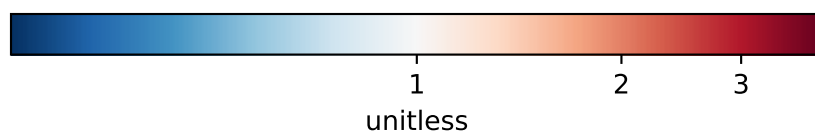
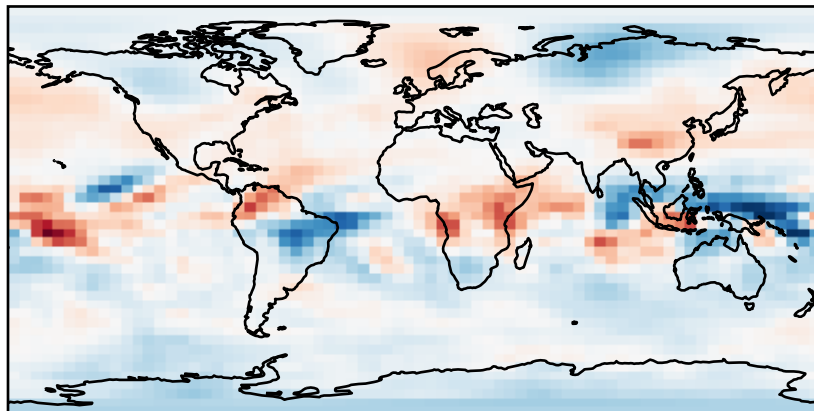
Difference  
Dev - Ref, Dynamic Range



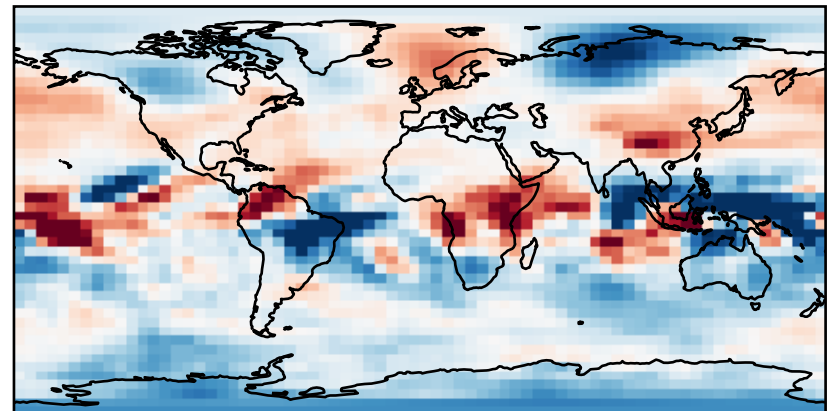
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

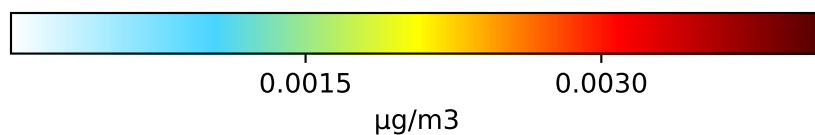
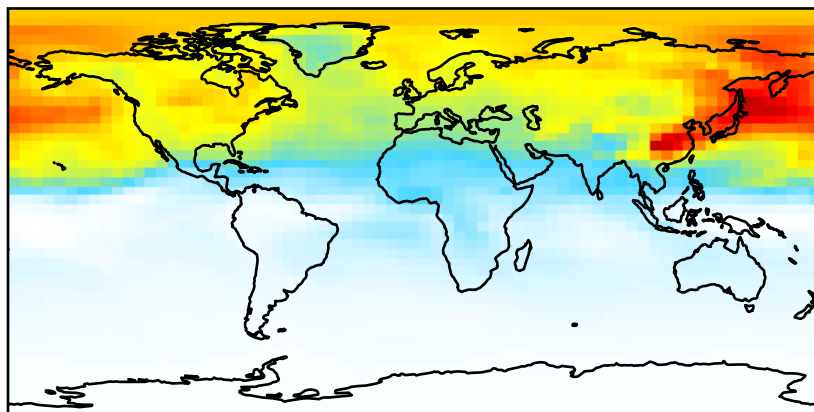


Ratio  
Dev/Ref, Fixed Range

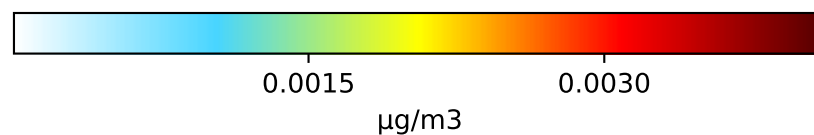
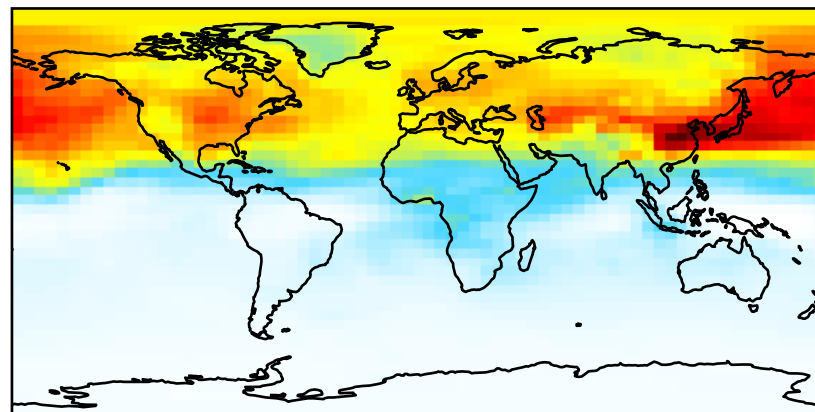


# SpeciesConc\_ASOG2 (Apr2019)

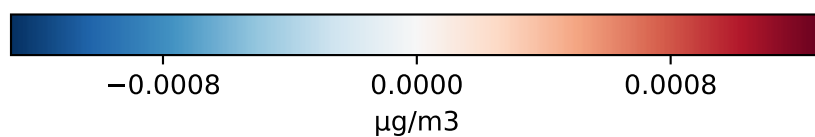
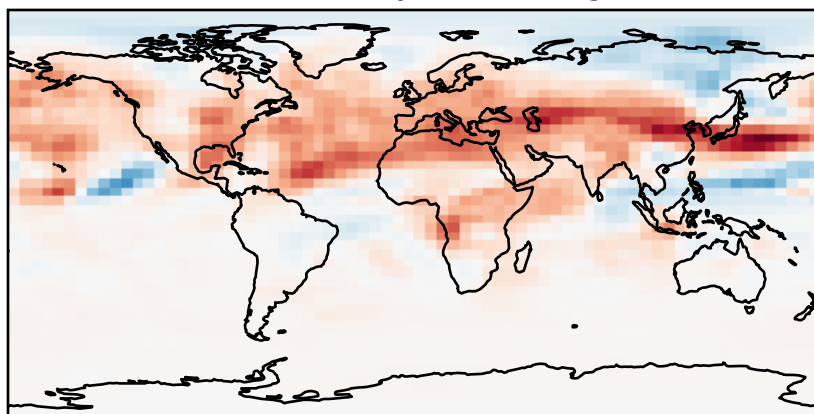
GCC\_12.9.0 (Ref)  
4.0x5.0



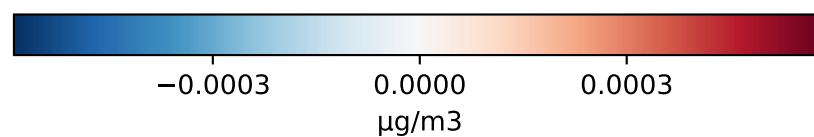
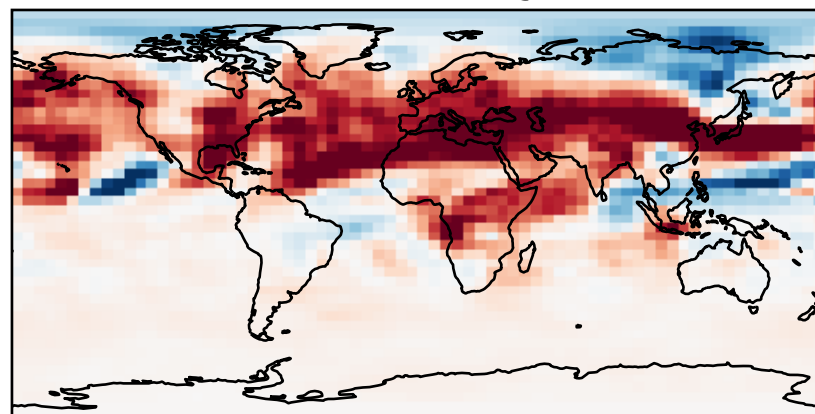
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



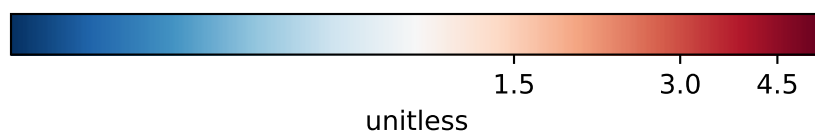
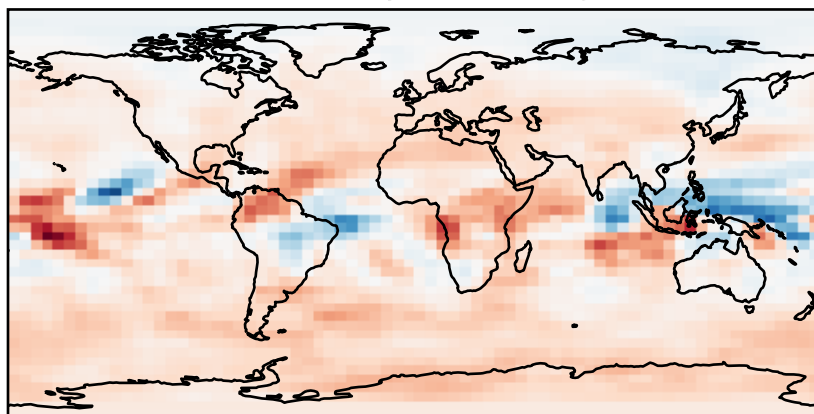
Difference  
Dev - Ref, Dynamic Range



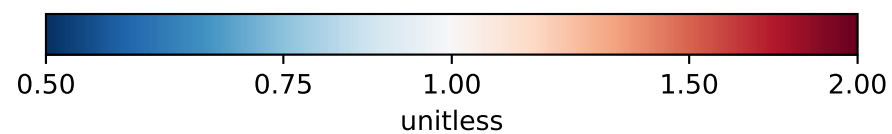
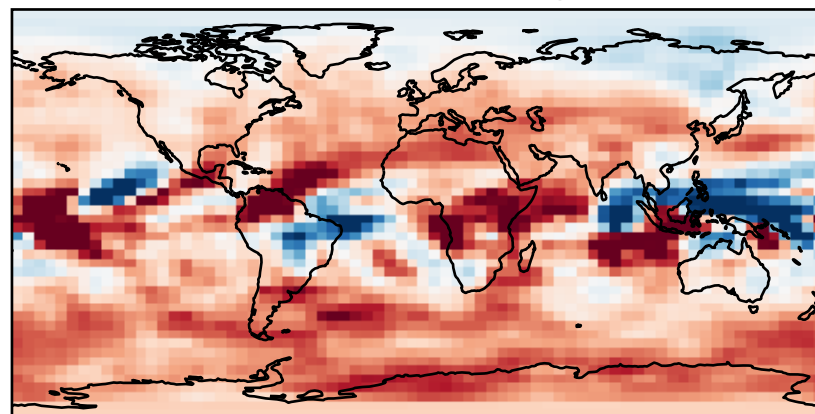
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

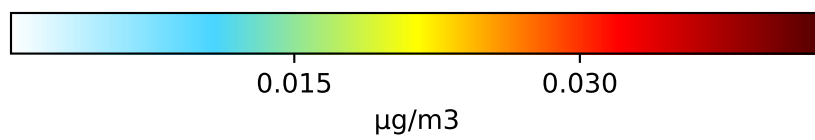
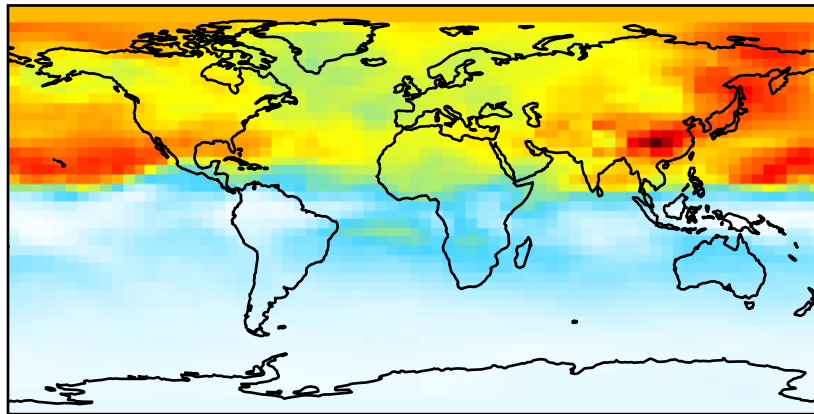


Ratio  
Dev/Ref, Fixed Range

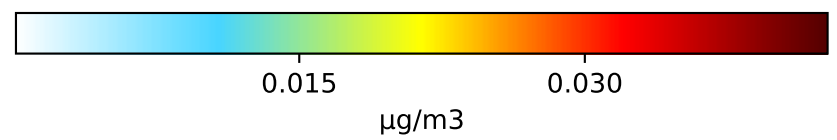
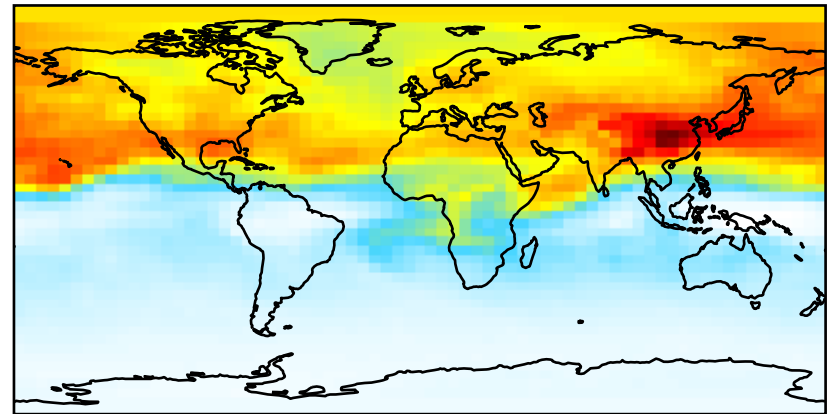


# SpeciesConc\_ASOG3 (Apr2019)

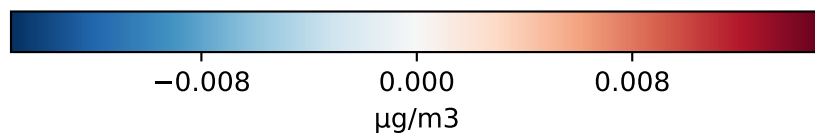
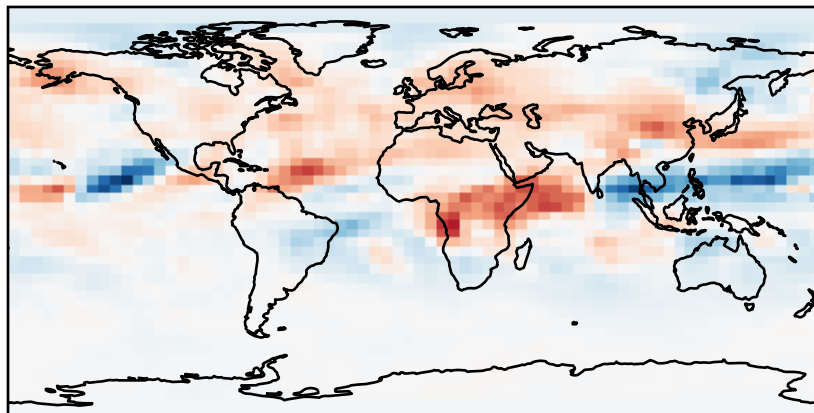
GCC\_12.9.0 (Ref)  
4.0x5.0



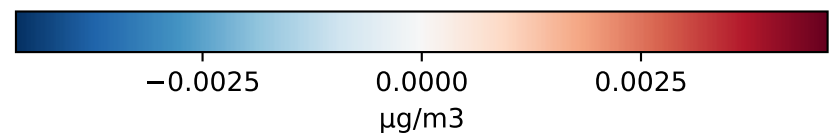
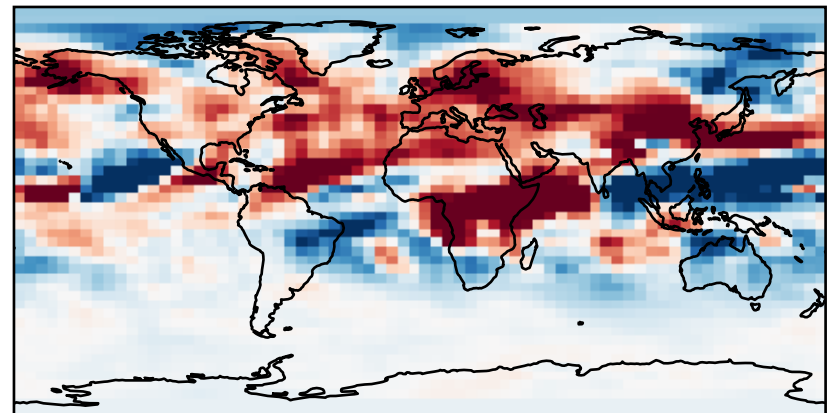
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



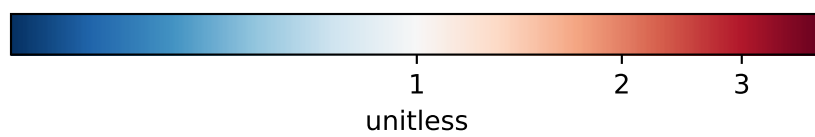
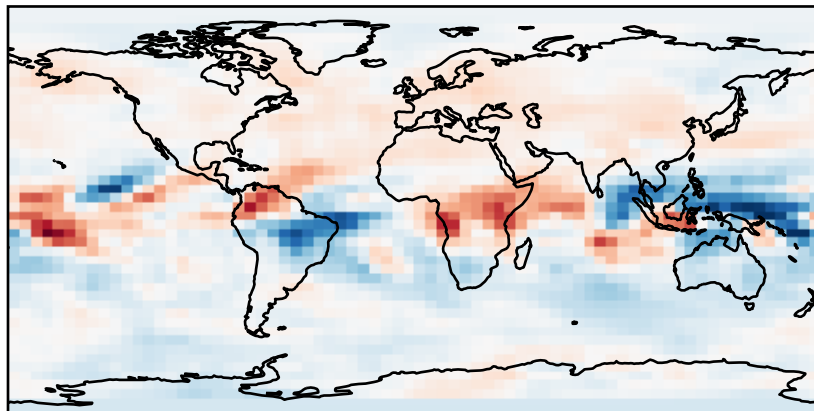
Difference  
Dev - Ref, Dynamic Range



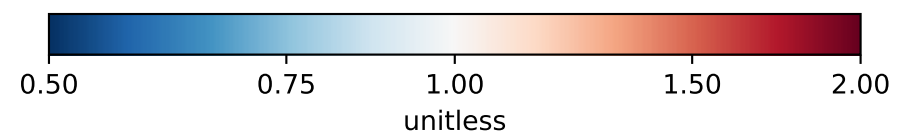
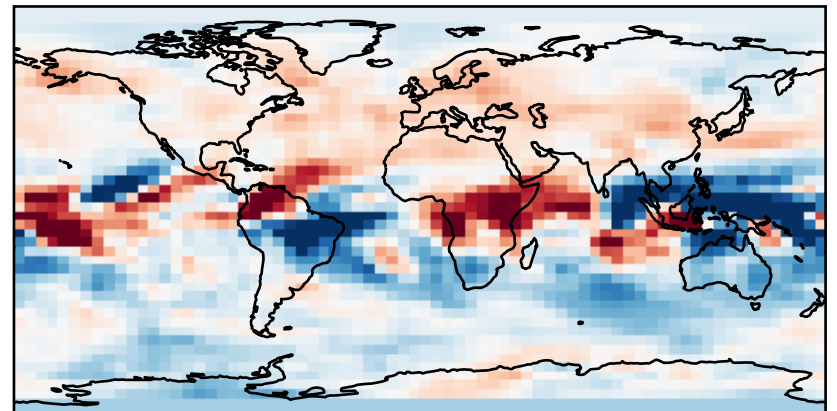
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

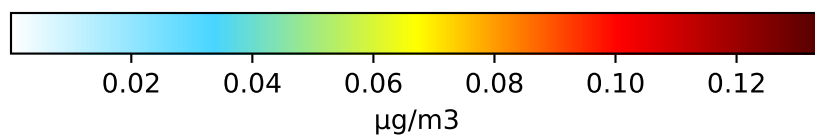
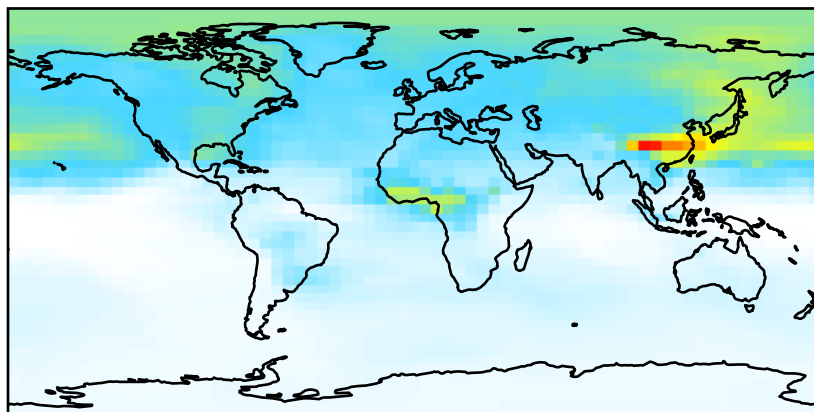


Ratio  
Dev/Ref, Fixed Range

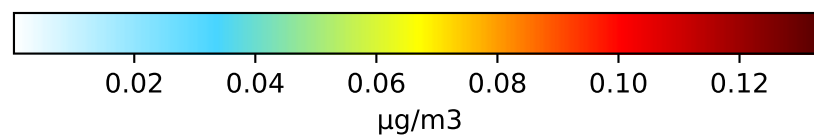
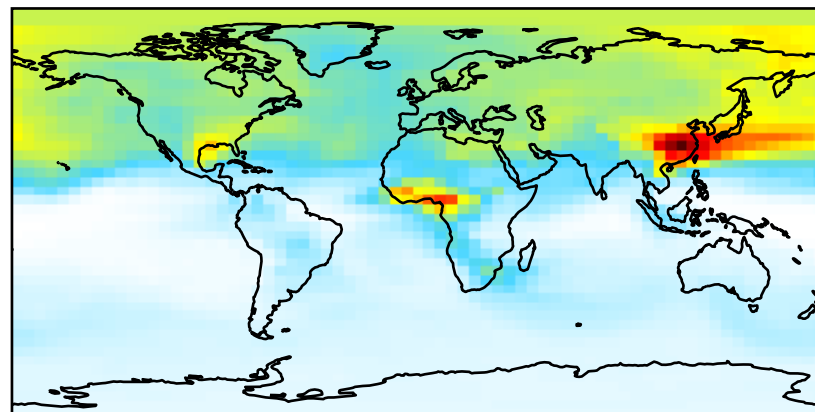


# SpeciesConc\_INDIOOL (Apr2019)

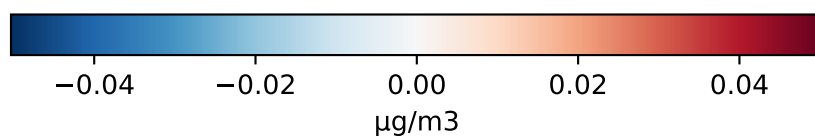
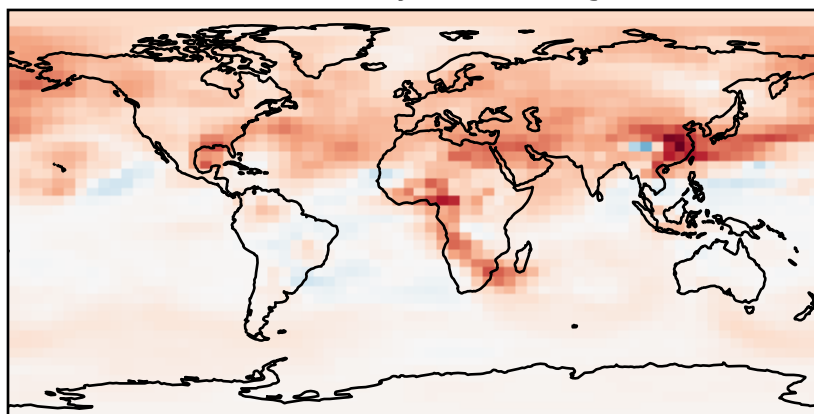
GCC\_12.9.0 (Ref)  
4.0x5.0



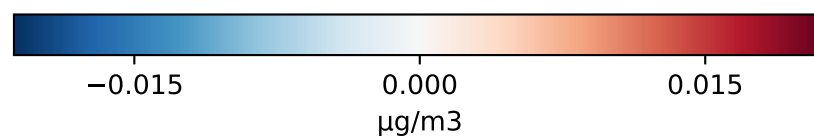
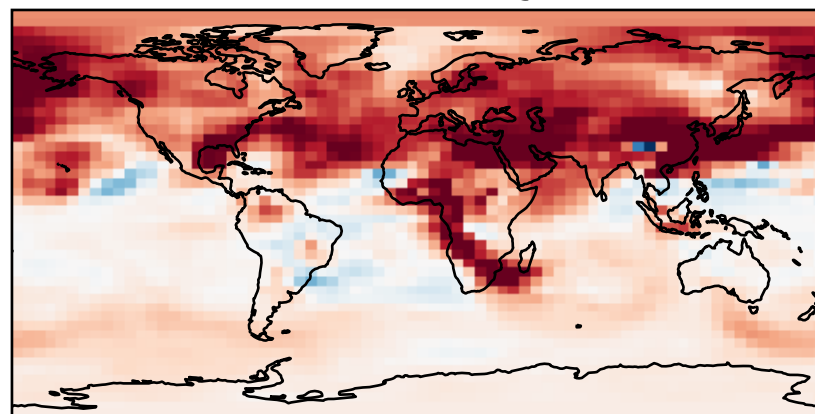
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



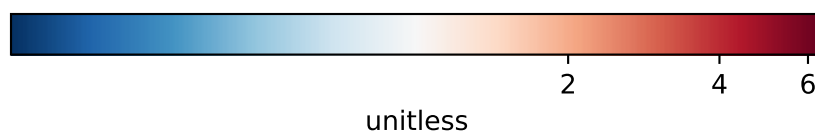
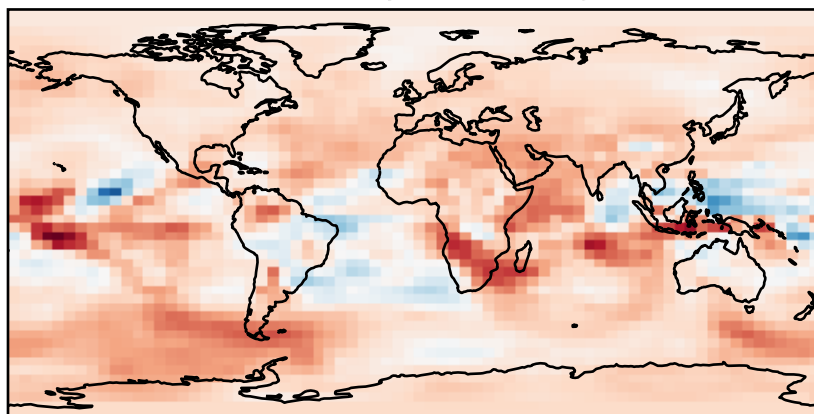
Difference  
Dev - Ref, Dynamic Range



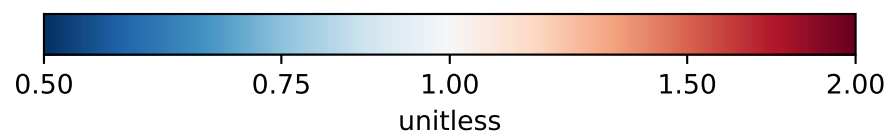
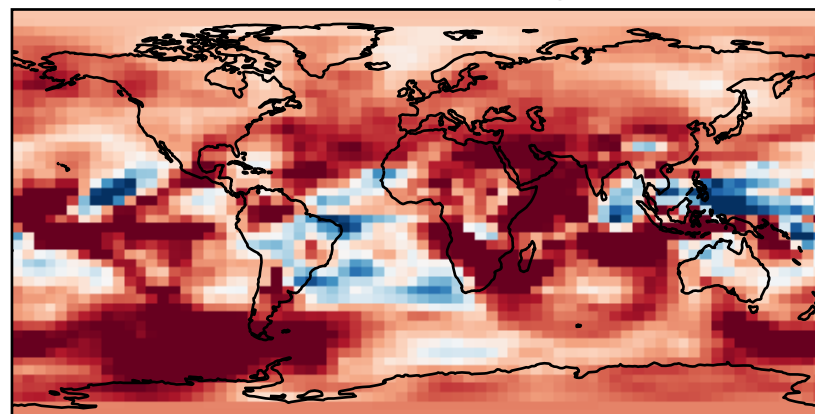
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



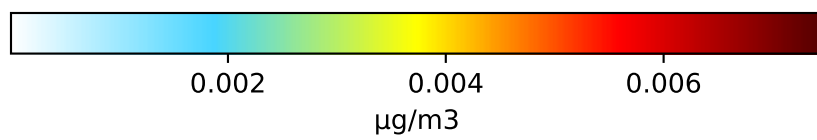
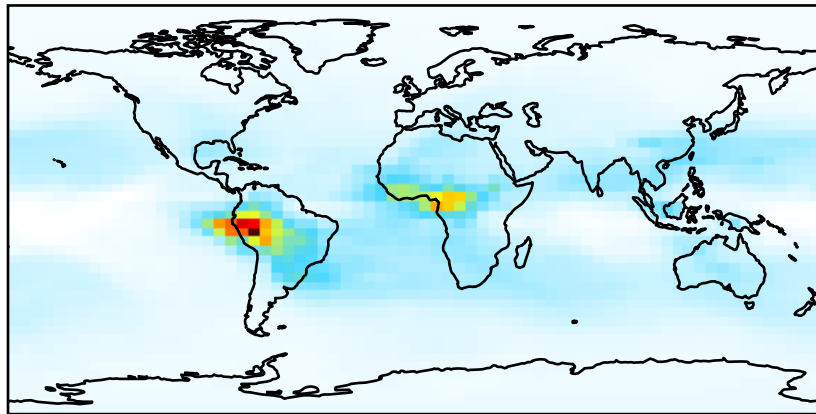
Ratio  
Dev/Ref, Fixed Range



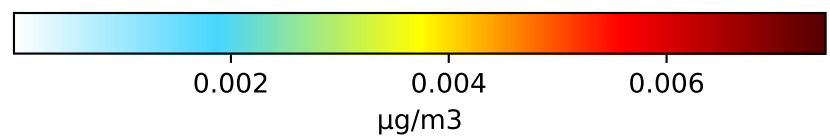
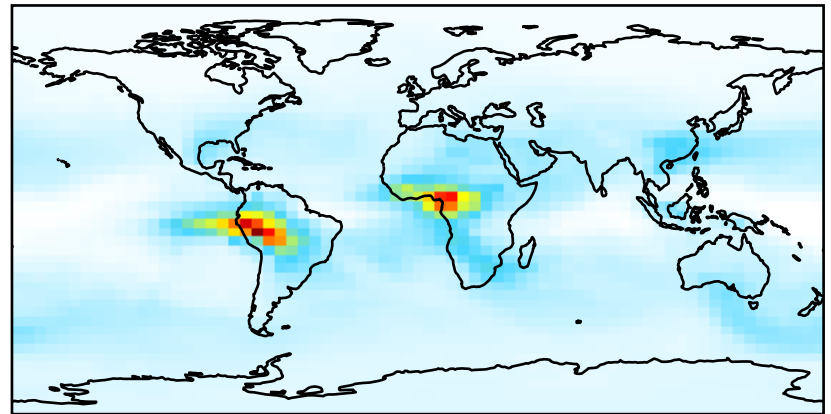


# SpeciesConc\_LVOCOA (Apr2019)

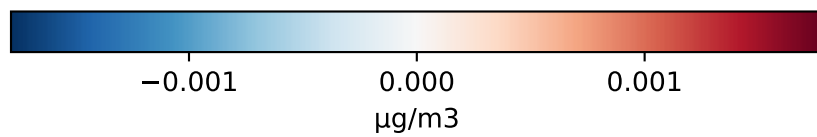
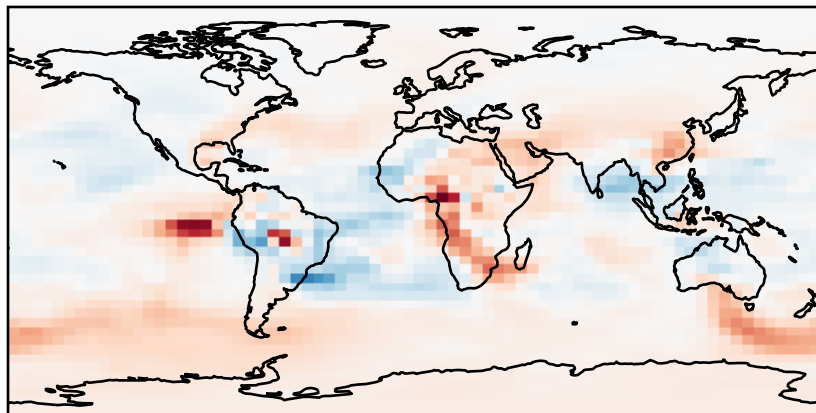
GCC\_12.9.0 (Ref)  
4.0x5.0



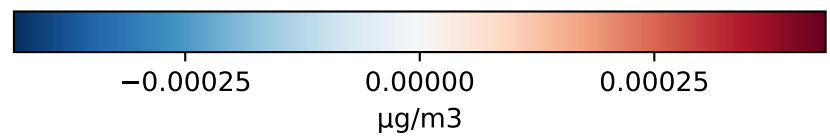
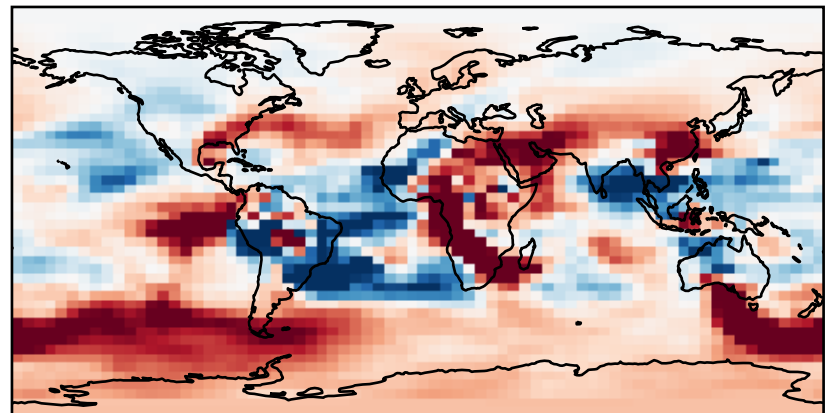
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



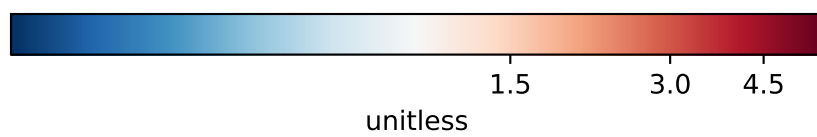
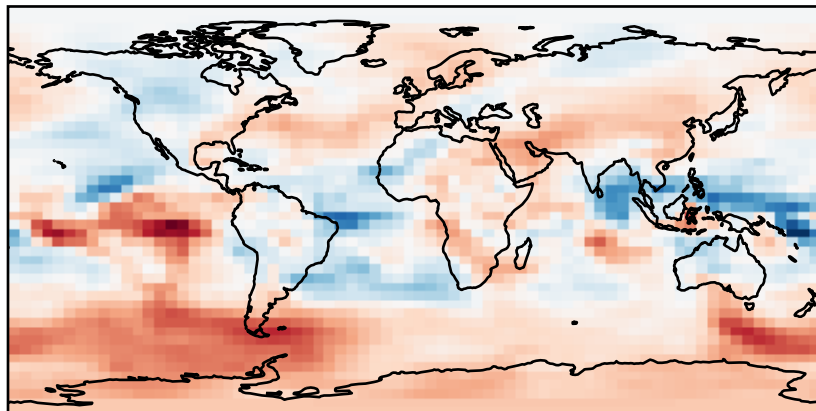
Difference  
Dev - Ref, Dynamic Range



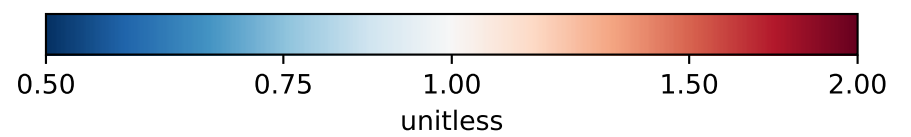
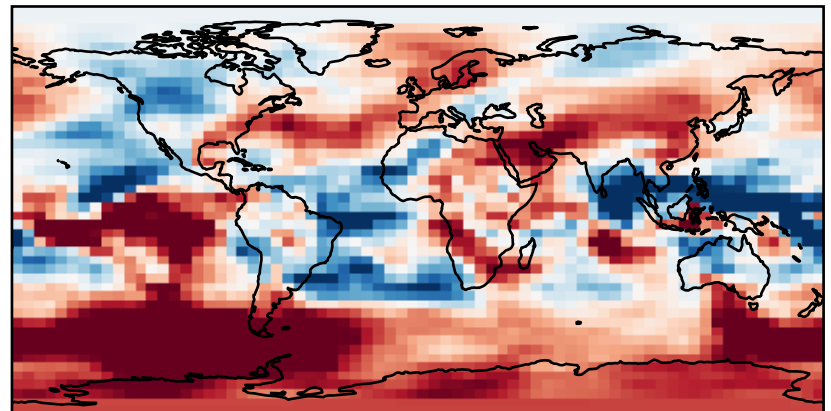
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

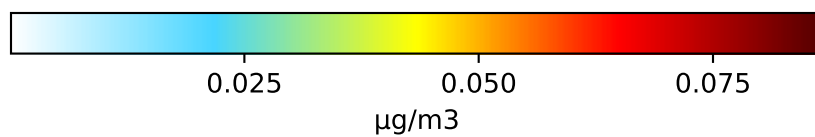
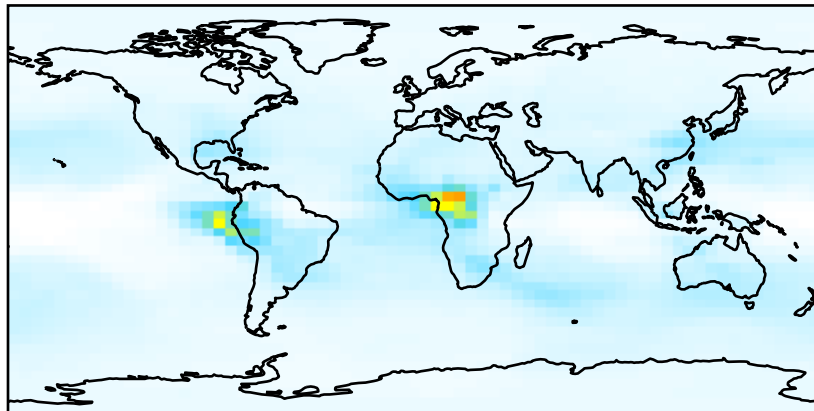


Ratio  
Dev/Ref, Fixed Range

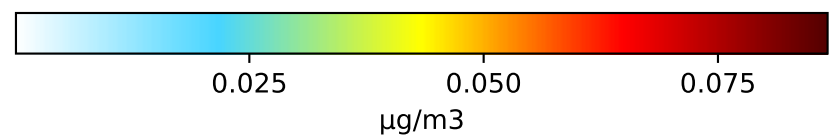
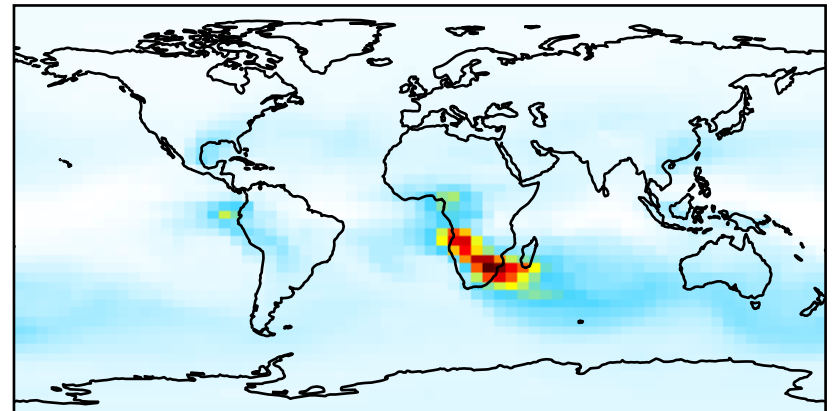


# SpeciesConc\_SOAIE (Apr2019)

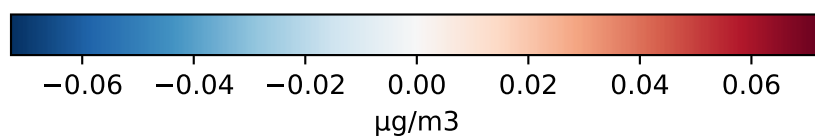
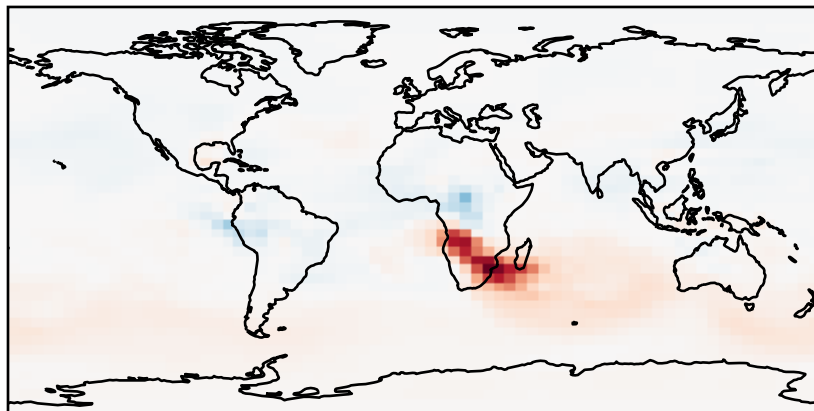
GCC\_12.9.0 (Ref)  
4.0x5.0



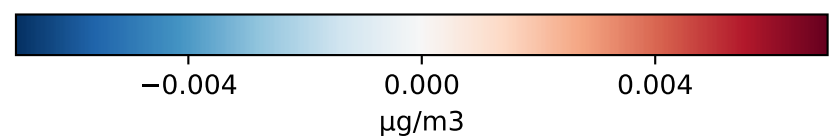
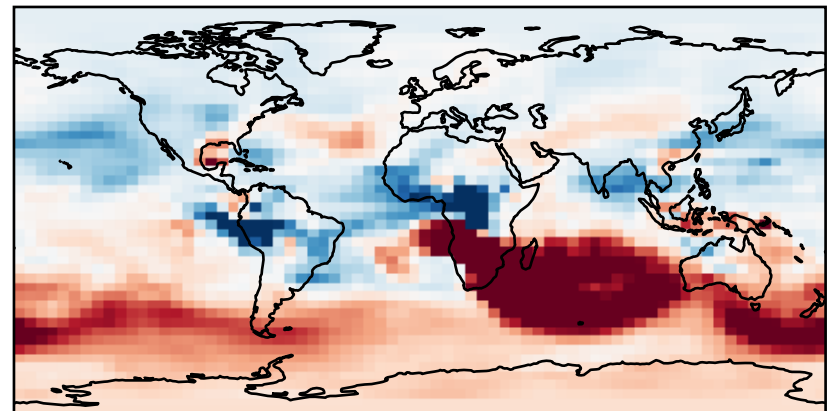
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



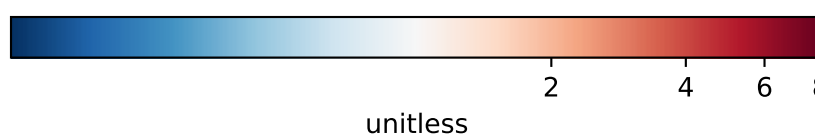
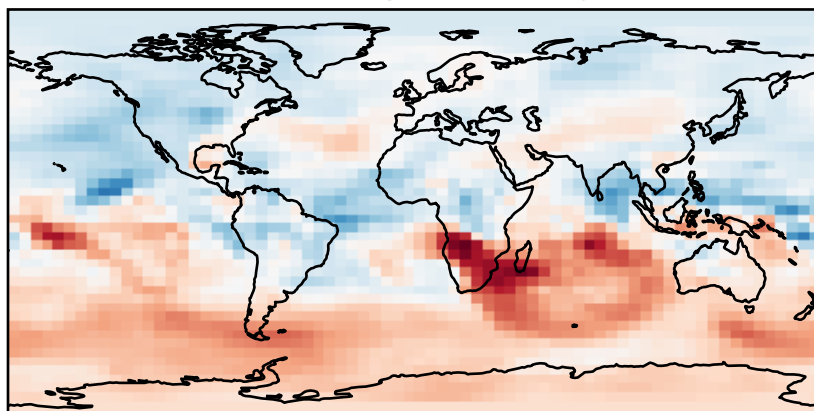
Difference  
Dev - Ref, Dynamic Range



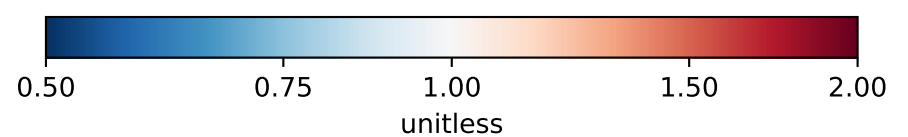
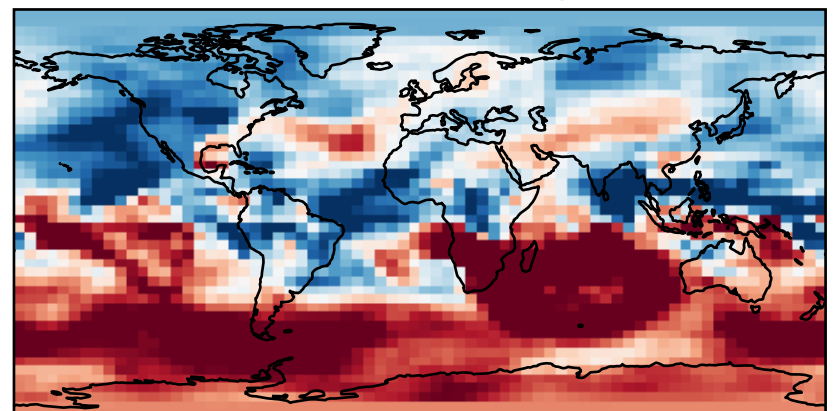
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

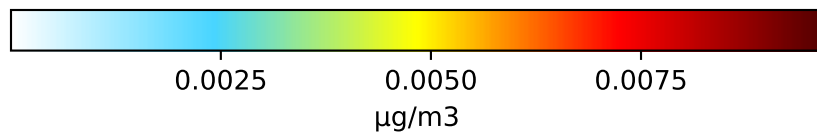
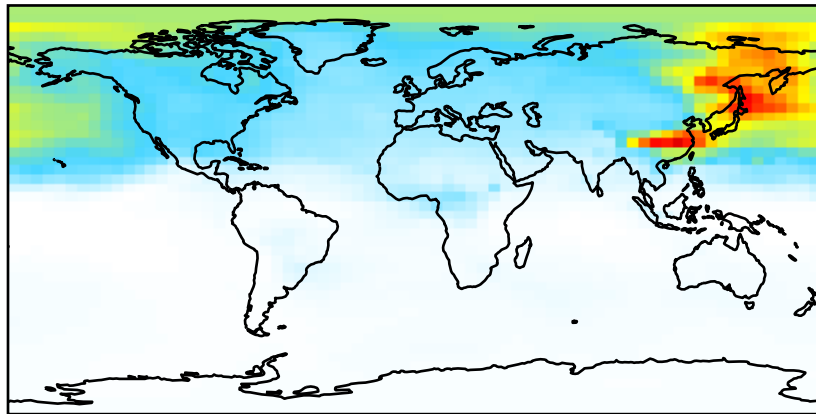


Ratio  
Dev/Ref, Fixed Range

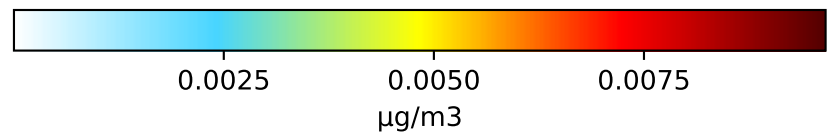
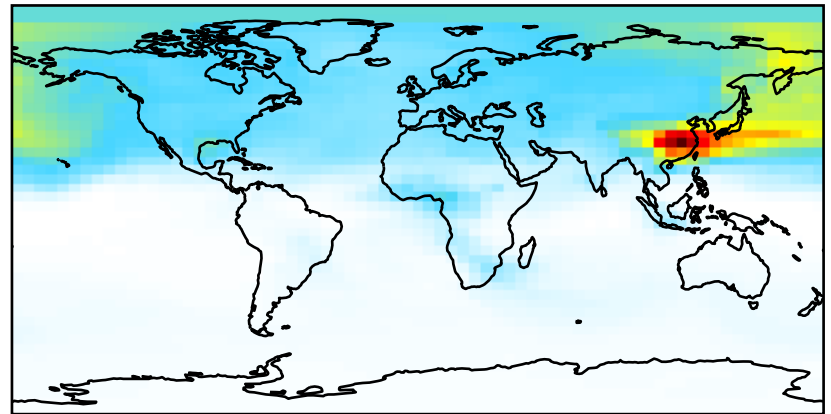


# SpeciesConc\_SOAGX (Apr2019)

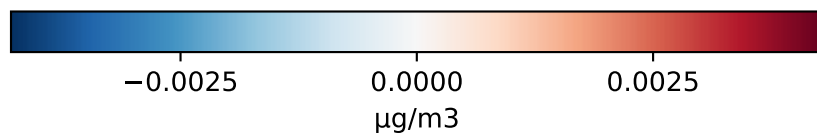
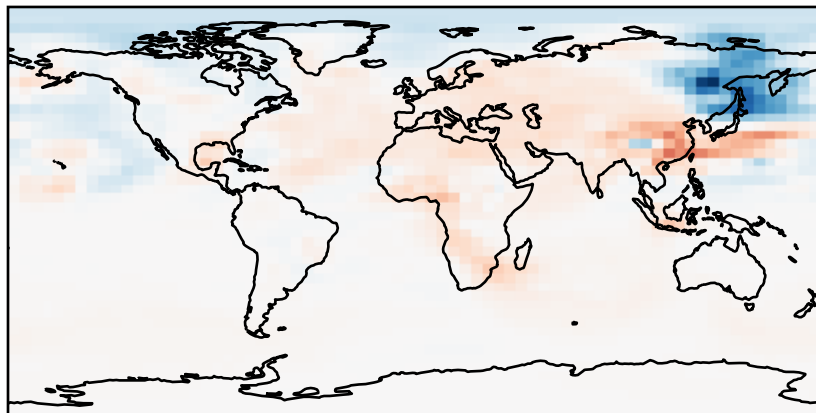
GCC\_12.9.0 (Ref)  
4.0x5.0



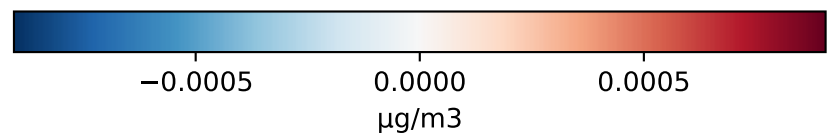
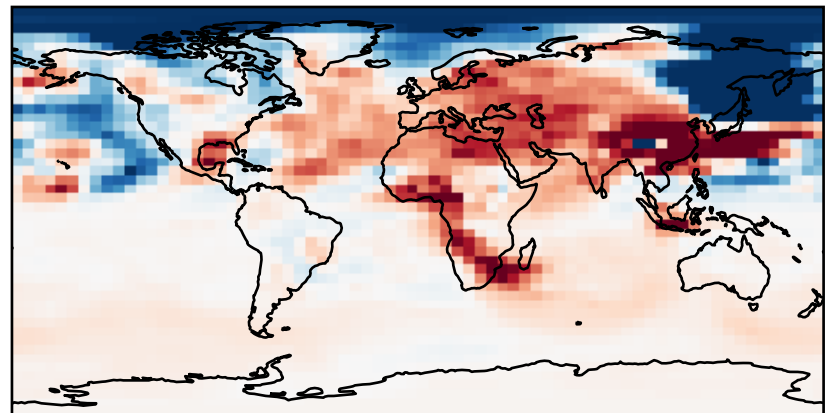
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



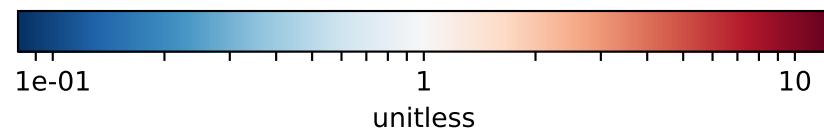
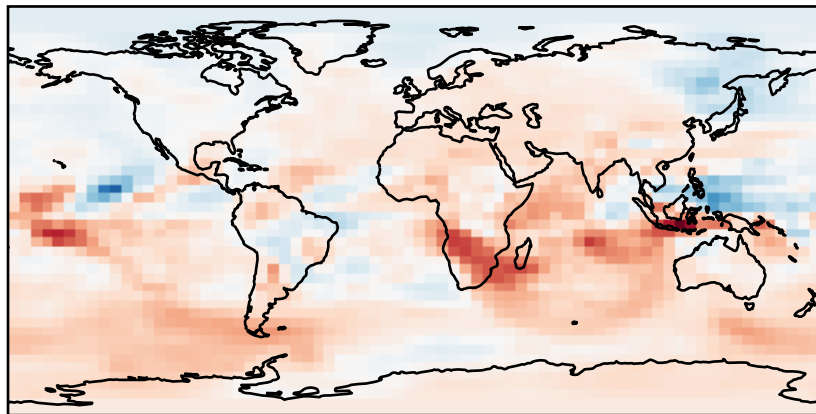
Difference  
Dev - Ref, Dynamic Range



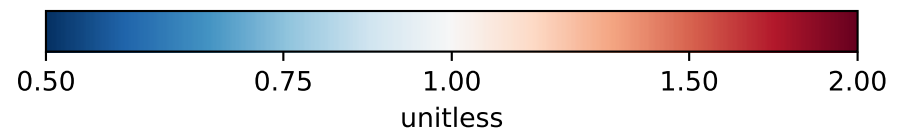
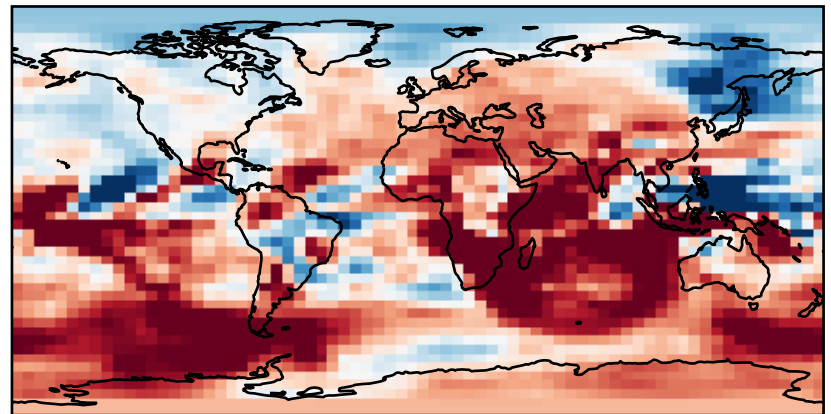
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

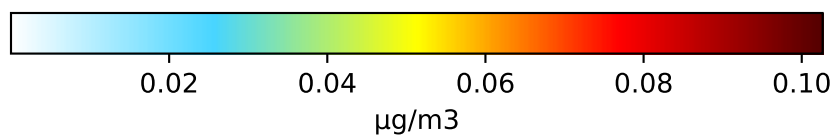
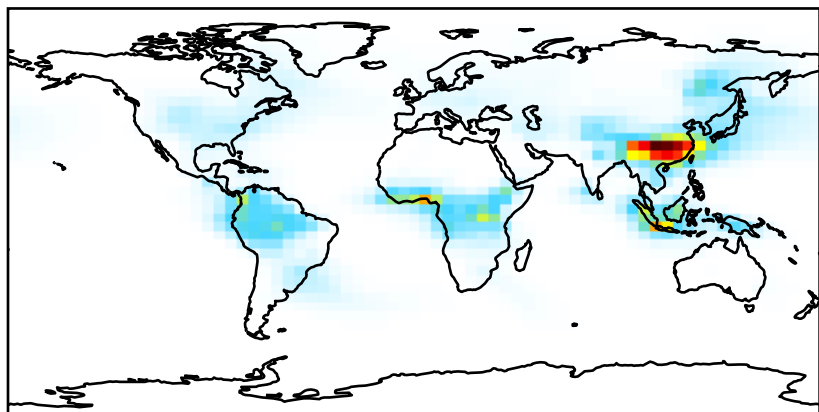


Ratio  
Dev/Ref, Fixed Range

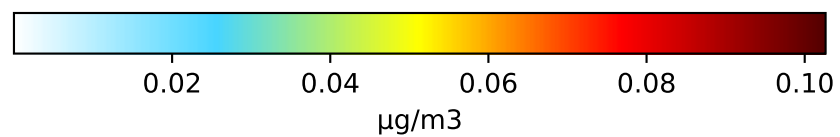
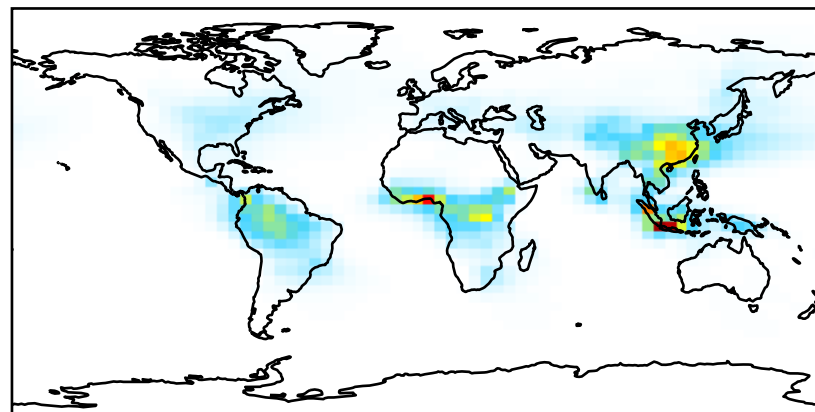


# SpeciesConc\_SOAP (Apr2019)

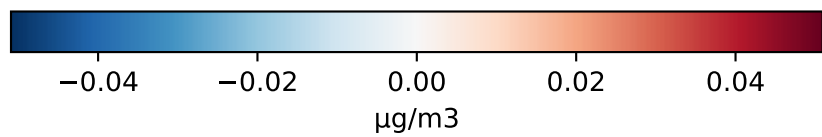
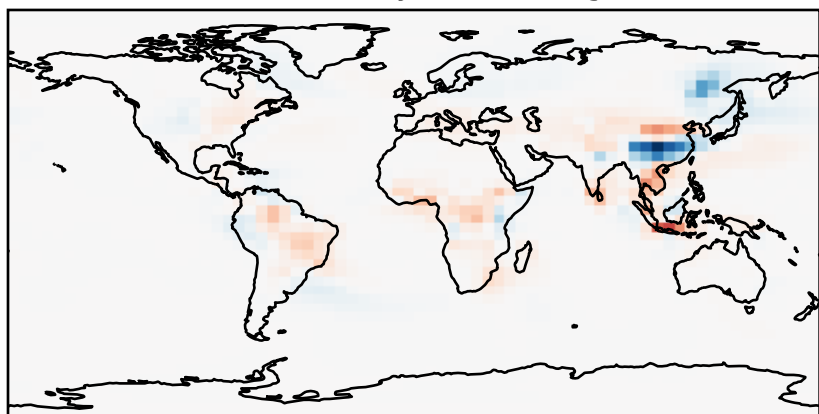
GCC\_12.9.0 (Ref)  
4.0x5.0



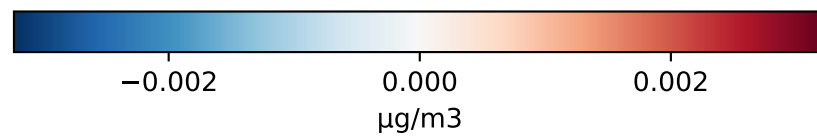
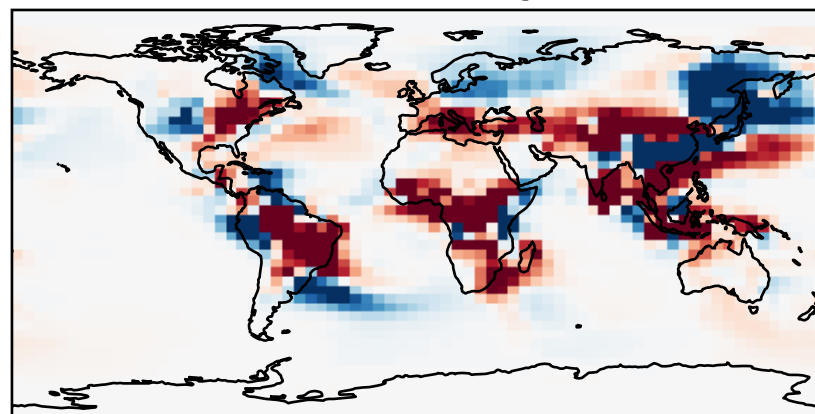
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



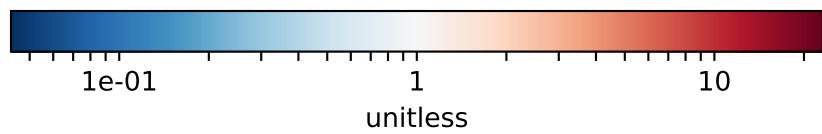
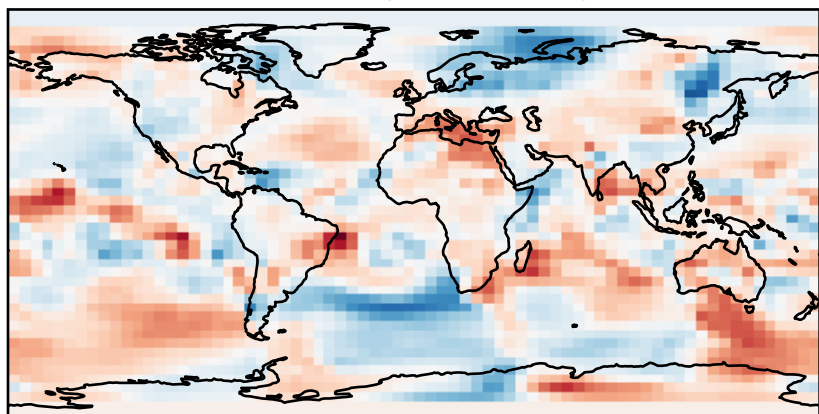
Difference  
Dev - Ref, Dynamic Range



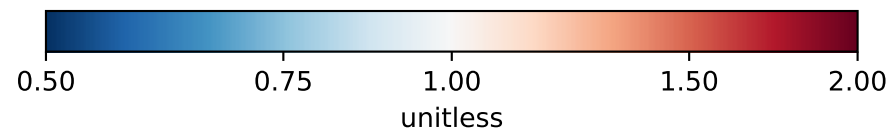
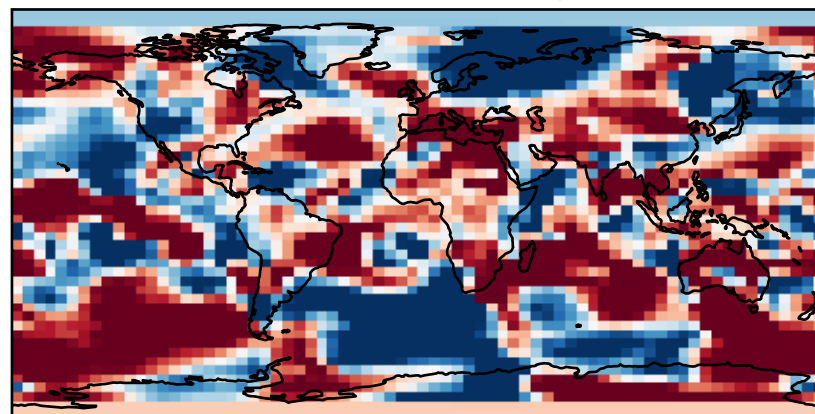
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



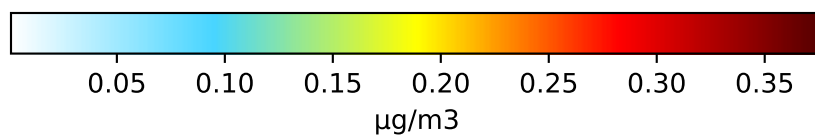
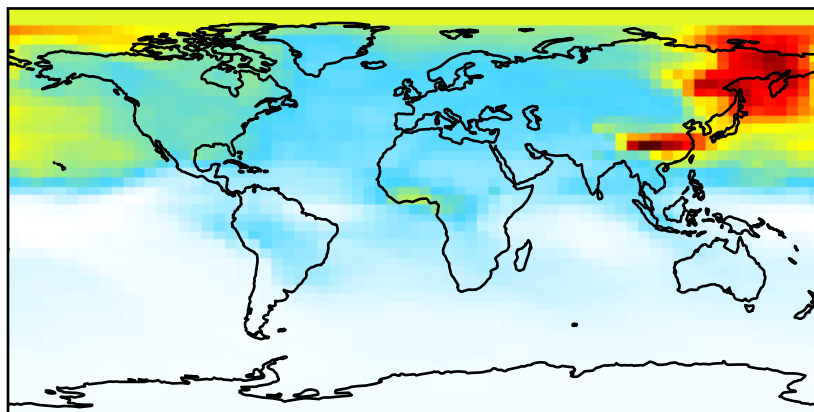
Ratio  
Dev/Ref, Fixed Range



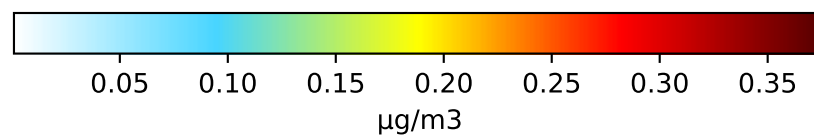
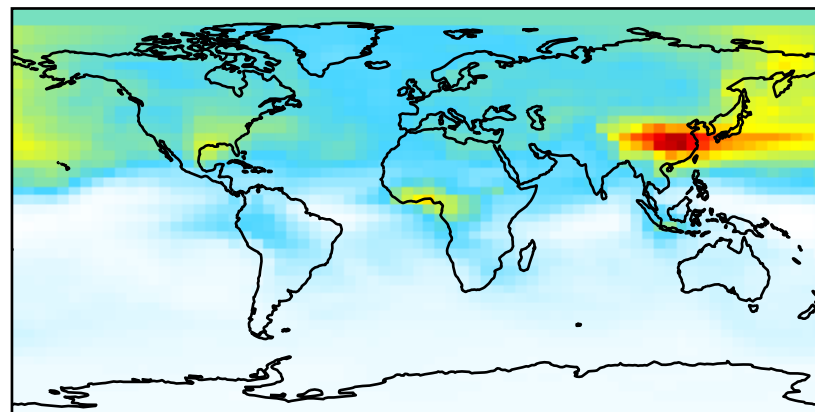


# SpeciesConc\_SOAS (Apr2019)

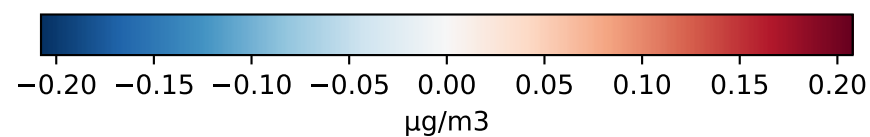
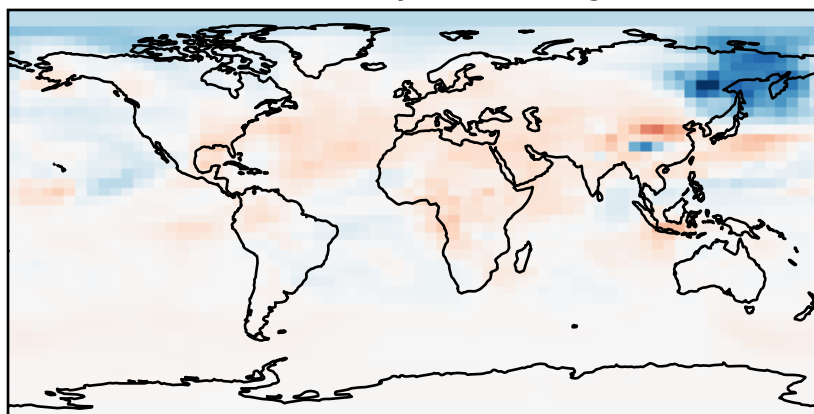
GCC\_12.9.0 (Ref)  
4.0x5.0



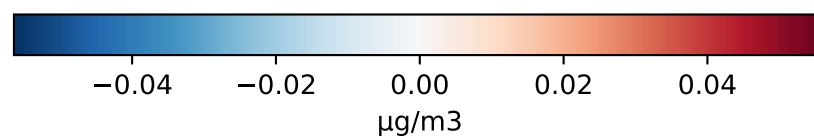
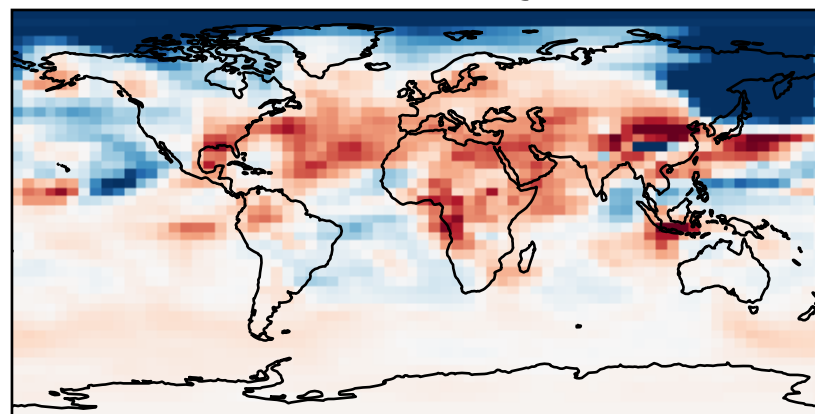
GCC\_13.0.0-beta.1 (Dev)  
4.0x5.0



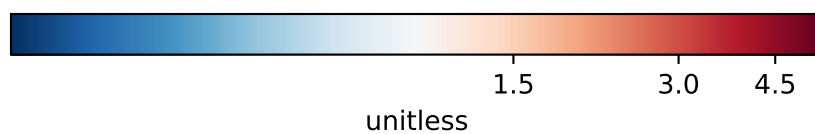
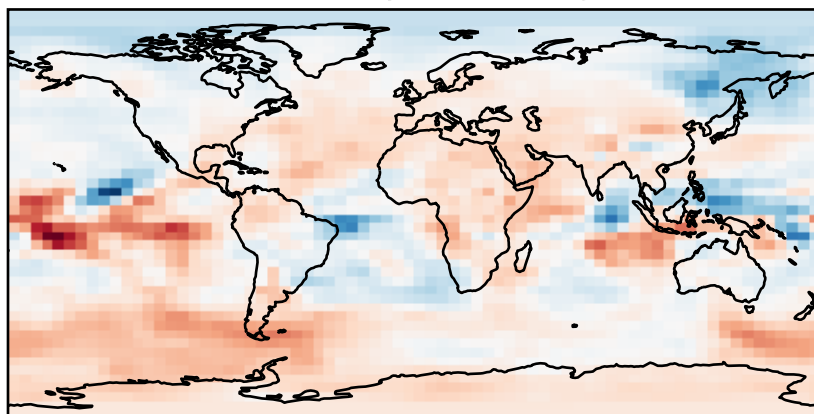
Difference  
Dev - Ref, Dynamic Range



Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

