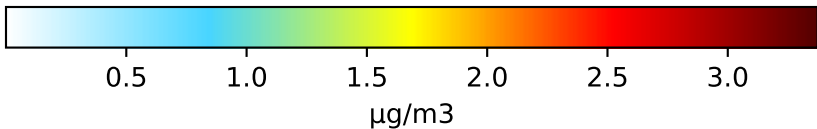
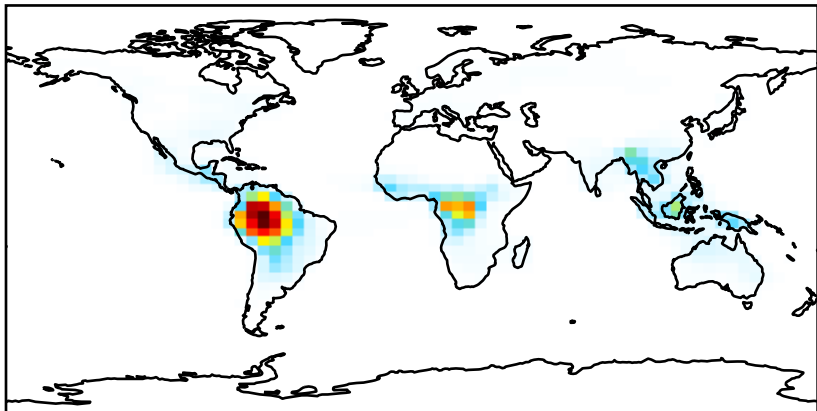
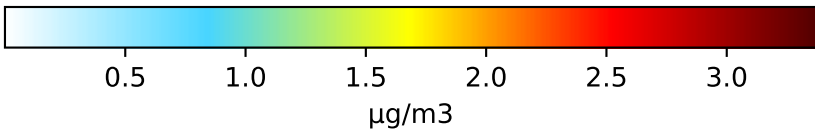
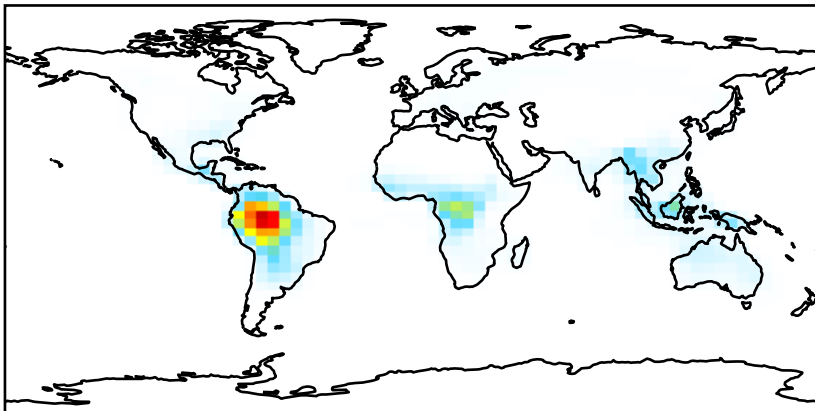


SpeciesConc_TSOA0 (Oct2019)

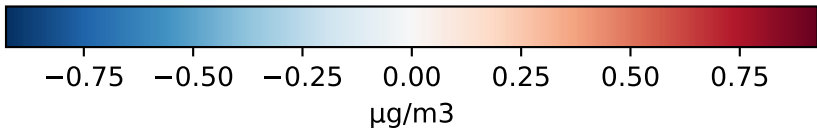
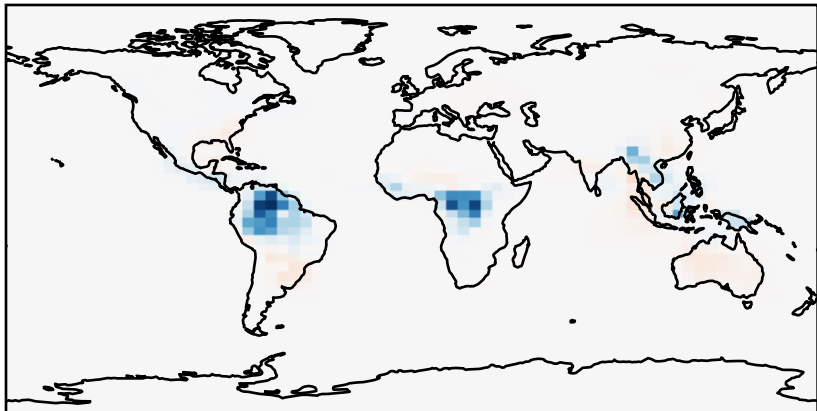
GCC_12.9.0 (Ref)
4.0x5.0



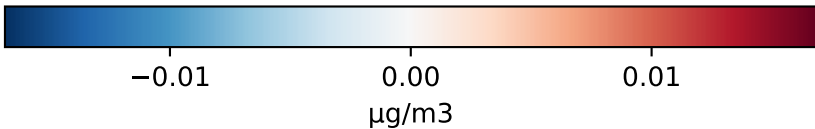
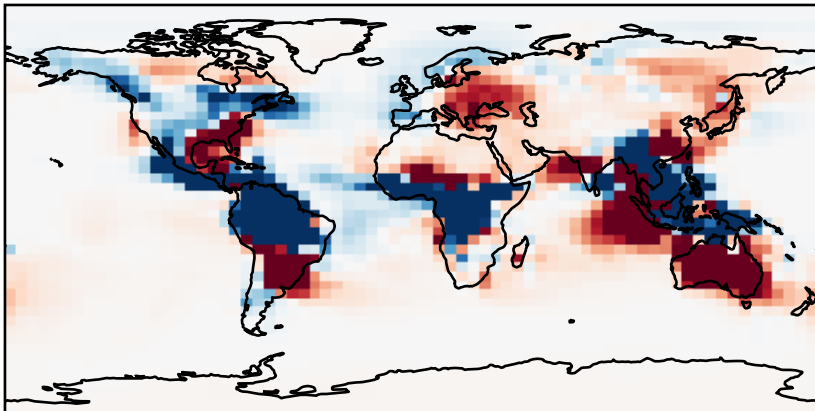
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



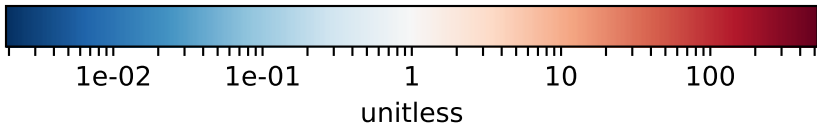
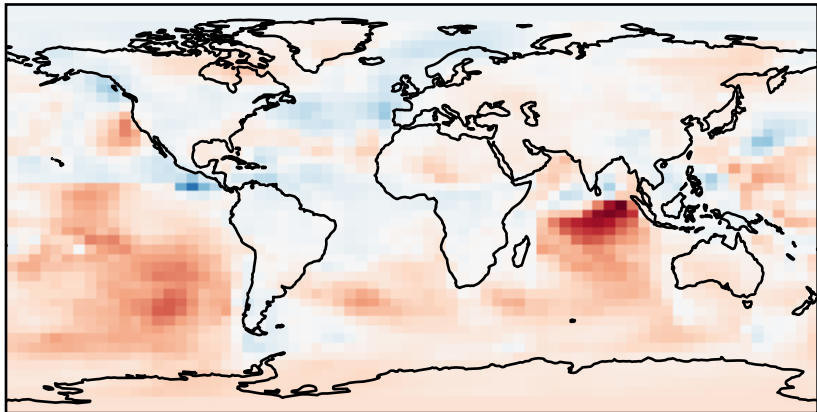
Difference
Dev - Ref, Dynamic Range



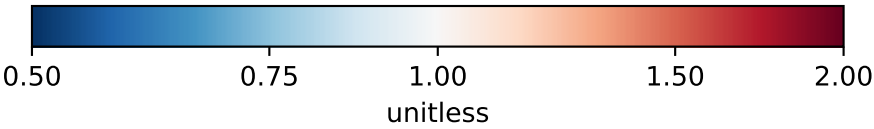
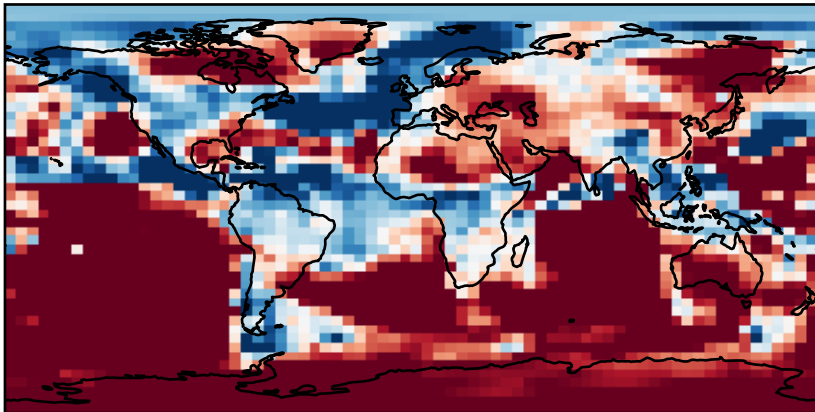
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

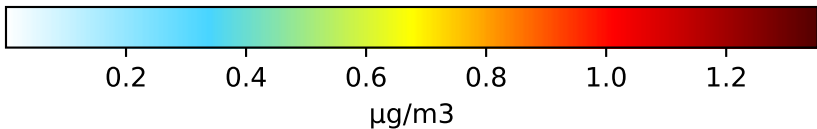
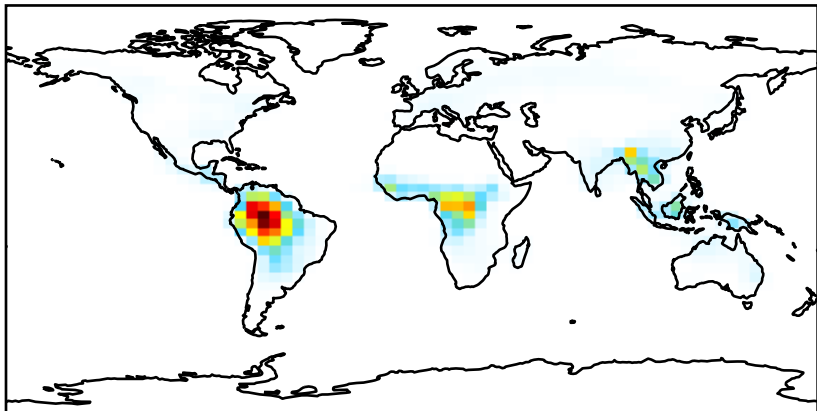


Ratio
Dev/Ref, Fixed Range

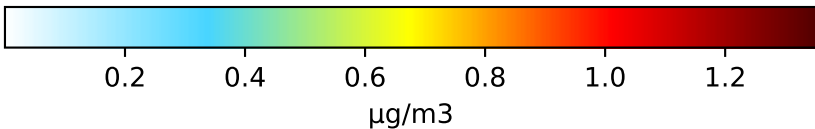
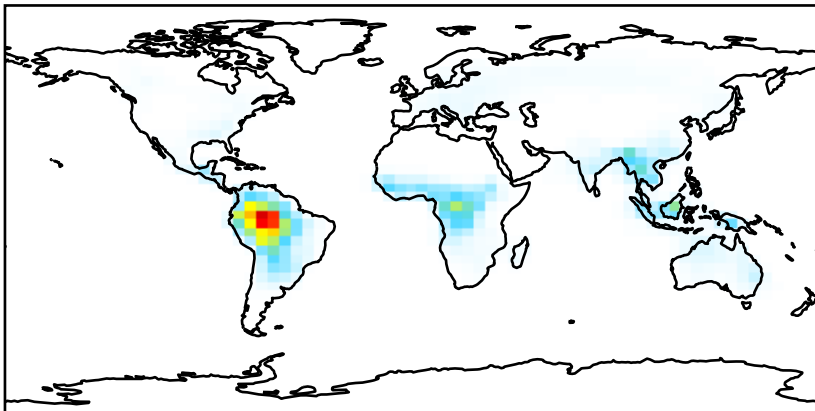


SpeciesConc_TSOA1 (Oct2019)

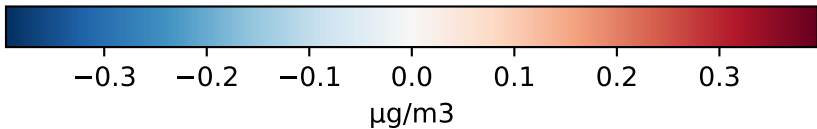
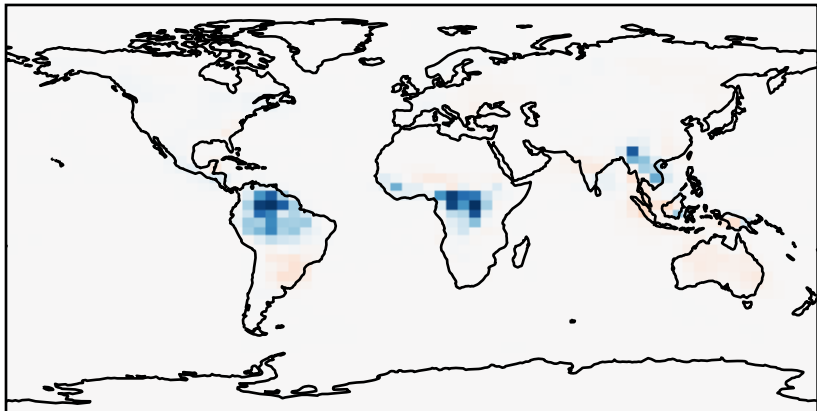
GCC_12.9.0 (Ref)
4.0x5.0



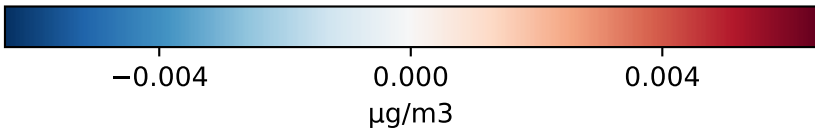
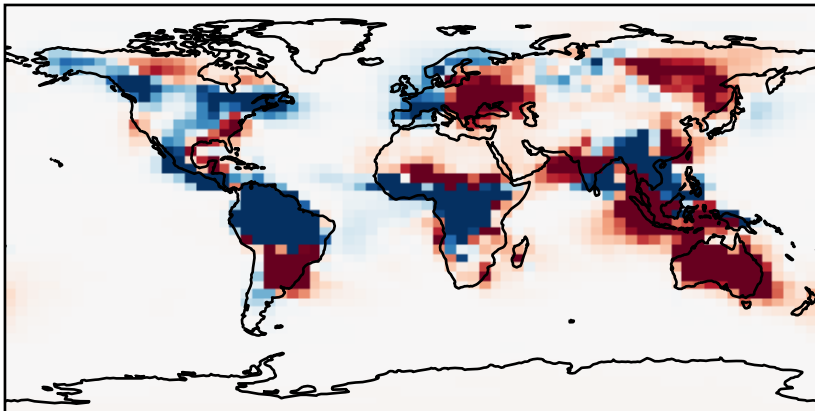
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



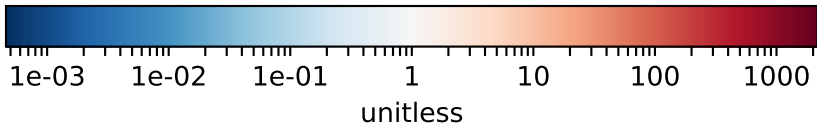
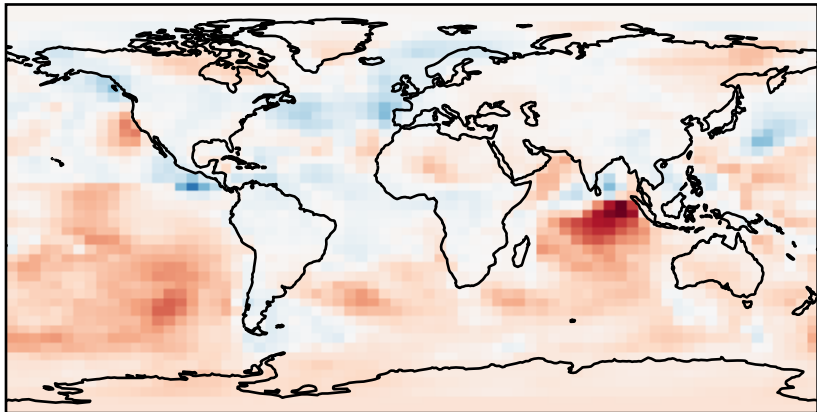
Difference
Dev - Ref, Dynamic Range



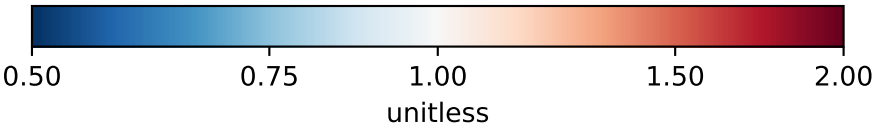
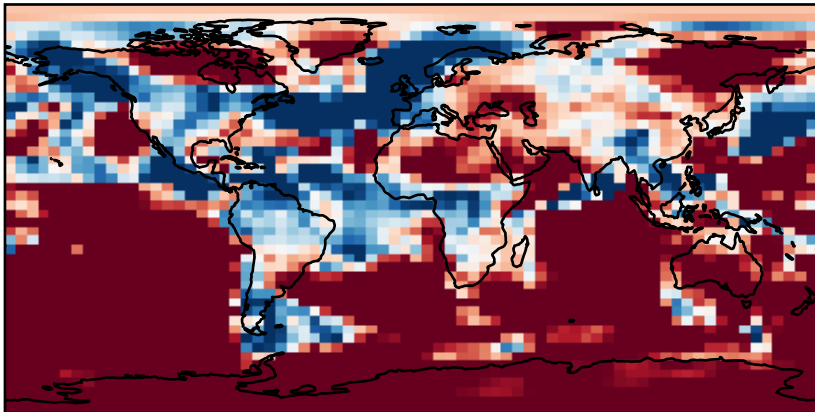
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

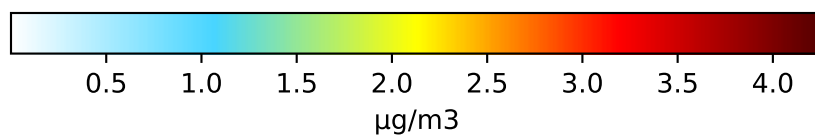
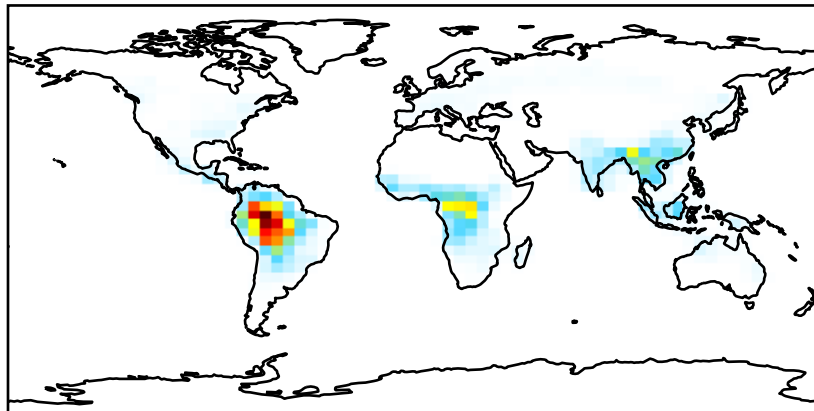


Ratio
Dev/Ref, Fixed Range

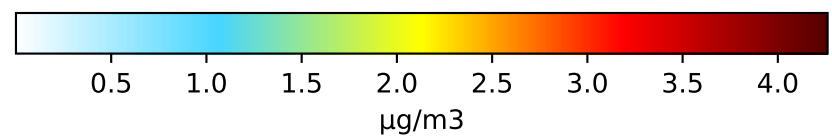
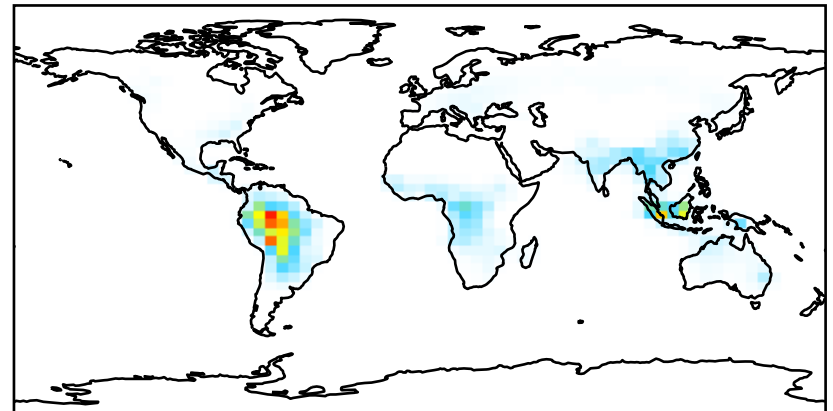


SpeciesConc_TSOA2 (Oct2019)

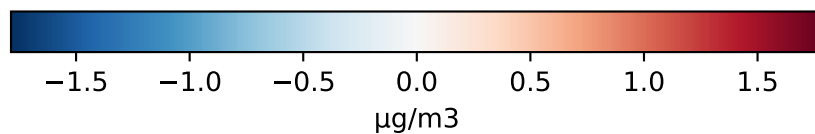
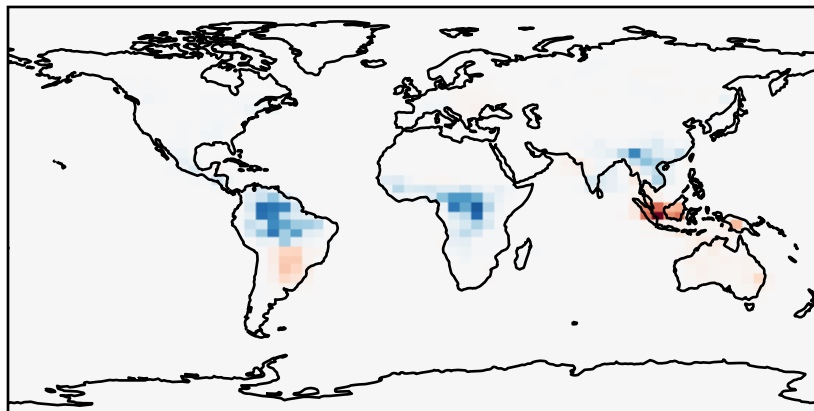
GCC_12.9.0 (Ref)
4.0x5.0



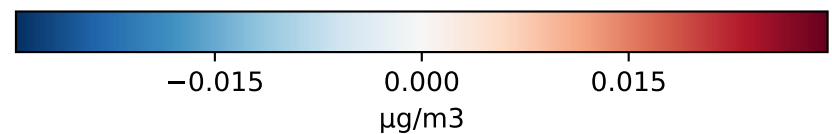
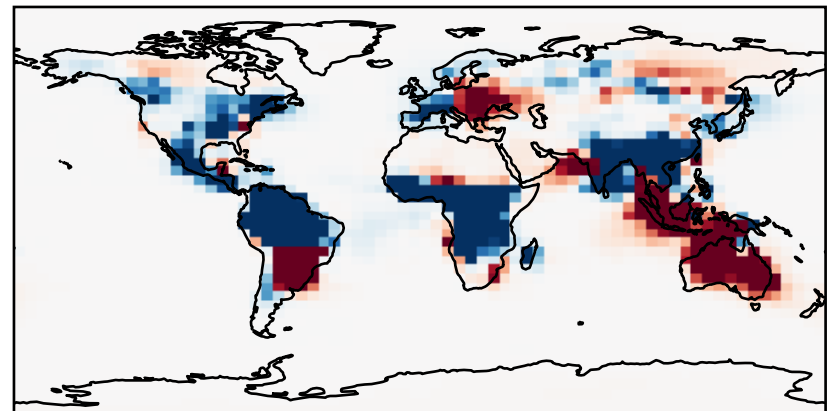
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



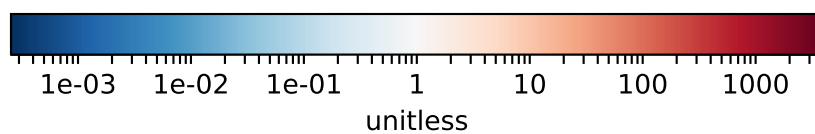
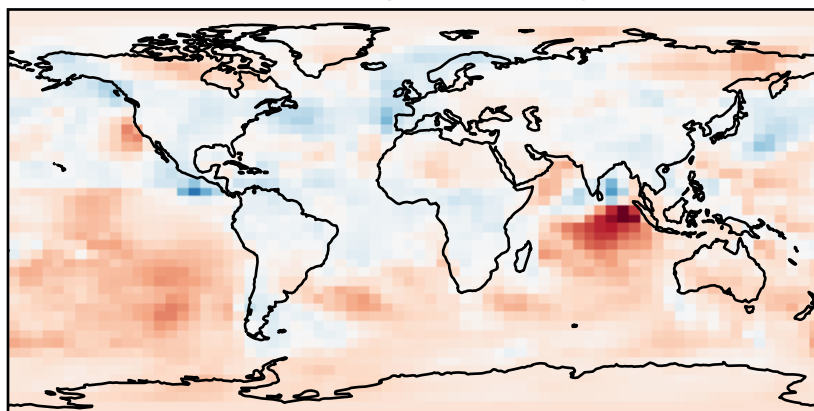
Difference
Dev - Ref, Dynamic Range



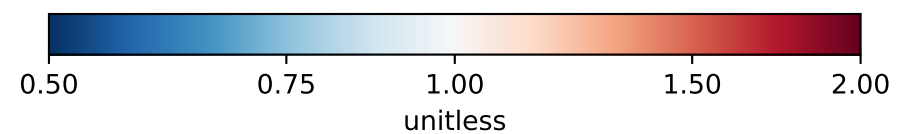
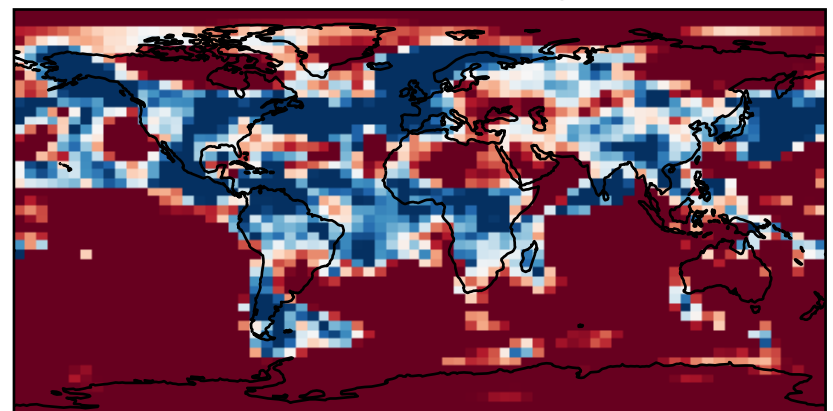
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

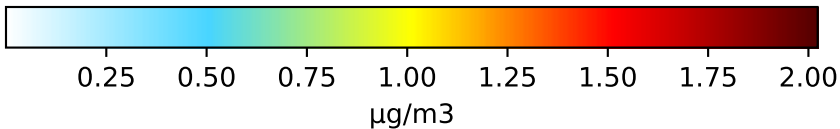
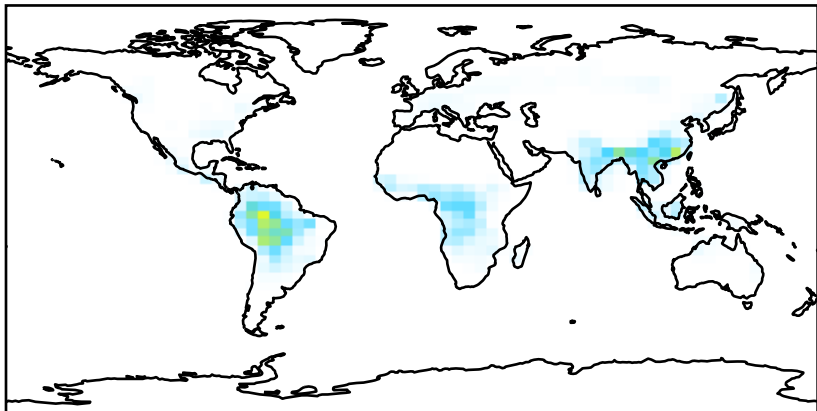


Ratio
Dev/Ref, Fixed Range

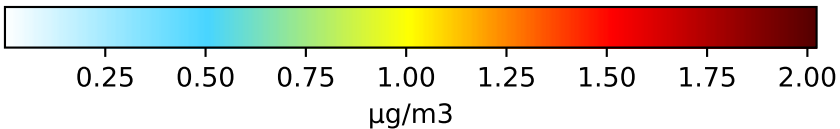
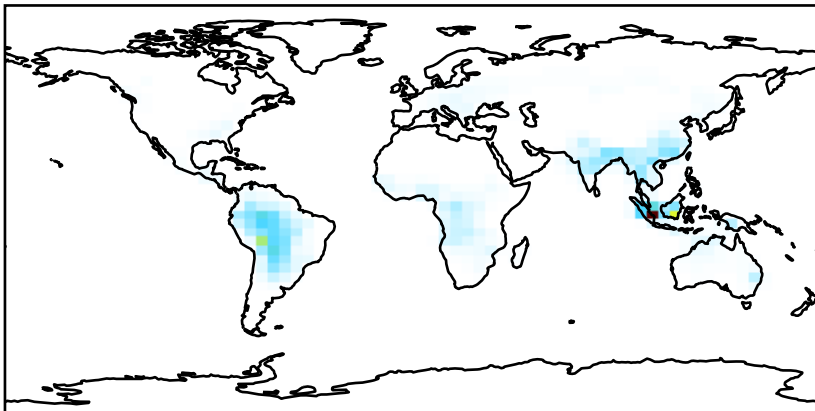


SpeciesConc_TSOA3 (Oct2019)

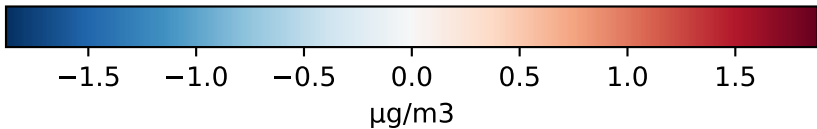
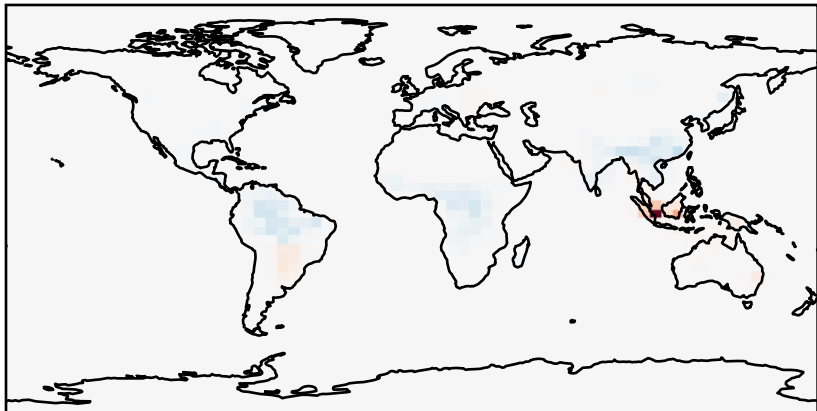
GCC_12.9.0 (Ref)
4.0x5.0



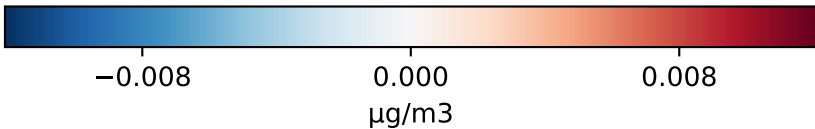
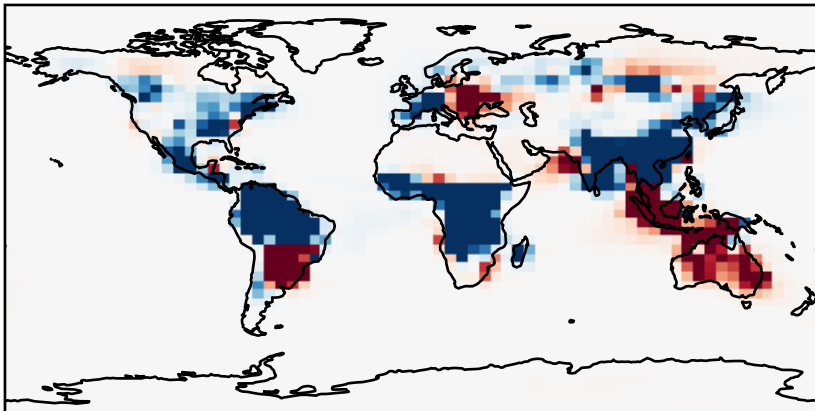
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



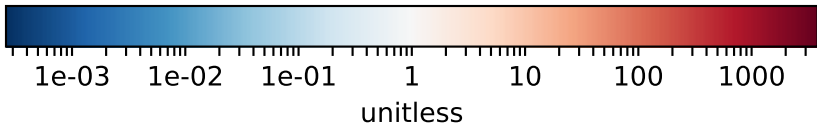
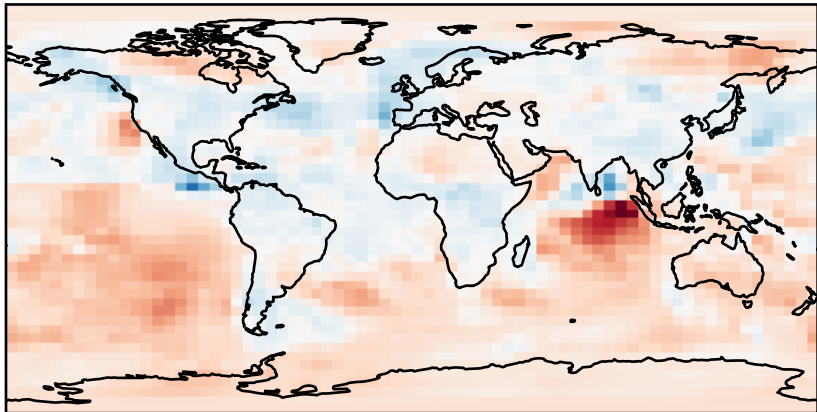
Difference
Dev - Ref, Dynamic Range



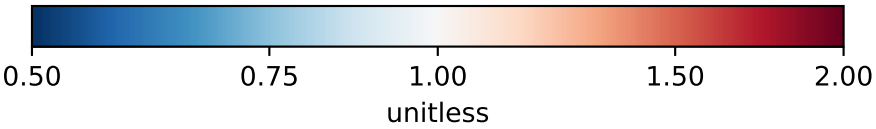
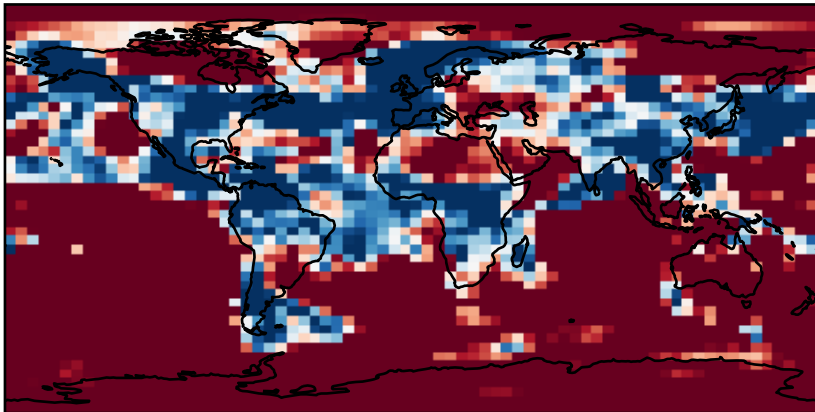
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

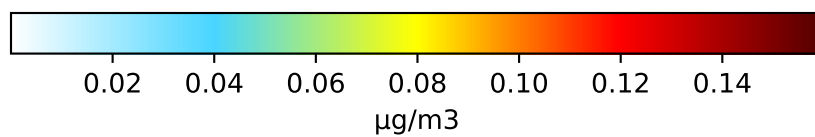
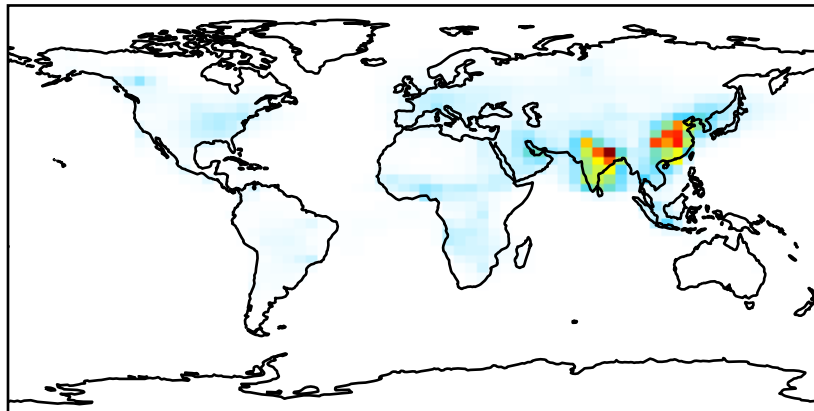


Ratio
Dev/Ref, Fixed Range

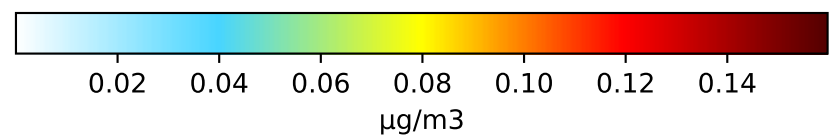
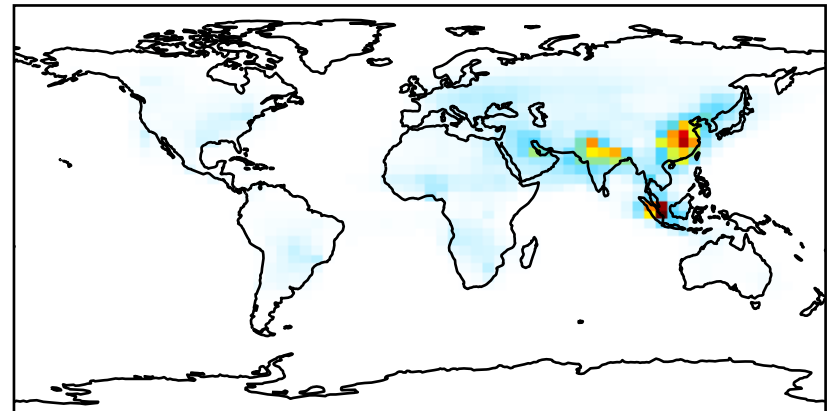


SpeciesConc_ASOA1 (Oct2019)

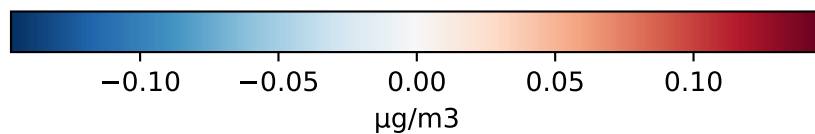
GCC_12.9.0 (Ref)
4.0x5.0



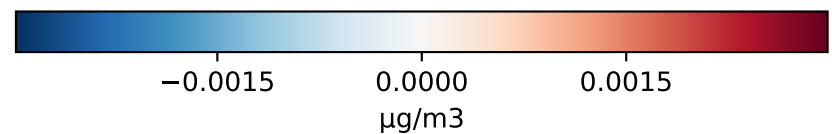
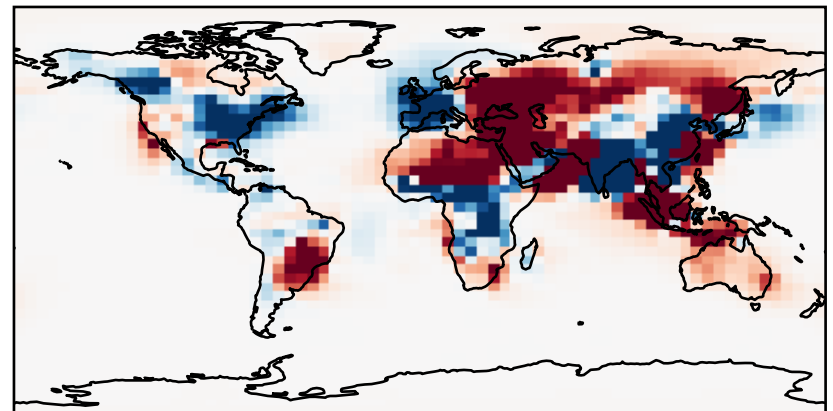
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



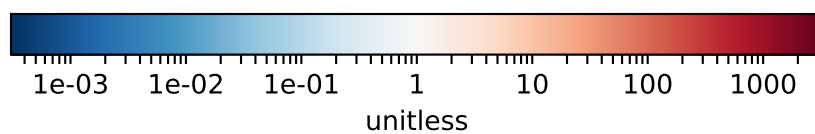
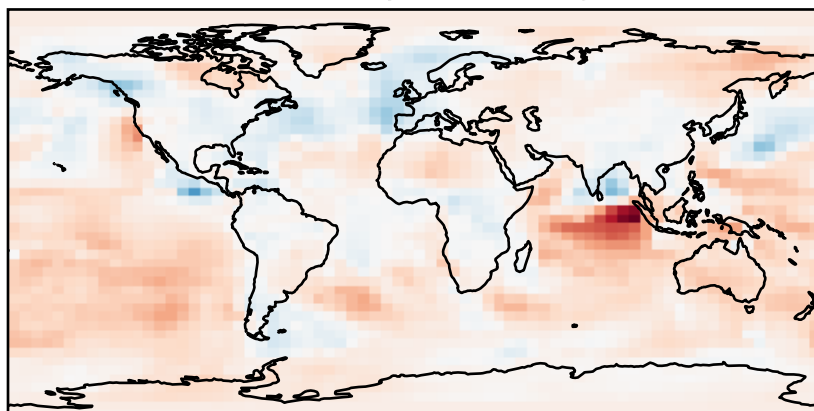
Difference
Dev - Ref, Dynamic Range



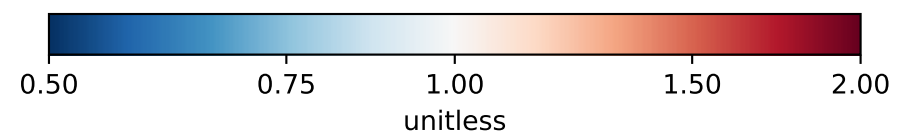
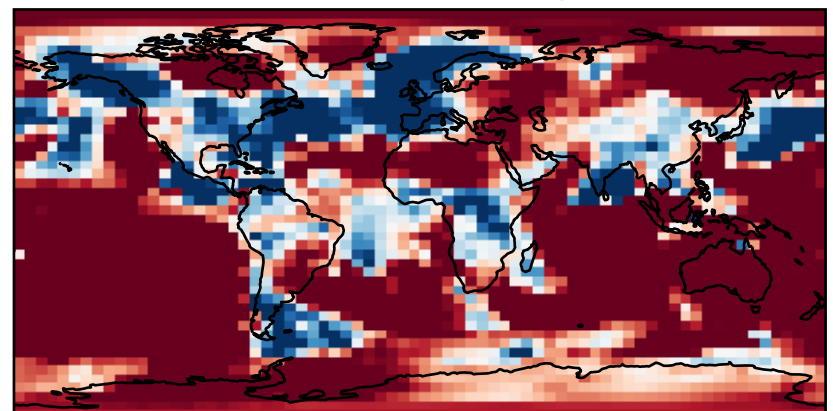
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

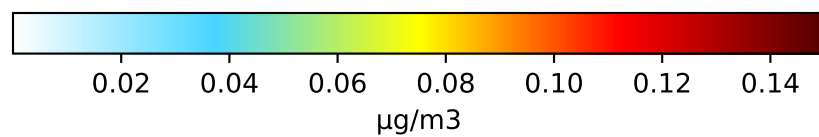
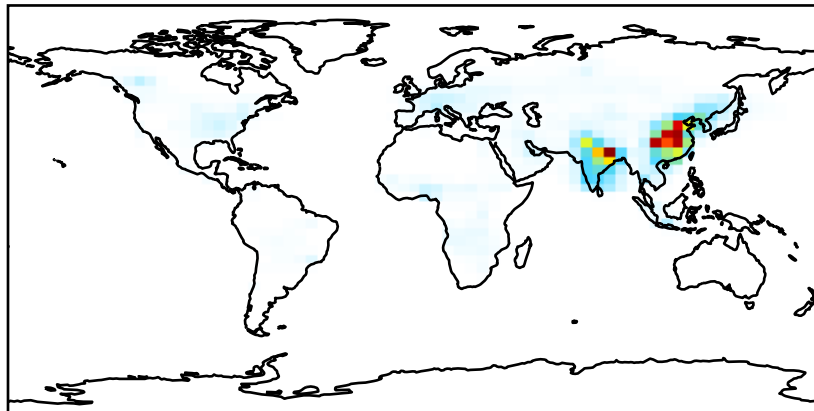


Ratio
Dev/Ref, Fixed Range

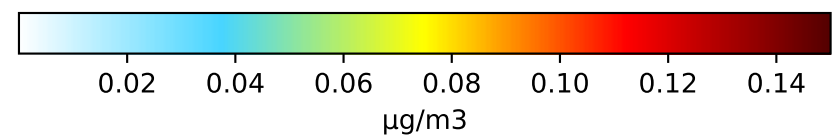
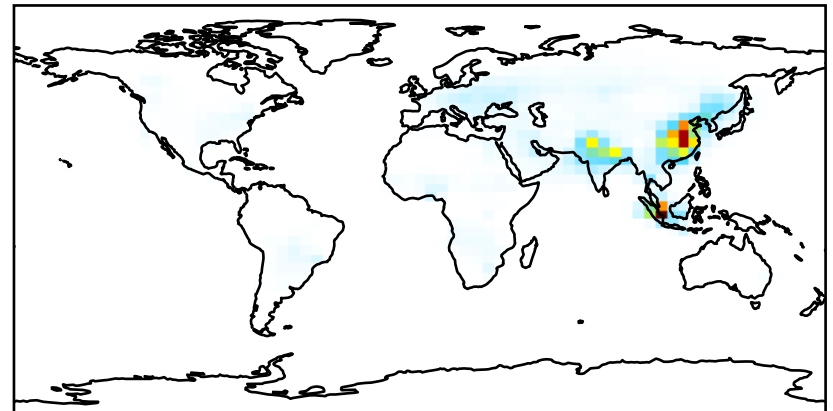


SpeciesConc_ASOA2 (Oct2019)

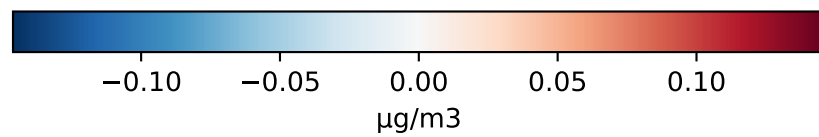
GCC_12.9.0 (Ref)
4.0x5.0



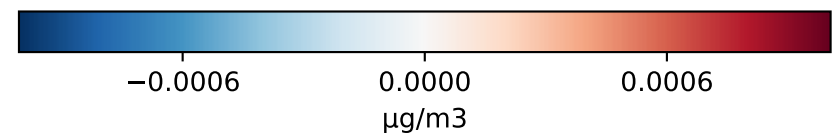
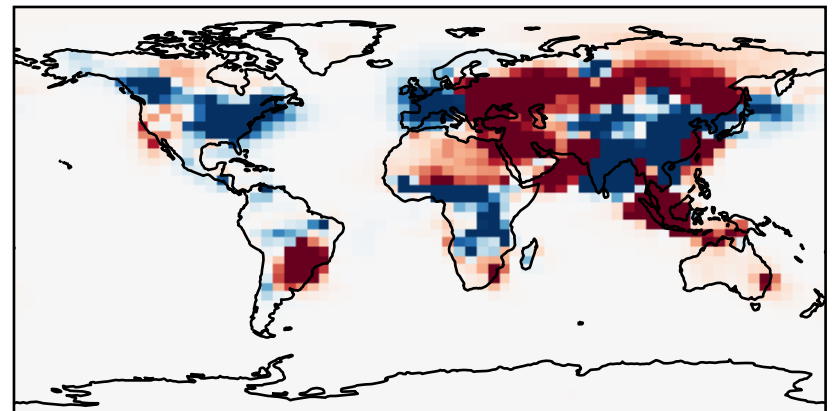
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



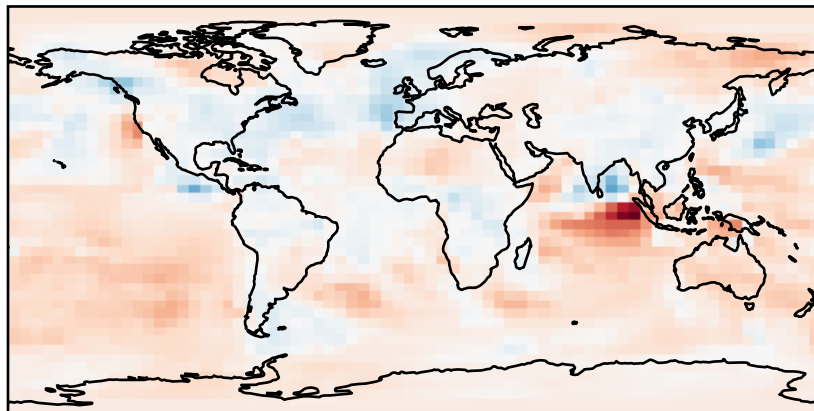
Difference
Dev - Ref, Dynamic Range



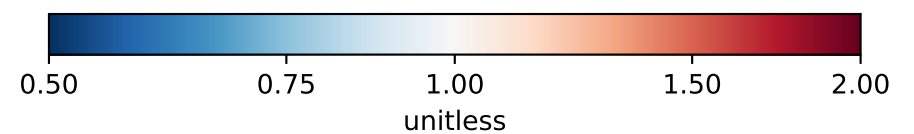
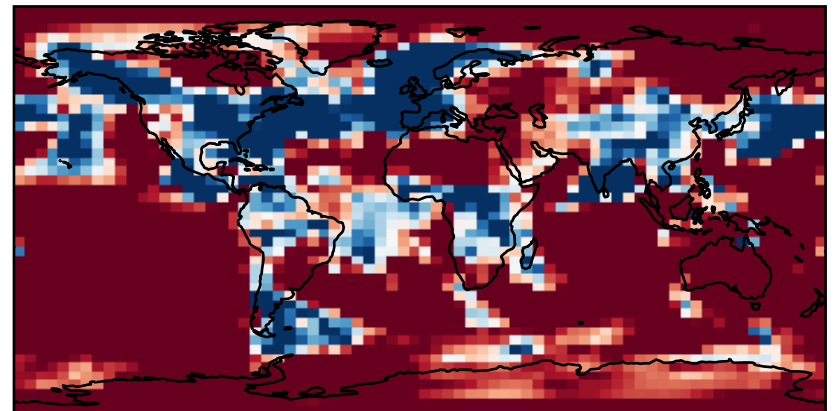
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

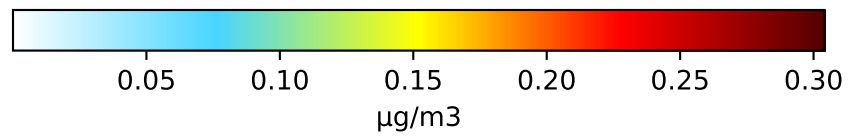
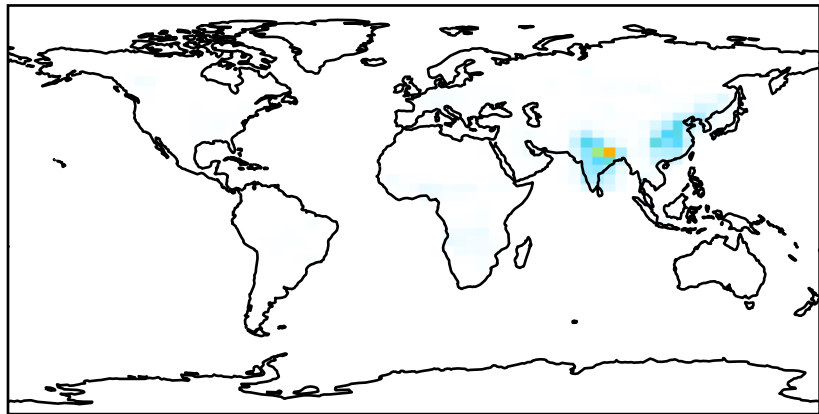


Ratio
Dev/Ref, Fixed Range

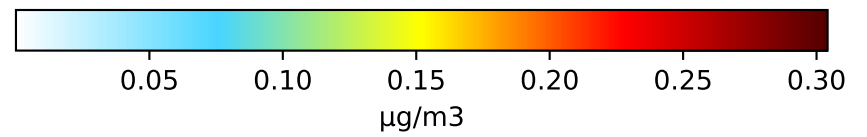
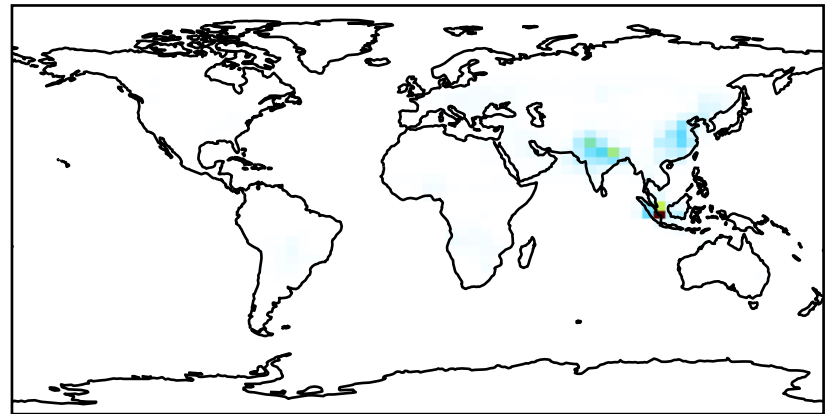


SpeciesConc_ASOA3 (Oct2019)

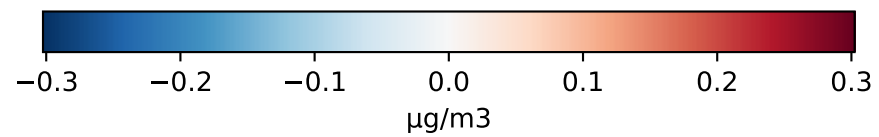
GCC_12.9.0 (Ref)
4.0x5.0



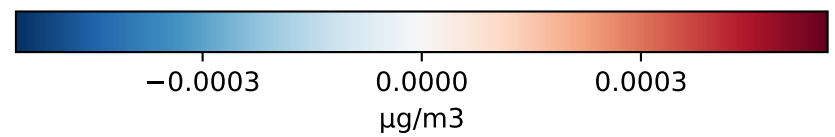
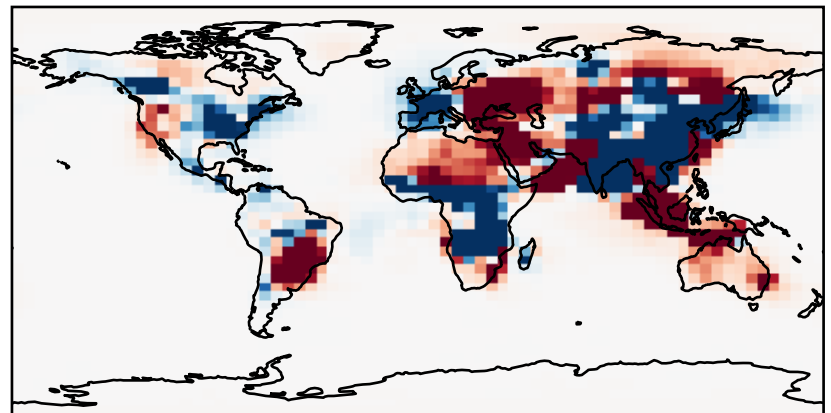
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



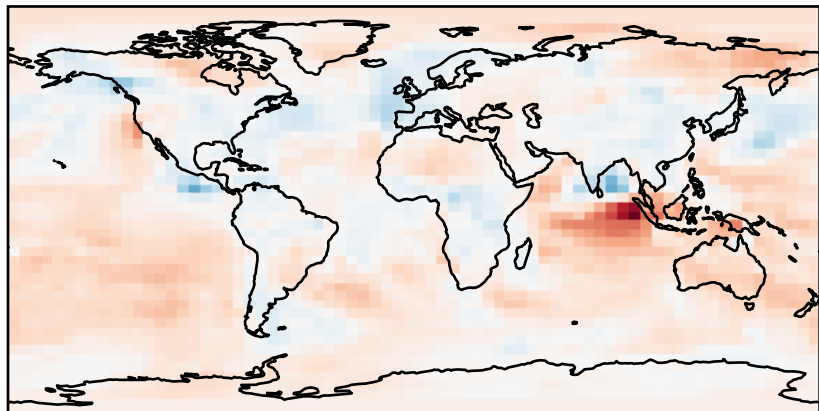
Difference
Dev - Ref, Dynamic Range



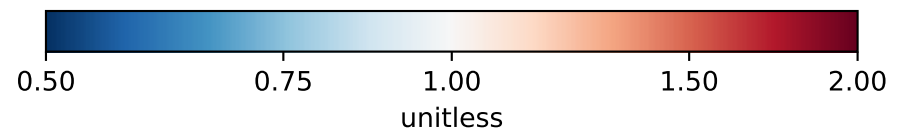
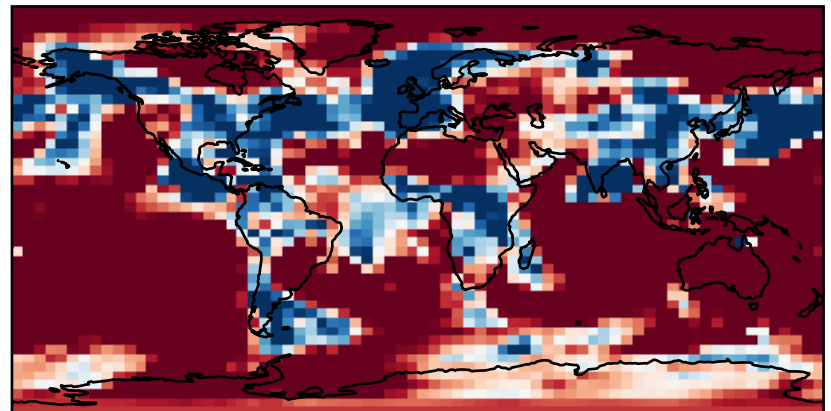
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

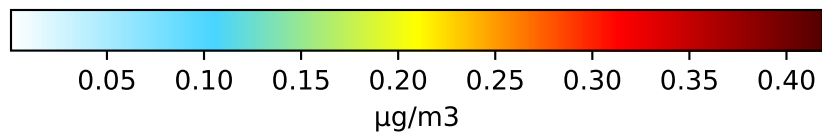
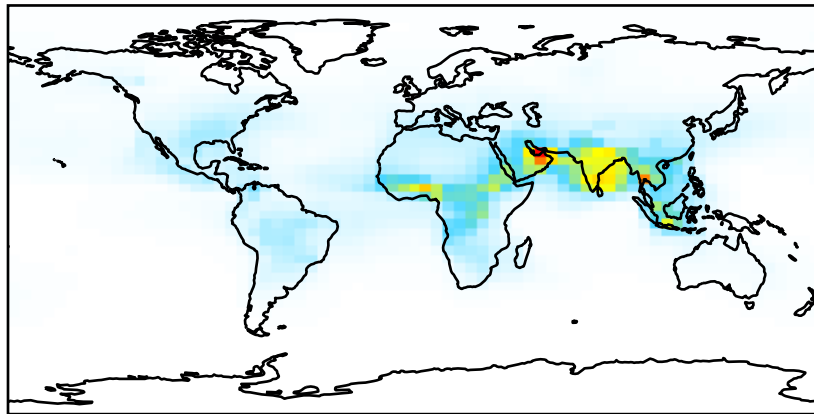


Ratio
Dev/Ref, Fixed Range

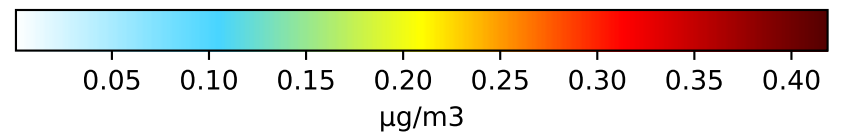
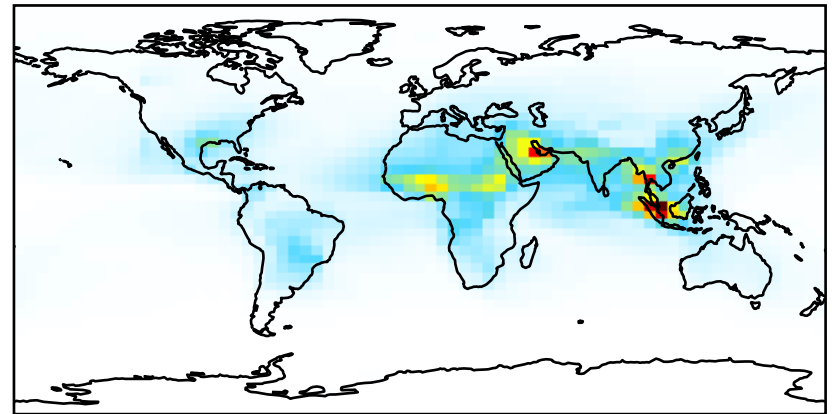


SpeciesConc_ASOAN (Oct2019)

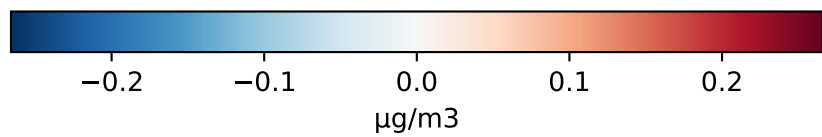
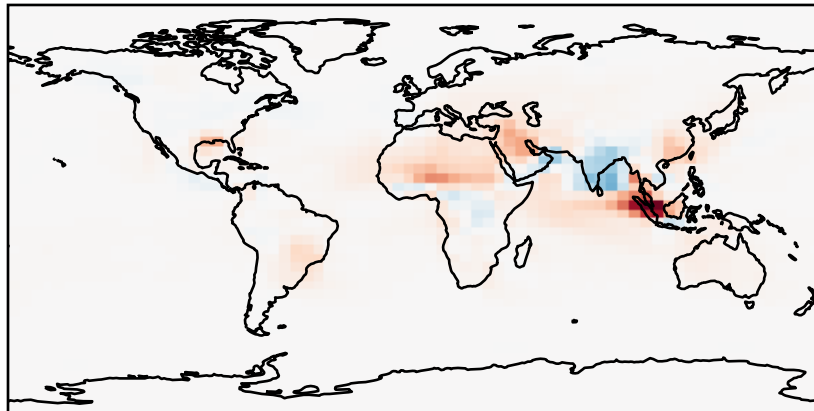
GCC_12.9.0 (Ref)
4.0x5.0



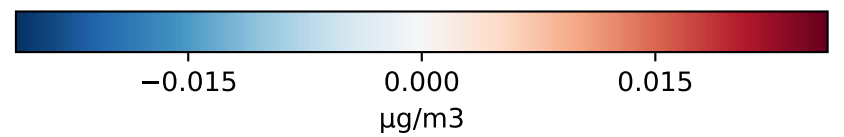
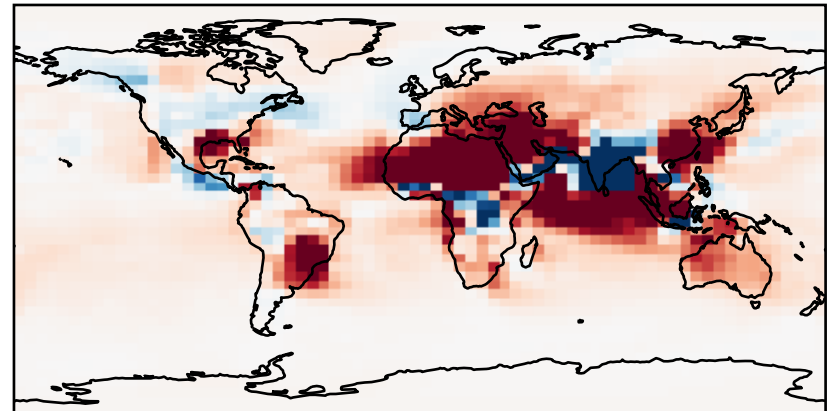
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



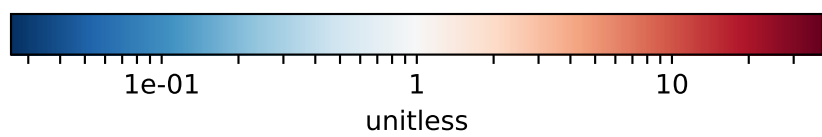
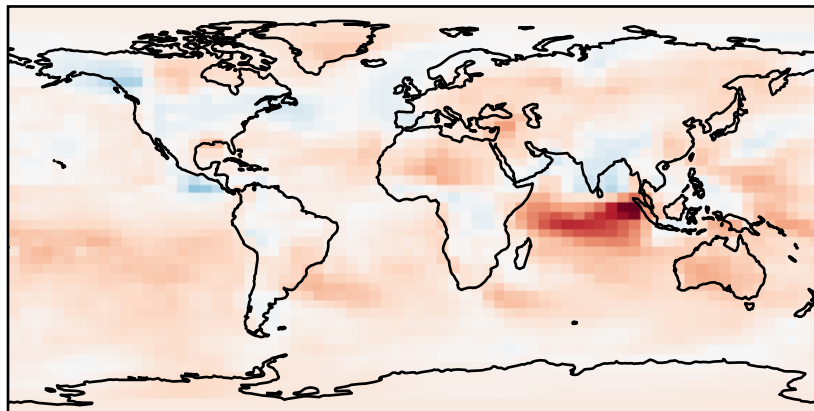
Difference
Dev - Ref, Dynamic Range



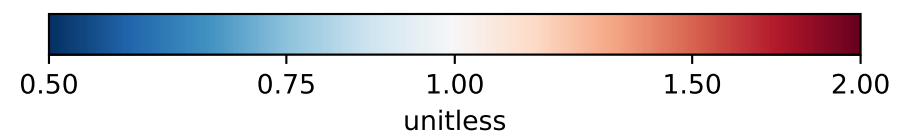
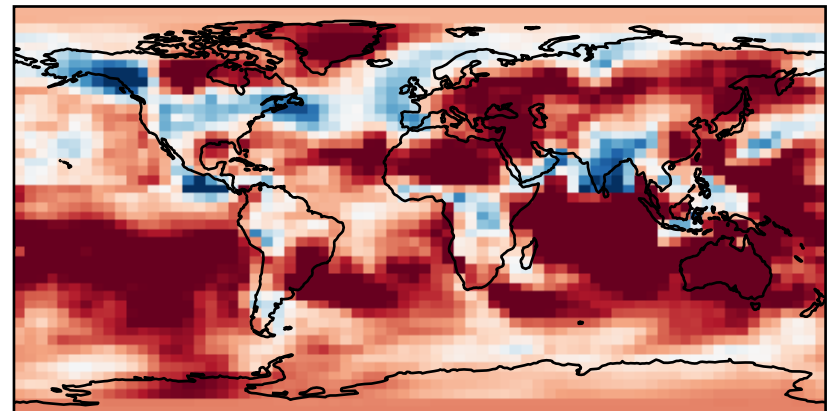
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

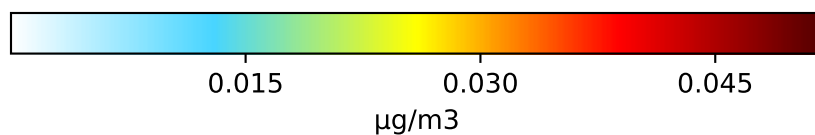
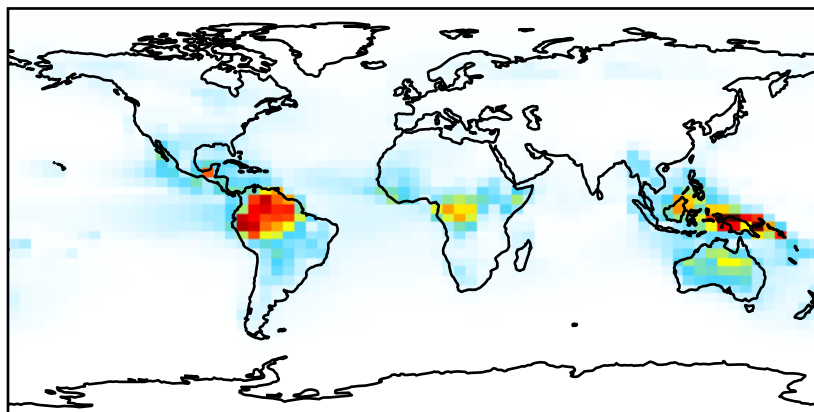


Ratio
Dev/Ref, Fixed Range

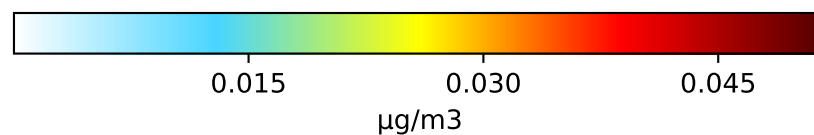
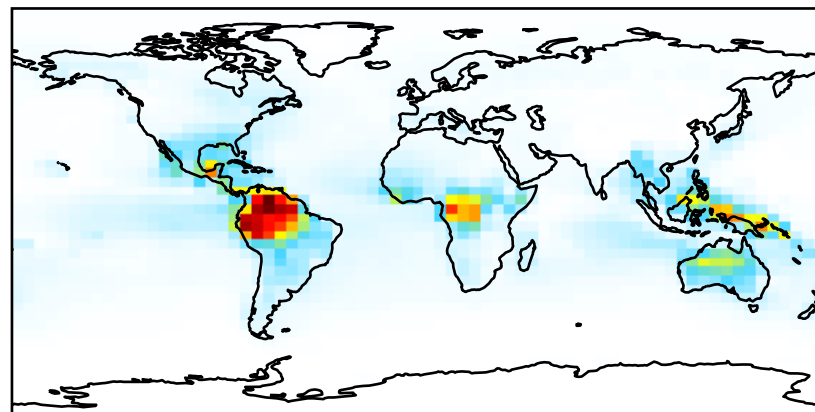


SpeciesConc_TSOG0 (Oct2019)

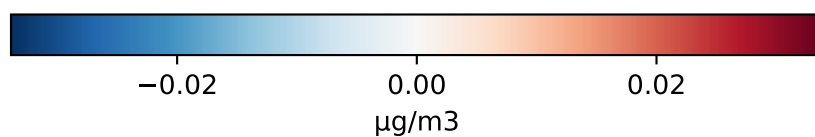
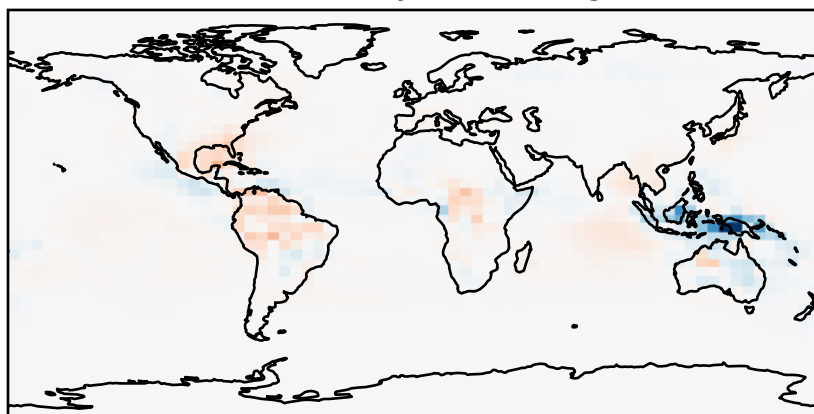
GCC_12.9.0 (Ref)
4.0x5.0



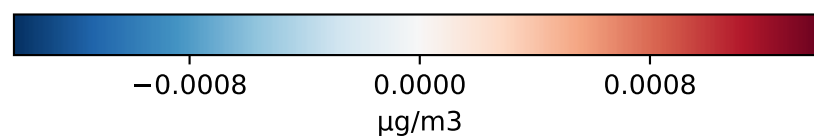
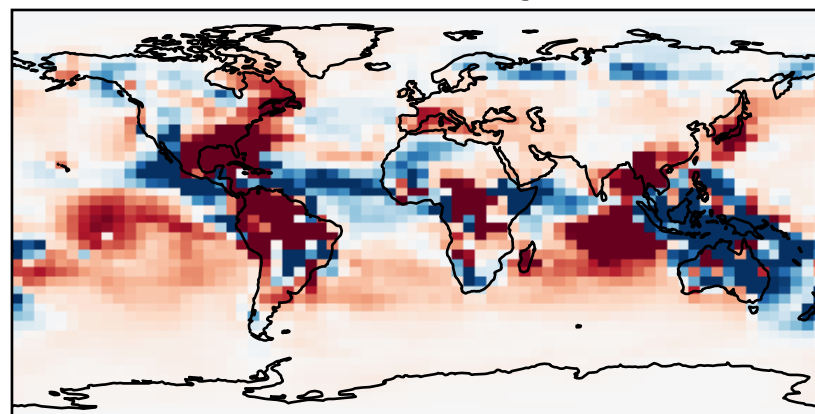
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



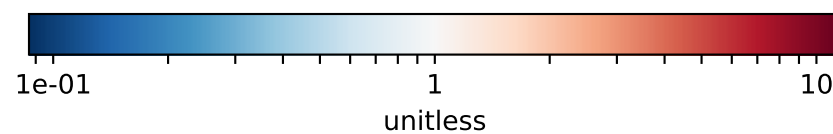
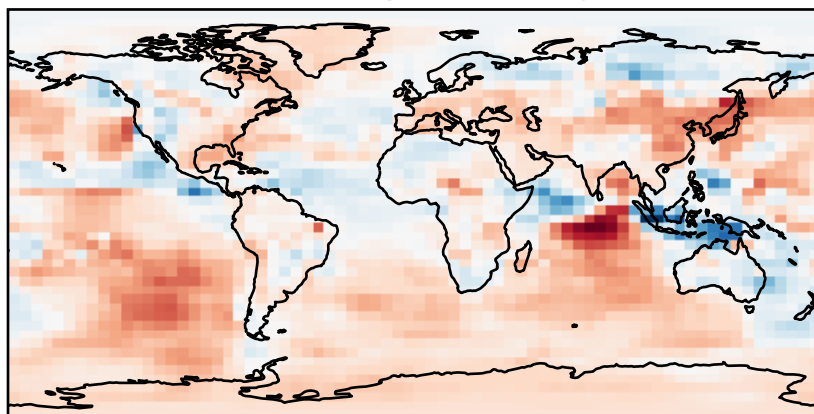
Difference
Dev - Ref, Dynamic Range



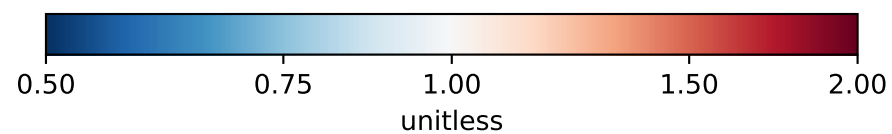
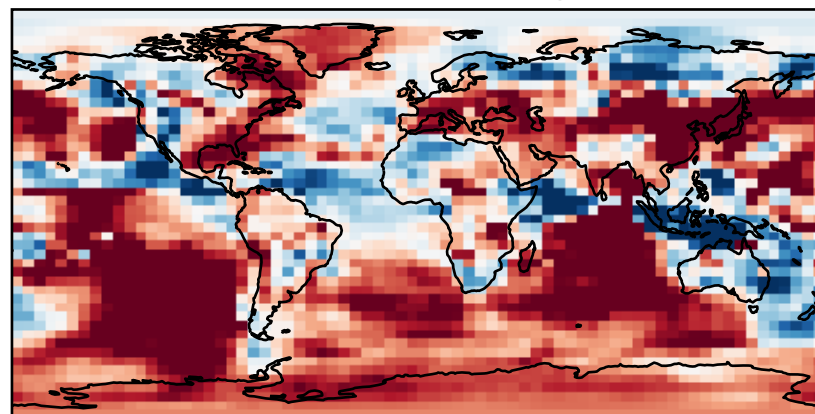
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

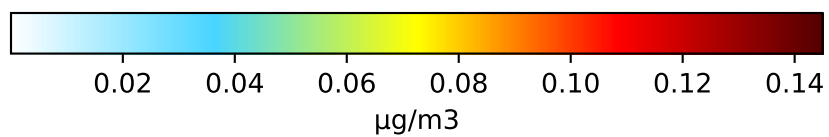
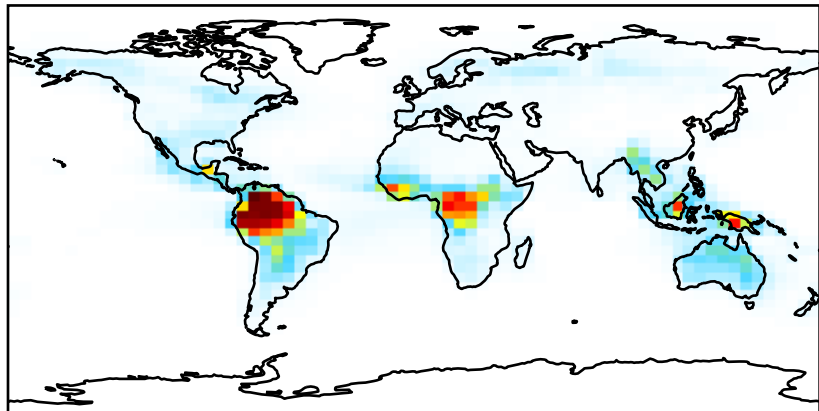


Ratio
Dev/Ref, Fixed Range

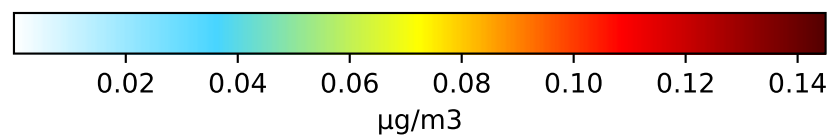
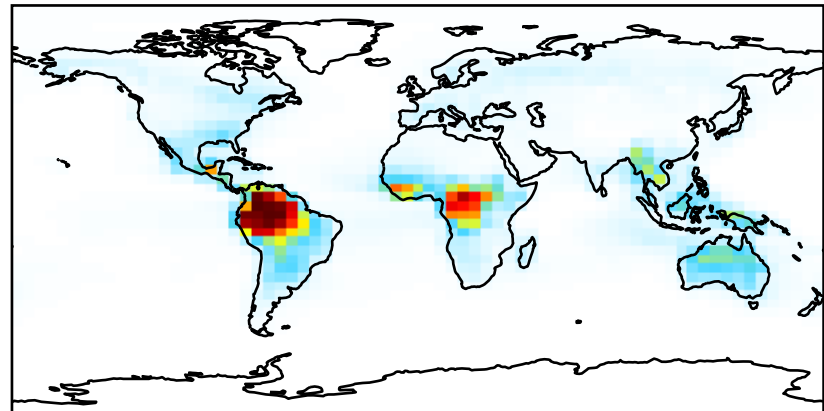


SpeciesConc_TSOG1 (Oct2019)

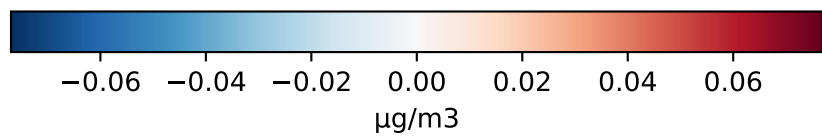
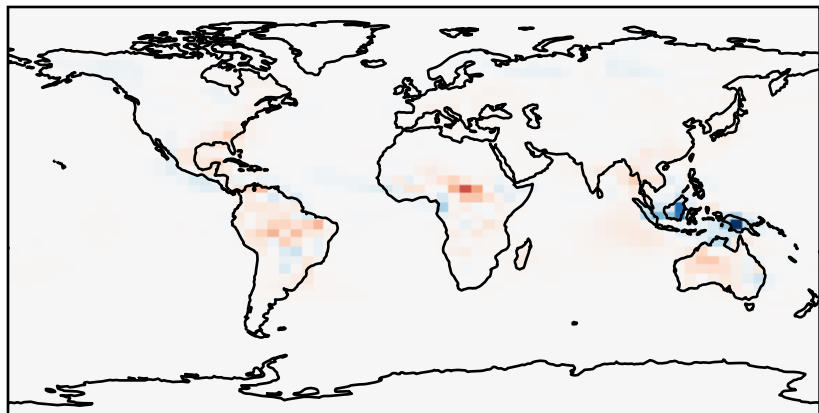
GCC_12.9.0 (Ref)
4.0x5.0



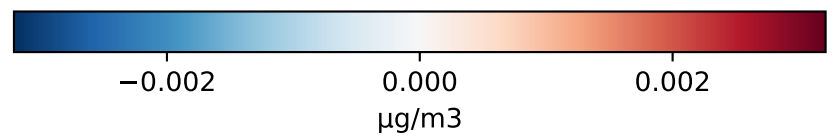
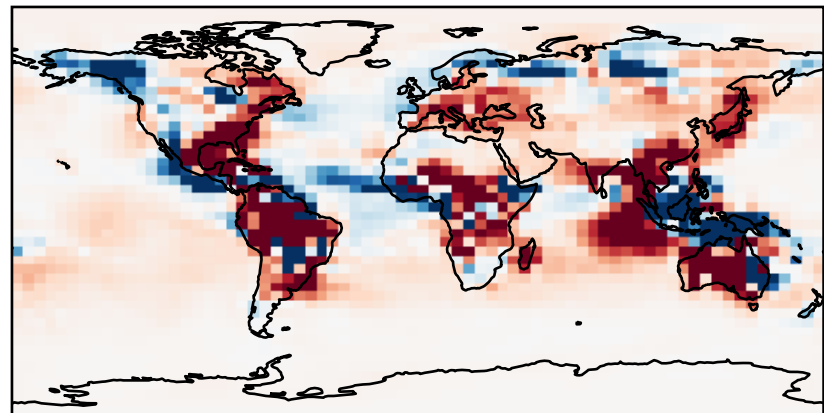
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



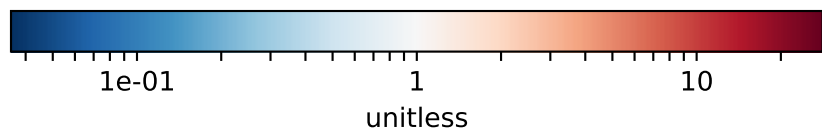
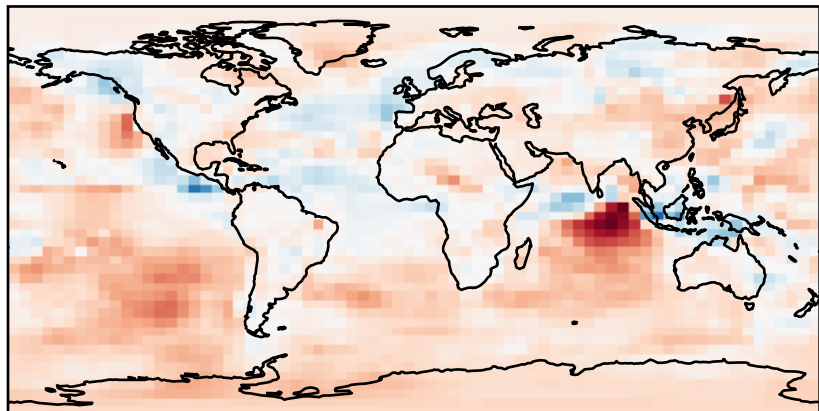
Difference
Dev - Ref, Dynamic Range



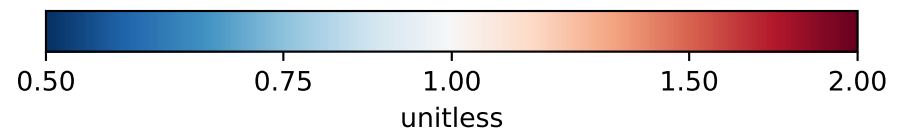
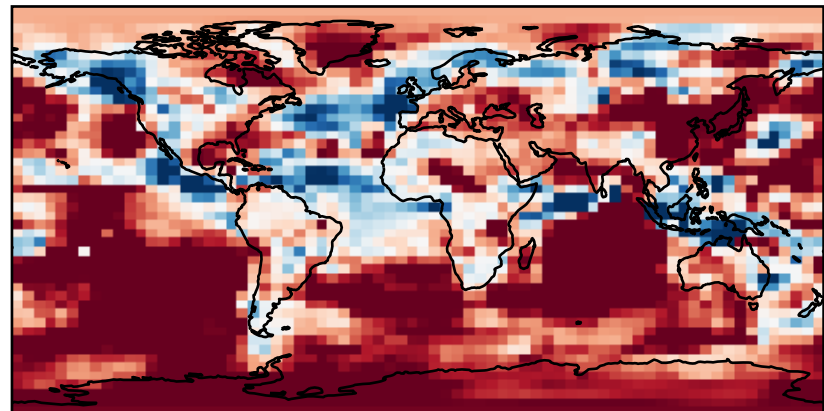
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

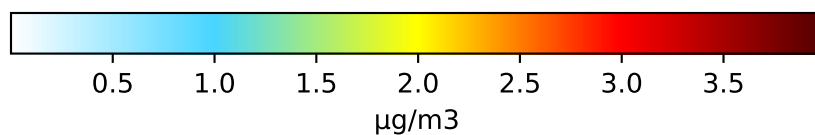
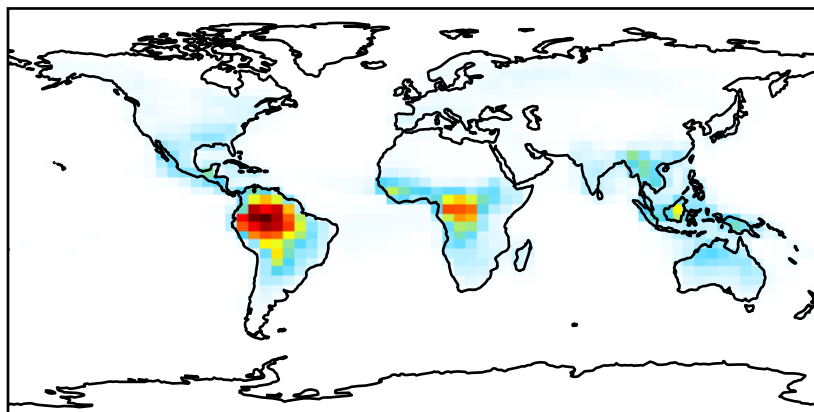


Ratio
Dev/Ref, Fixed Range

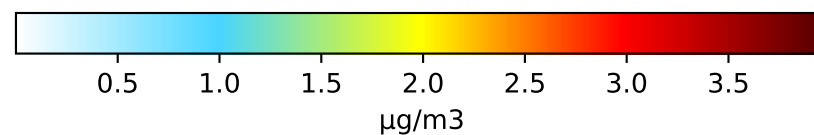
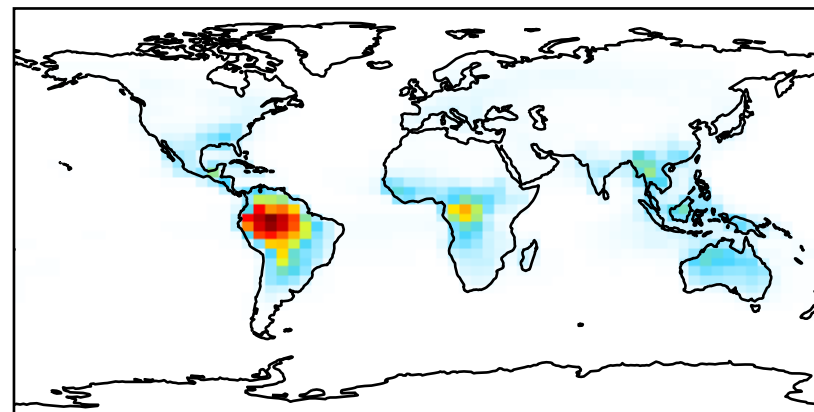


SpeciesConc_TSOG2 (Oct2019)

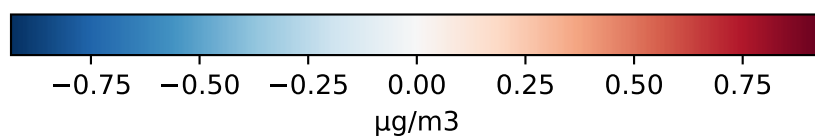
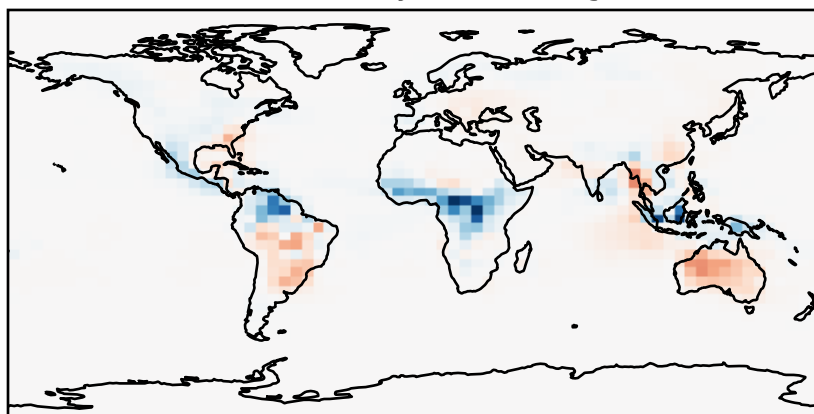
GCC_12.9.0 (Ref)
4.0x5.0



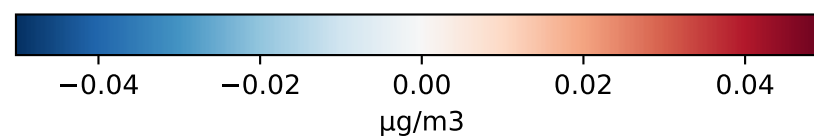
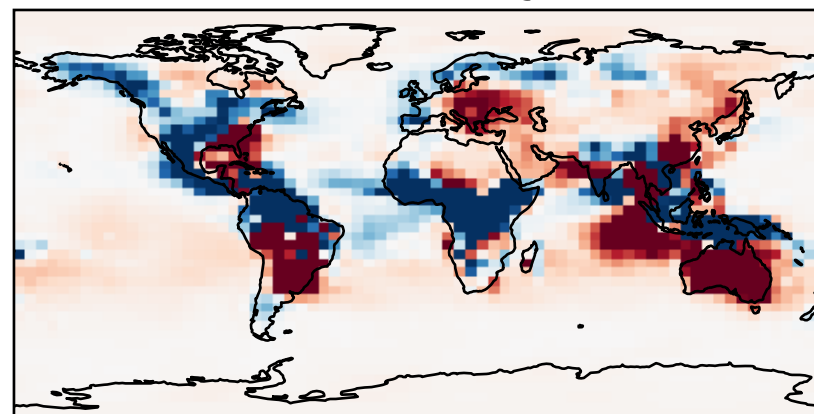
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



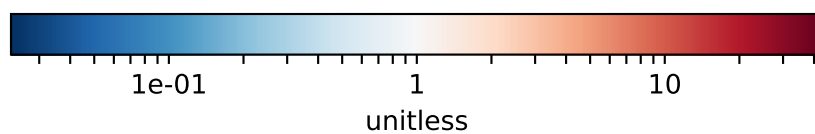
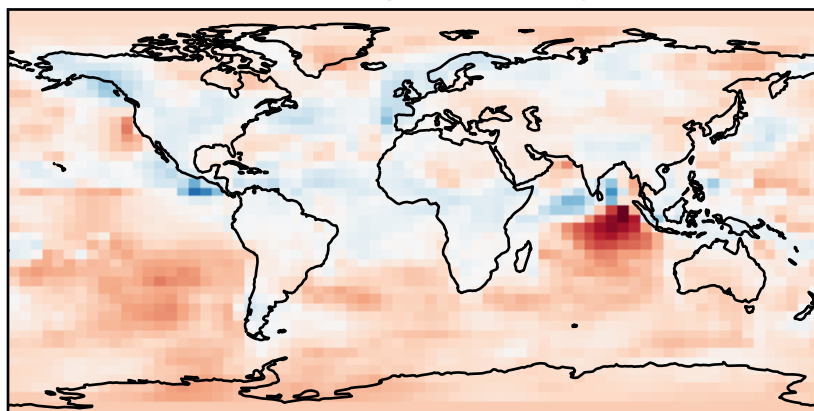
Difference
Dev - Ref, Dynamic Range



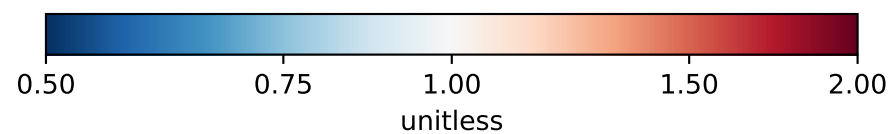
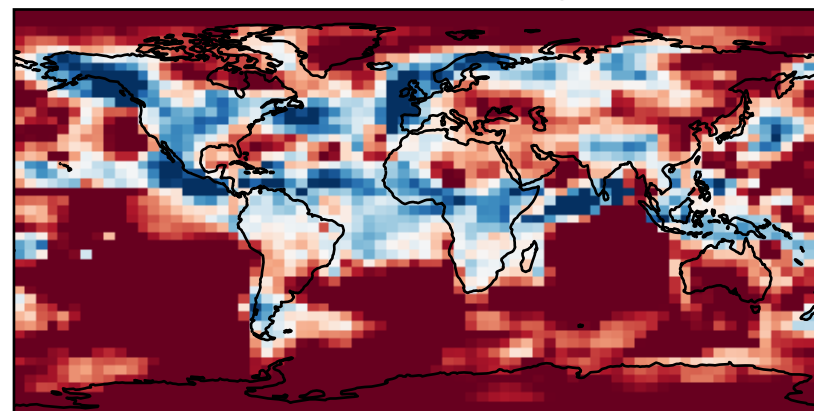
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

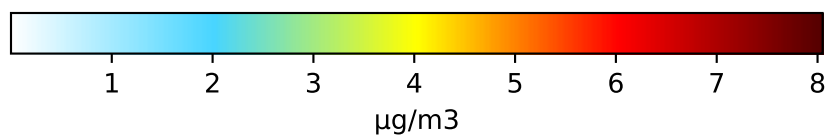
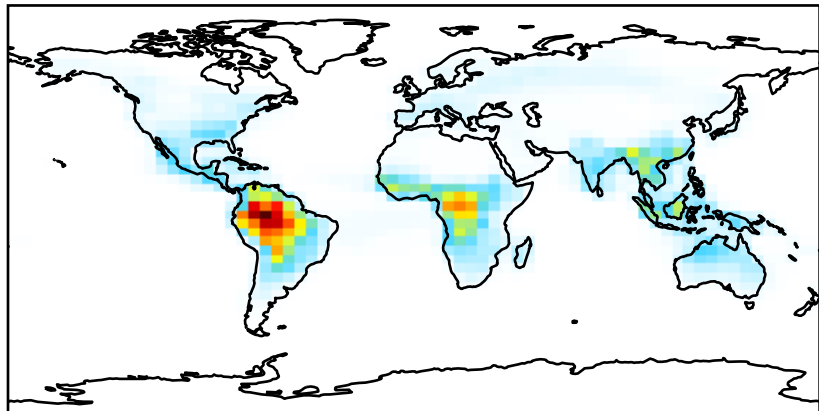


Ratio
Dev/Ref, Fixed Range

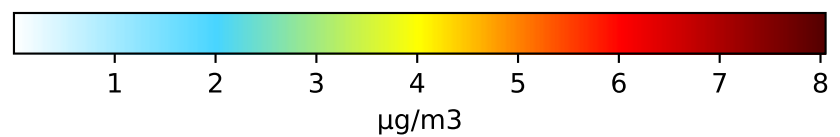
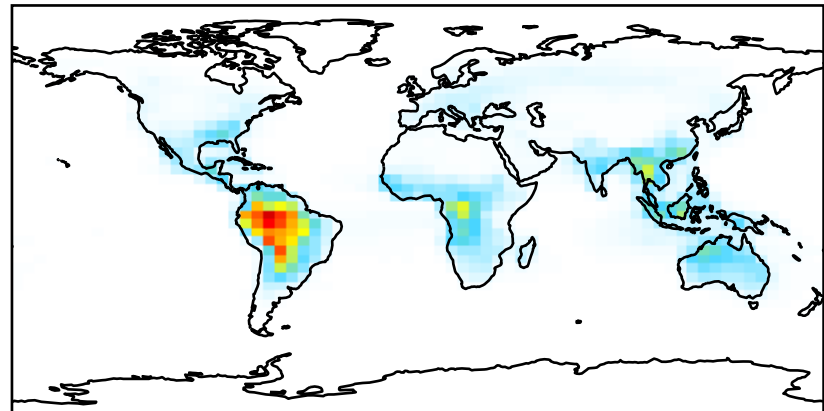


SpeciesConc_TSOG3 (Oct2019)

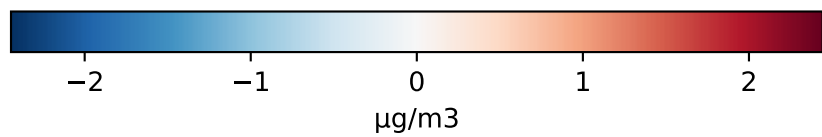
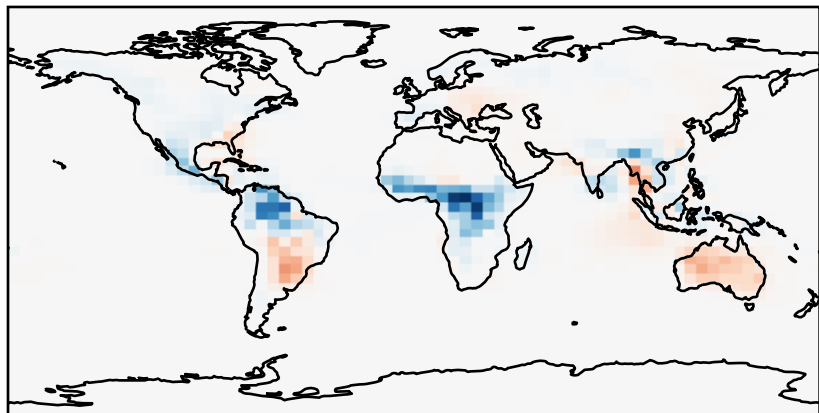
GCC_12.9.0 (Ref)
4.0x5.0



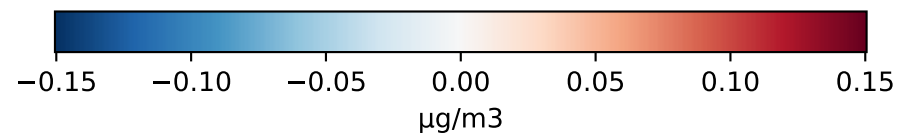
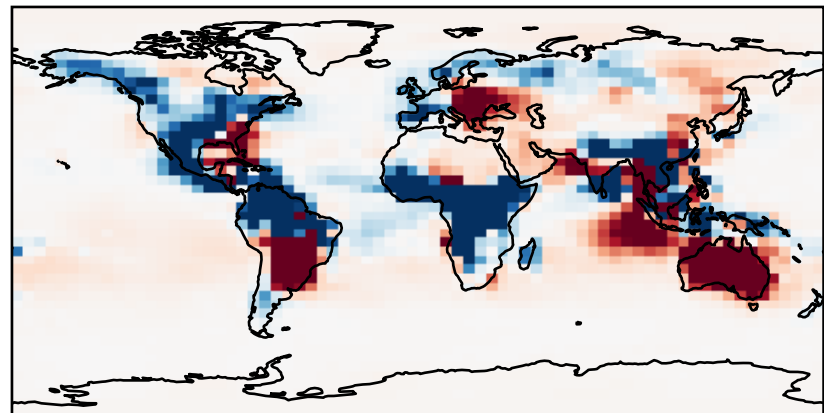
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



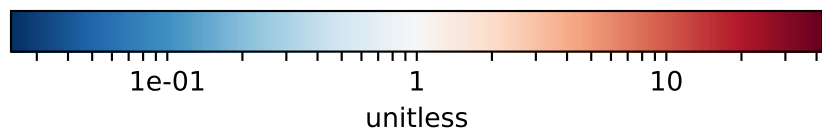
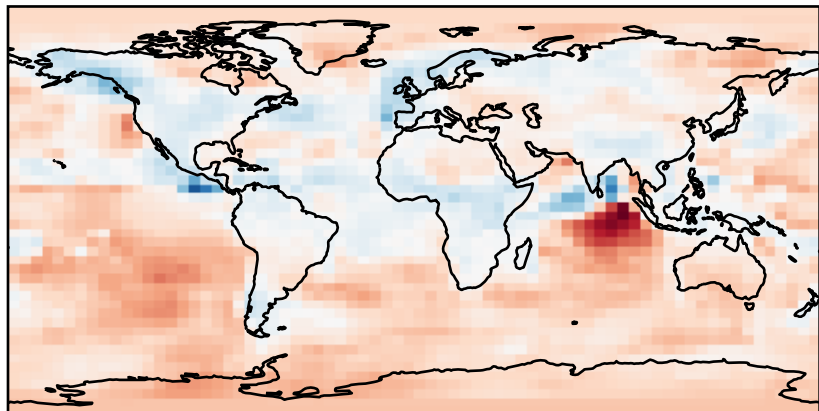
Difference
Dev - Ref, Dynamic Range



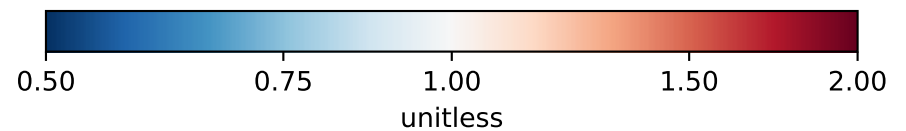
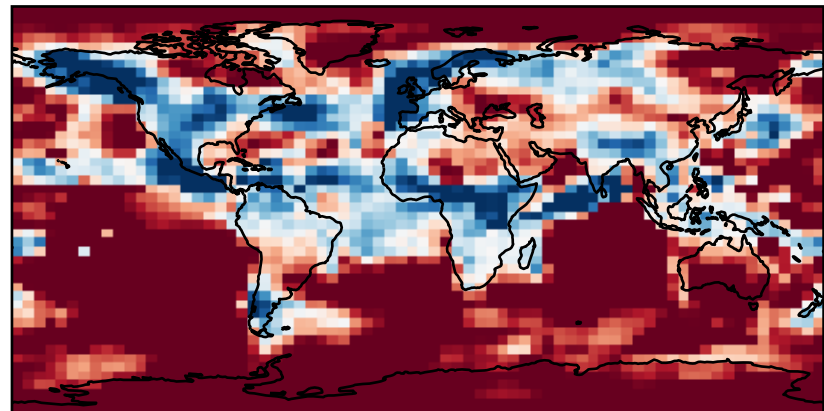
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

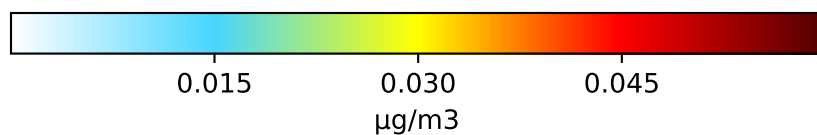
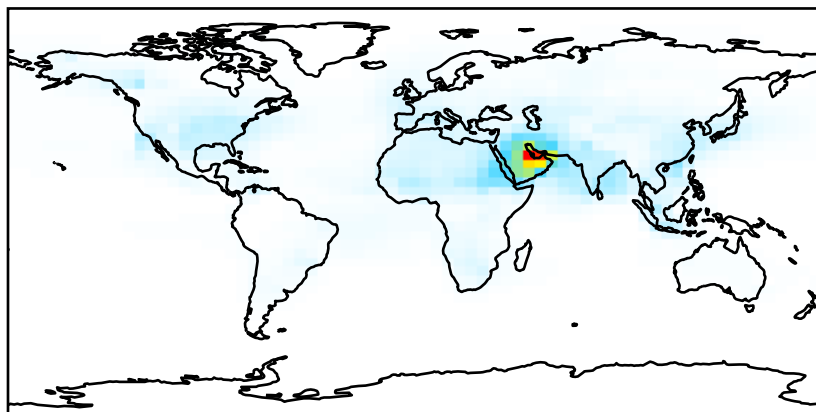


Ratio
Dev/Ref, Fixed Range

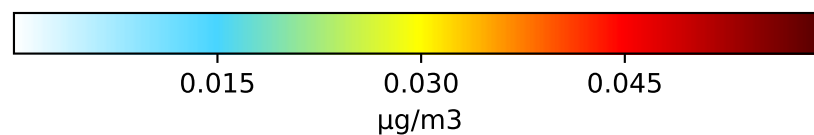
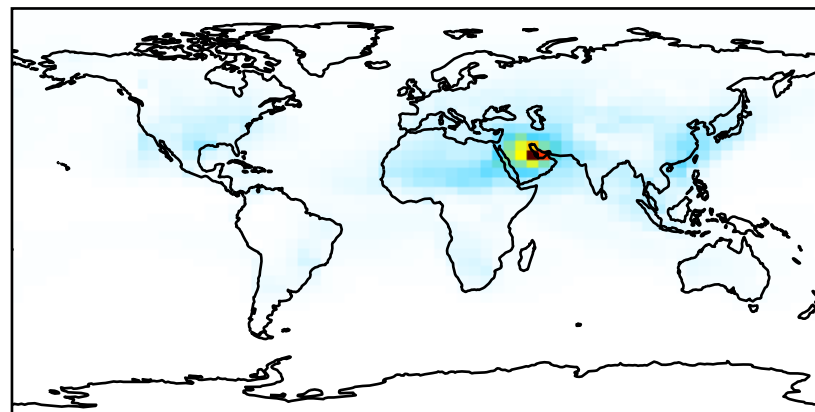


SpeciesConc_ASOG1 (Oct2019)

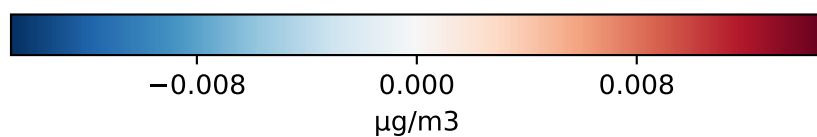
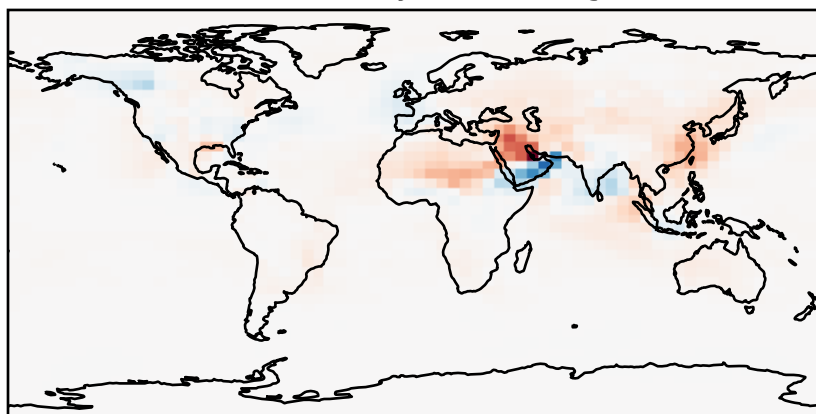
GCC_12.9.0 (Ref)
4.0x5.0



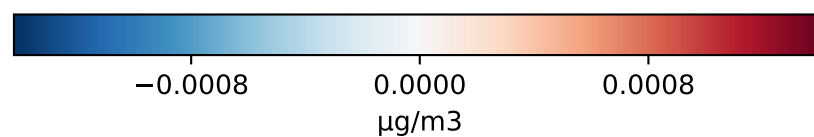
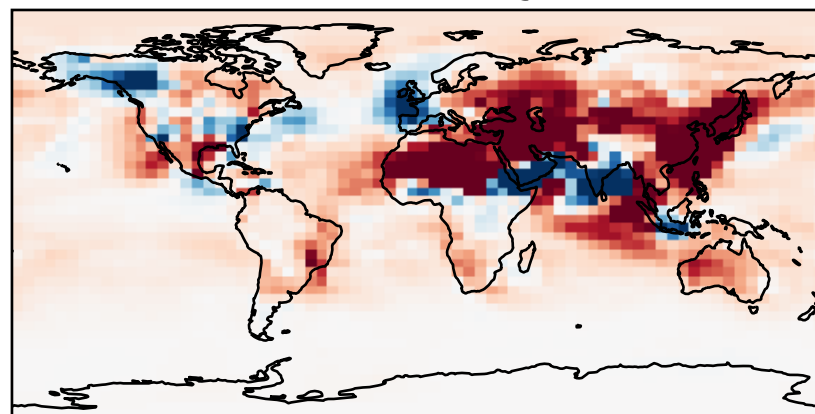
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



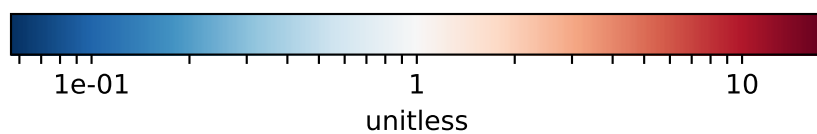
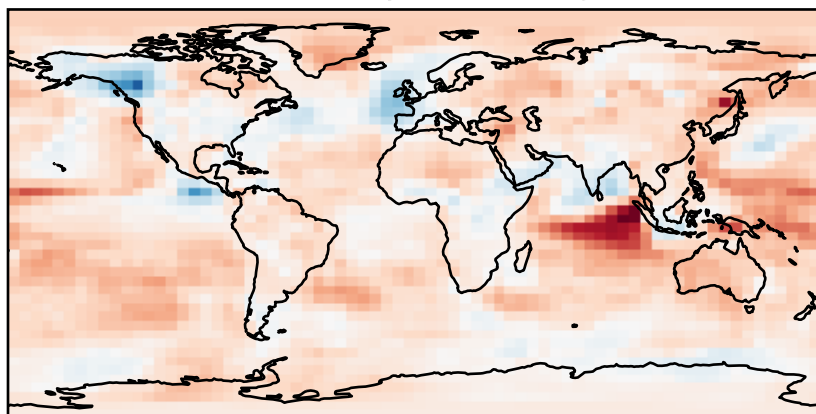
Difference
Dev - Ref, Dynamic Range



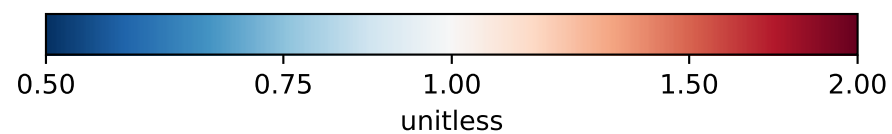
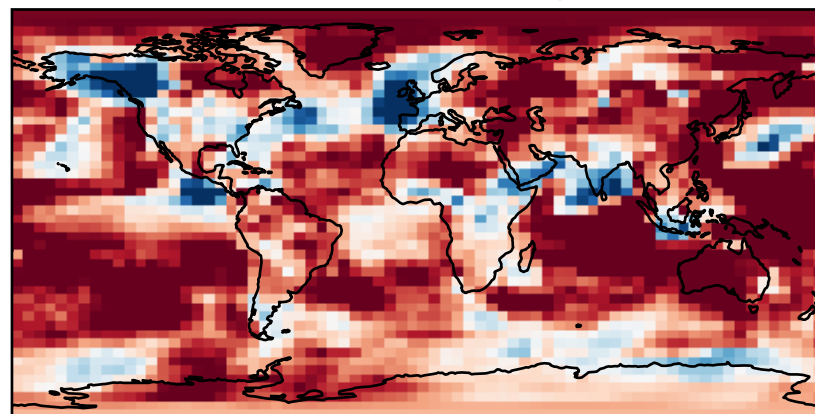
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

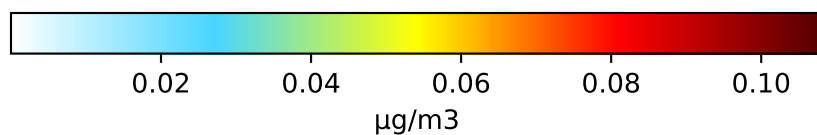
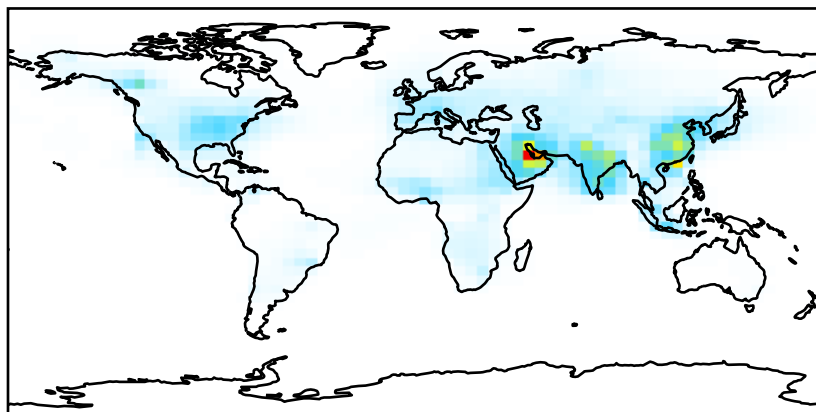


Ratio
Dev/Ref, Fixed Range

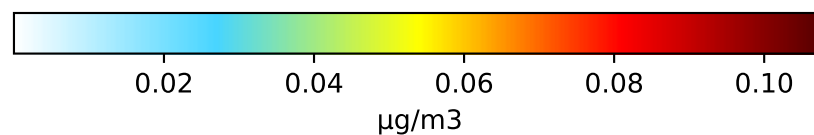
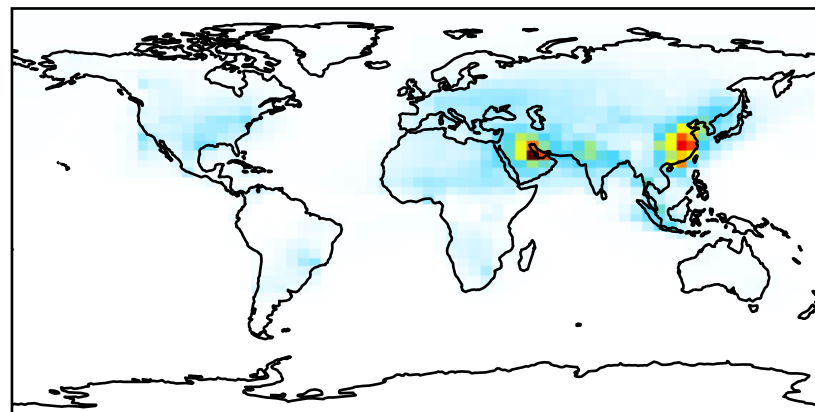


SpeciesConc_ASOG2 (Oct2019)

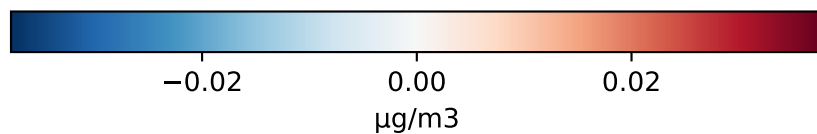
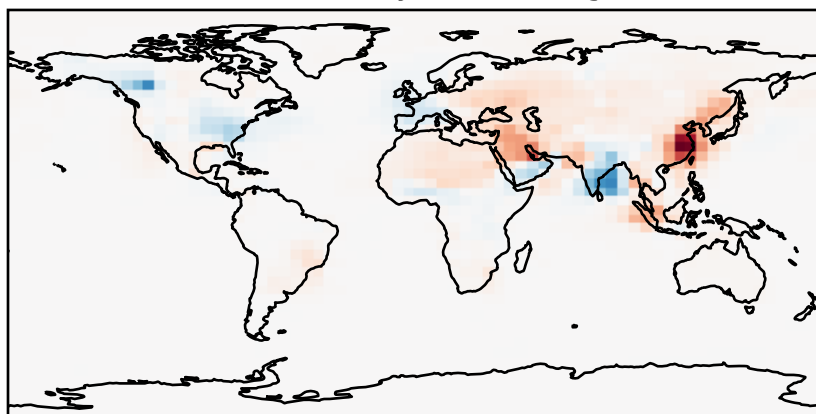
GCC_12.9.0 (Ref)
4.0x5.0



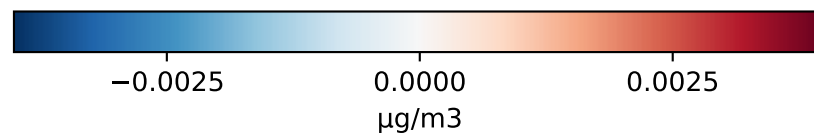
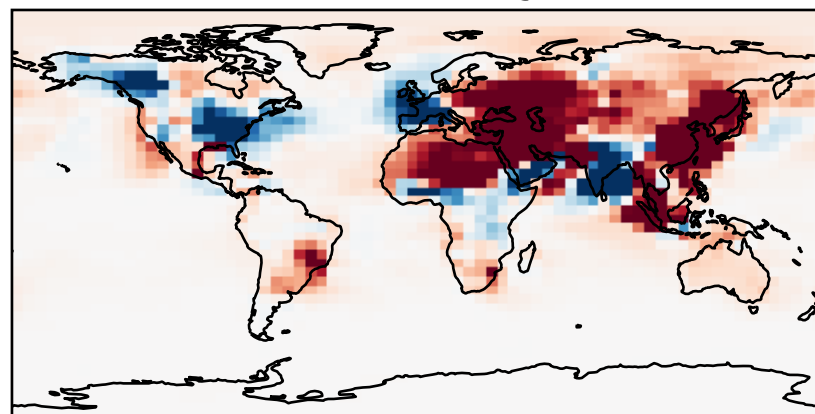
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



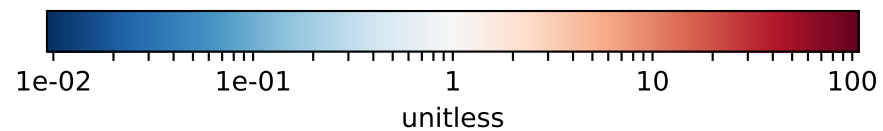
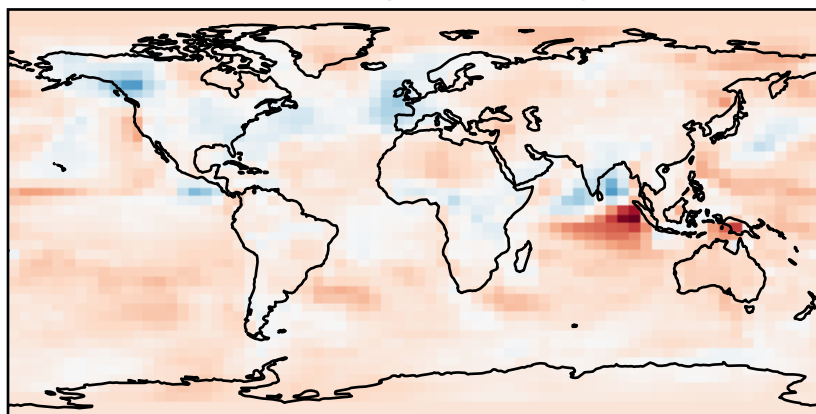
Difference
Dev - Ref, Dynamic Range



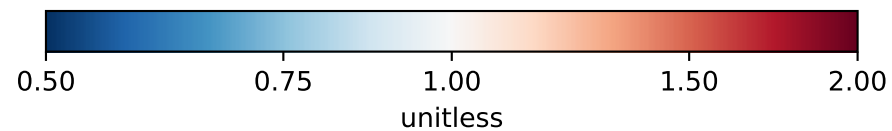
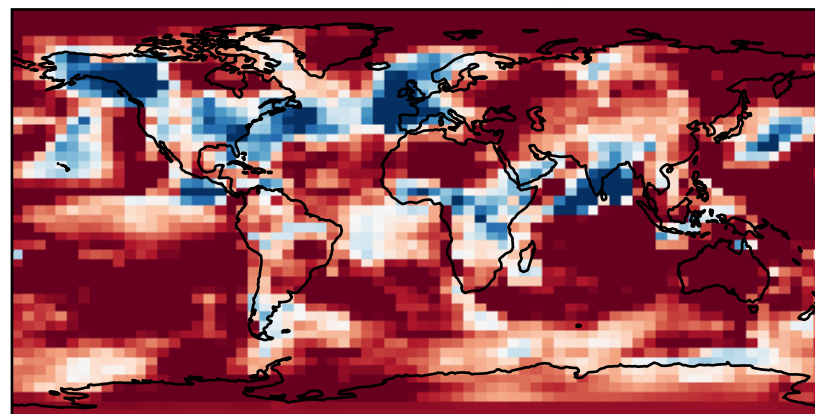
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

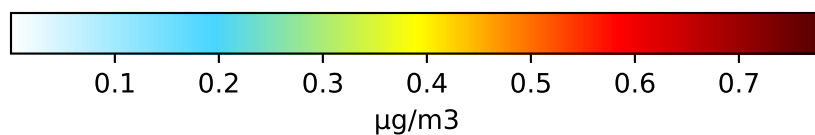
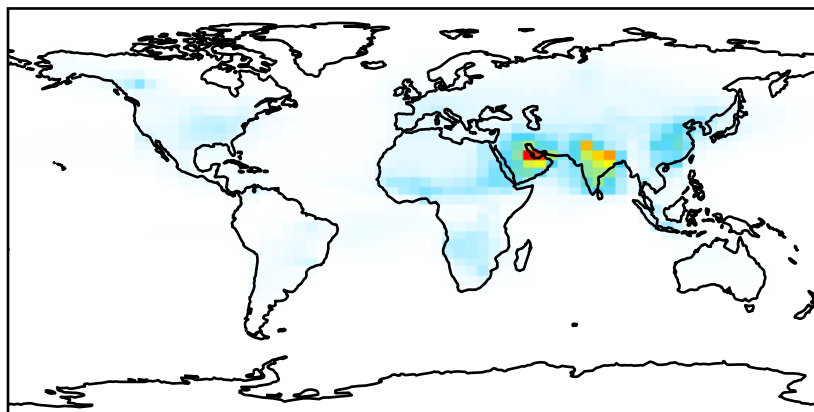


Ratio
Dev/Ref, Fixed Range

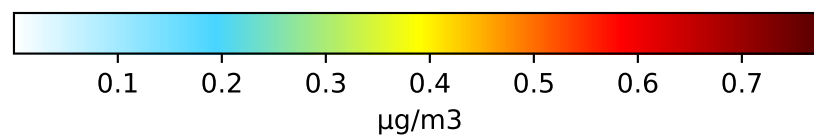
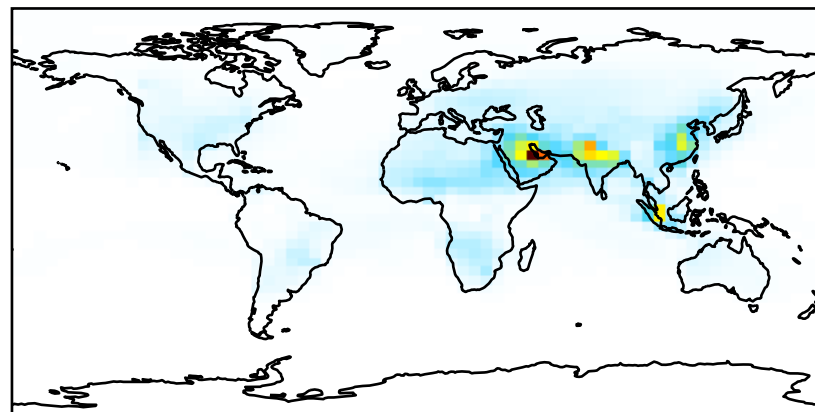


SpeciesConc_ASOG3 (Oct2019)

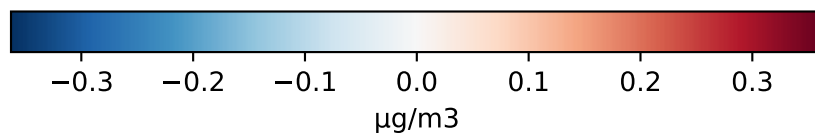
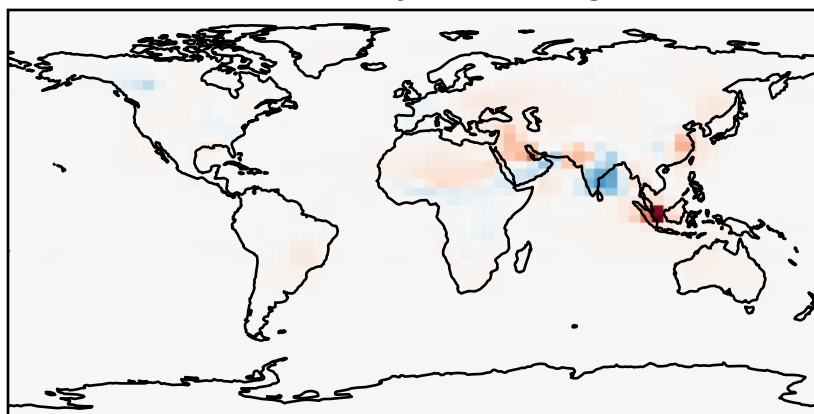
GCC_12.9.0 (Ref)
4.0x5.0



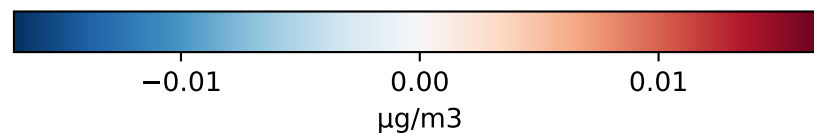
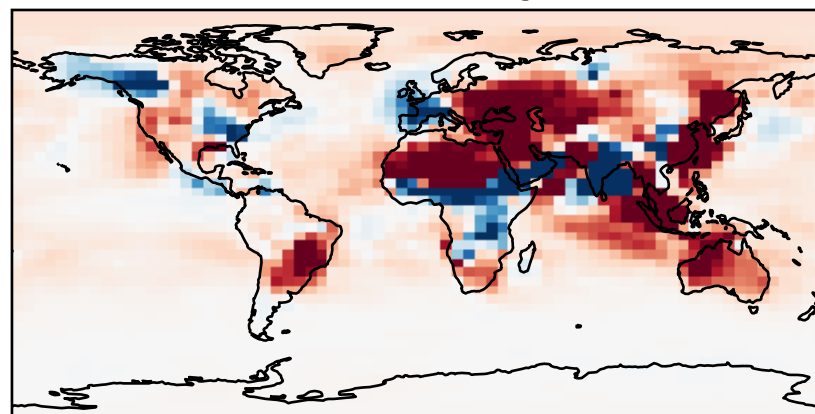
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



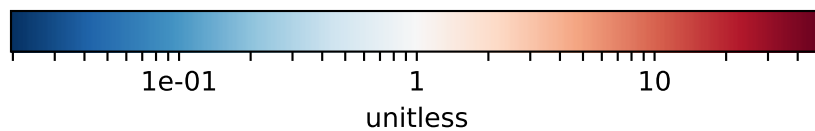
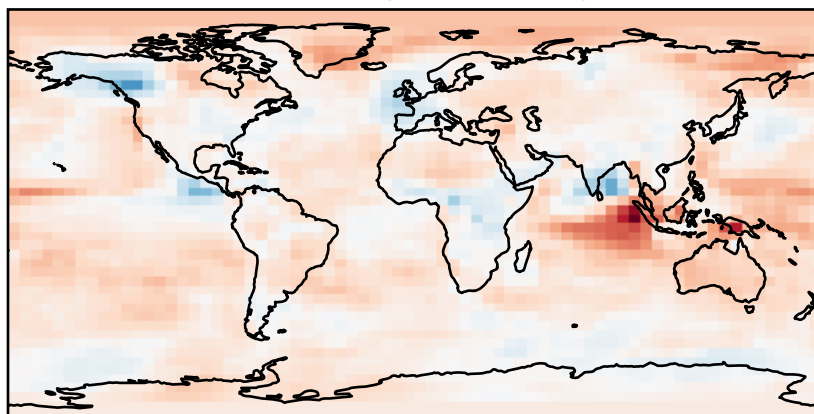
Difference
Dev - Ref, Dynamic Range



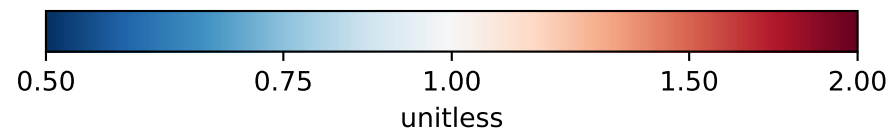
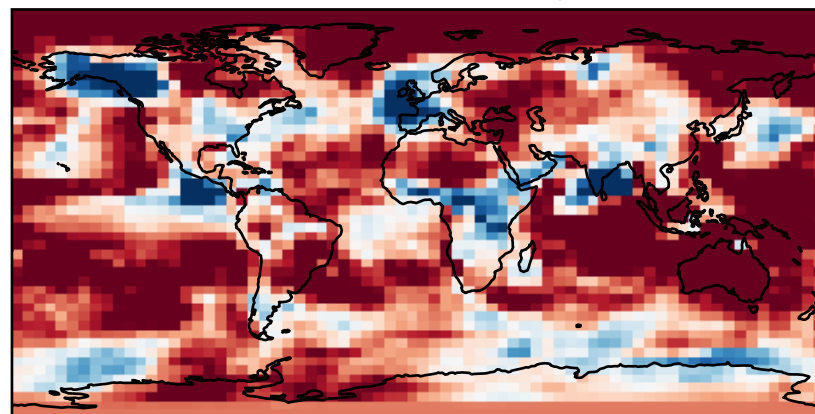
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

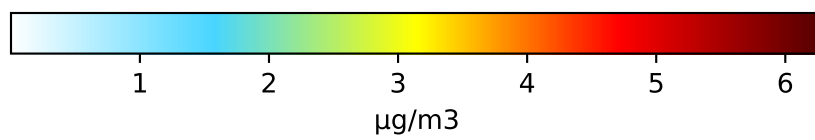
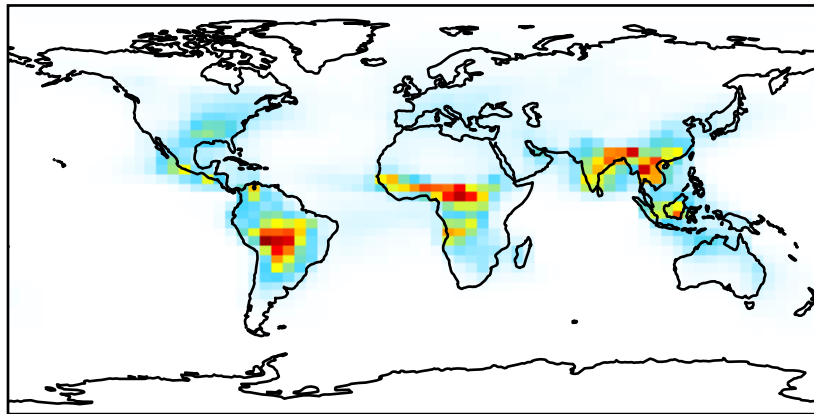


Ratio
Dev/Ref, Fixed Range

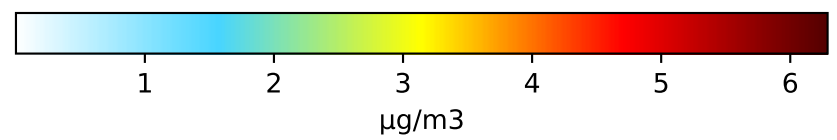
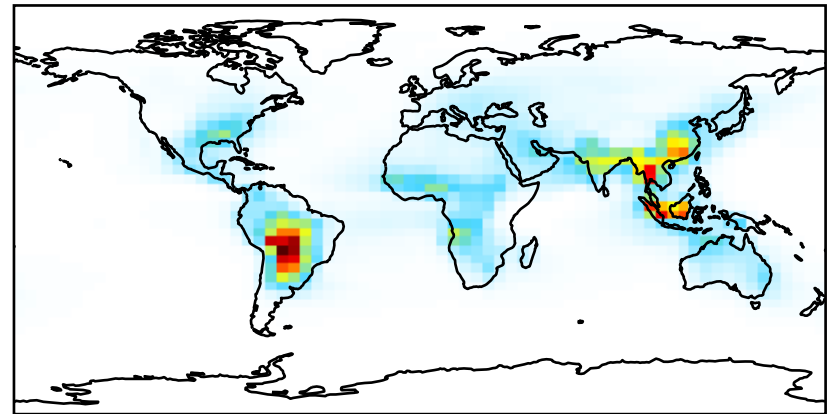


SpeciesConc_INDIOOL (Oct2019)

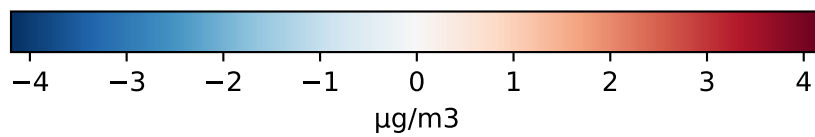
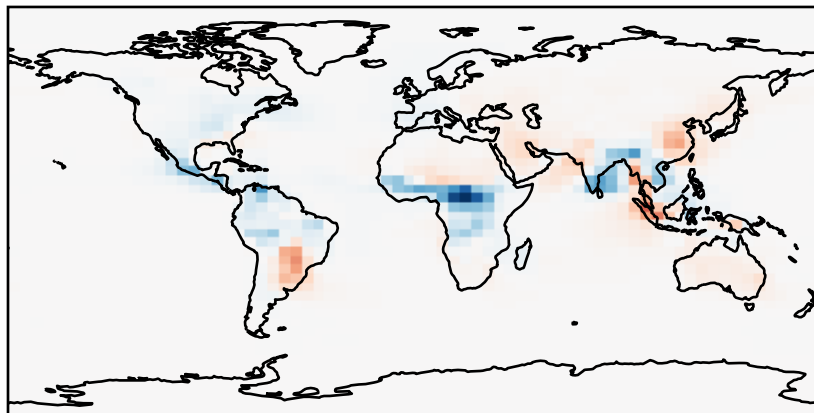
GCC_12.9.0 (Ref)
4.0x5.0



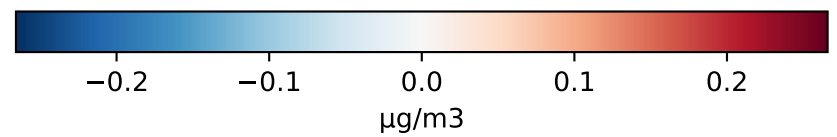
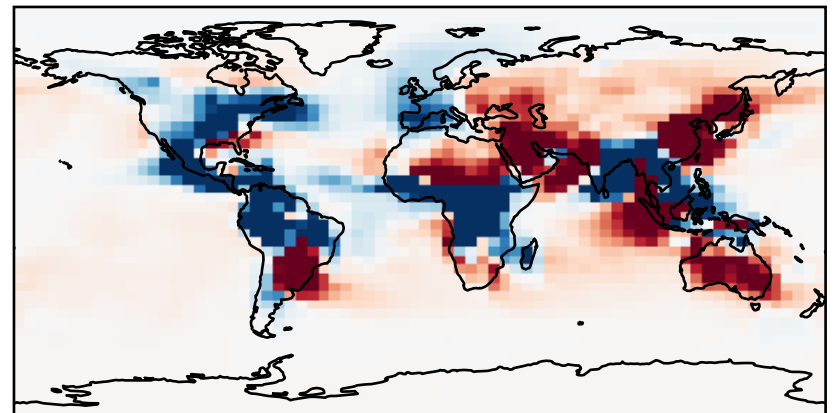
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



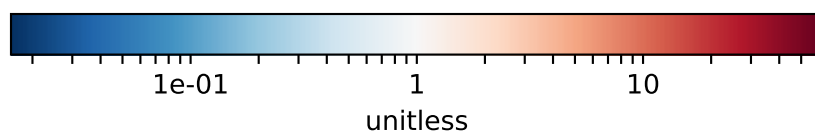
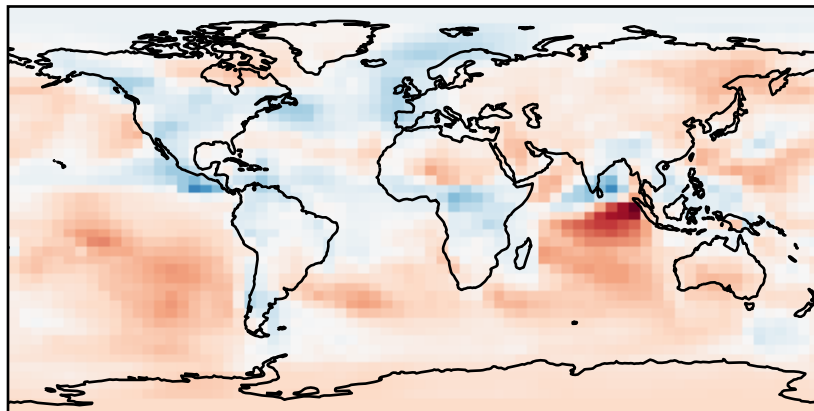
Difference
Dev - Ref, Dynamic Range



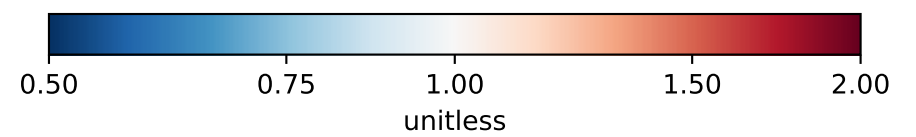
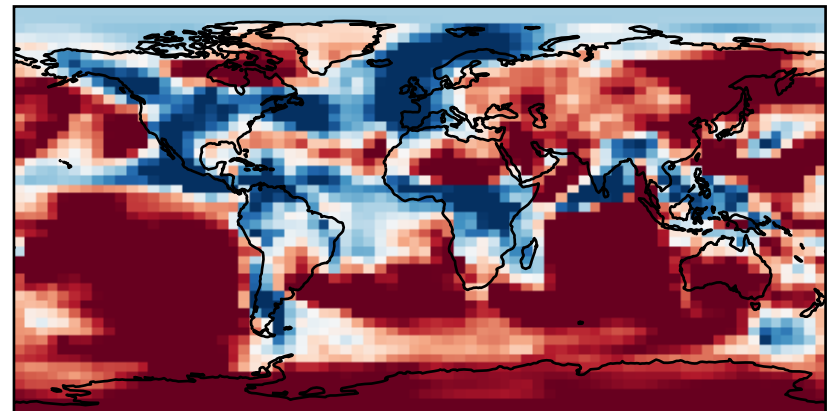
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

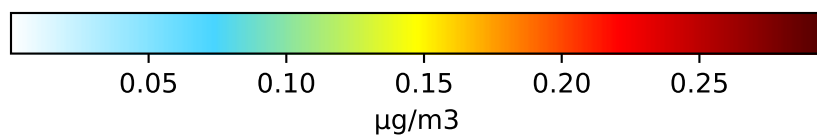
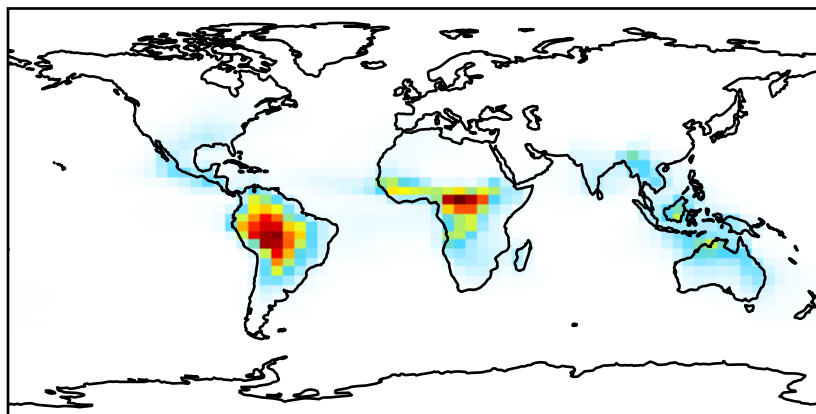


Ratio
Dev/Ref, Fixed Range

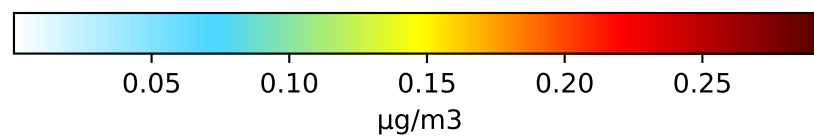
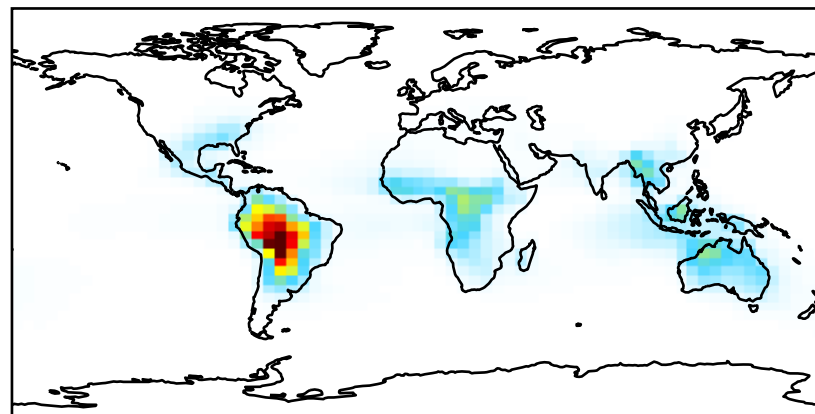


SpeciesConc_LVOCOA (Oct2019)

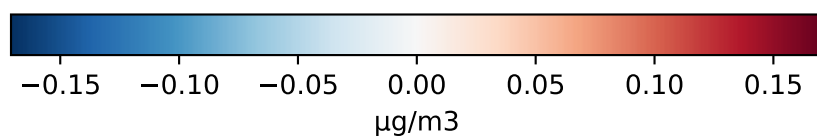
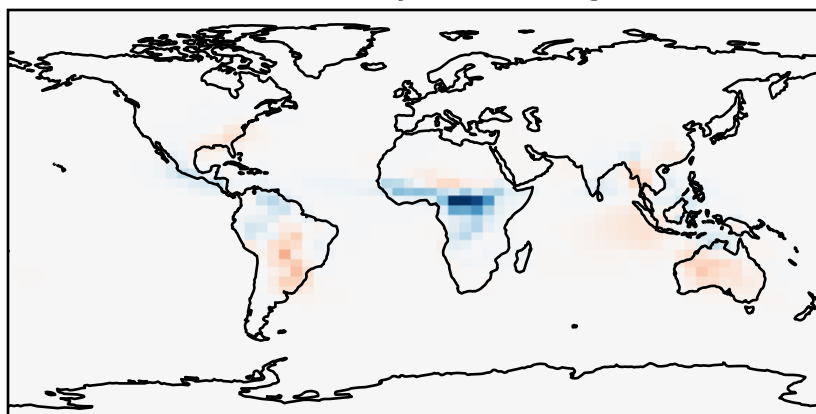
GCC_12.9.0 (Ref)
4.0x5.0



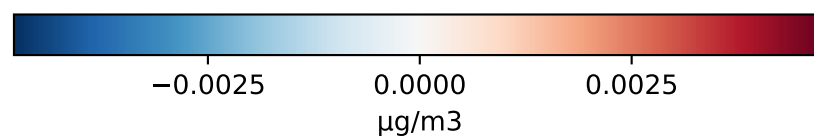
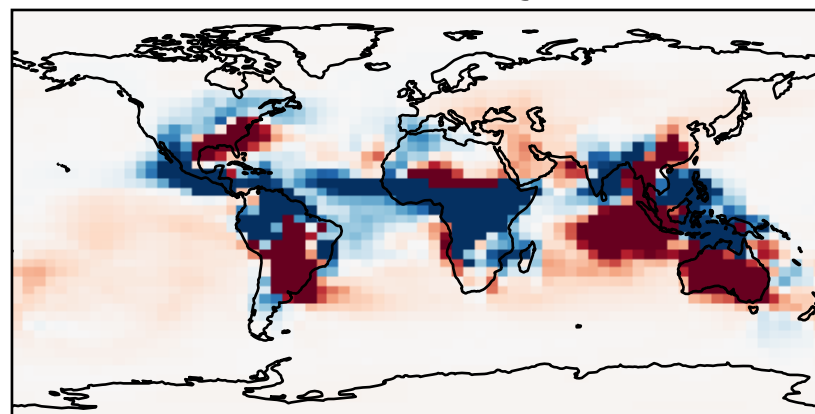
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



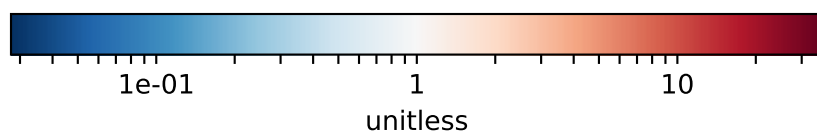
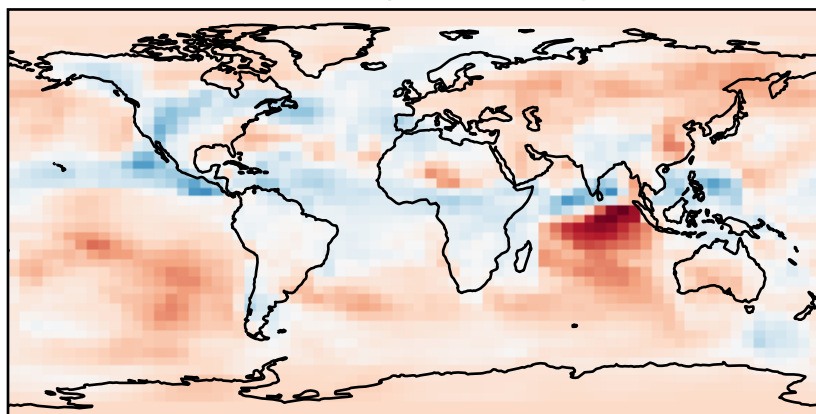
Difference
Dev - Ref, Dynamic Range



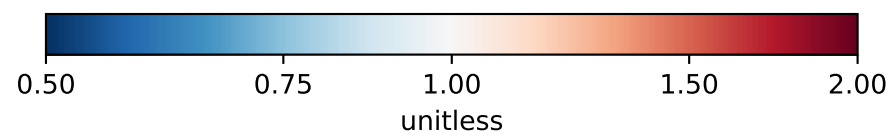
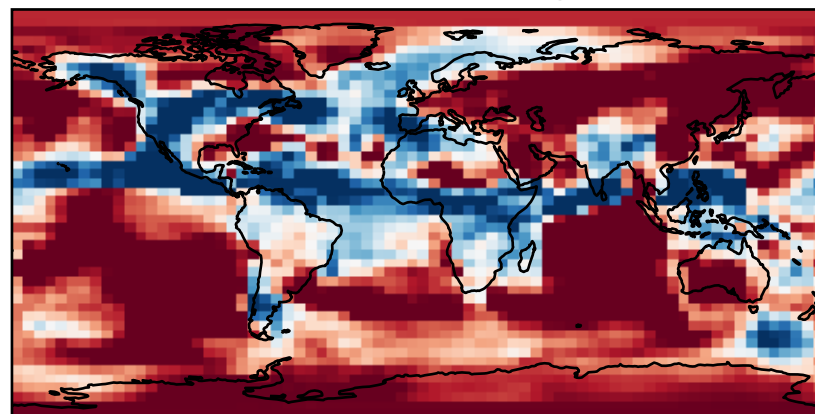
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

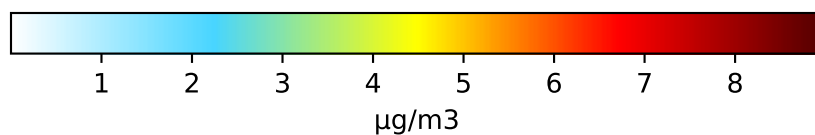
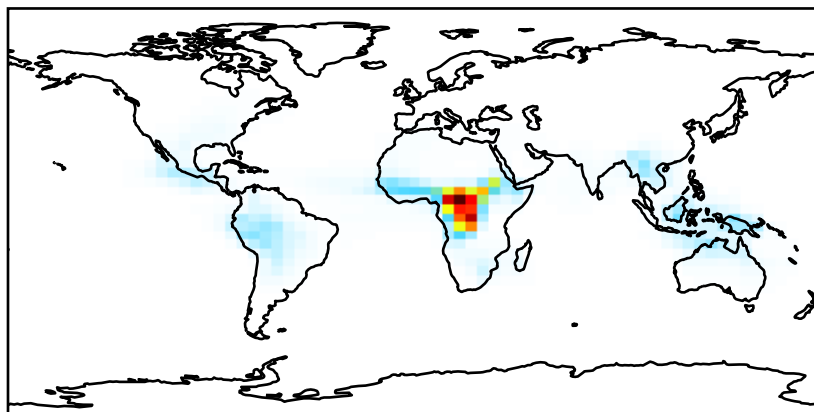


Ratio
Dev/Ref, Fixed Range

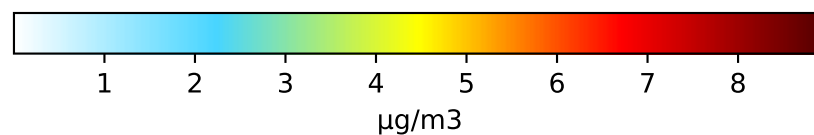
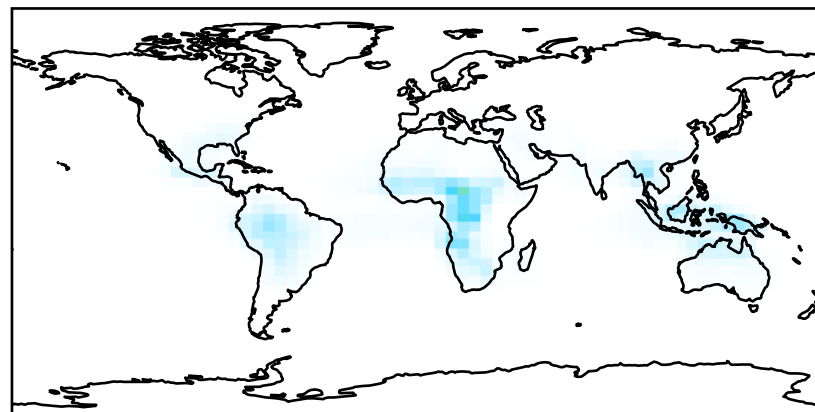


SpeciesConc_SOAIE (Oct2019)

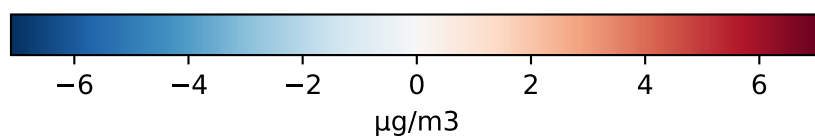
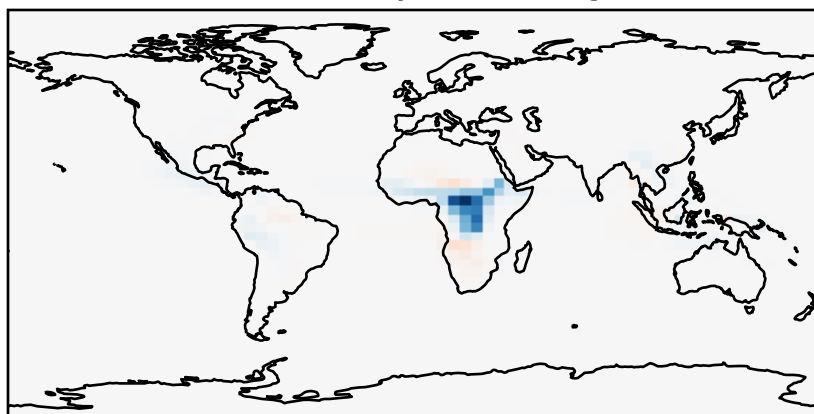
GCC_12.9.0 (Ref)
4.0x5.0



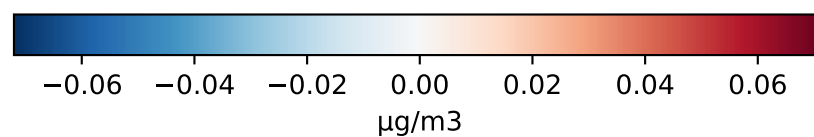
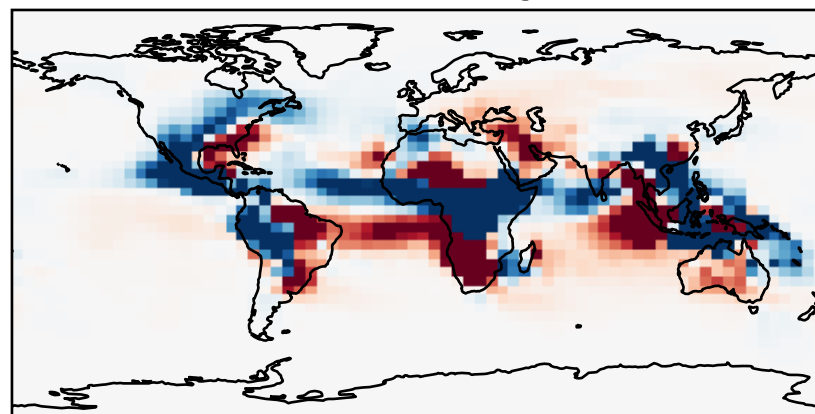
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



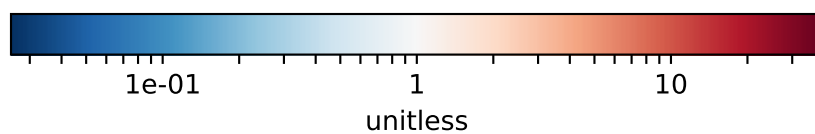
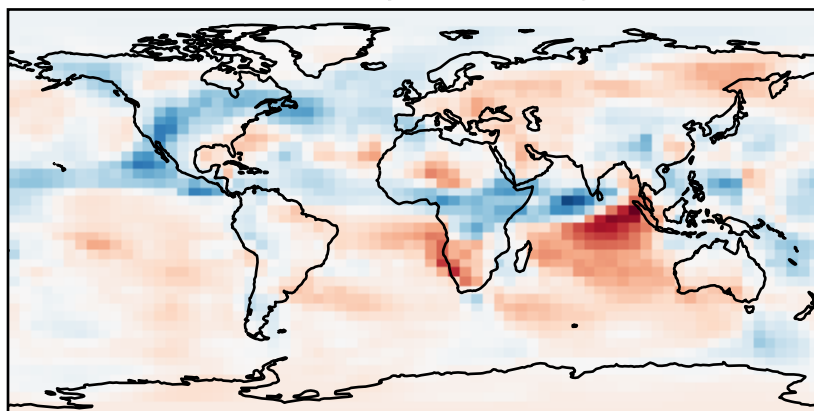
Difference
Dev - Ref, Dynamic Range



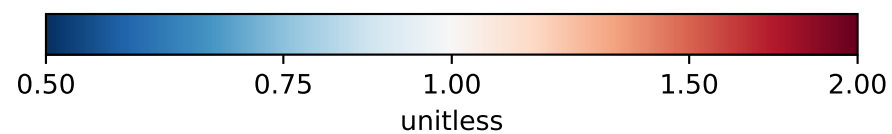
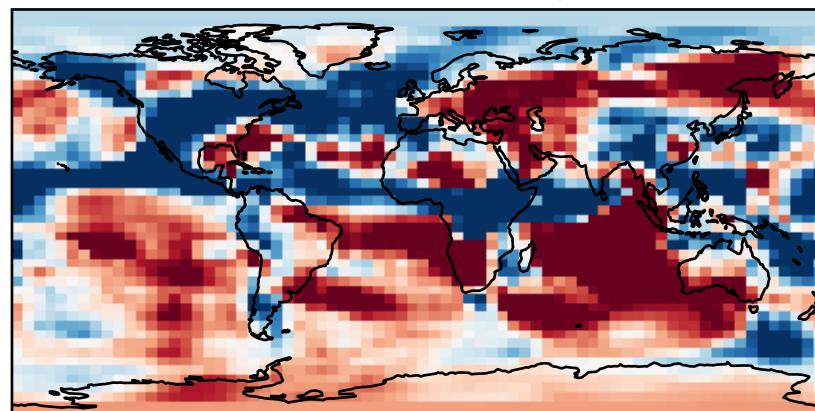
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

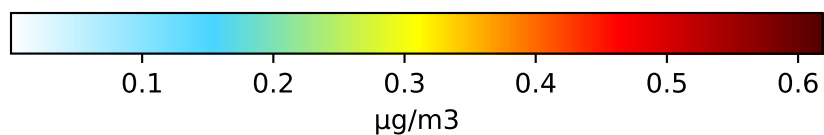
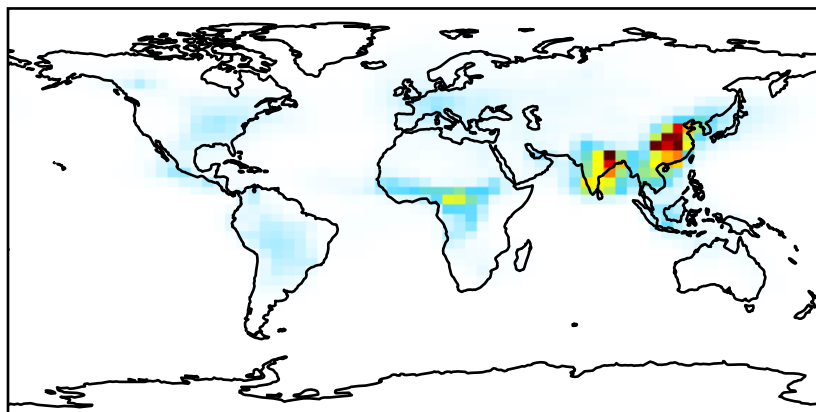


Ratio
Dev/Ref, Fixed Range

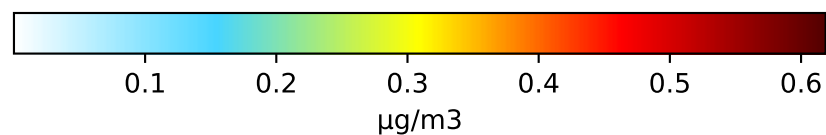
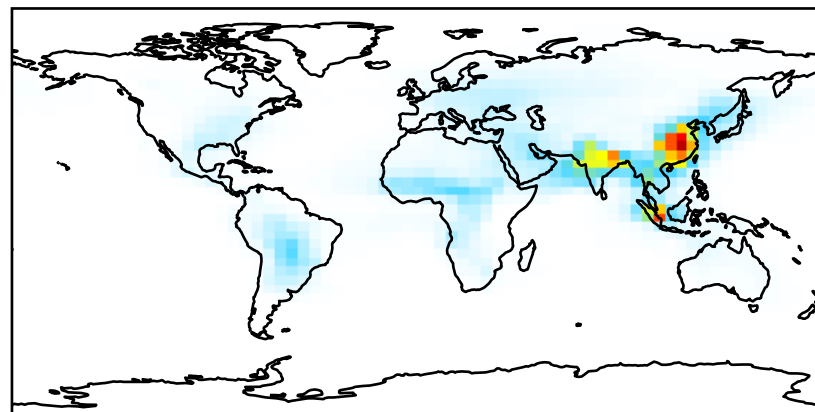


SpeciesConc_SOAGX (Oct2019)

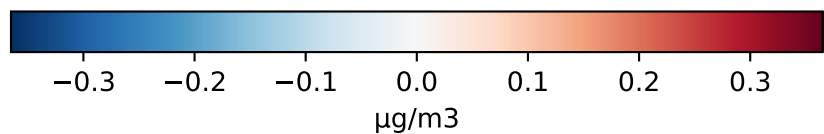
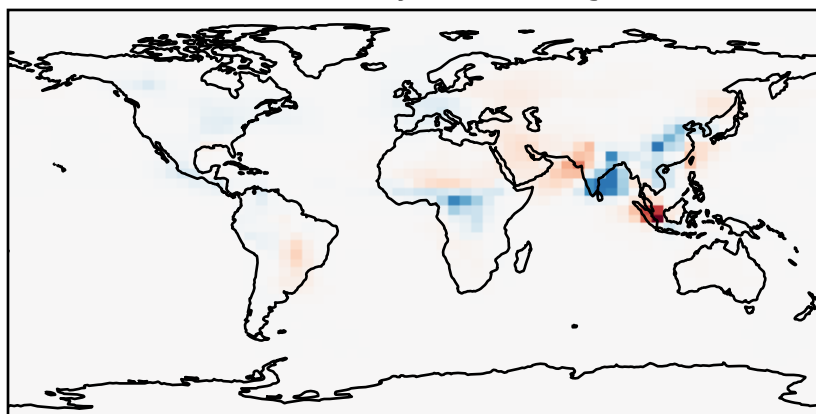
GCC_12.9.0 (Ref)
4.0x5.0



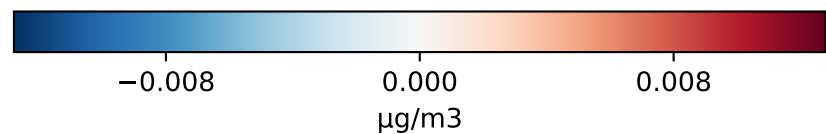
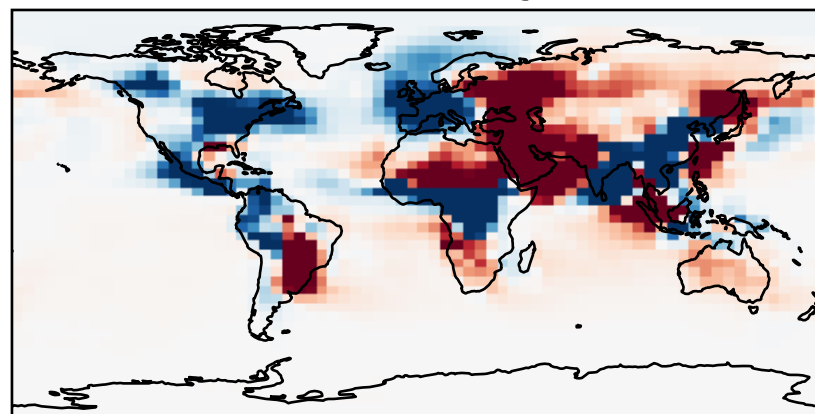
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



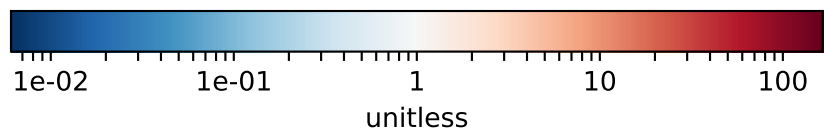
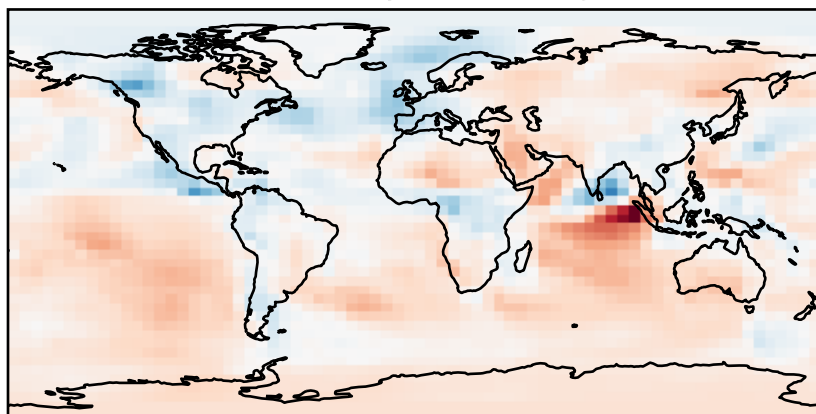
Difference
Dev - Ref, Dynamic Range



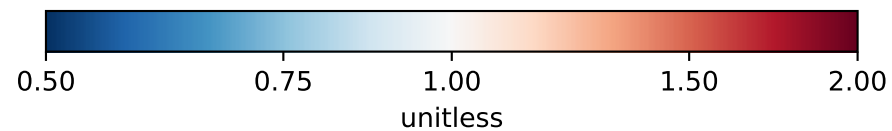
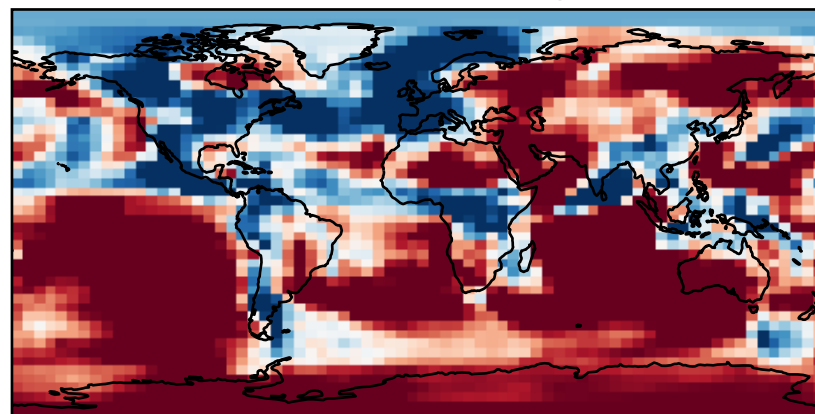
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

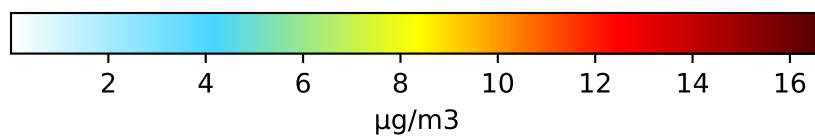
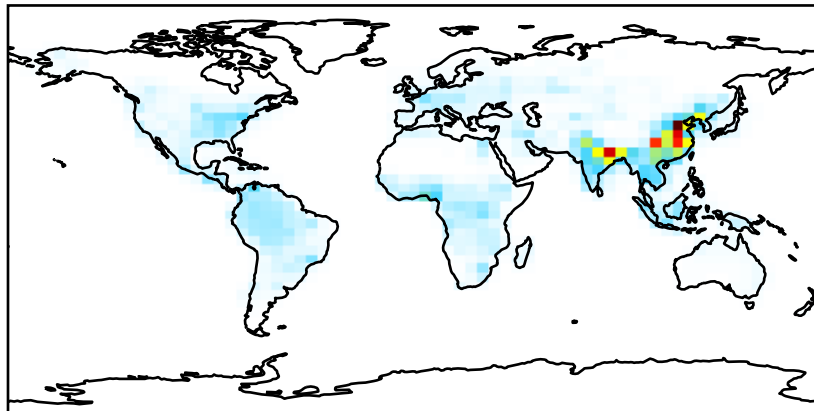


Ratio
Dev/Ref, Fixed Range

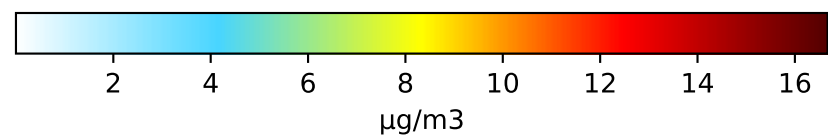
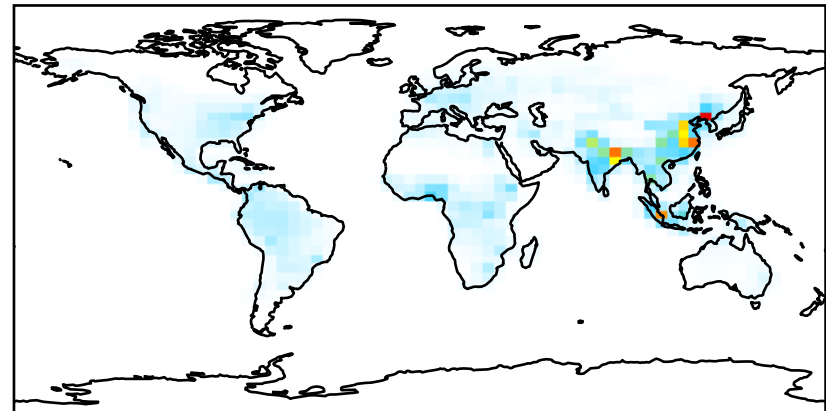


SpeciesConc_SOAP (Oct2019)

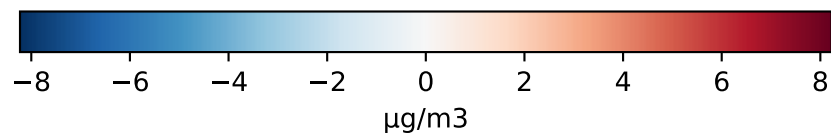
GCC_12.9.0 (Ref)
4.0x5.0



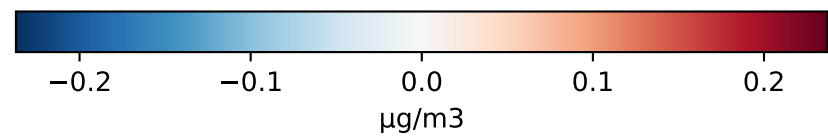
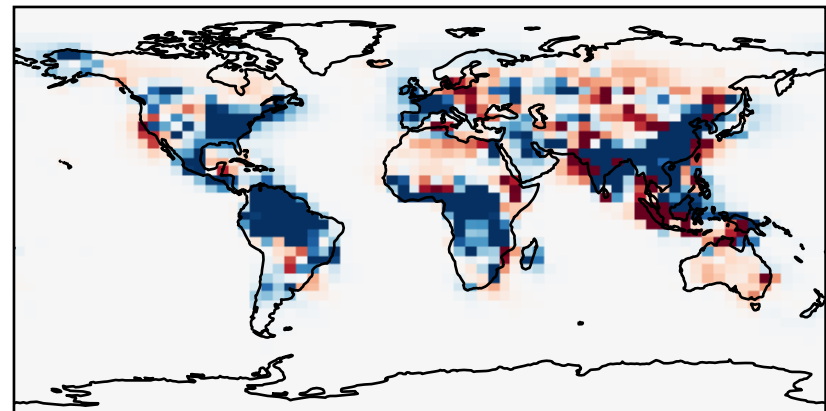
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



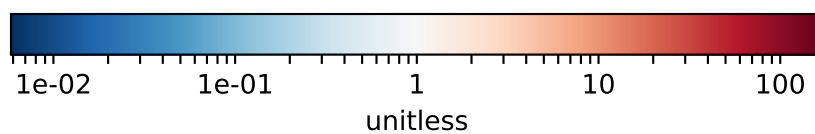
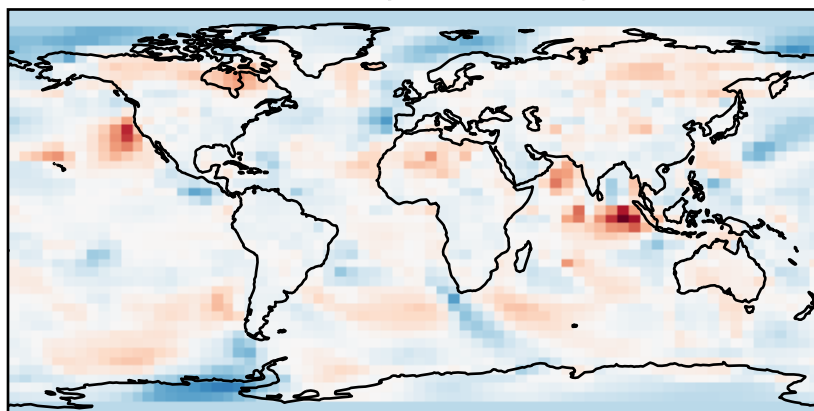
Difference
Dev - Ref, Dynamic Range



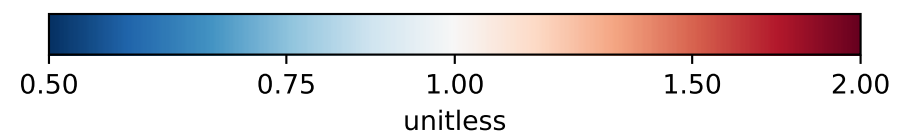
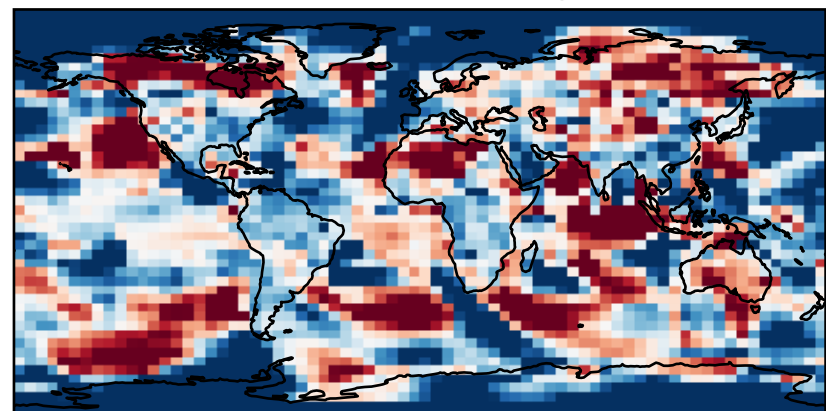
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

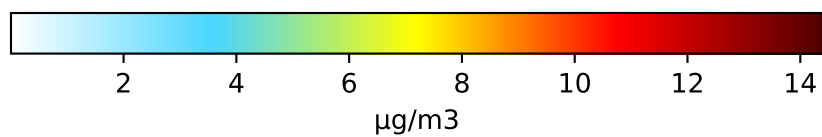
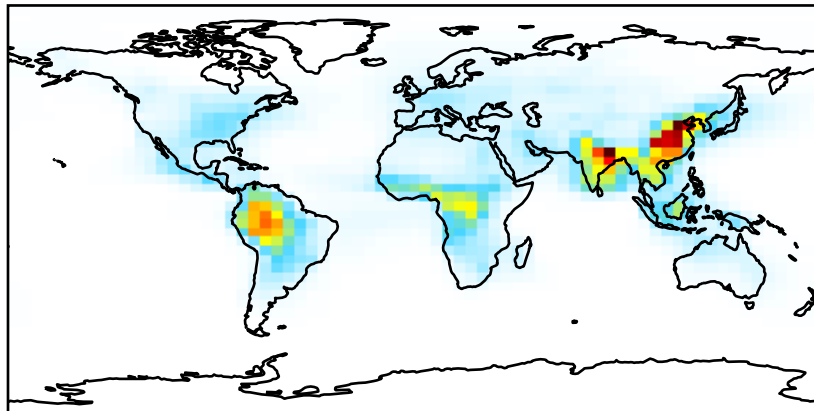


Ratio
Dev/Ref, Fixed Range

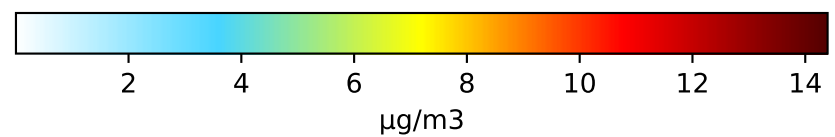
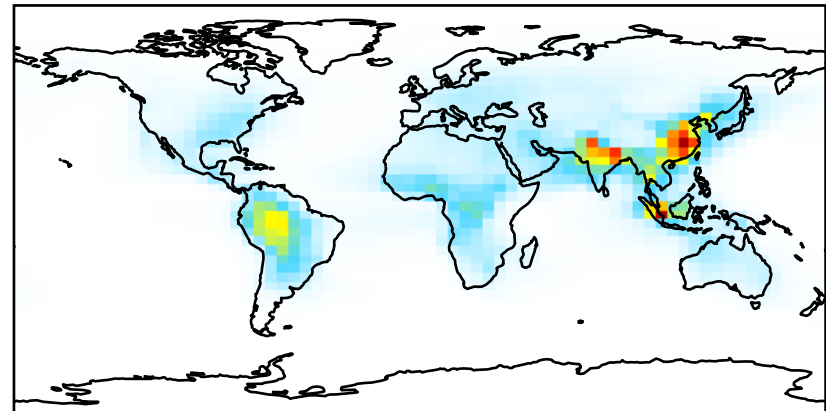


SpeciesConc_SOAS (Oct2019)

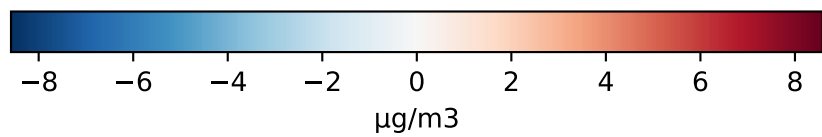
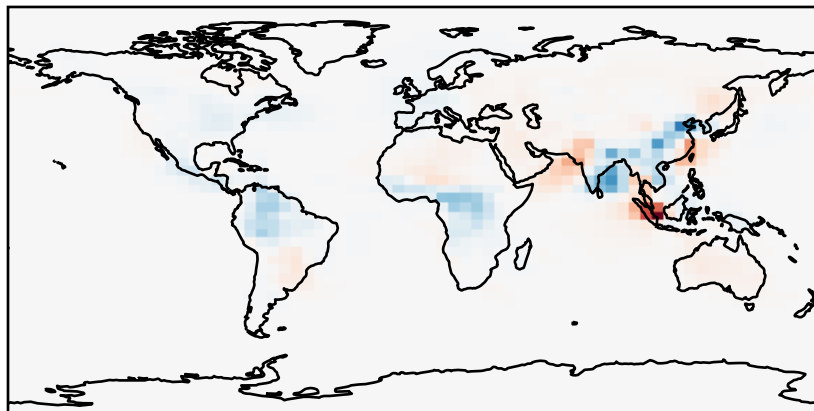
GCC_12.9.0 (Ref)
4.0x5.0



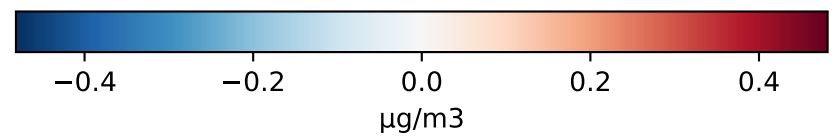
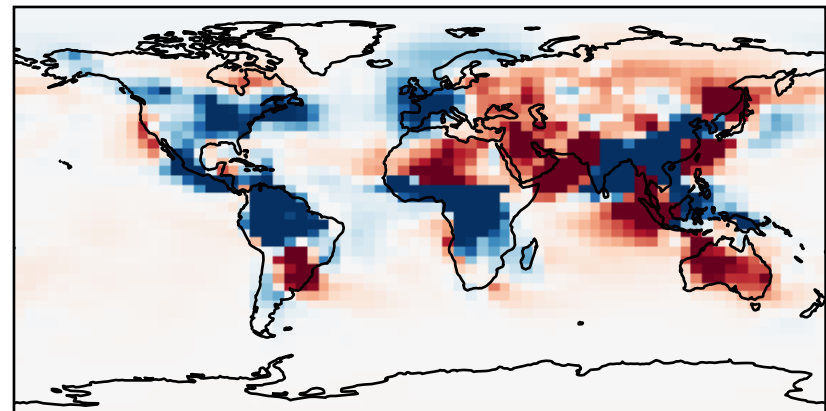
GCC_13.0.0-beta.1 (Dev)
4.0x5.0



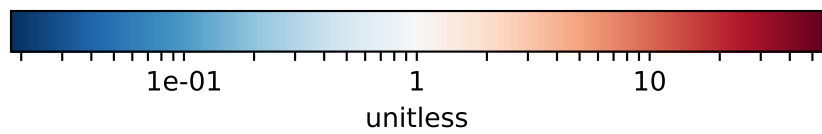
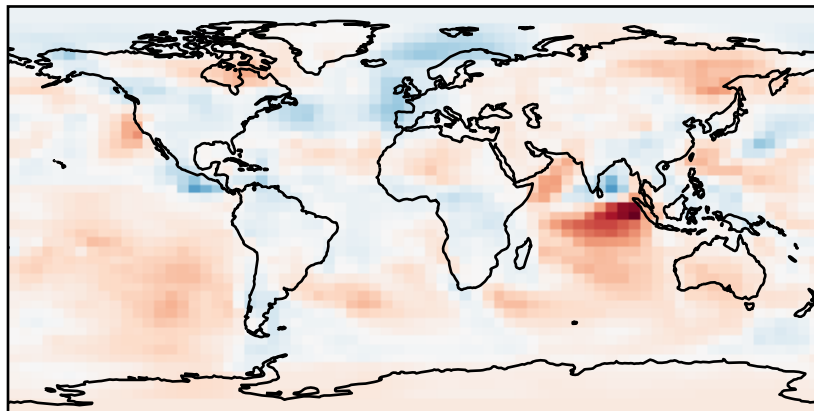
Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range



Ratio
Dev/Ref, Fixed Range

