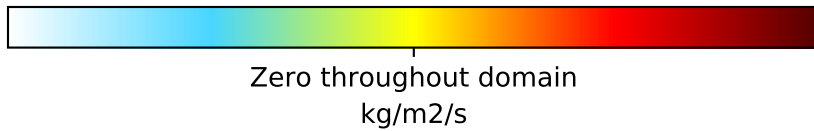
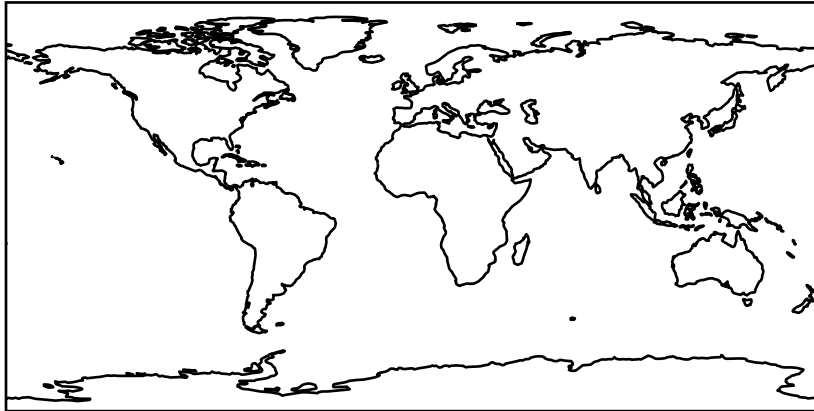
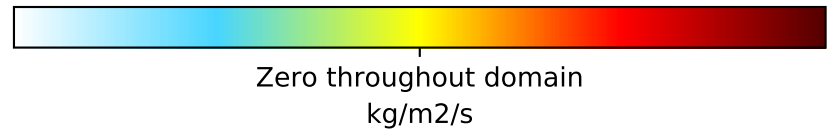
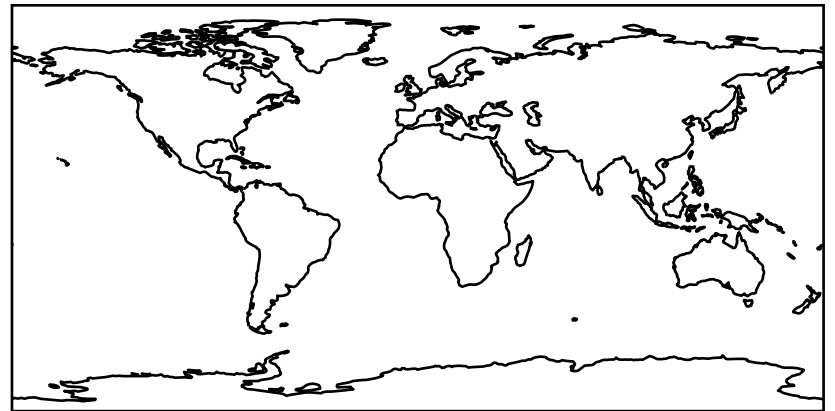


EmisALD2_Ship (Jul2019)

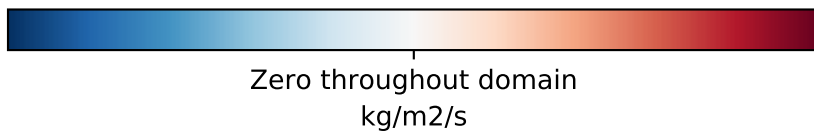
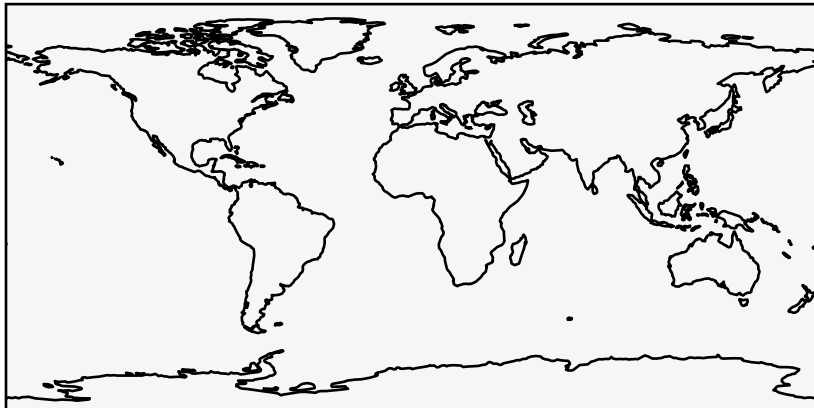
GCC_12.9.0 (Ref)
4.0x5.0



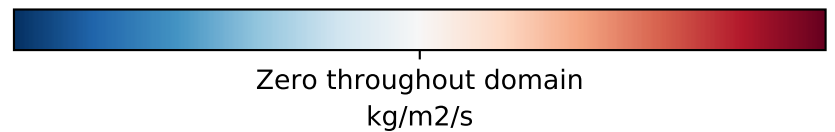
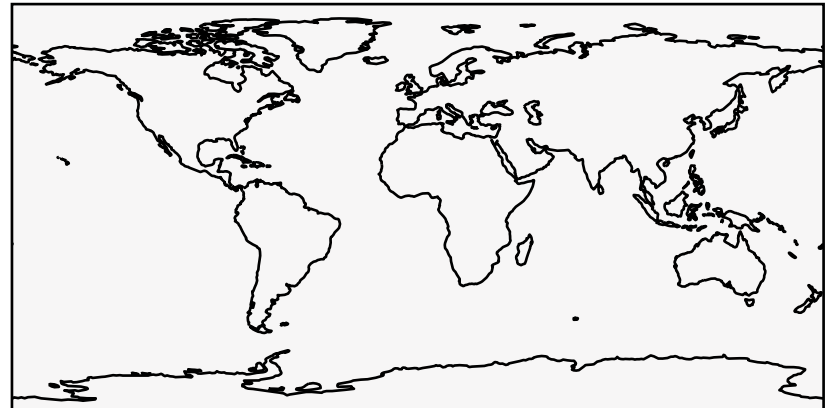
GCC_13.0.0-beta.1 (Dev)
c48



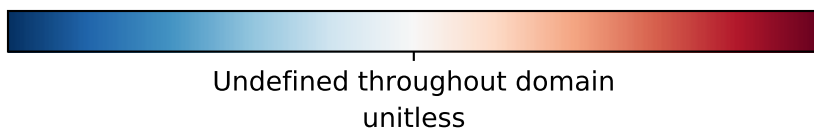
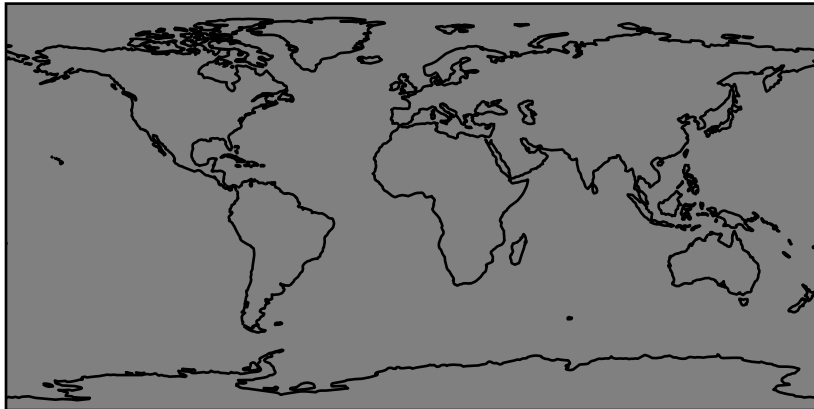
Difference (1x1.25)
Dev - Ref, Dynamic Range



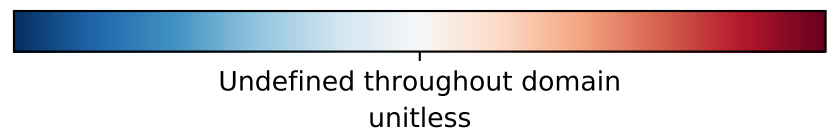
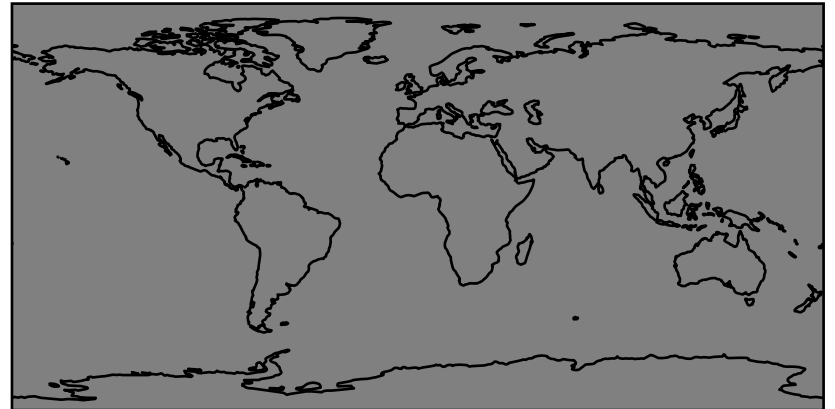
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

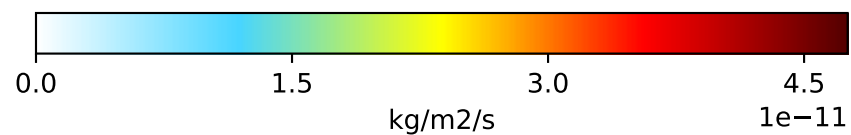
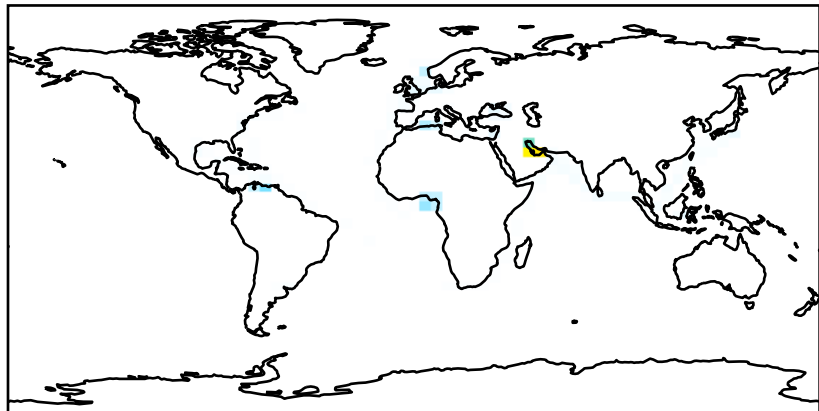


Ratio (1x1.25)
Dev/Ref, Fixed Range

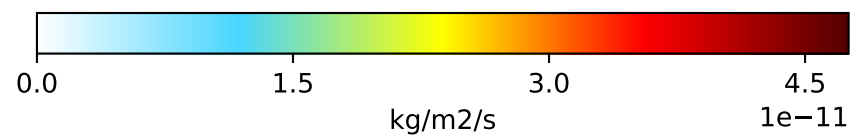
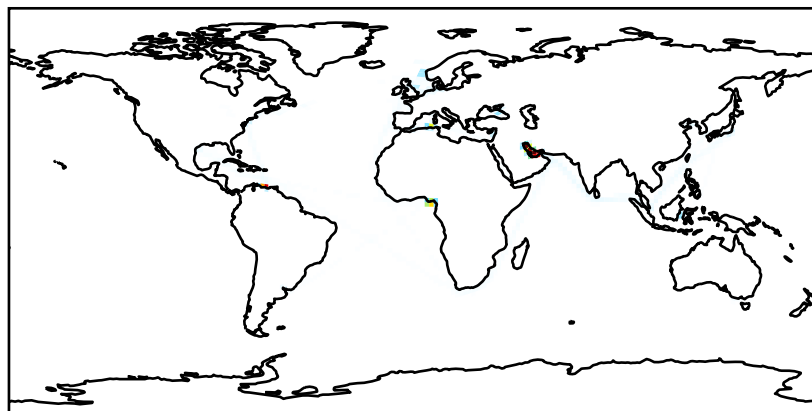


EmisALK4_Ship (Jul2019)

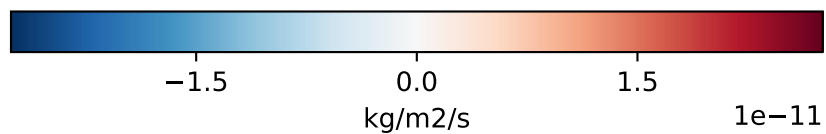
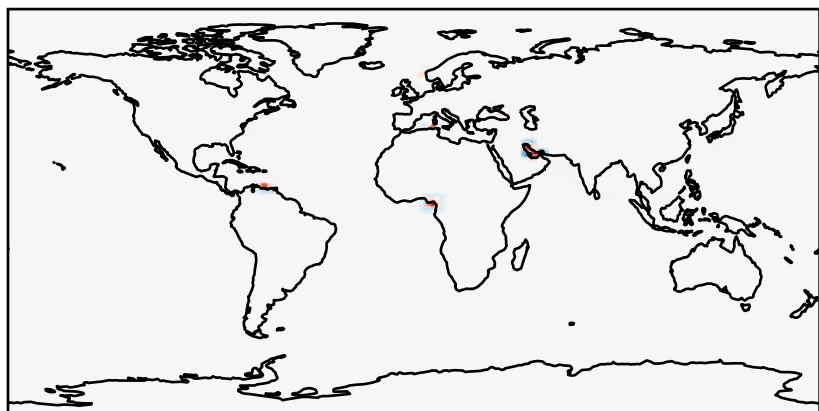
GCC_12.9.0 (Ref)
4.0x5.0



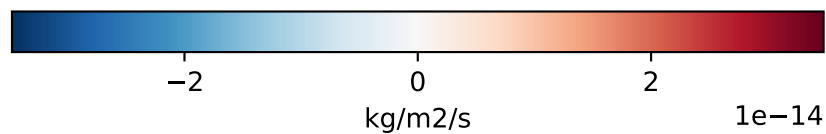
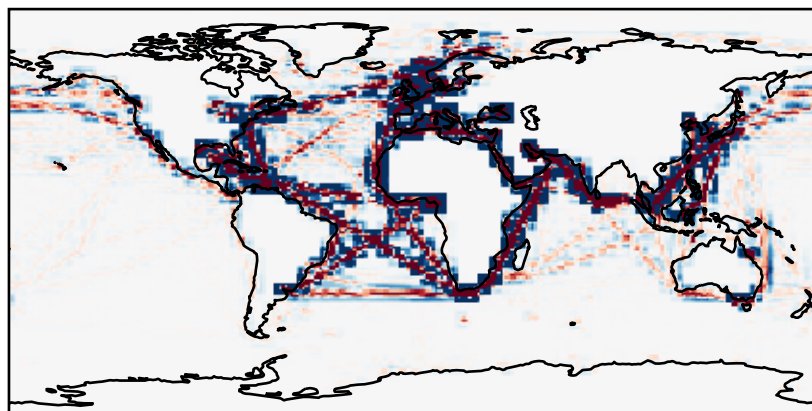
GCC_13.0.0-beta.1 (Dev)
c48



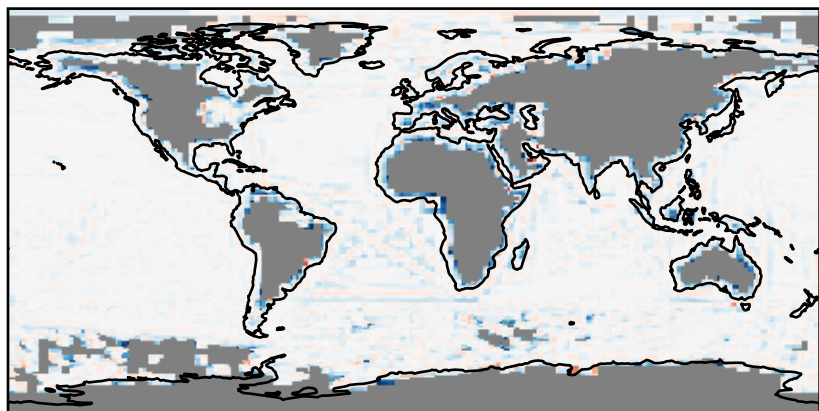
Difference (1x1.25)
Dev - Ref, Dynamic Range



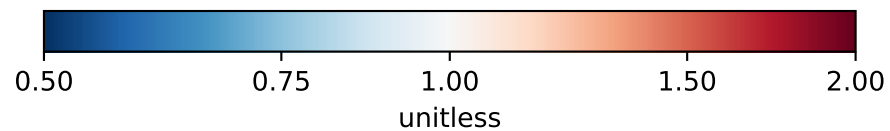
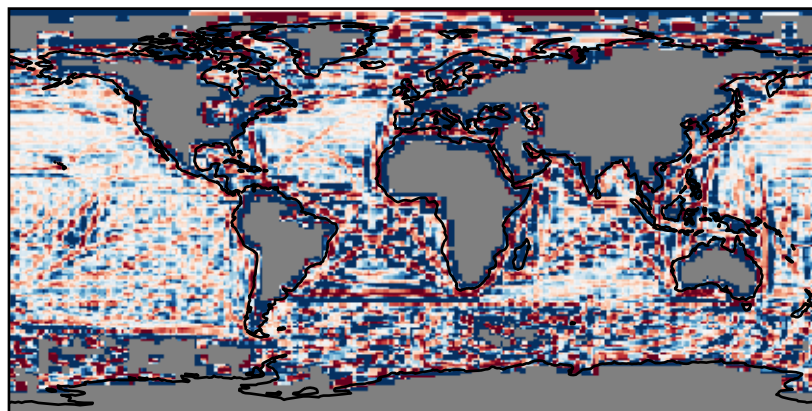
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

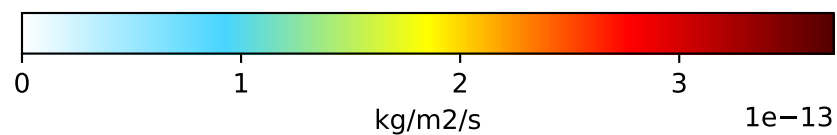
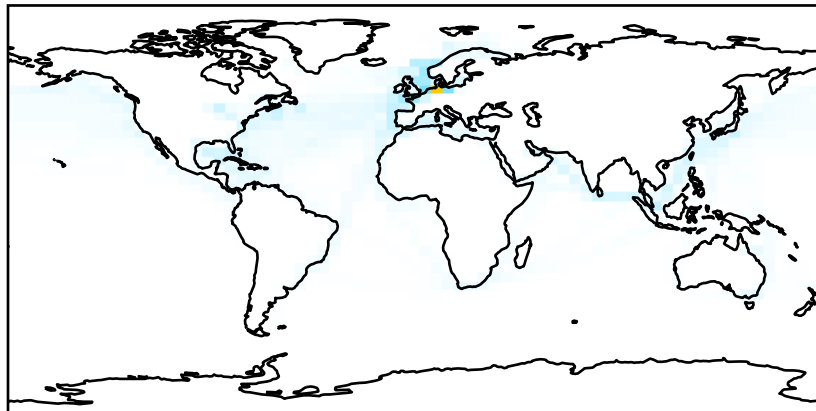


Ratio (1x1.25)
Dev/Ref, Fixed Range

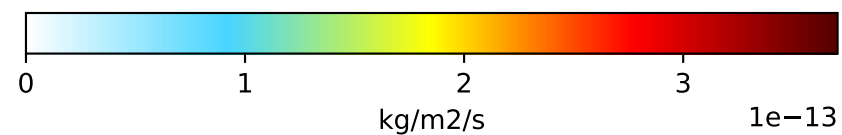
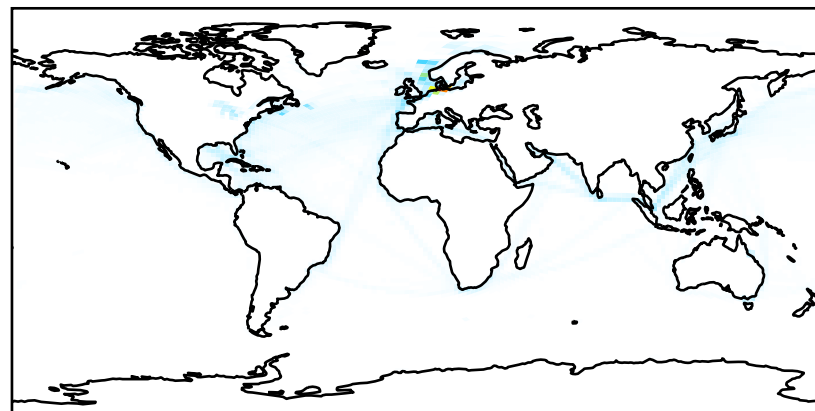


EmisBCPI_Ship (Jul2019)

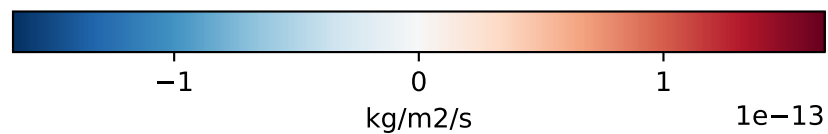
GCC_12.9.0 (Ref)
4.0x5.0



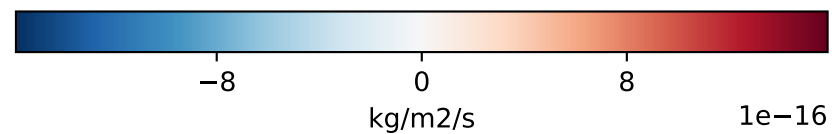
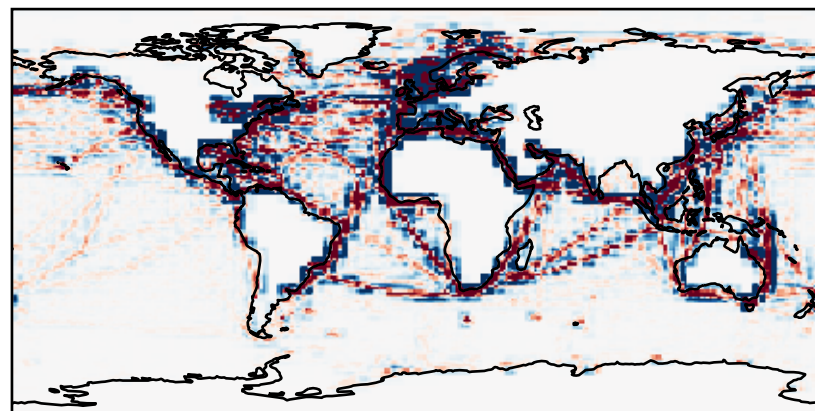
GCC_13.0.0-beta.1 (Dev)
c48



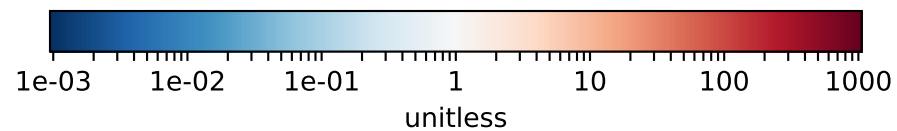
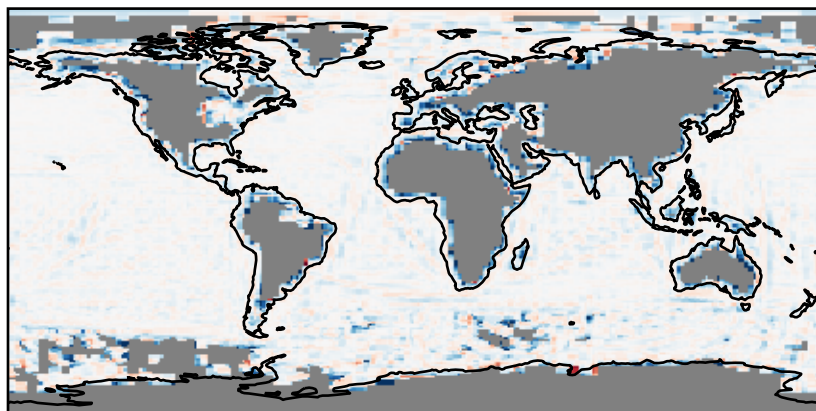
Difference (1x1.25)
Dev - Ref, Dynamic Range



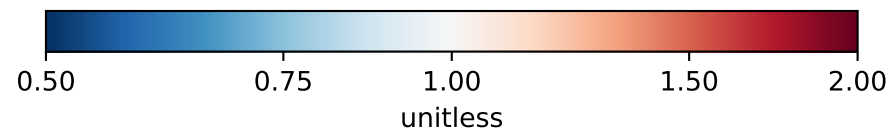
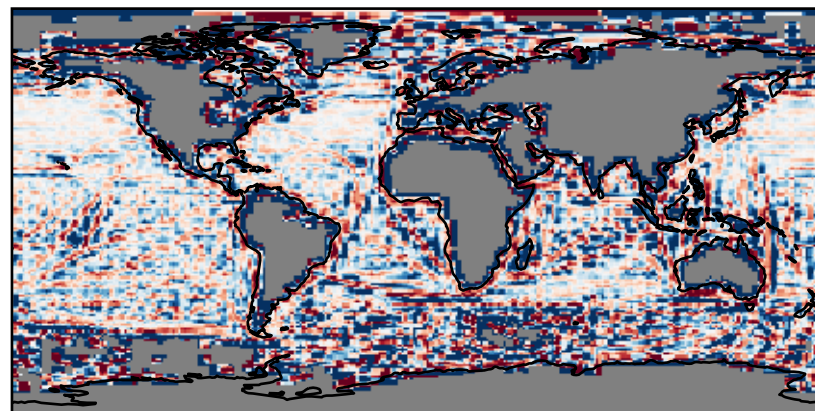
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

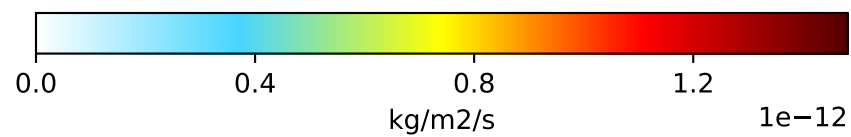
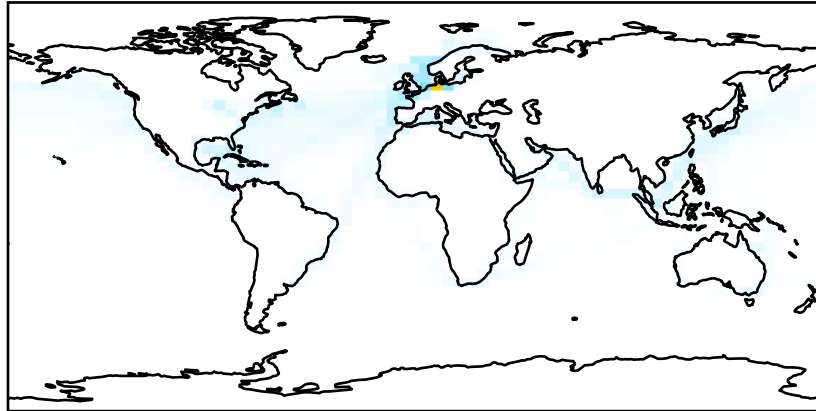


Ratio (1x1.25)
Dev/Ref, Fixed Range

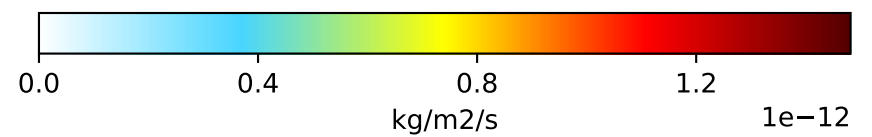
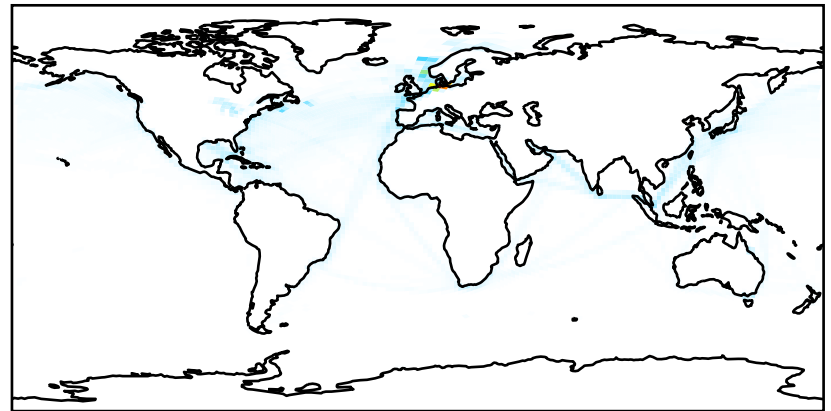


EmisBCPO_Ship (Jul2019)

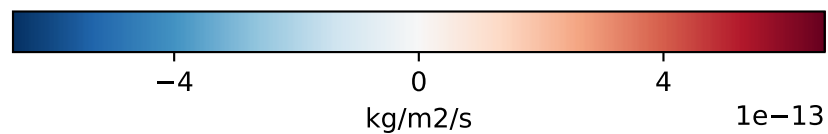
GCC_12.9.0 (Ref)
4.0x5.0



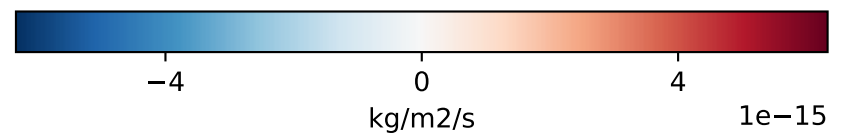
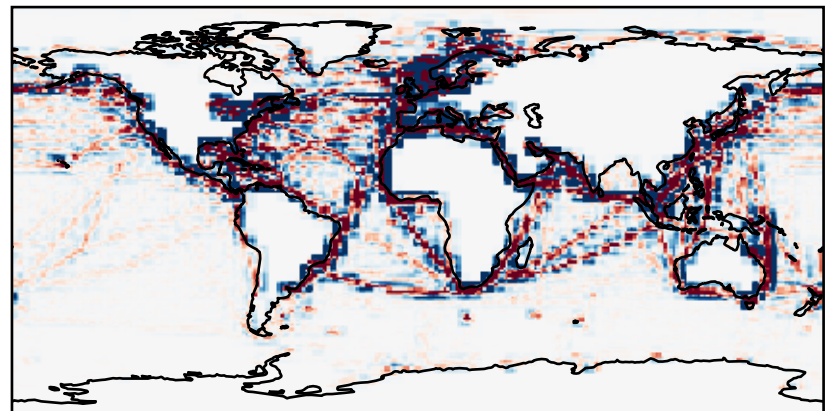
GCC_13.0.0-beta.1 (Dev)
c48



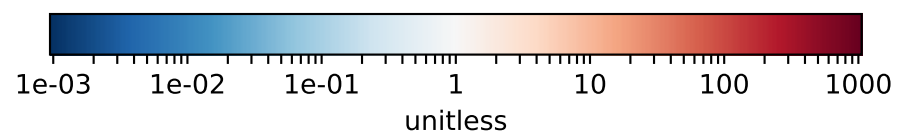
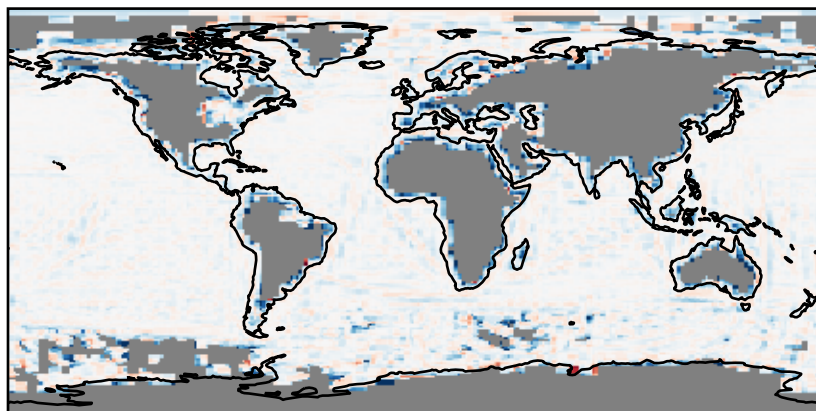
Difference (1x1.25)
Dev - Ref, Dynamic Range



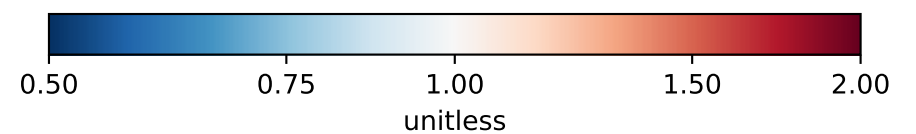
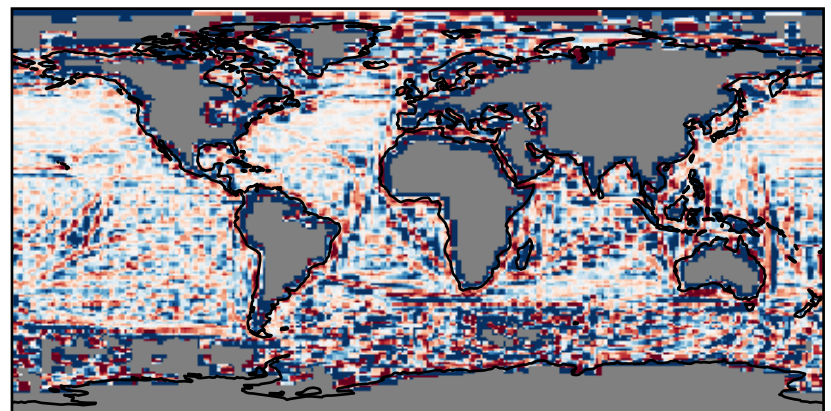
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

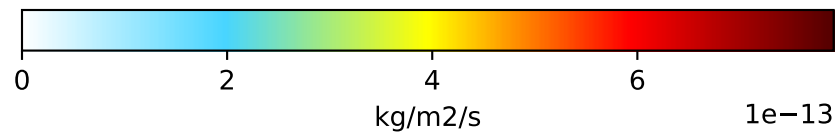
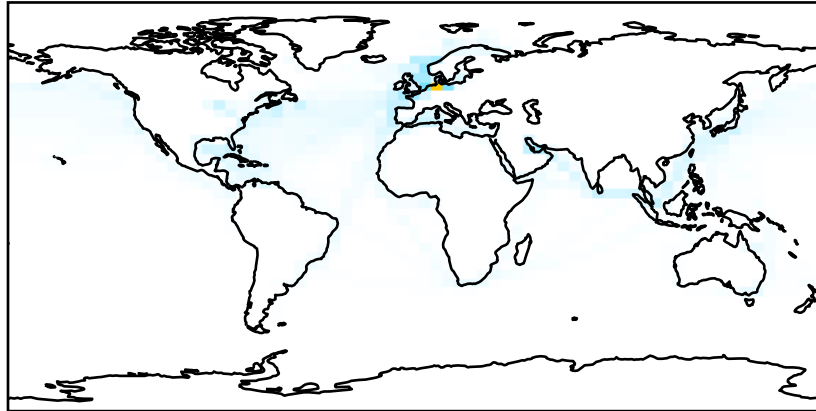


Ratio (1x1.25)
Dev/Ref, Fixed Range

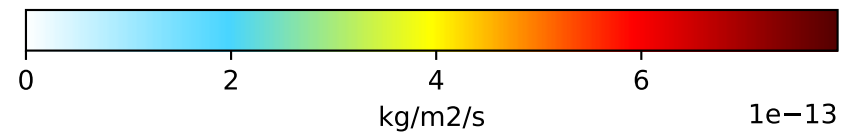
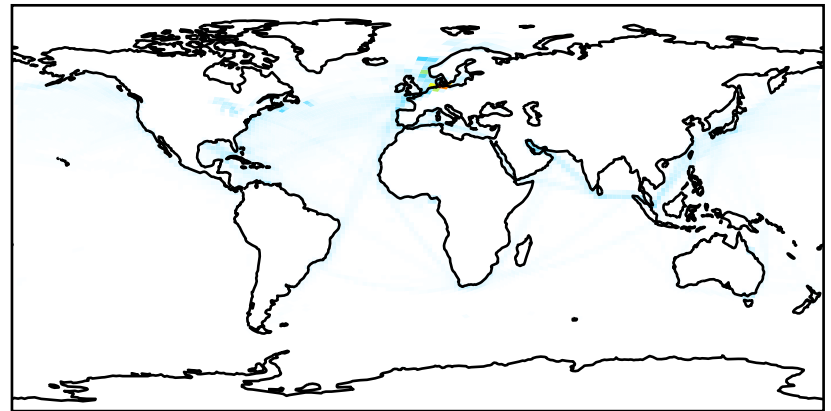


EmisBENZ_Ship (Jul2019)

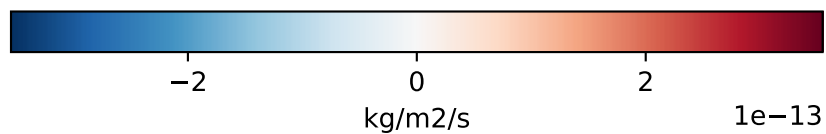
GCC_12.9.0 (Ref)
4.0x5.0



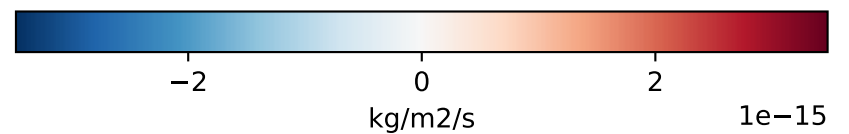
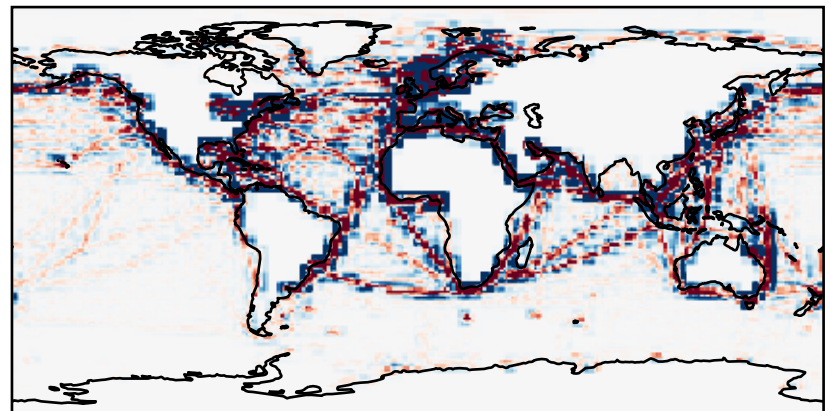
GCC_13.0.0-beta.1 (Dev)
c48



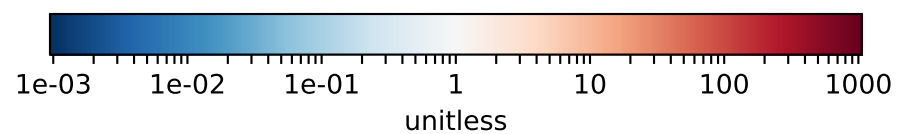
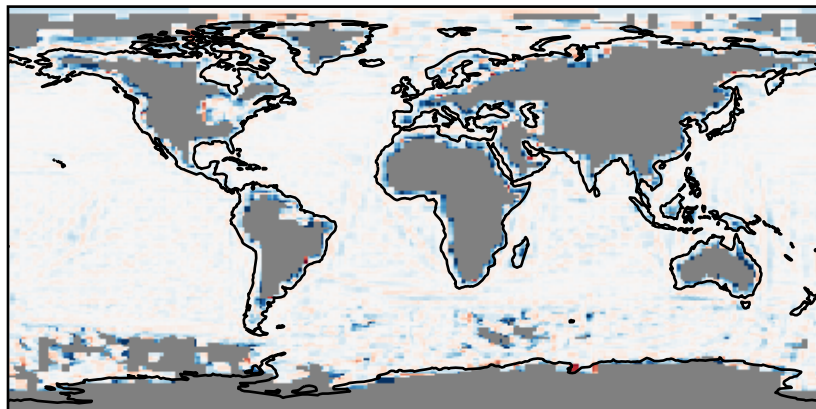
Difference (1x1.25)
Dev - Ref, Dynamic Range



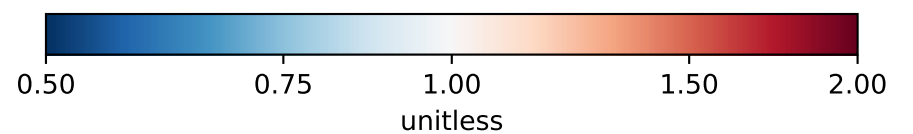
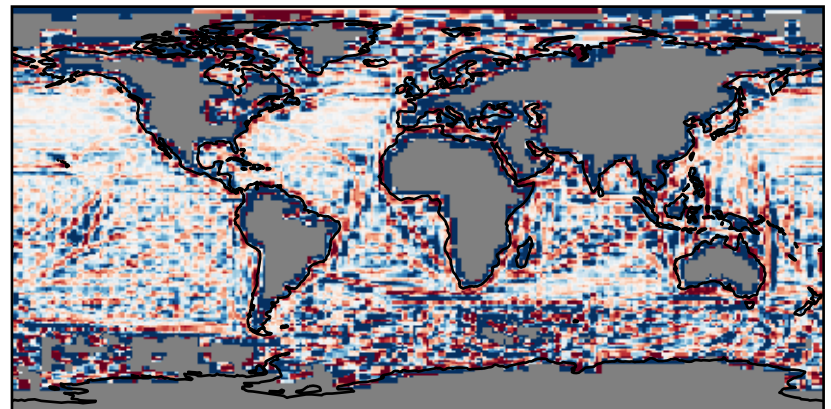
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

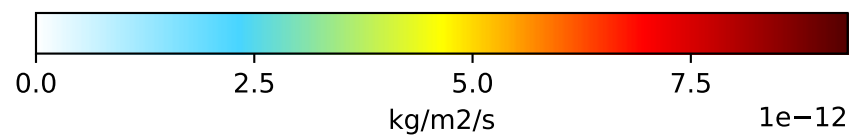
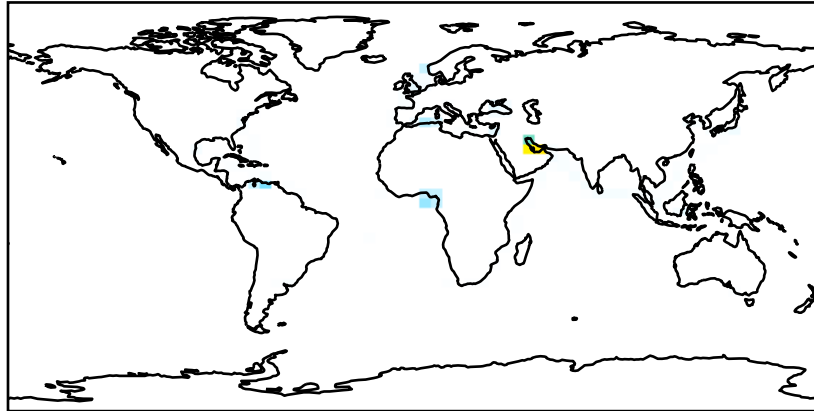


Ratio (1x1.25)
Dev/Ref, Fixed Range

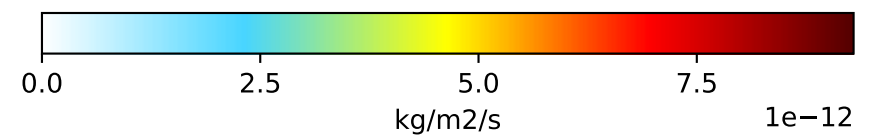
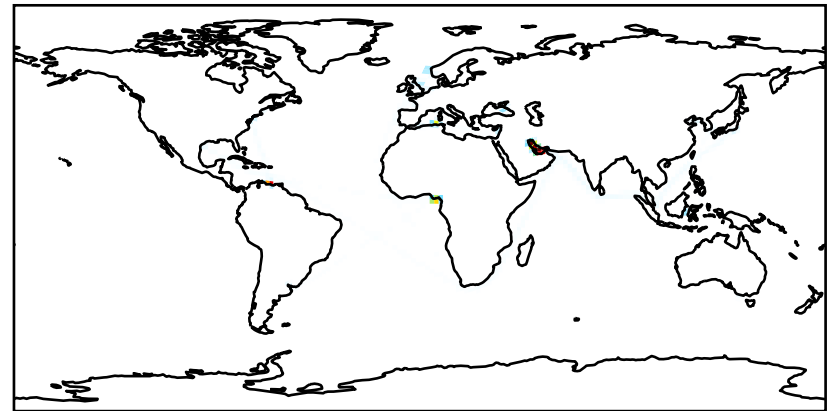


EmisC2H6_Ship (Jul2019)

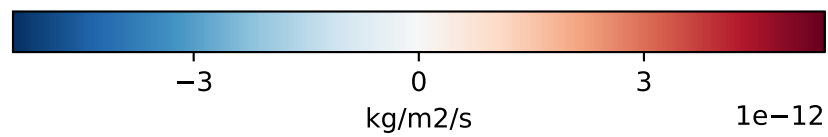
GCC_12.9.0 (Ref)
4.0x5.0



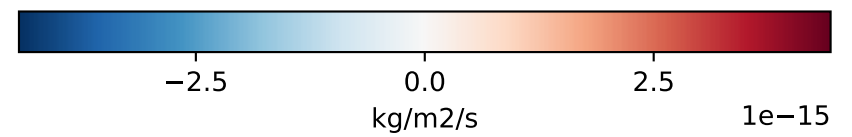
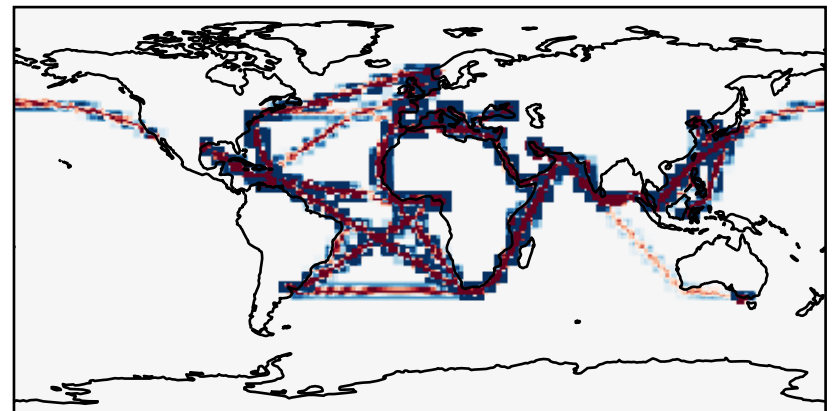
GCC_13.0.0-beta.1 (Dev)
c48



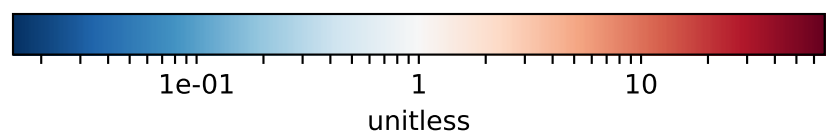
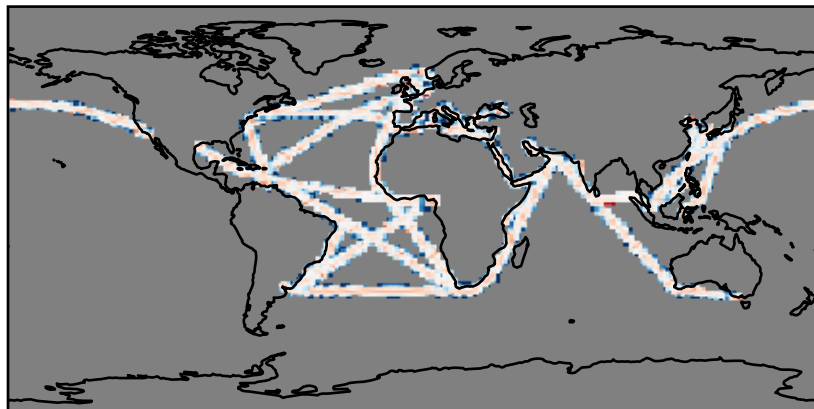
Difference (1x1.25)
Dev - Ref, Dynamic Range



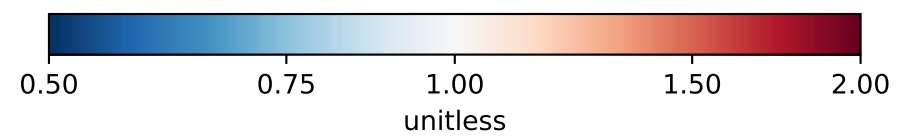
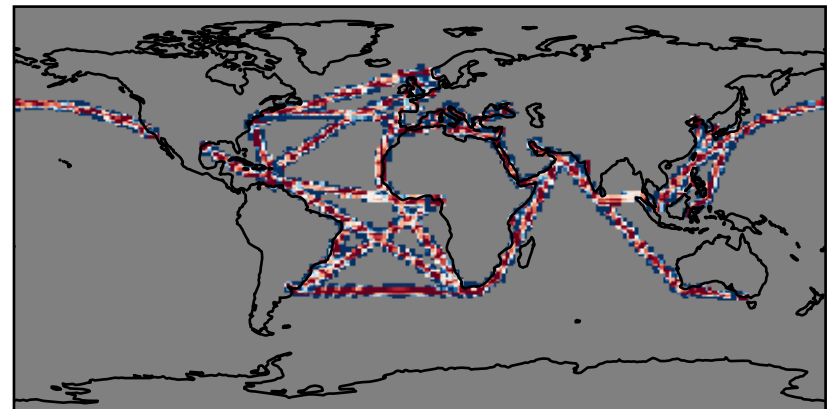
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

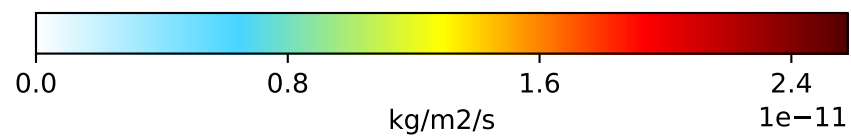
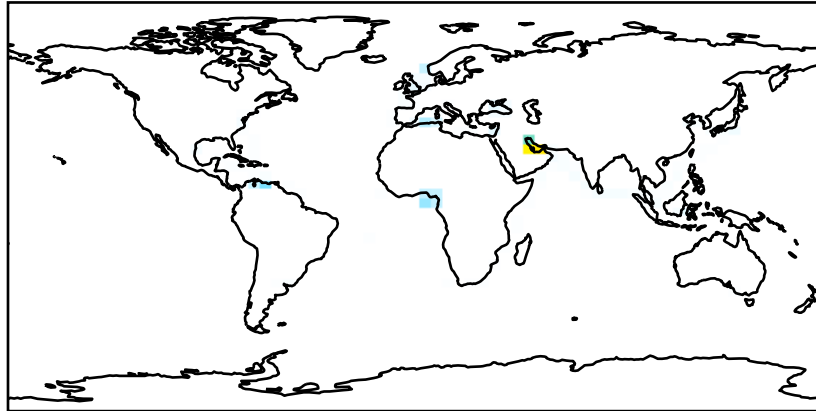


Ratio (1x1.25)
Dev/Ref, Fixed Range

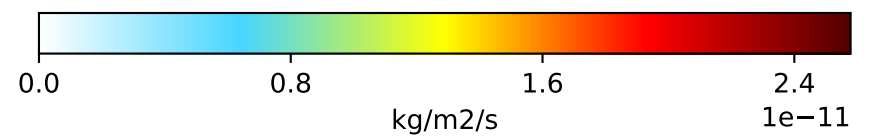
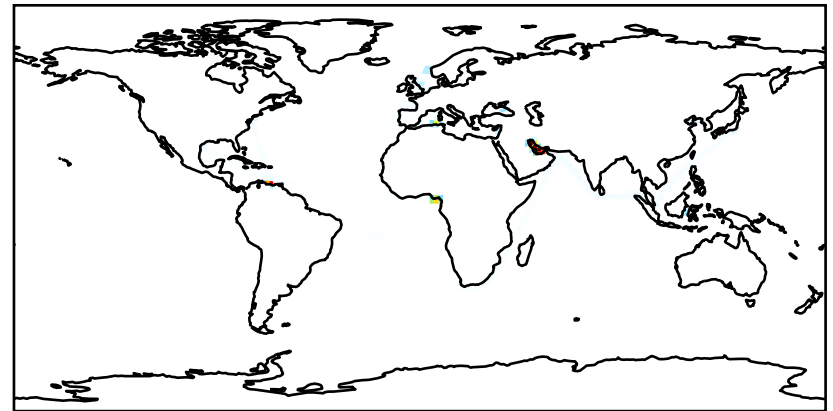


EmisC3H8_Ship (Jul2019)

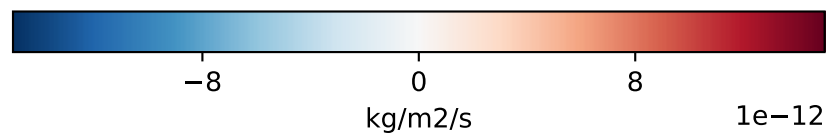
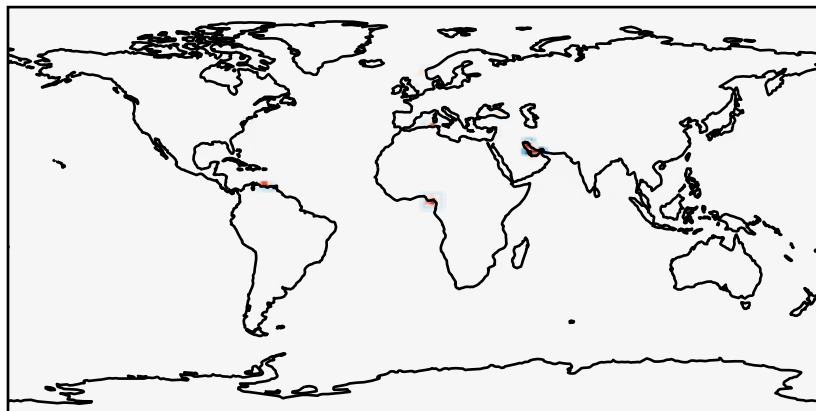
GCC_12.9.0 (Ref)
4.0x5.0



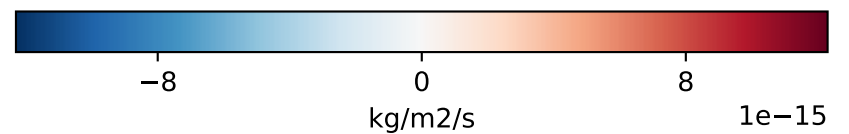
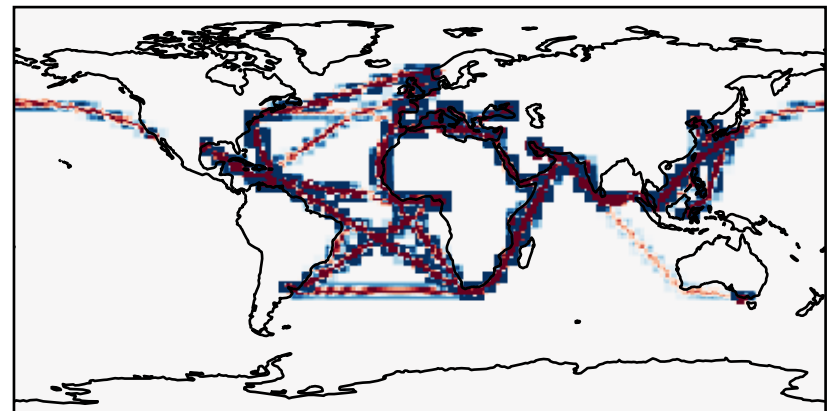
GCC_13.0.0-beta.1 (Dev)
c48



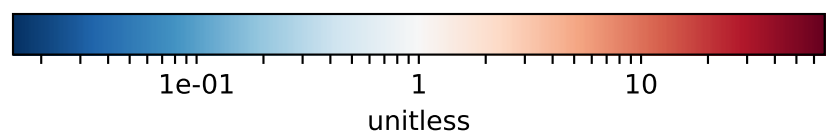
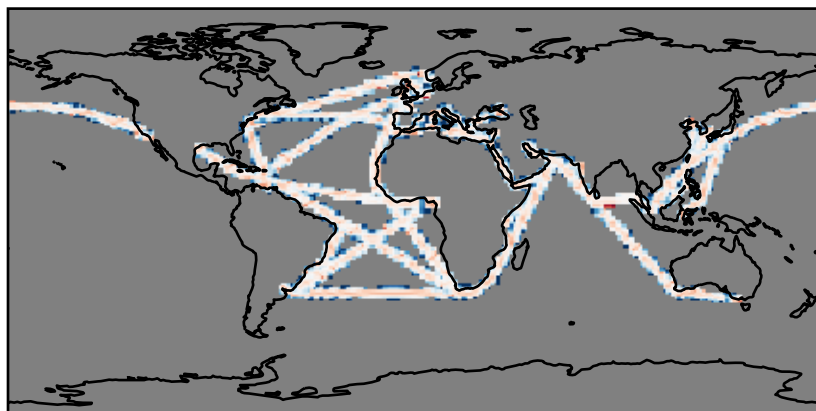
Difference (1x1.25)
Dev - Ref, Dynamic Range



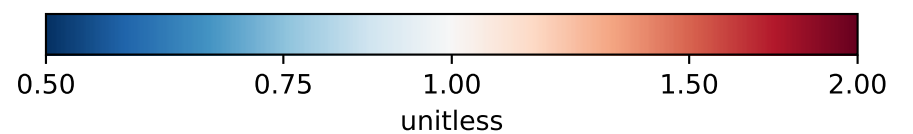
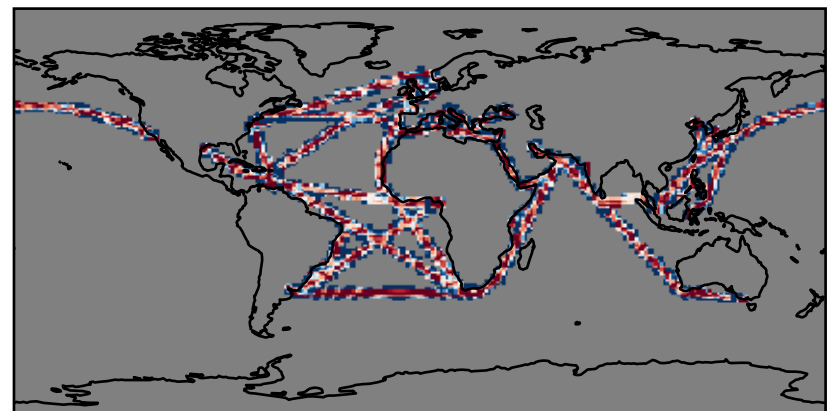
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

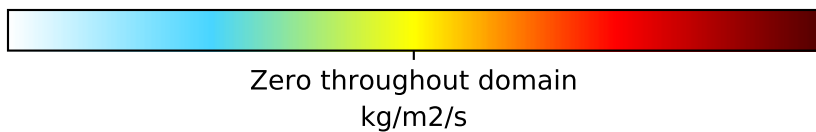


Ratio (1x1.25)
Dev/Ref, Fixed Range

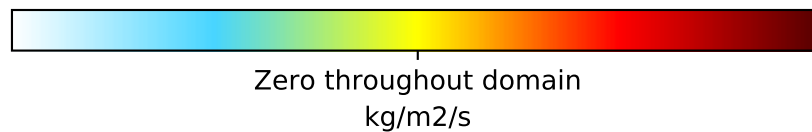
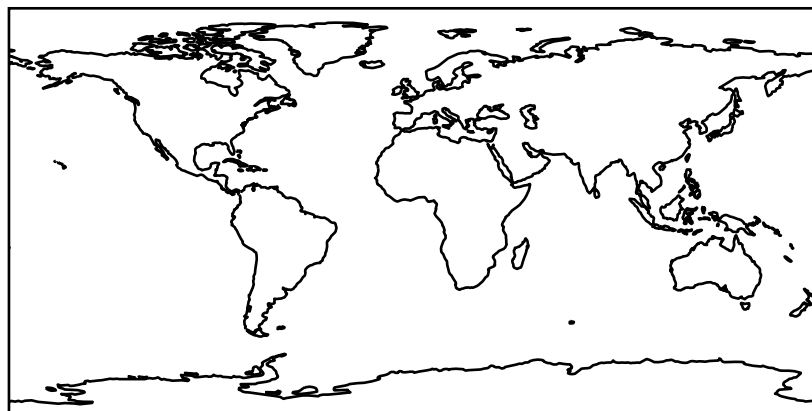


EmisCH2O_Ship (Jul2019)

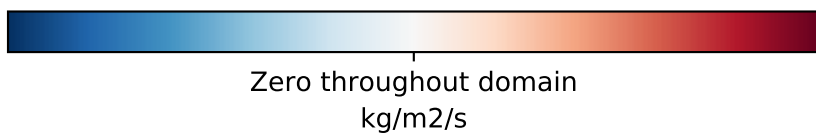
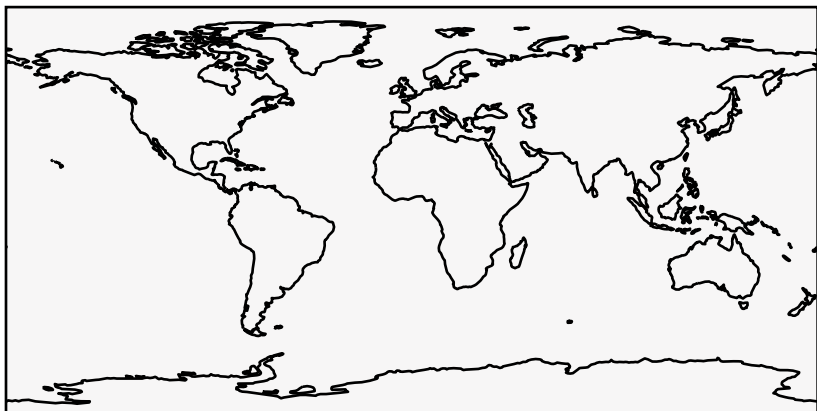
GCC_12.9.0 (Ref)
4.0x5.0



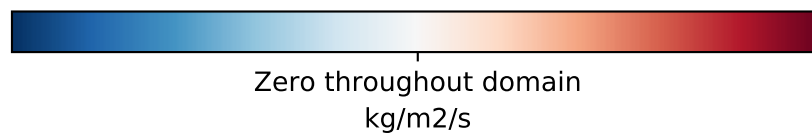
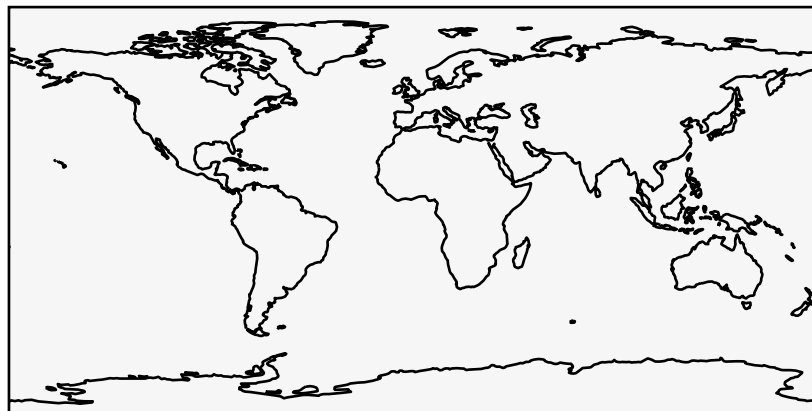
GCC_13.0.0-beta.1 (Dev)
c48



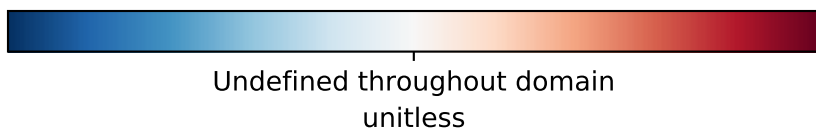
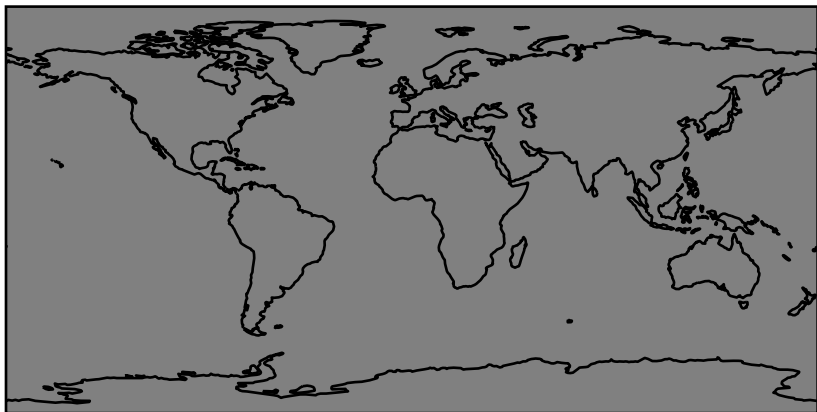
Difference (1x1.25)
Dev - Ref, Dynamic Range



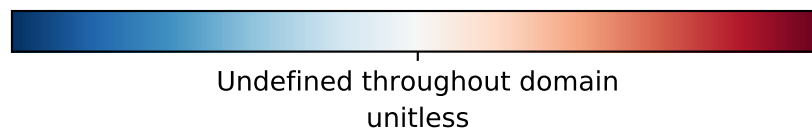
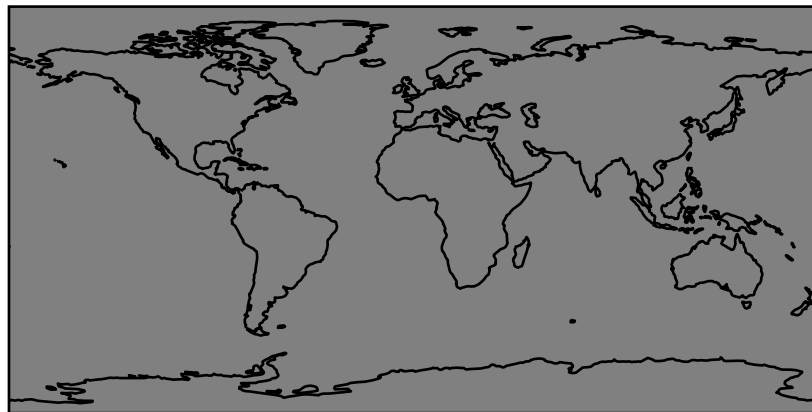
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

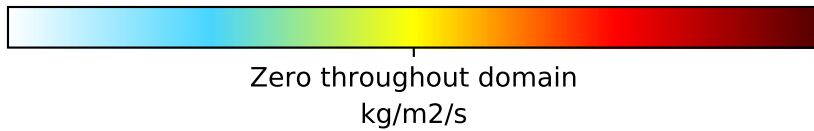
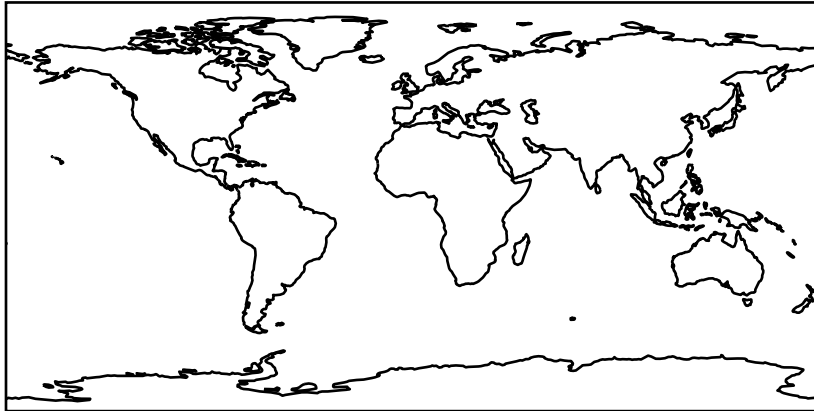


Ratio (1x1.25)
Dev/Ref, Fixed Range

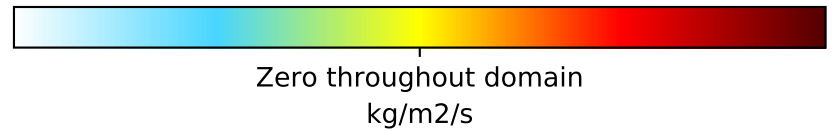
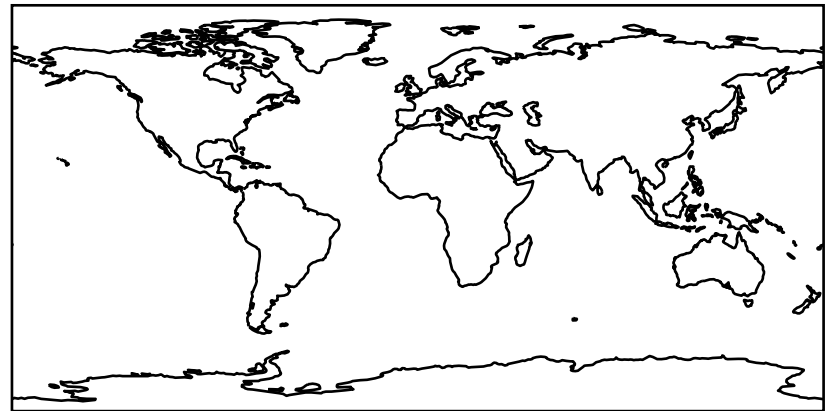


EmisCO2_Ship (Jul2019)

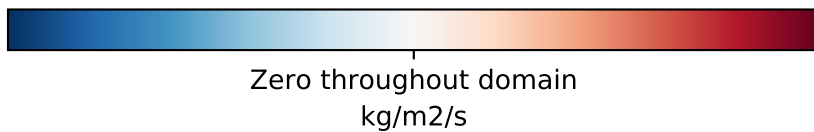
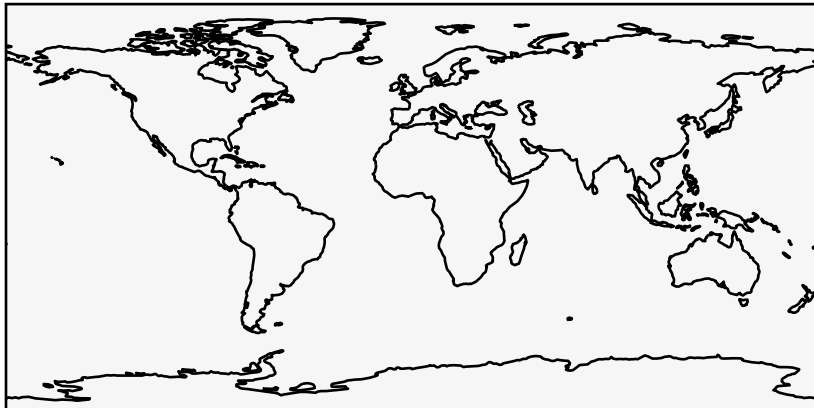
GCC_12.9.0 (Ref)
4.0x5.0



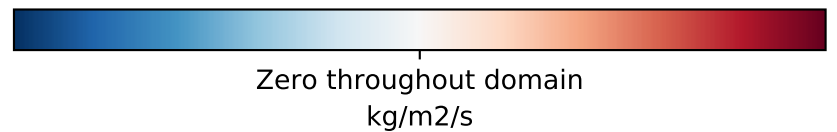
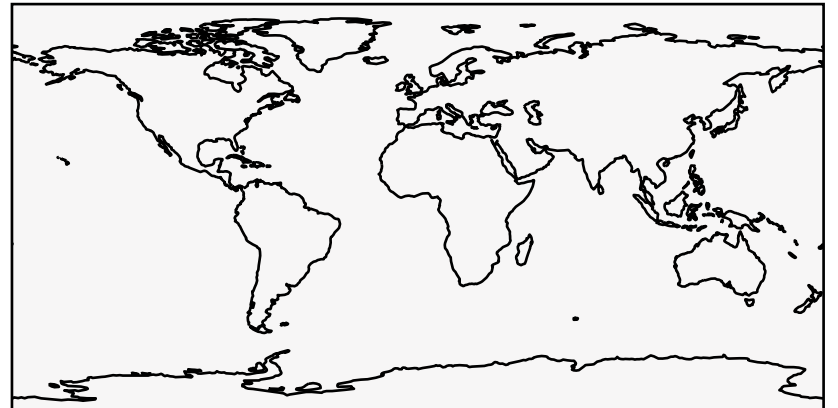
GCC_13.0.0-beta.1 (Dev)
c48



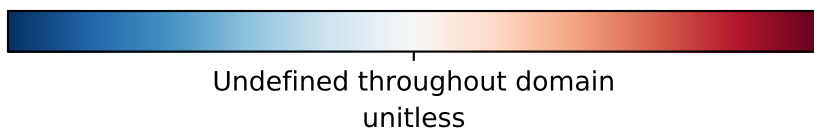
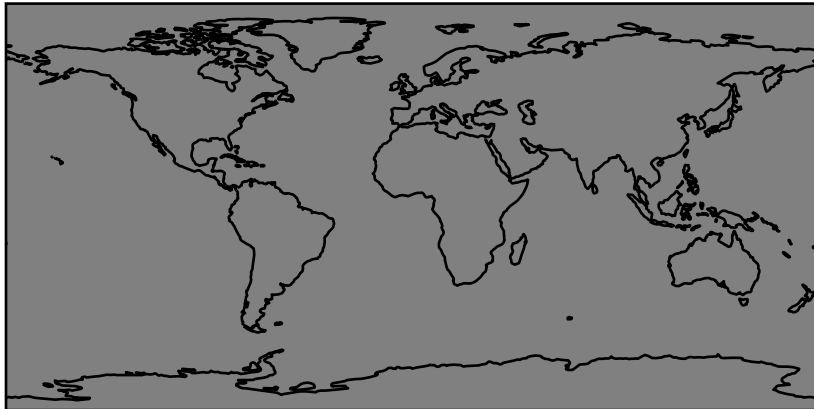
Difference (1x1.25)
Dev - Ref, Dynamic Range



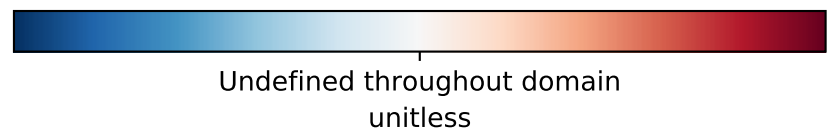
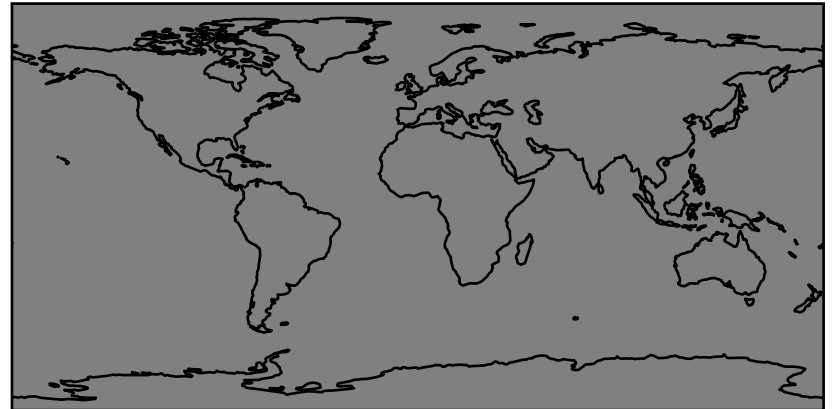
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

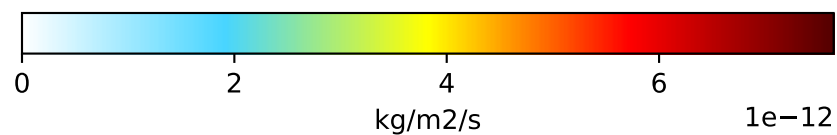
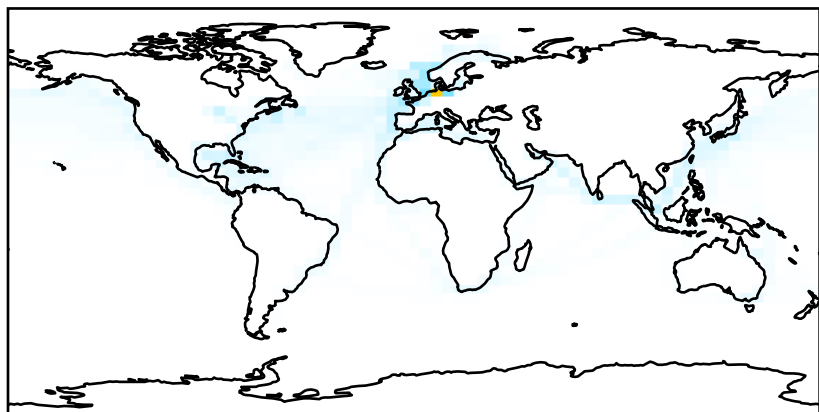


Ratio (1x1.25)
Dev/Ref, Fixed Range

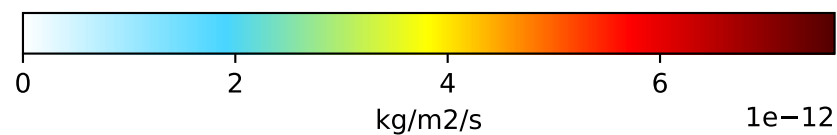
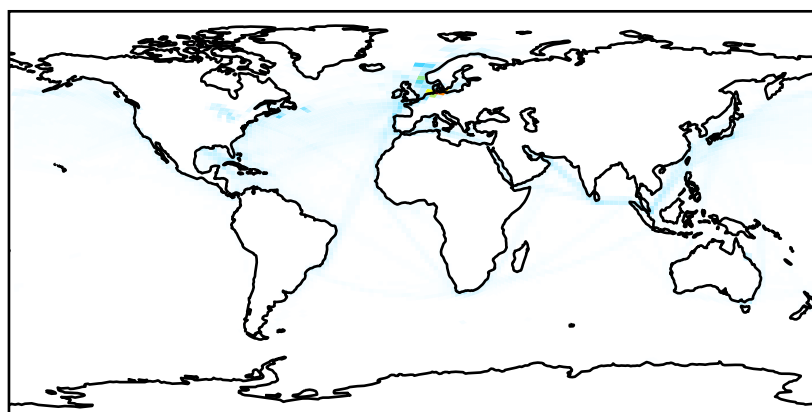


EmisCO_Ship (Jul2019)

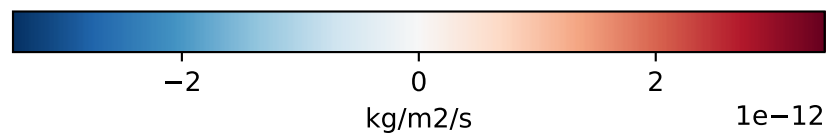
GCC_12.9.0 (Ref)
4.0x5.0



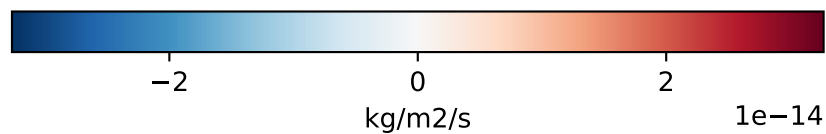
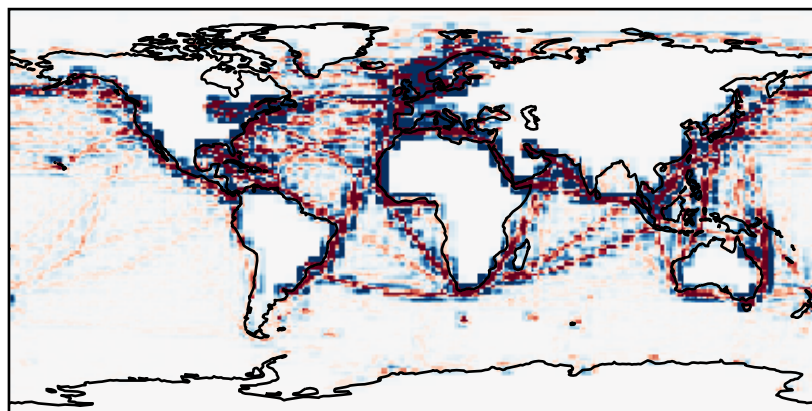
GCC_13.0.0-beta.1 (Dev)
c48



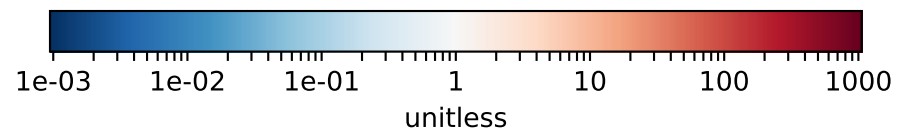
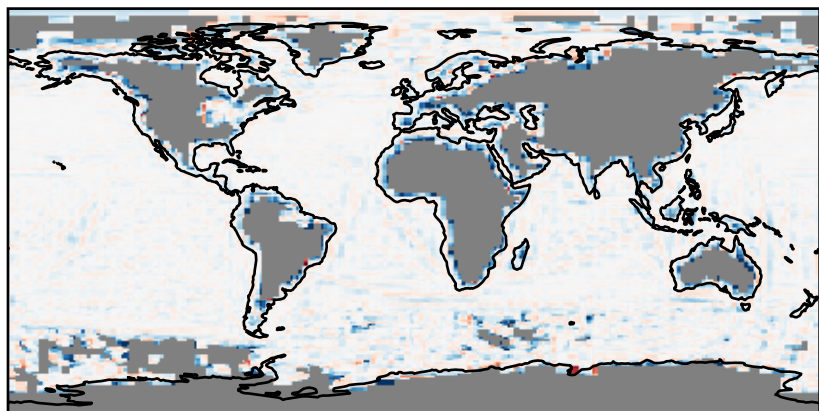
Difference (1x1.25)
Dev - Ref, Dynamic Range



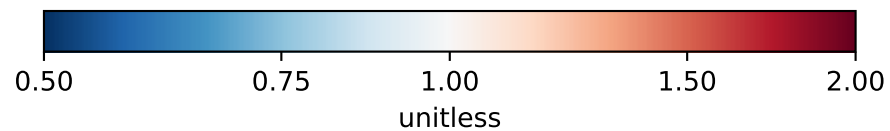
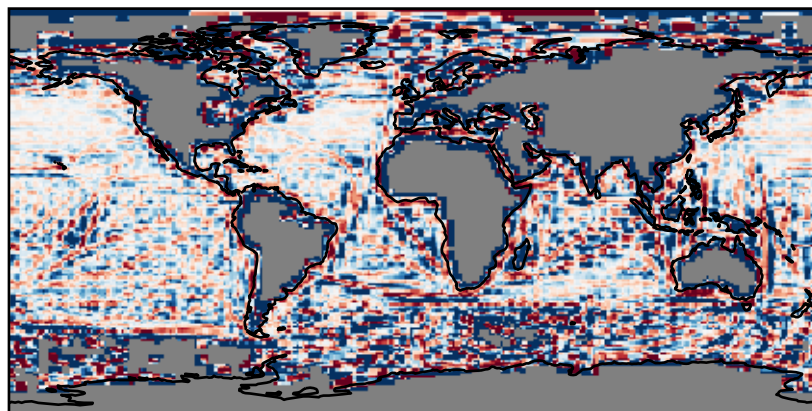
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

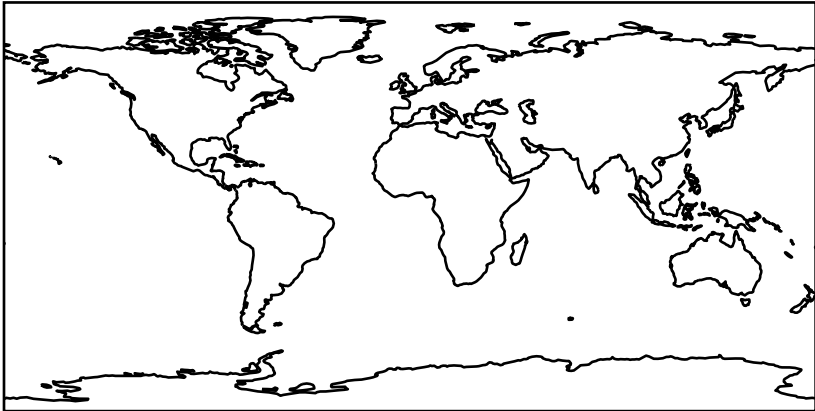


Ratio (1x1.25)
Dev/Ref, Fixed Range



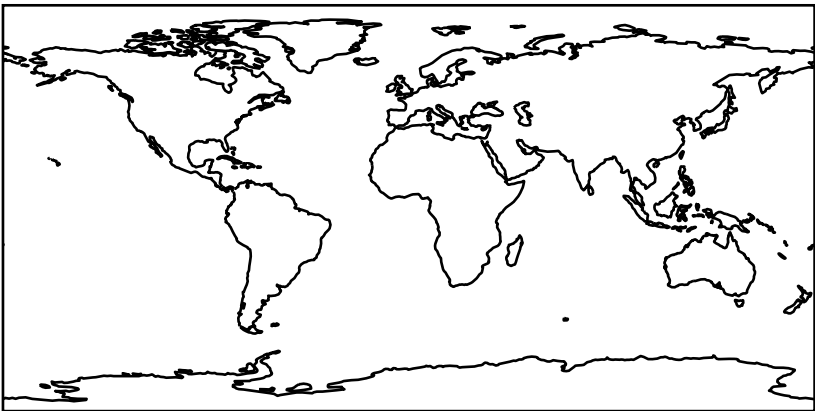
EmisEOH_Ship (Jul2019)

GCC_12.9.0 (Ref)
4.0x5.0



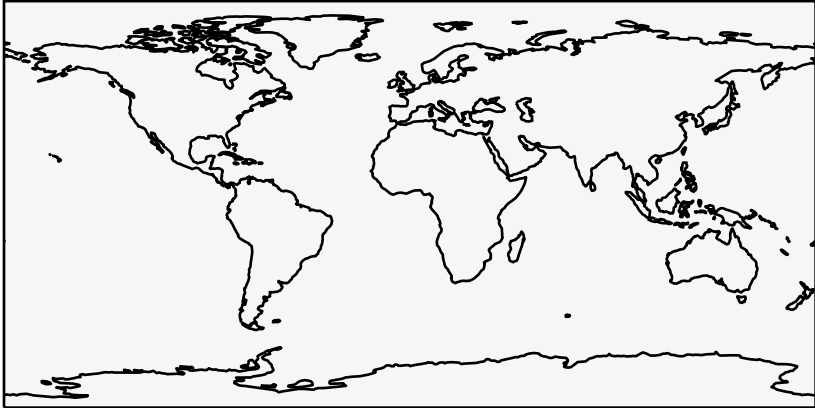
Zero throughout domain
kg/m2/s

GCC_13.0.0-beta.1 (Dev)
c48



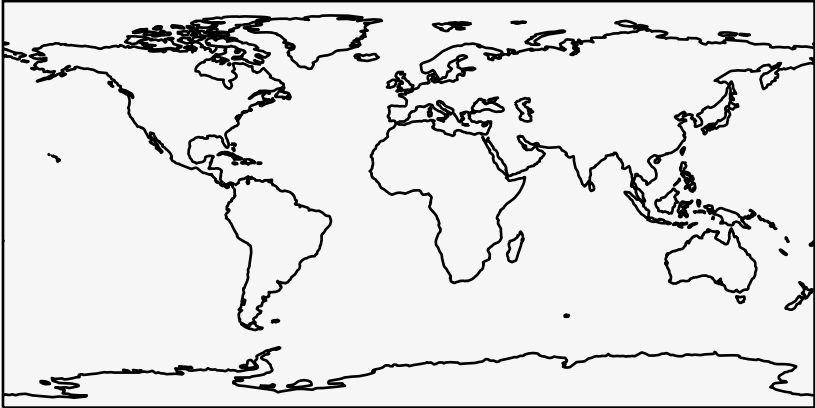
Zero throughout domain
kg/m2/s

Difference (1x1.25)
Dev - Ref, Dynamic Range



Zero throughout domain
kg/m2/s

Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Zero throughout domain
kg/m2/s

Ratio (1x1.25)
Dev/Ref, Dynamic Range



Undefined throughout domain
unitless

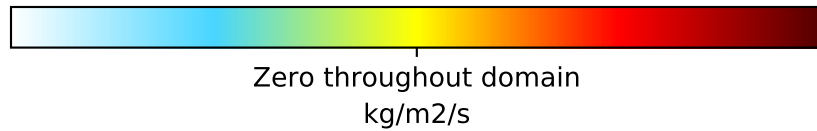
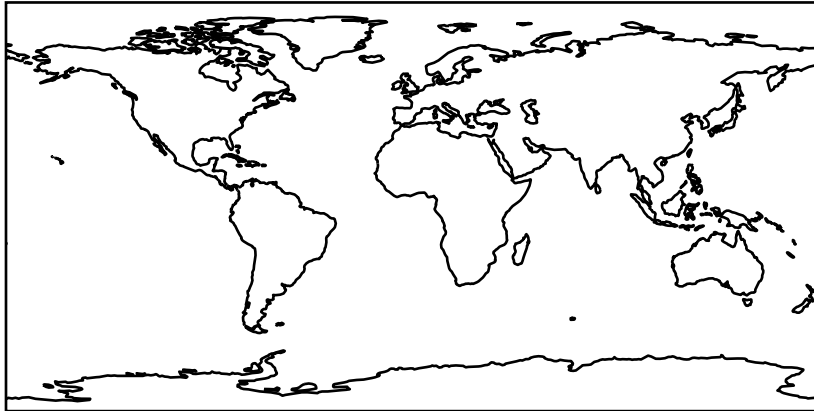
Ratio (1x1.25)
Dev/Ref, Fixed Range



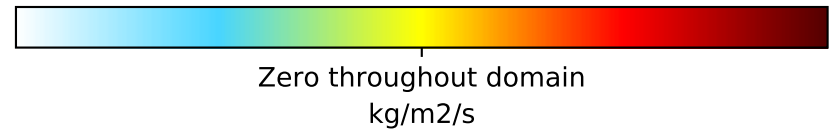
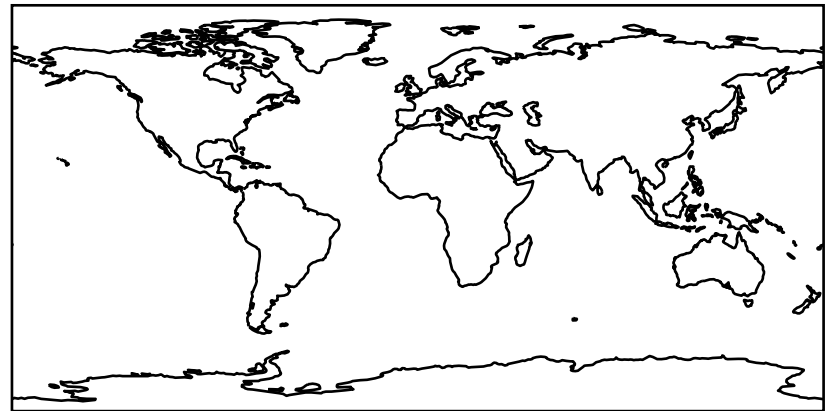
Undefined throughout domain
unitless

EmisHCOOH_Ship (Jul2019)

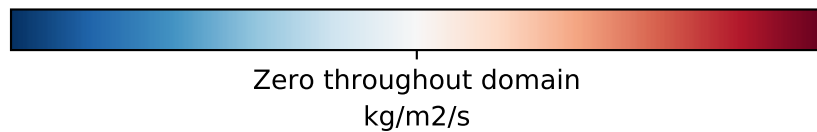
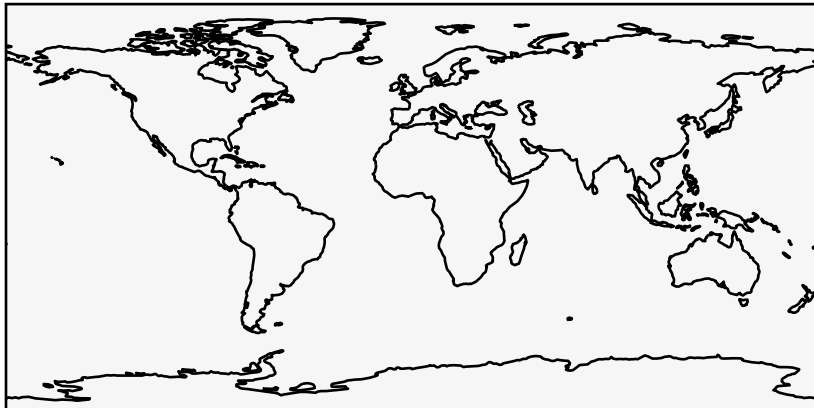
GCC_12.9.0 (Ref)
4.0x5.0



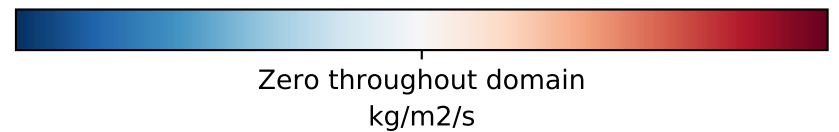
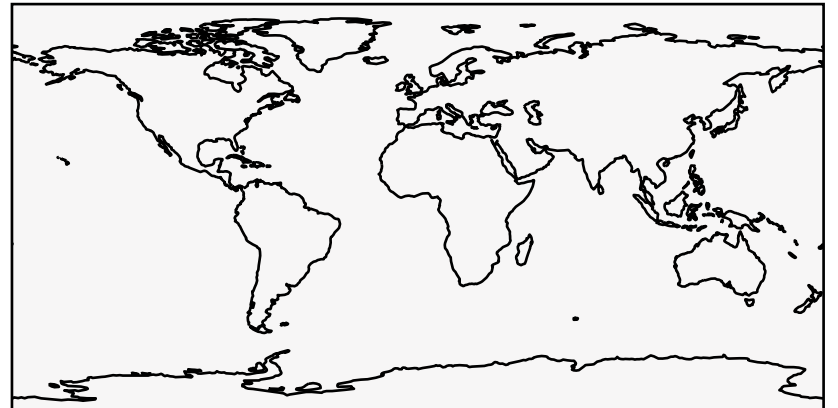
GCC_13.0.0-beta.1 (Dev)
c48



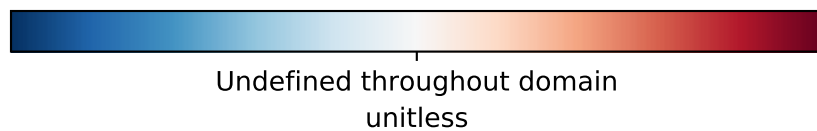
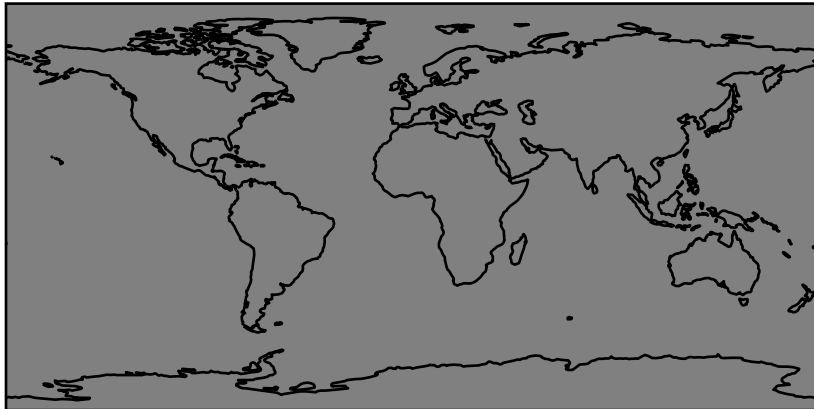
Difference (1x1.25)
Dev - Ref, Dynamic Range



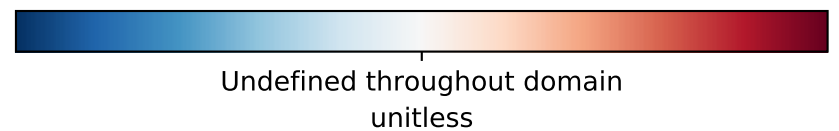
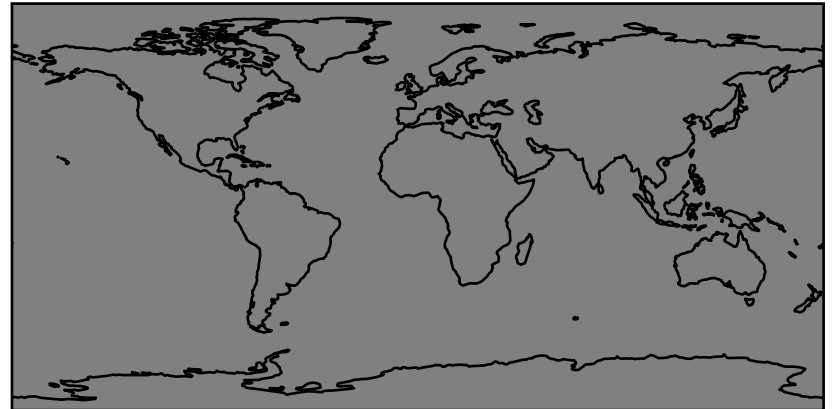
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

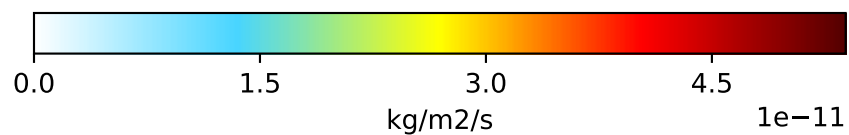
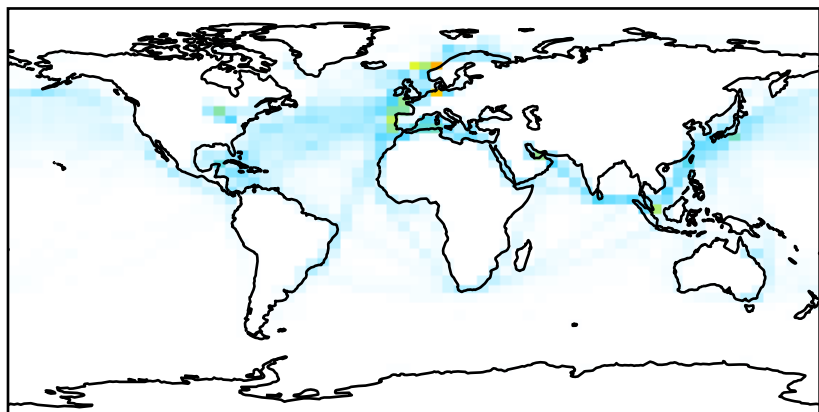


Ratio (1x1.25)
Dev/Ref, Fixed Range

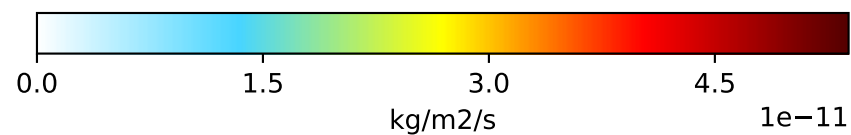
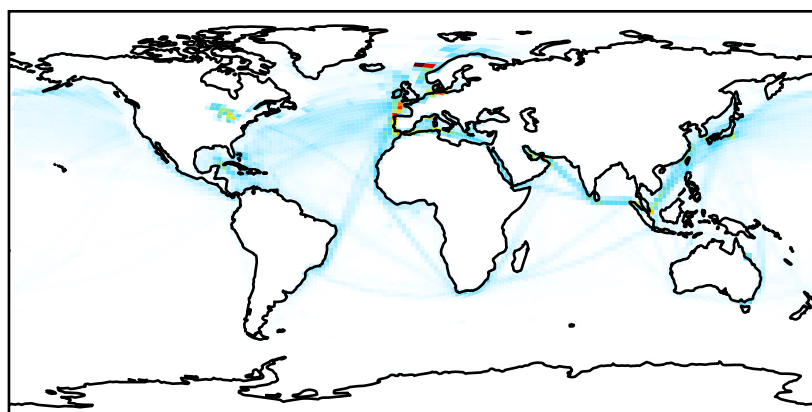


EmisHNO3_Ship (Jul2019)

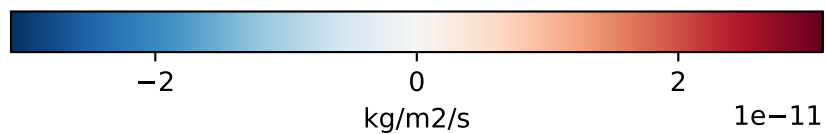
GCC_12.9.0 (Ref)
4.0x5.0



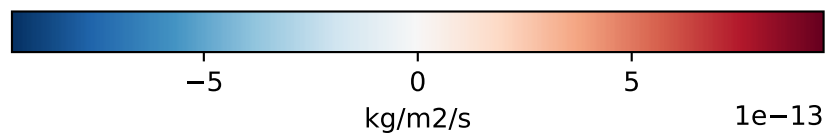
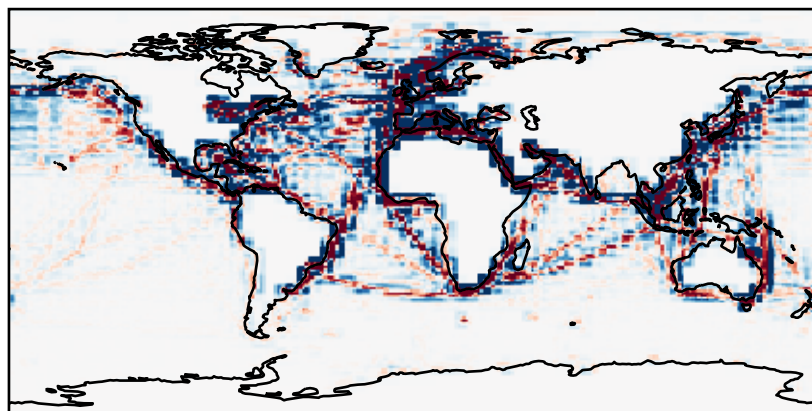
GCC_13.0.0-beta.1 (Dev)
c48



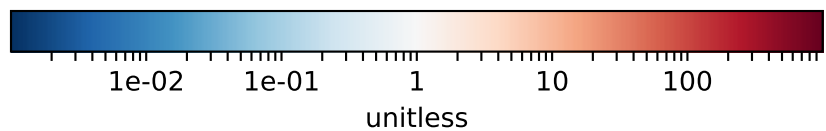
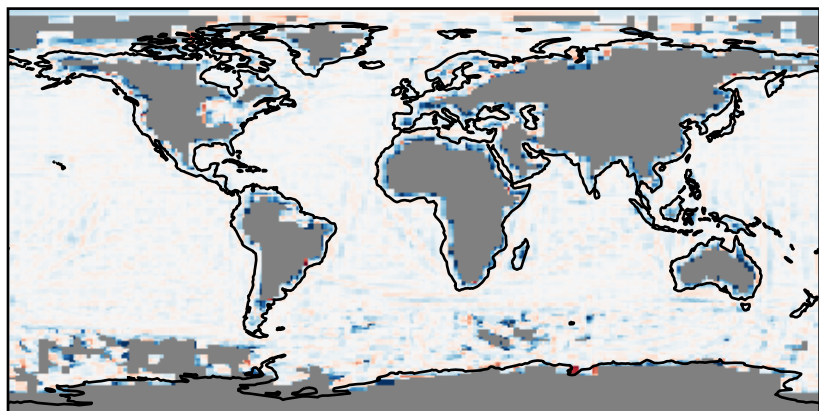
Difference (1x1.25)
Dev - Ref, Dynamic Range



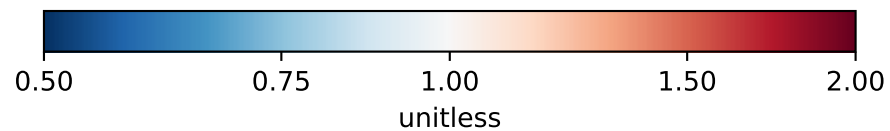
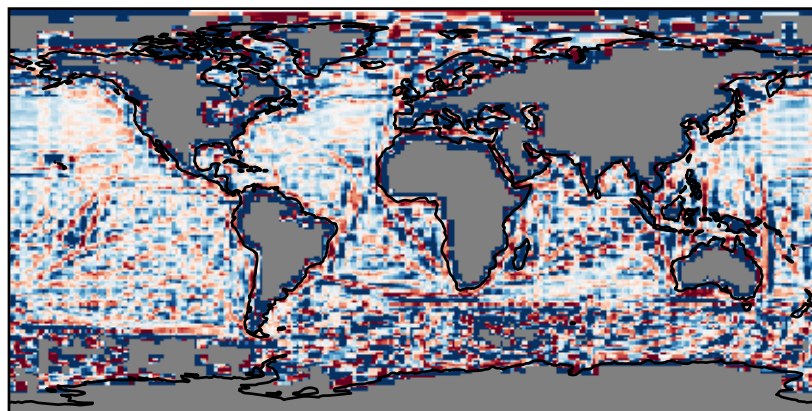
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

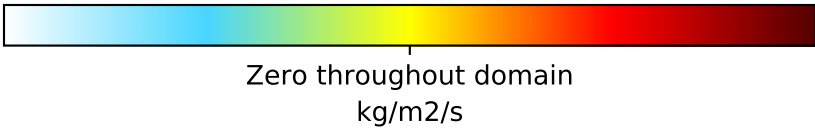
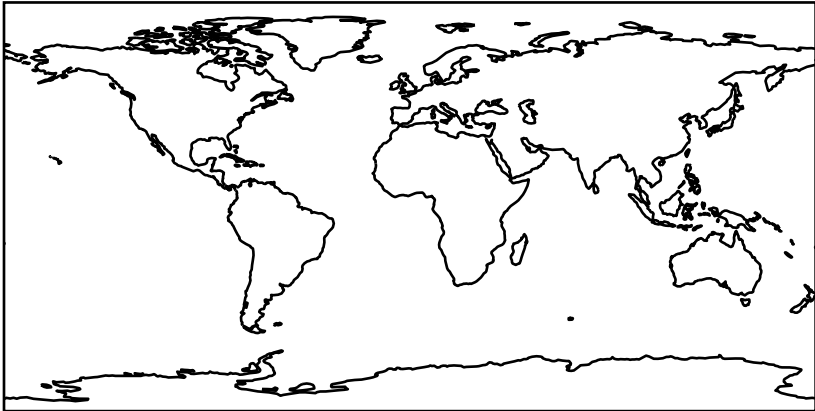


Ratio (1x1.25)
Dev/Ref, Fixed Range

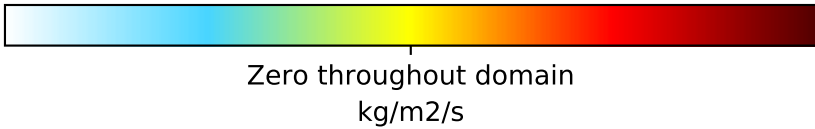
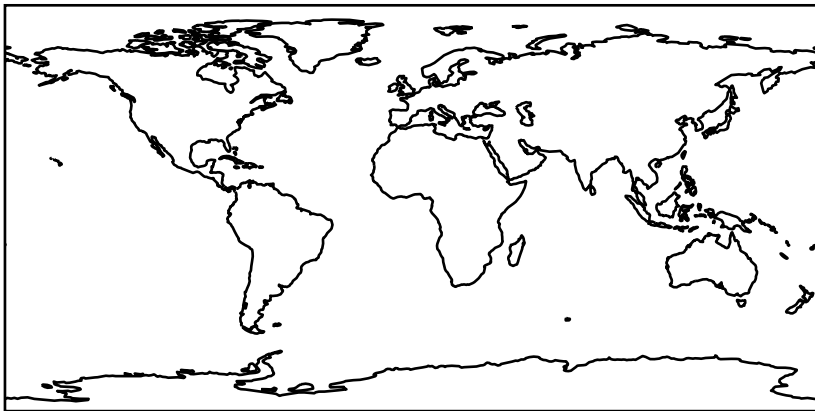


EmisMEK_Ship (Jul2019)

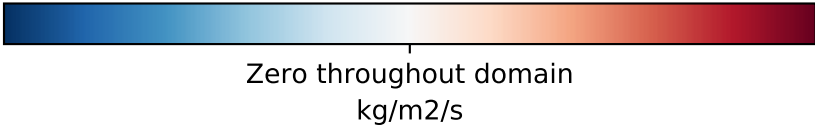
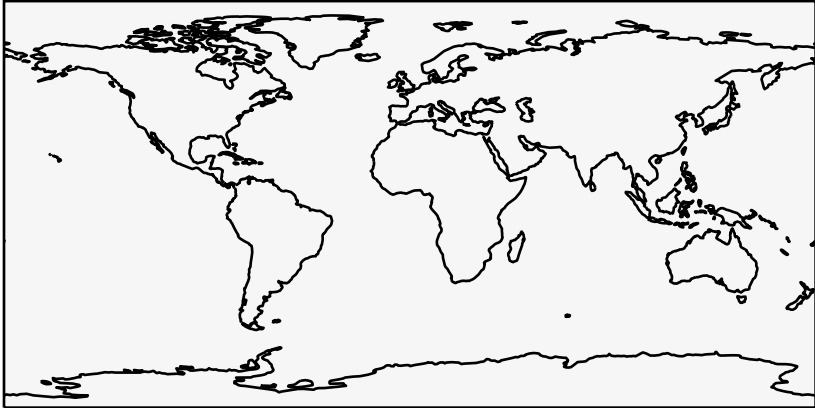
GCC_12.9.0 (Ref)
4.0x5.0



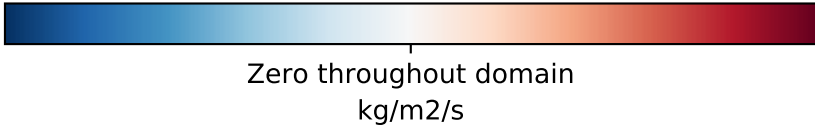
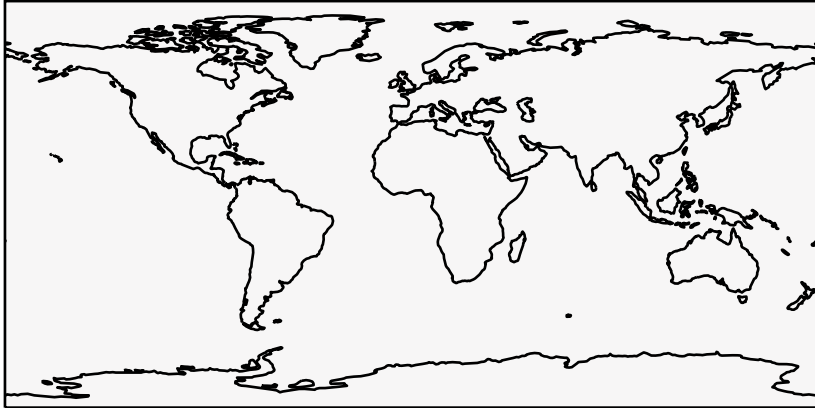
GCC_13.0.0-beta.1 (Dev)
c48



Difference (1x1.25)
Dev - Ref, Dynamic Range



Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

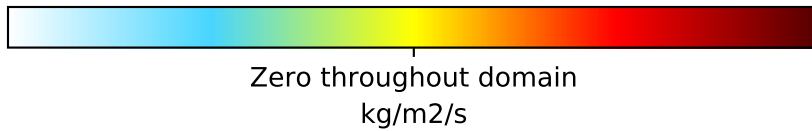


Ratio (1x1.25)
Dev/Ref, Fixed Range

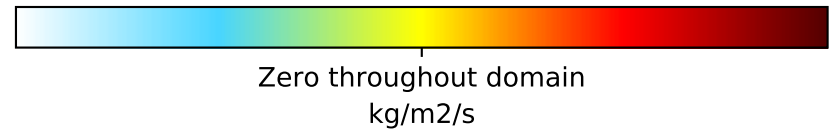
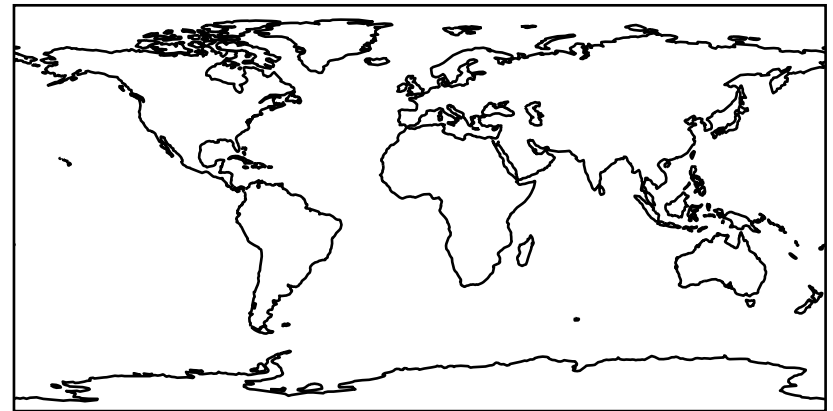


EmisMOH_Ship (Jul2019)

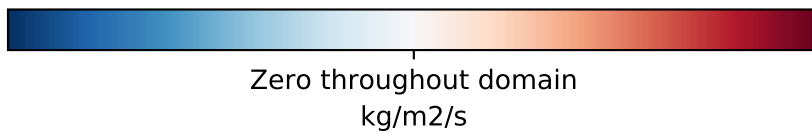
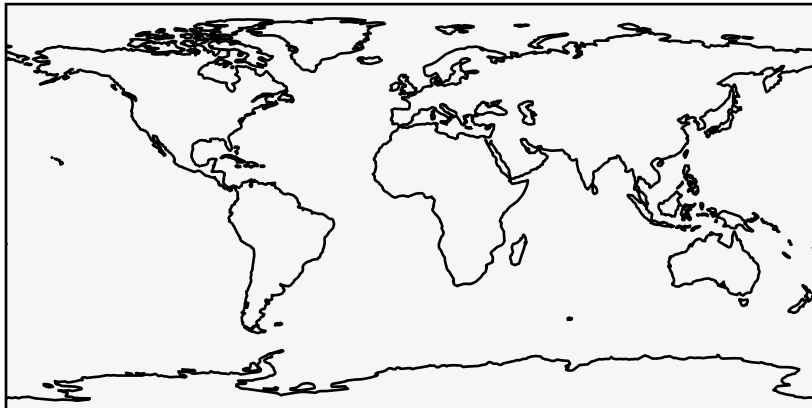
GCC_12.9.0 (Ref)
4.0x5.0



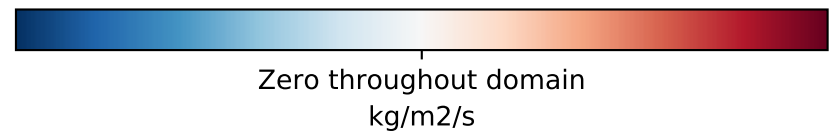
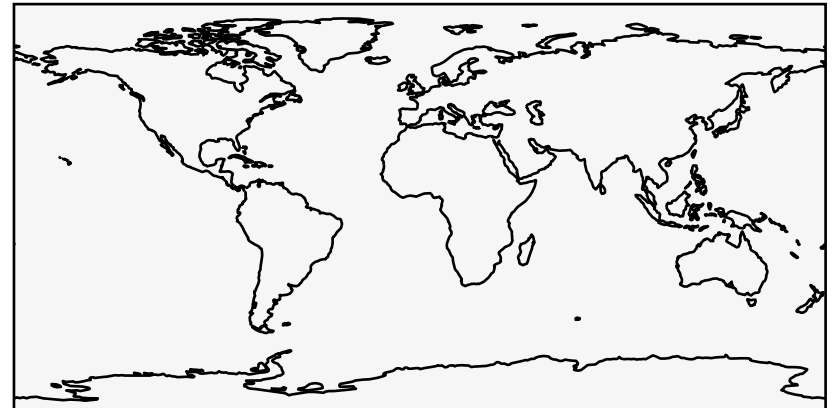
GCC_13.0.0-beta.1 (Dev)
c48



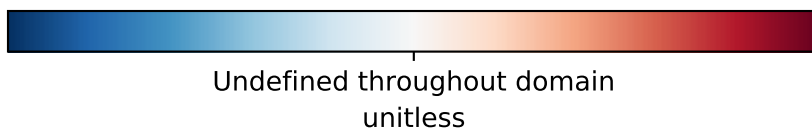
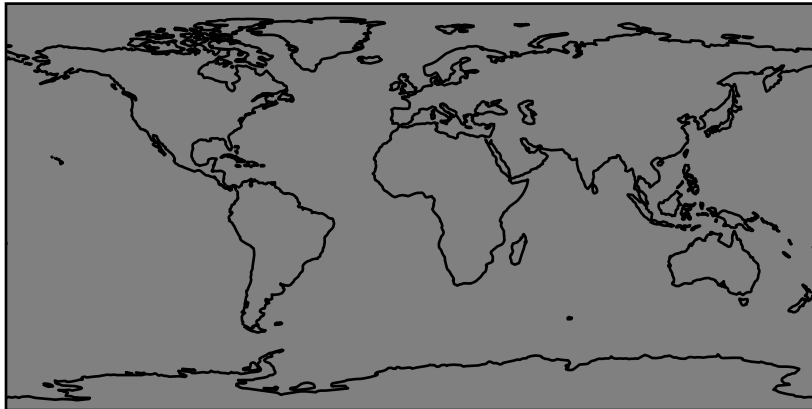
Difference (1x1.25)
Dev - Ref, Dynamic Range



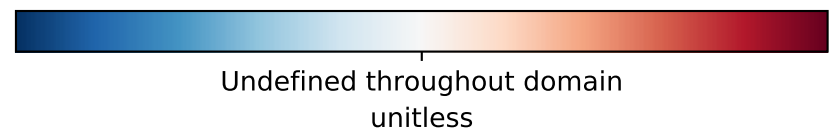
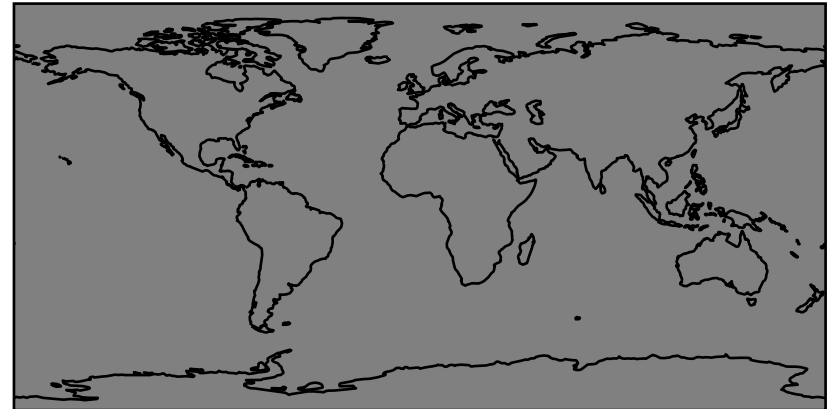
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

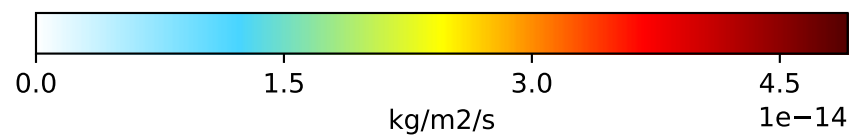
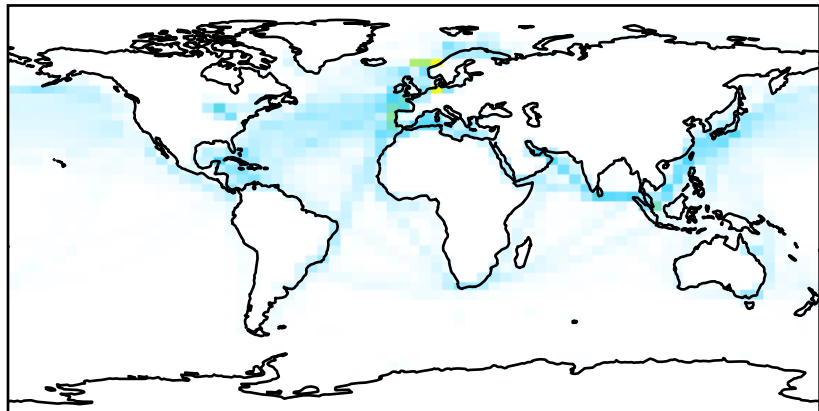


Ratio (1x1.25)
Dev/Ref, Fixed Range

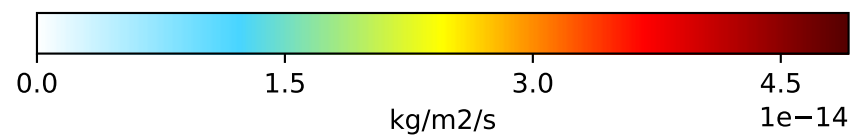
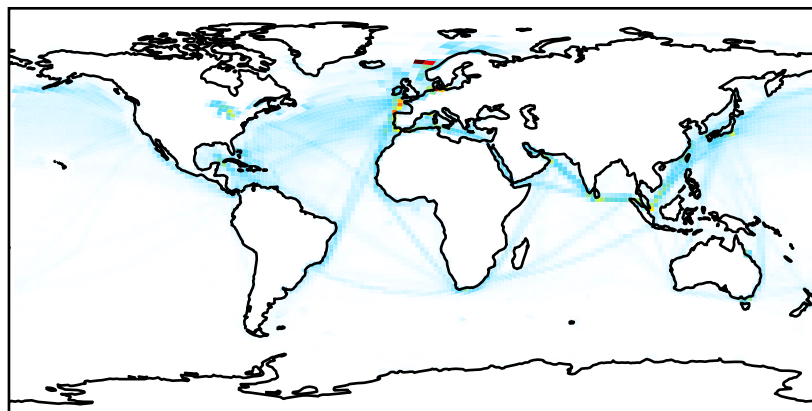


EmisNH3_Ship (Jul2019)

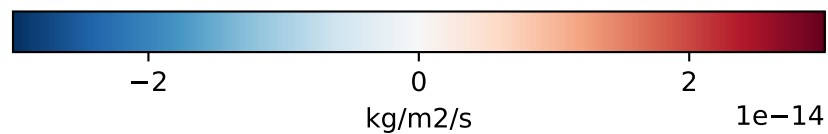
GCC_12.9.0 (Ref)
4.0x5.0



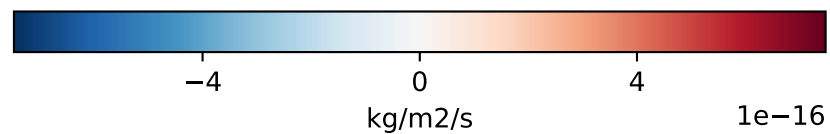
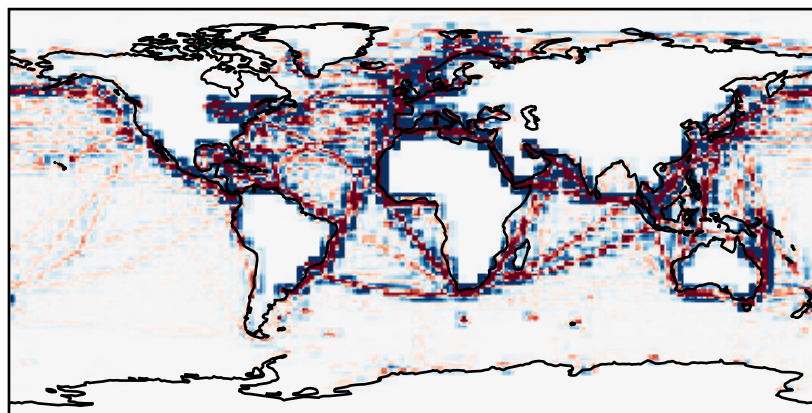
GCC_13.0.0-beta.1 (Dev)
c48



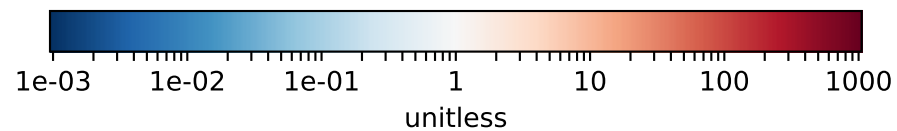
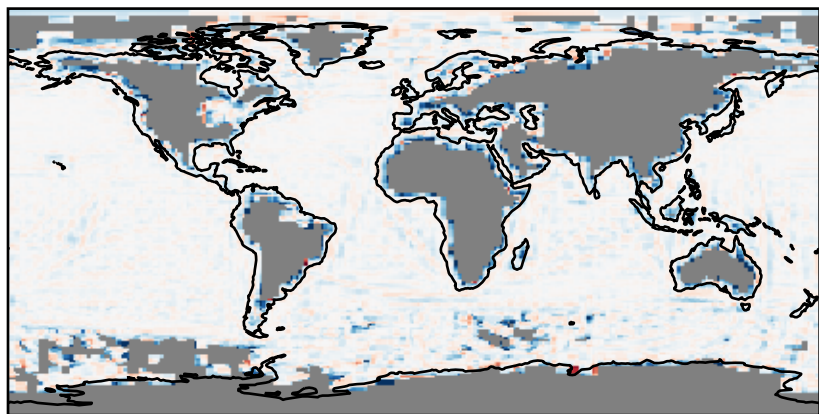
Difference (1x1.25)
Dev - Ref, Dynamic Range



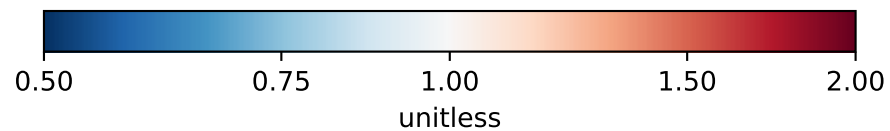
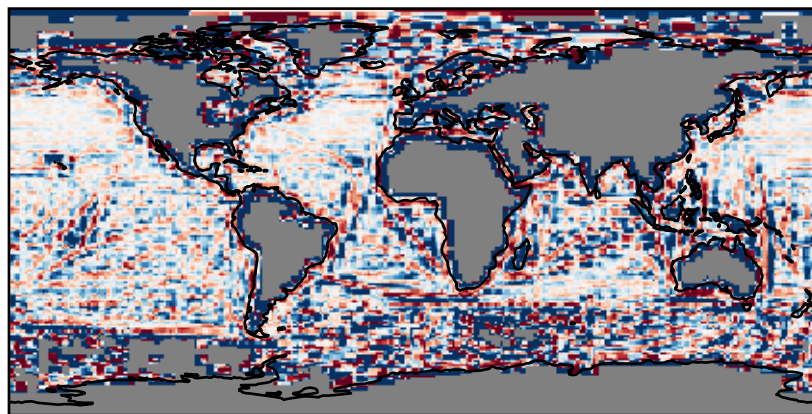
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

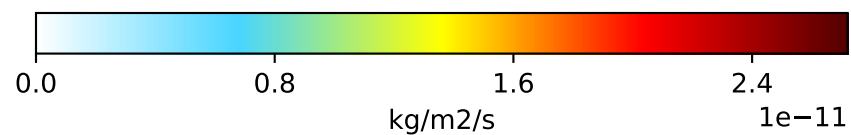
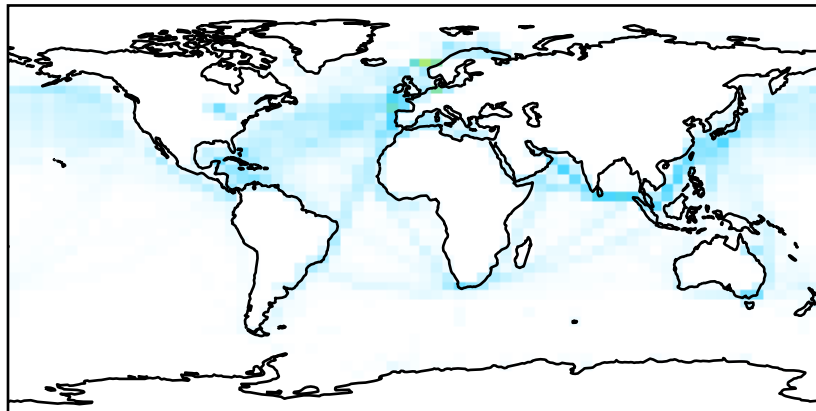


Ratio (1x1.25)
Dev/Ref, Fixed Range

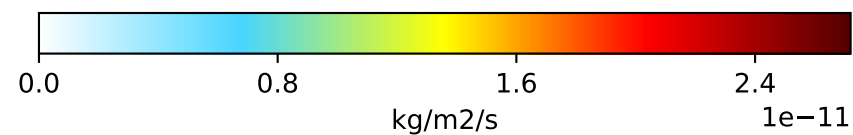
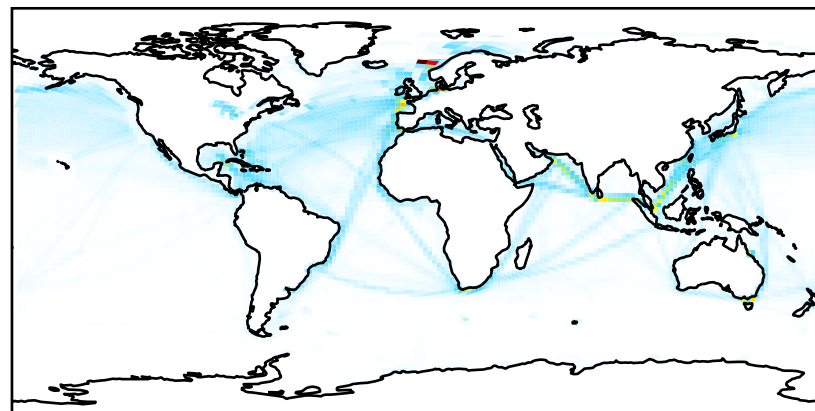


EmisNO2_Ship (Jul2019)

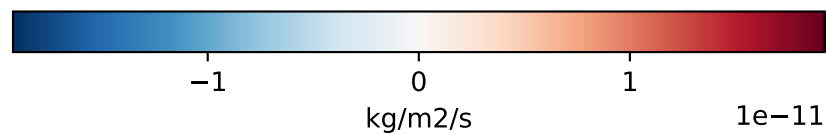
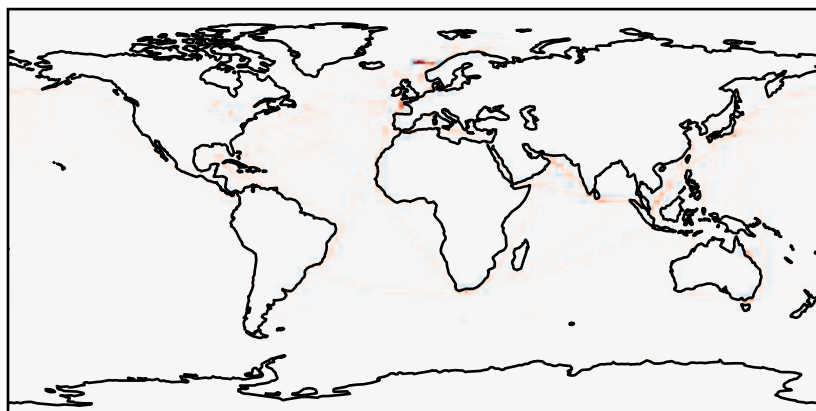
GCC_12.9.0 (Ref)
4.0x5.0



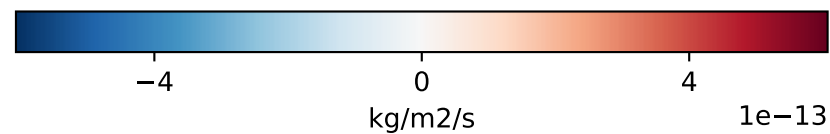
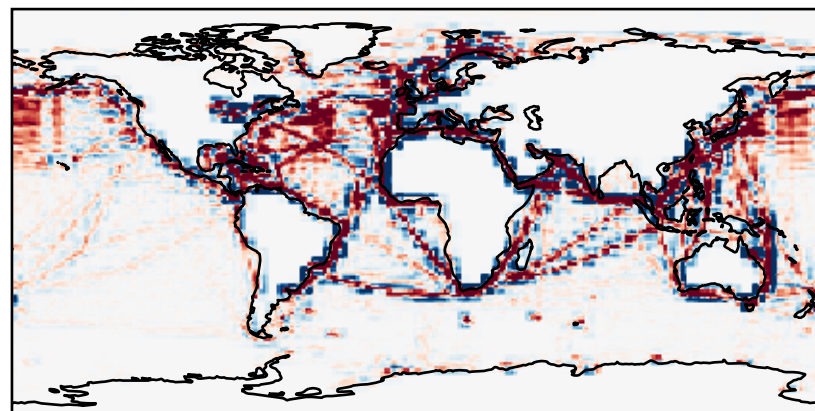
GCC_13.0.0-beta.1 (Dev)
c48



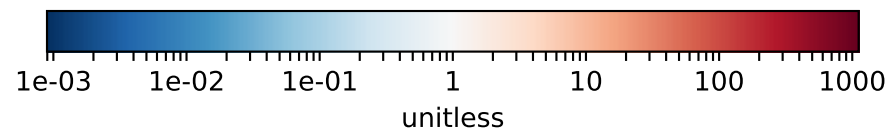
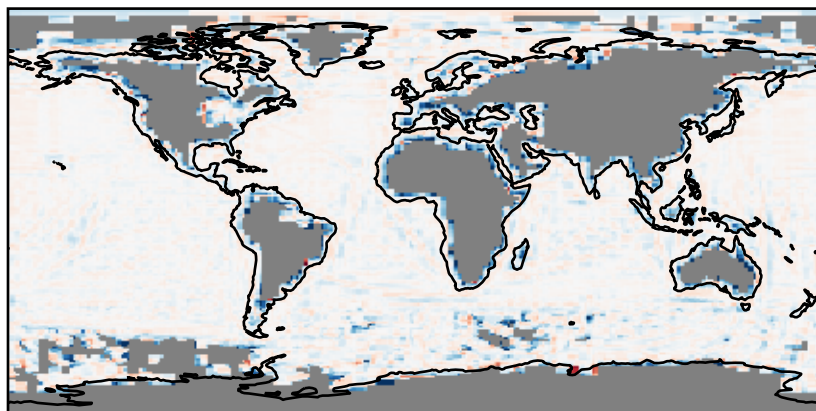
Difference (1x1.25)
Dev - Ref, Dynamic Range



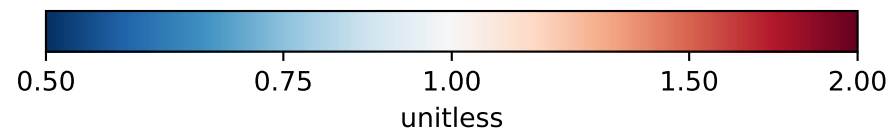
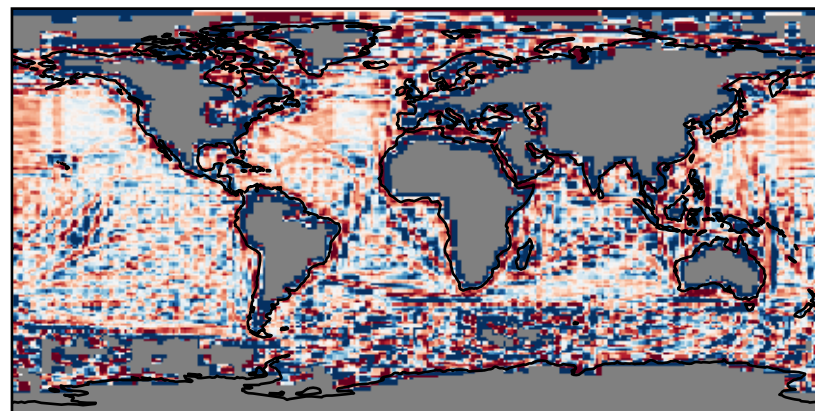
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

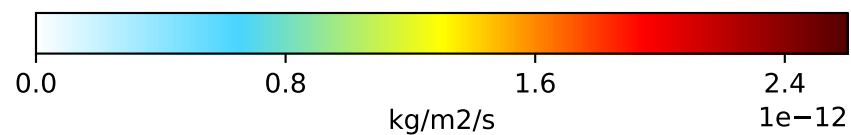
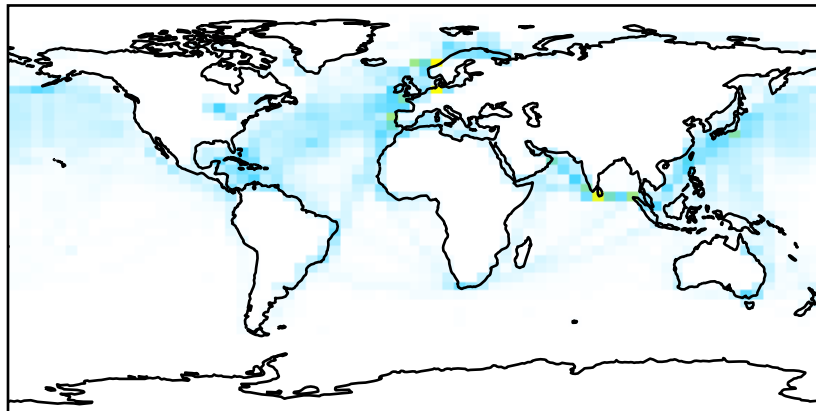


Ratio (1x1.25)
Dev/Ref, Fixed Range

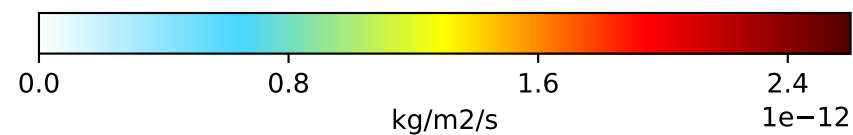
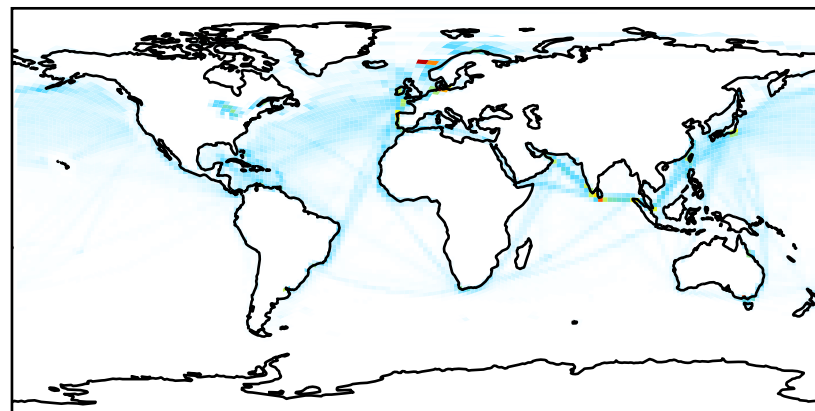


EmisNO_Ship (Jul2019)

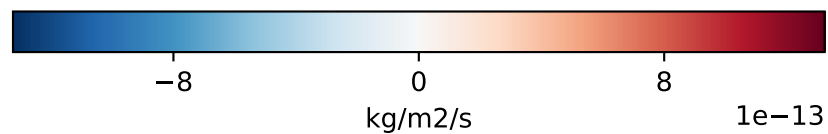
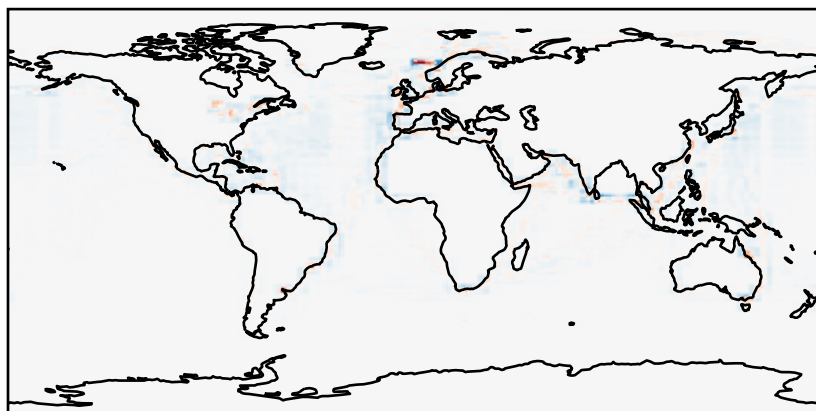
GCC_12.9.0 (Ref)
4.0x5.0



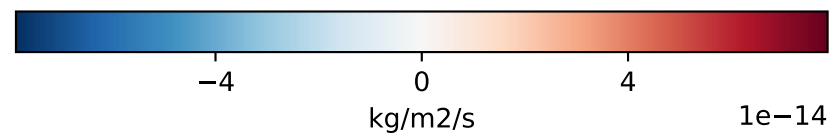
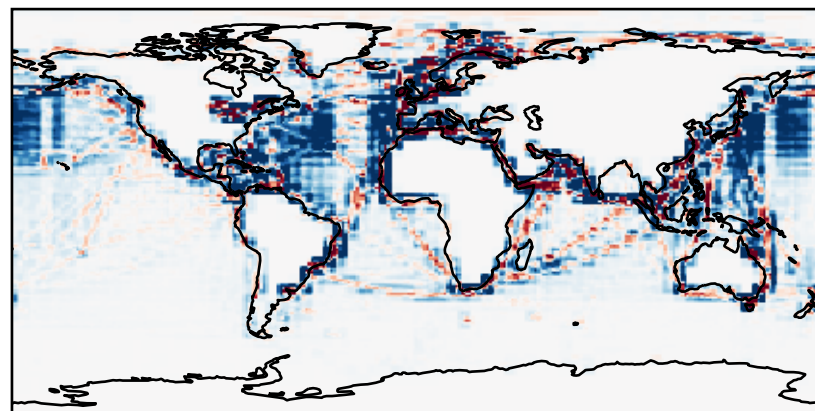
GCC_13.0.0-beta.1 (Dev)
c48



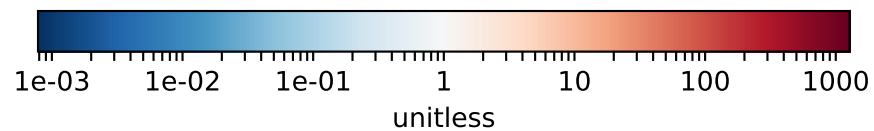
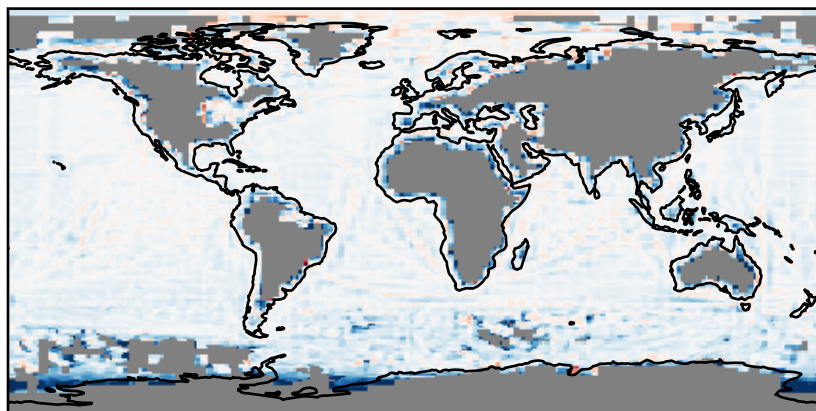
Difference (1x1.25)
Dev - Ref, Dynamic Range



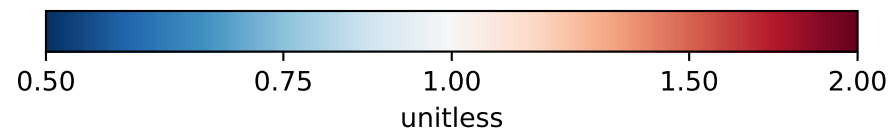
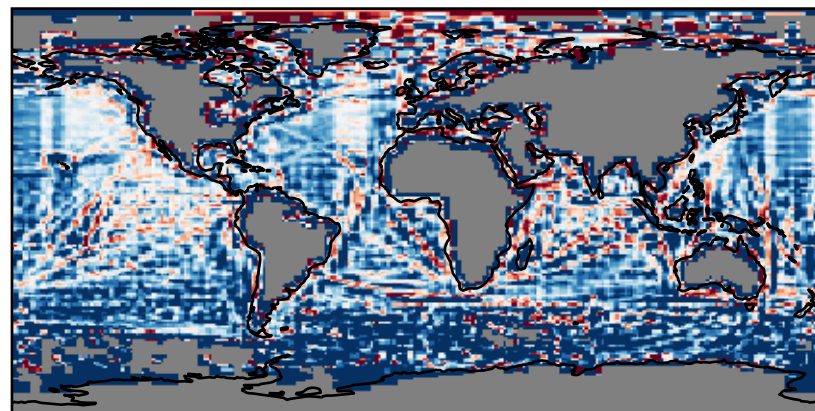
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

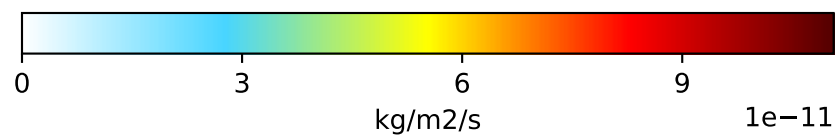
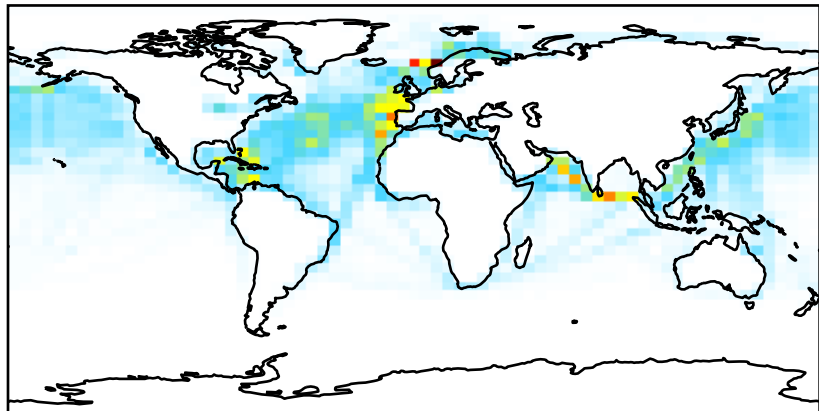


Ratio (1x1.25)
Dev/Ref, Fixed Range

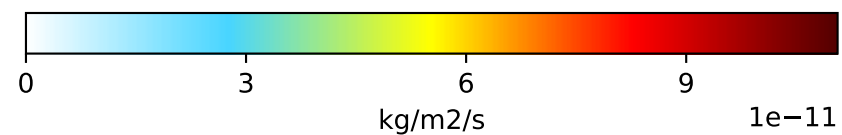
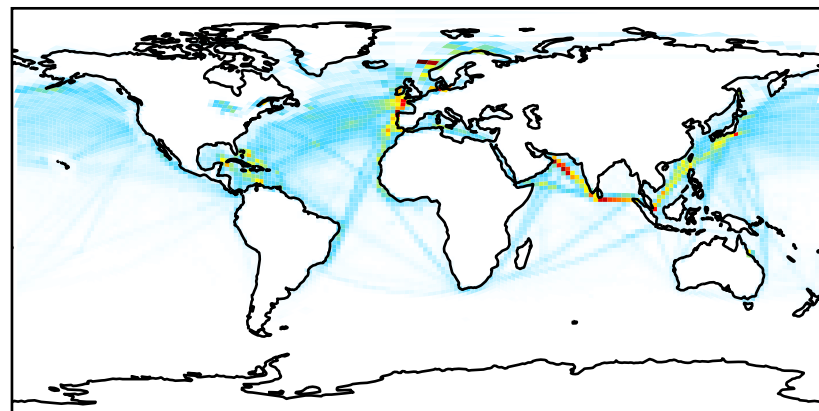


EmisO3_Ship (Jul2019)

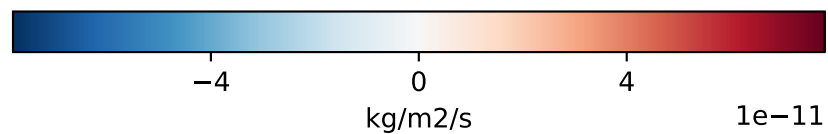
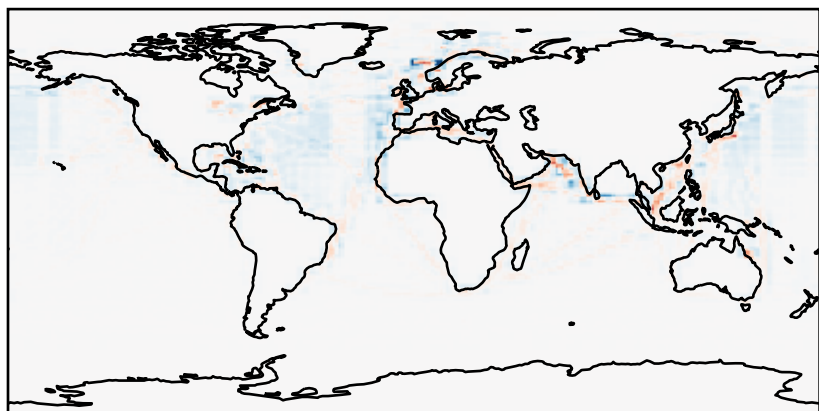
GCC_12.9.0 (Ref)
4.0x5.0



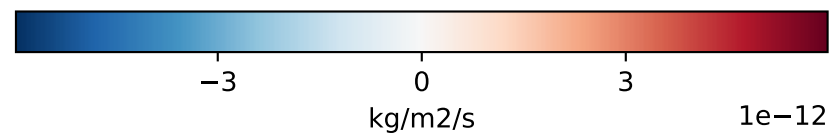
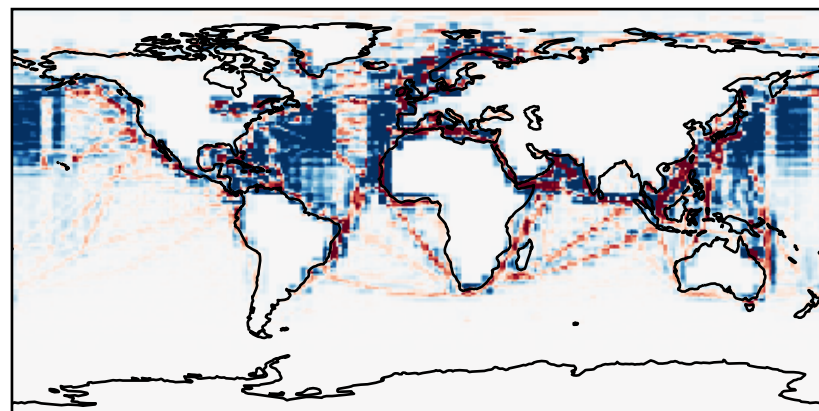
GCC_13.0.0-beta.1 (Dev)
c48



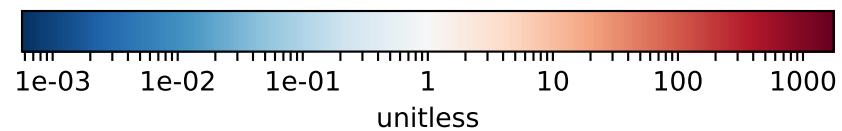
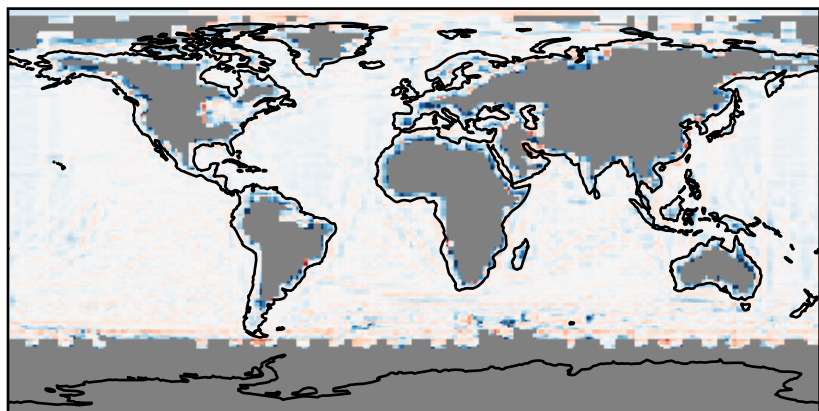
Difference (1x1.25)
Dev - Ref, Dynamic Range



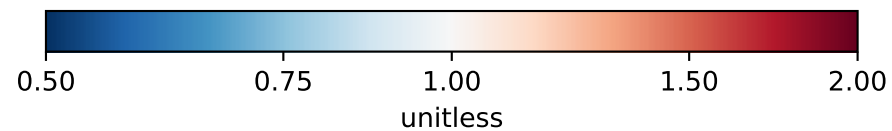
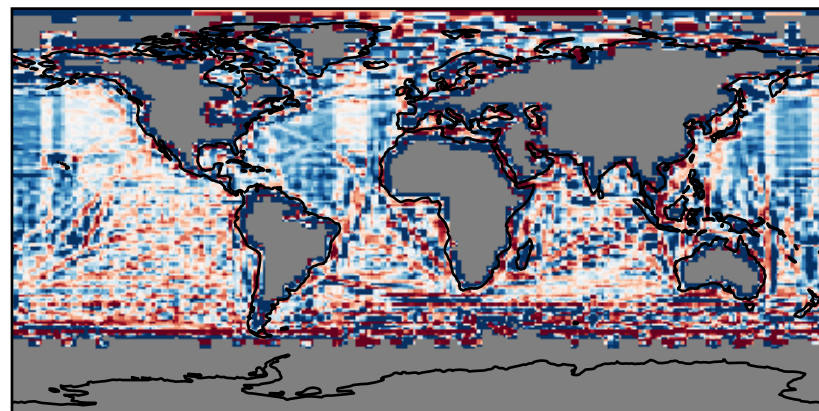
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

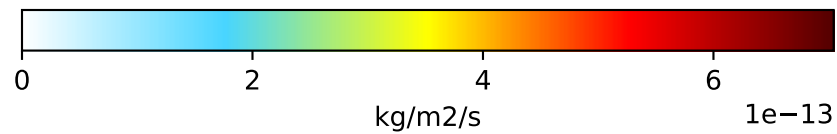
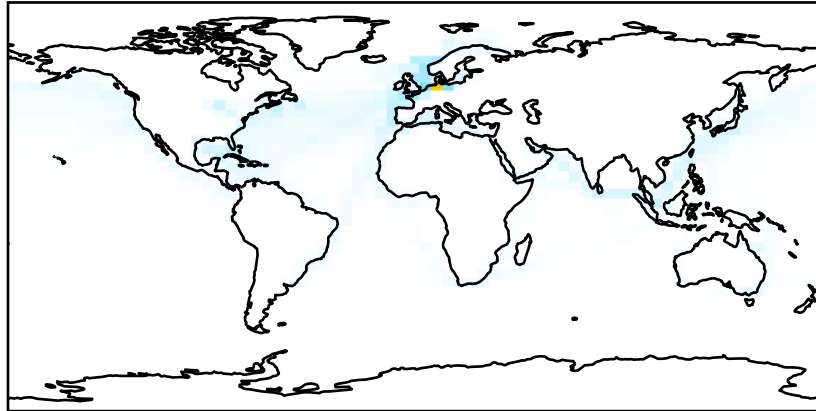


Ratio (1x1.25)
Dev/Ref, Fixed Range

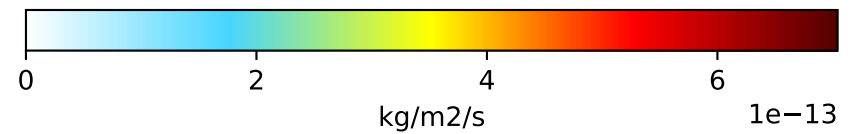
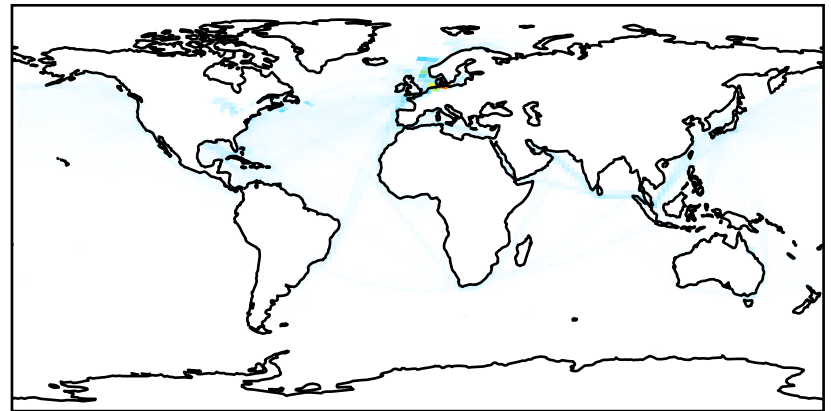


EmisOCPI_Ship (Jul2019)

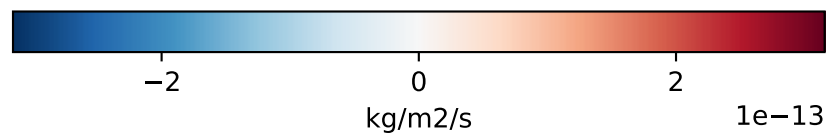
GCC_12.9.0 (Ref)
4.0x5.0



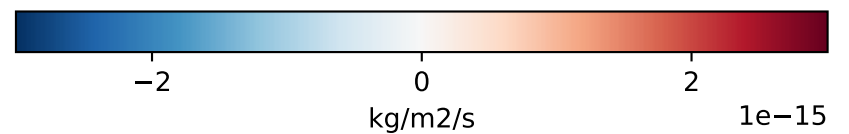
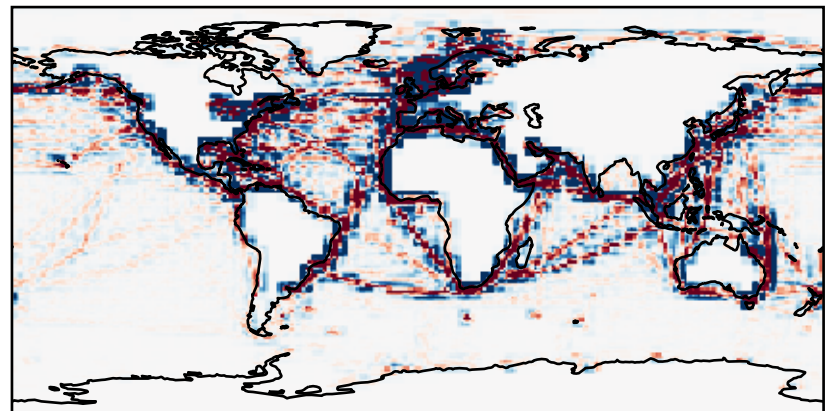
GCC_13.0.0-beta.1 (Dev)
c48



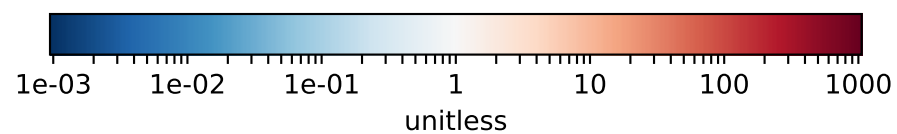
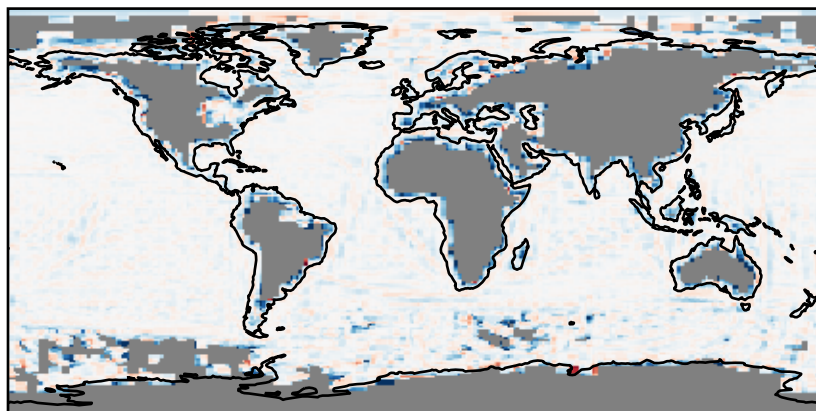
Difference (1x1.25)
Dev - Ref, Dynamic Range



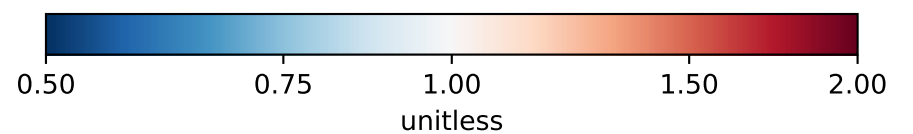
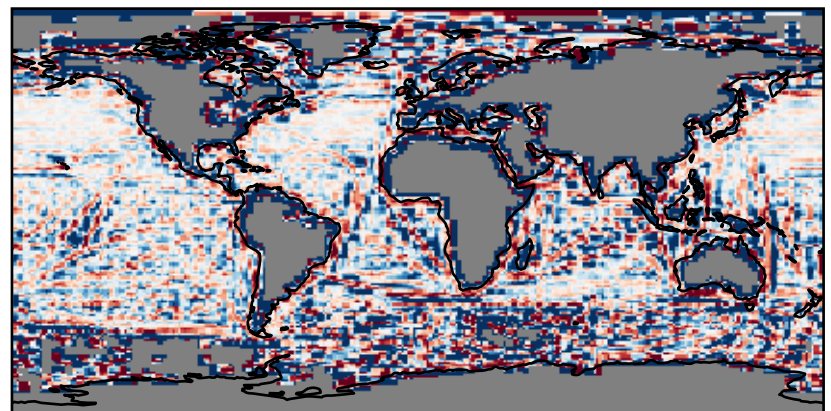
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

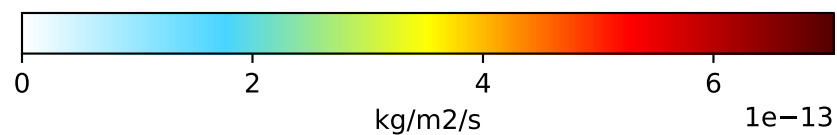
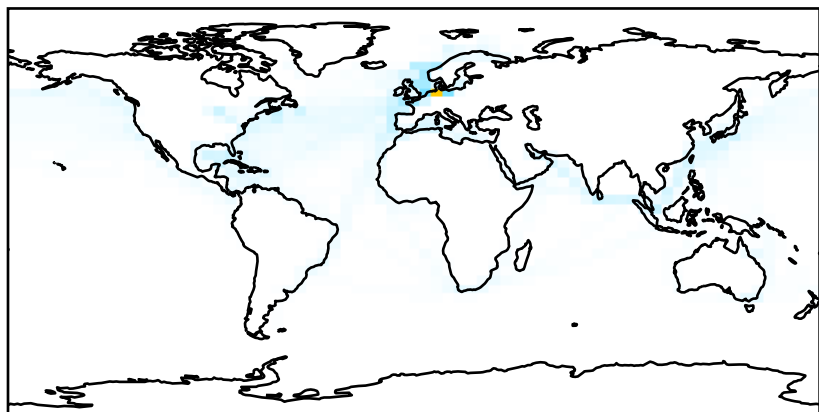


Ratio (1x1.25)
Dev/Ref, Fixed Range

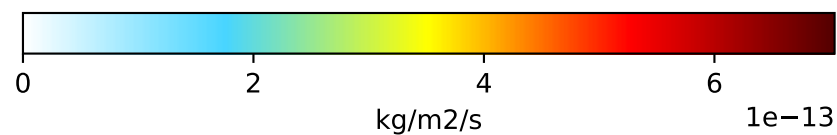
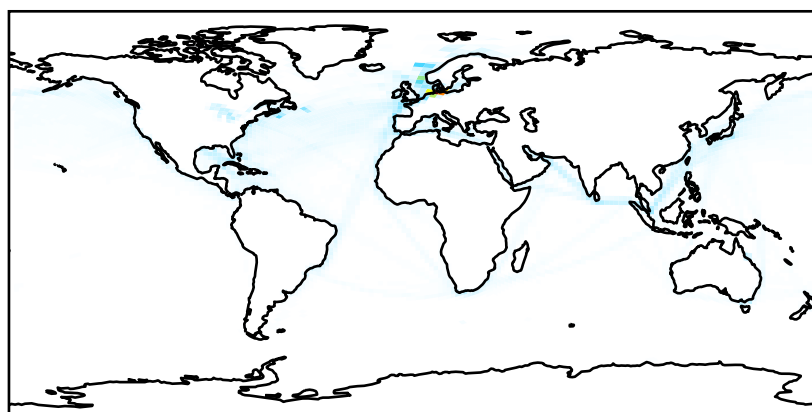


EmisOCPO_Ship (Jul2019)

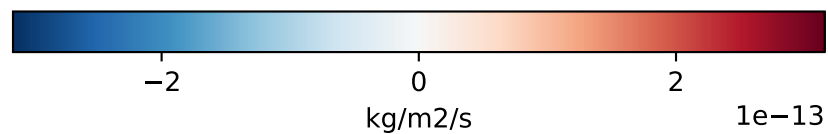
GCC_12.9.0 (Ref)
4.0x5.0



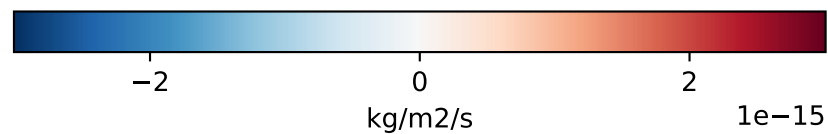
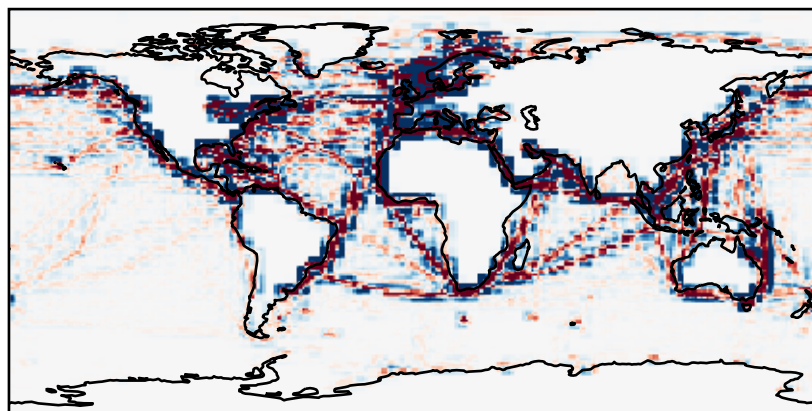
GCC_13.0.0-beta.1 (Dev)
c48



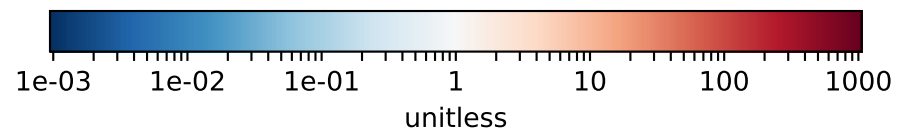
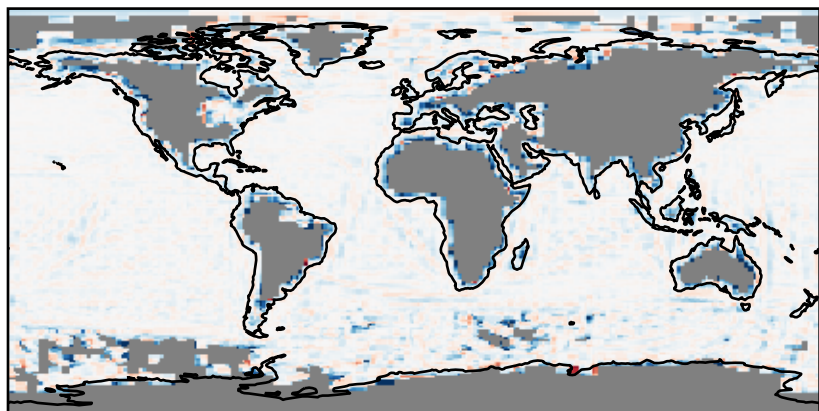
Difference (1x1.25)
Dev - Ref, Dynamic Range



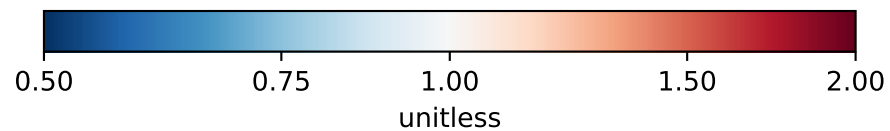
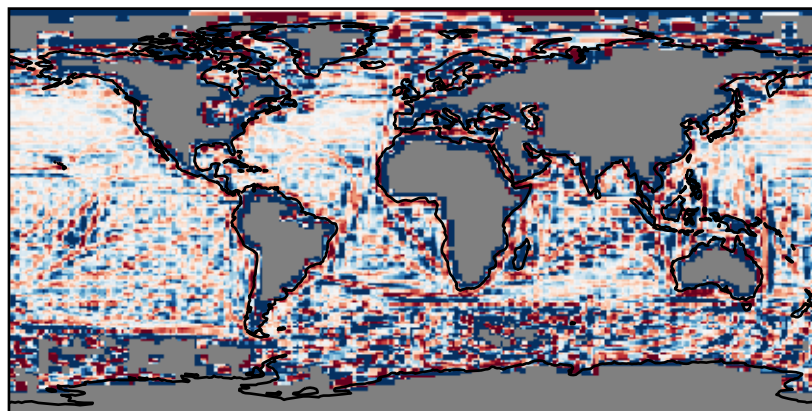
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

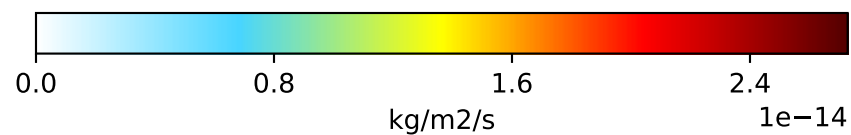
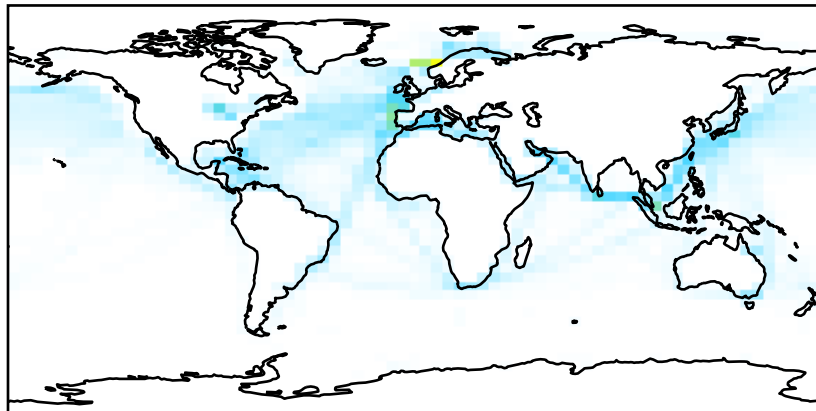


Ratio (1x1.25)
Dev/Ref, Fixed Range

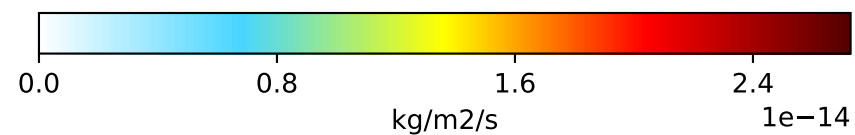
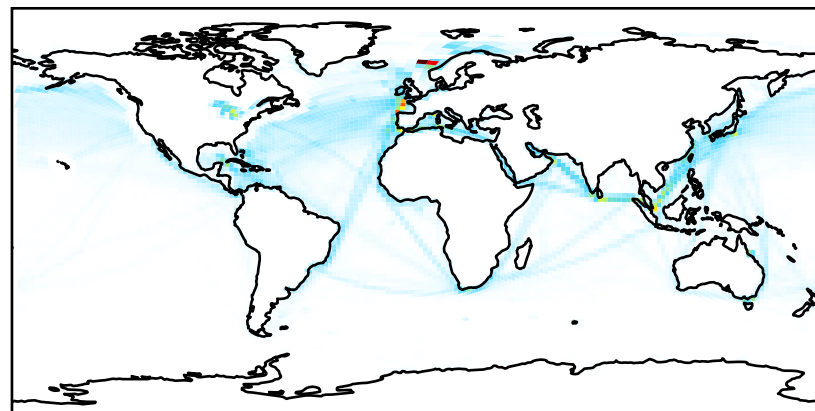


EmispFe_Ship (Jul2019)

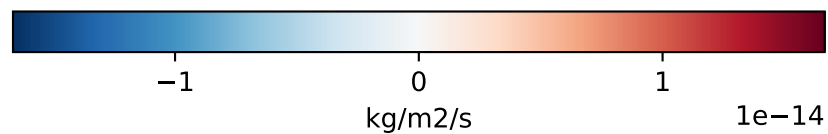
GCC_12.9.0 (Ref)
4.0x5.0



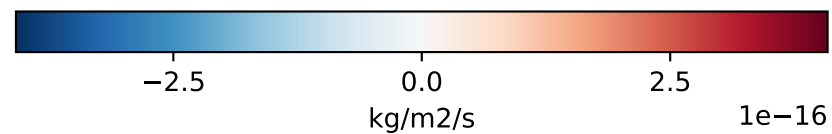
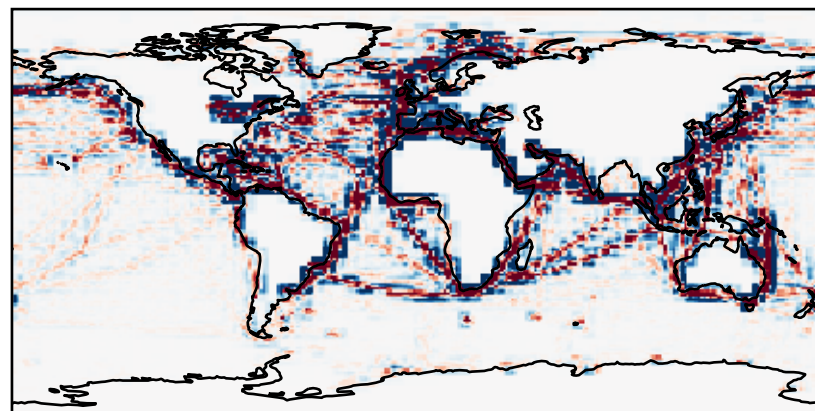
GCC_13.0.0-beta.1 (Dev)
c48



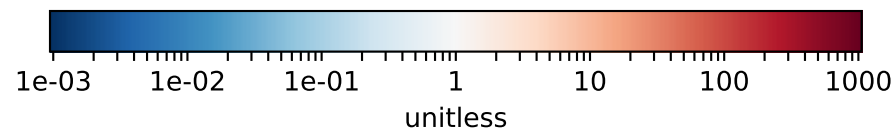
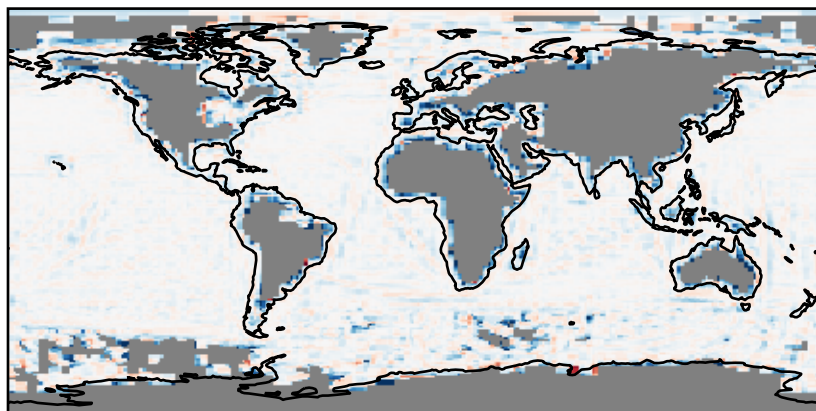
Difference (1x1.25)
Dev - Ref, Dynamic Range



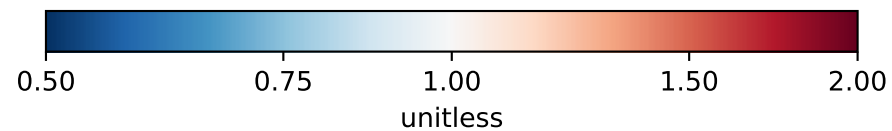
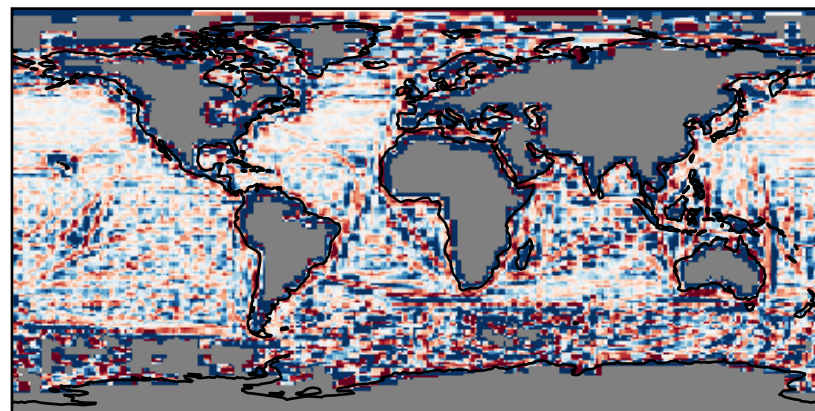
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

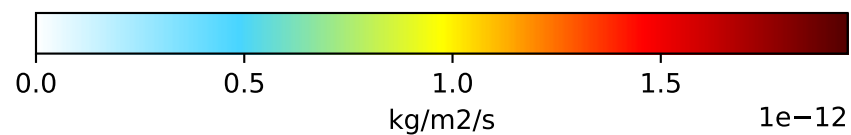
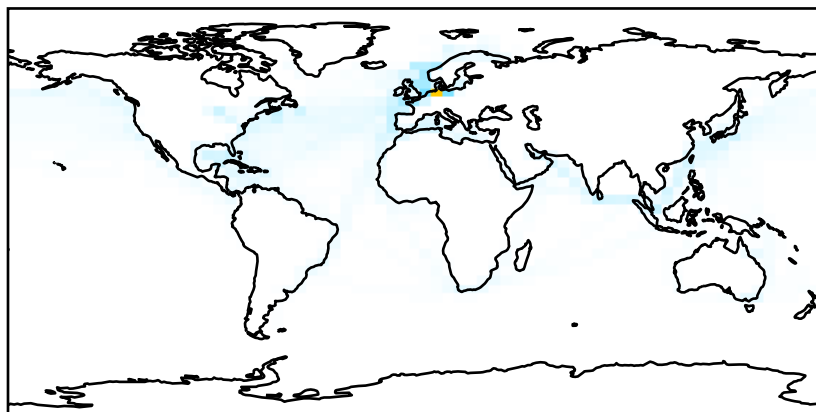


Ratio (1x1.25)
Dev/Ref, Fixed Range

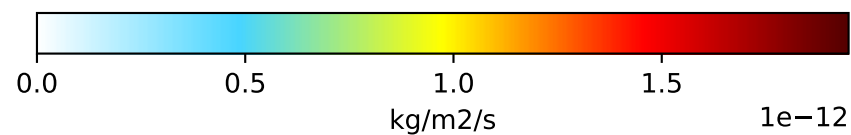
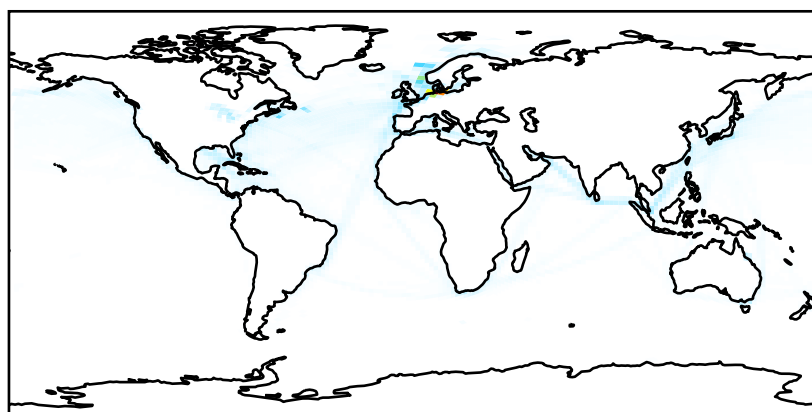


EmisPRPE_Ship (Jul2019)

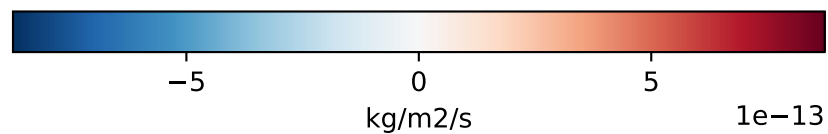
GCC_12.9.0 (Ref)
4.0x5.0



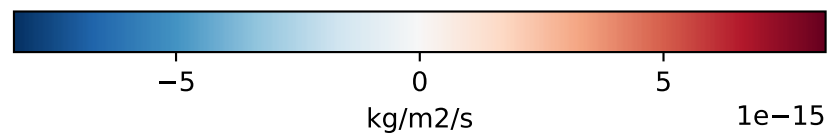
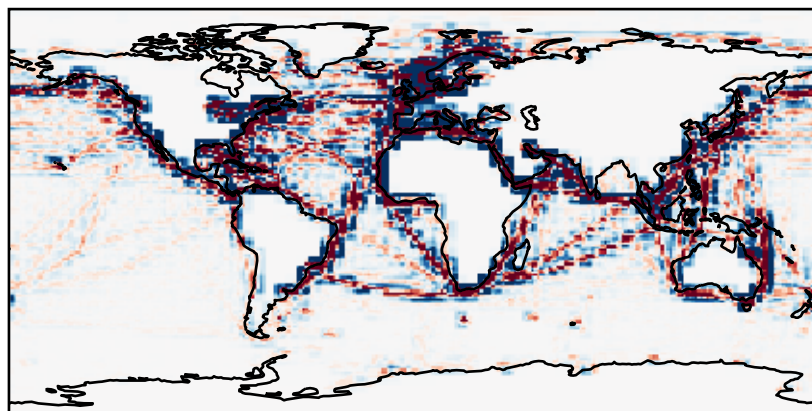
GCC_13.0.0-beta.1 (Dev)
c48



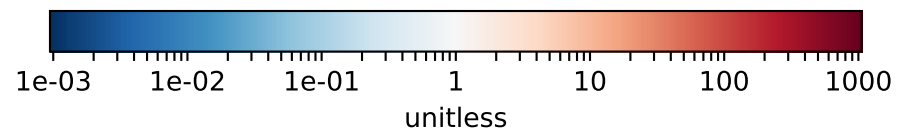
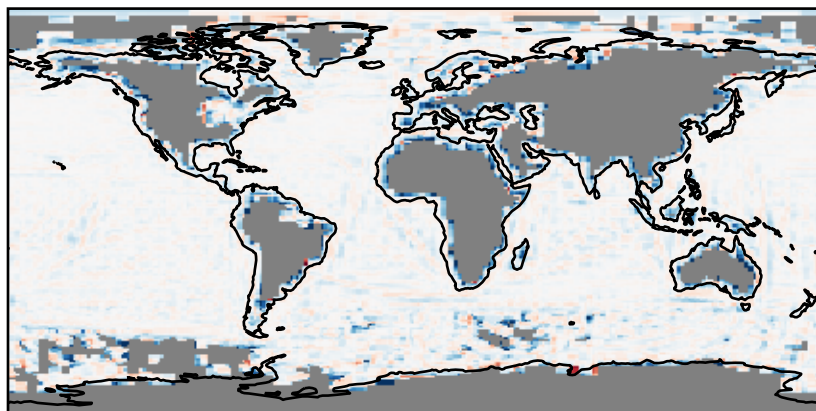
Difference (1x1.25)
Dev - Ref, Dynamic Range



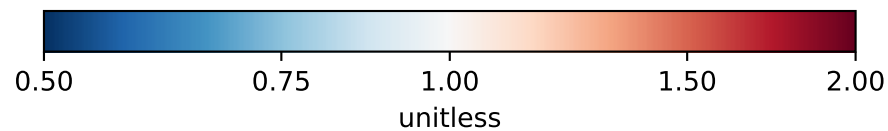
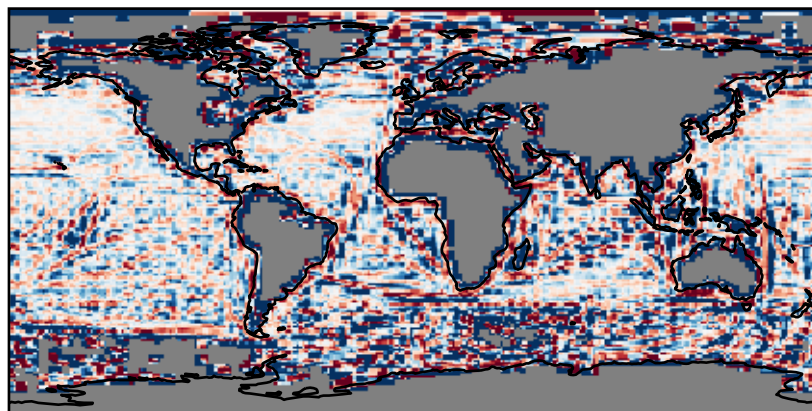
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

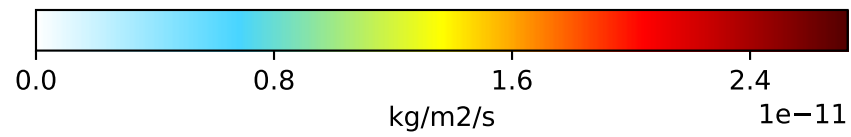
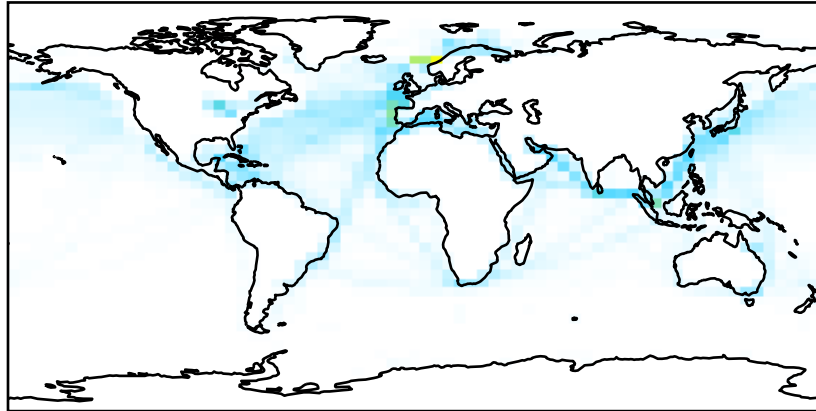


Ratio (1x1.25)
Dev/Ref, Fixed Range

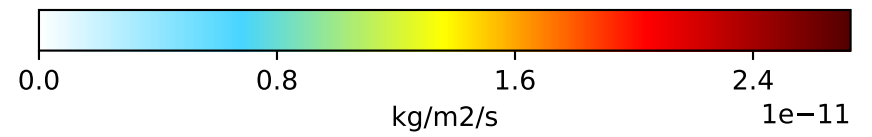
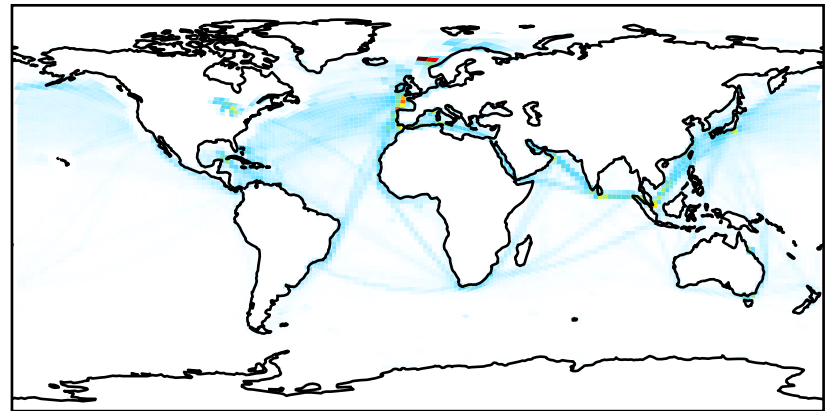


EmisSO2_Ship (Jul2019)

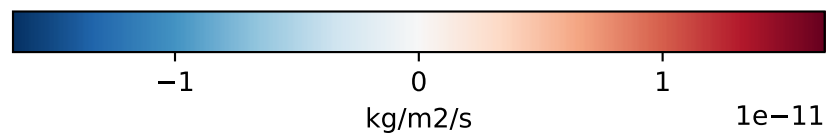
GCC_12.9.0 (Ref)
4.0x5.0



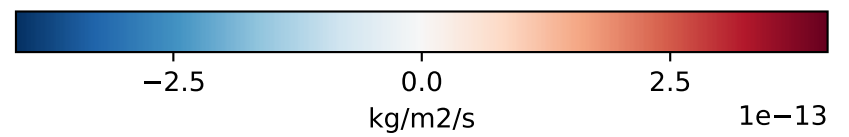
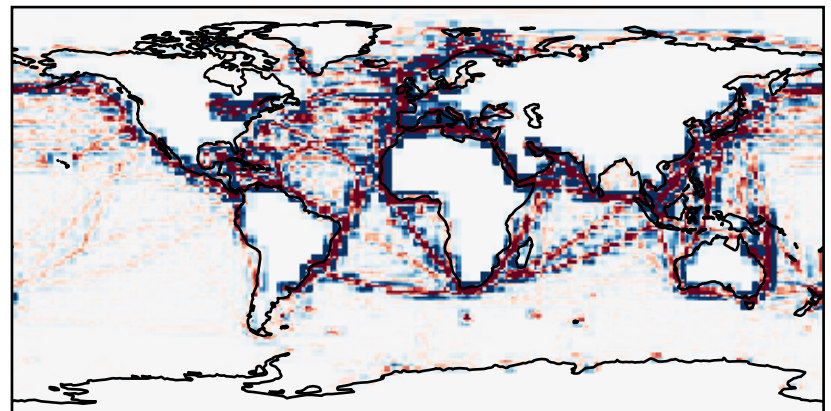
GCC_13.0.0-beta.1 (Dev)
c48



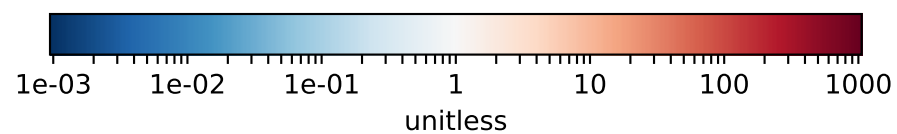
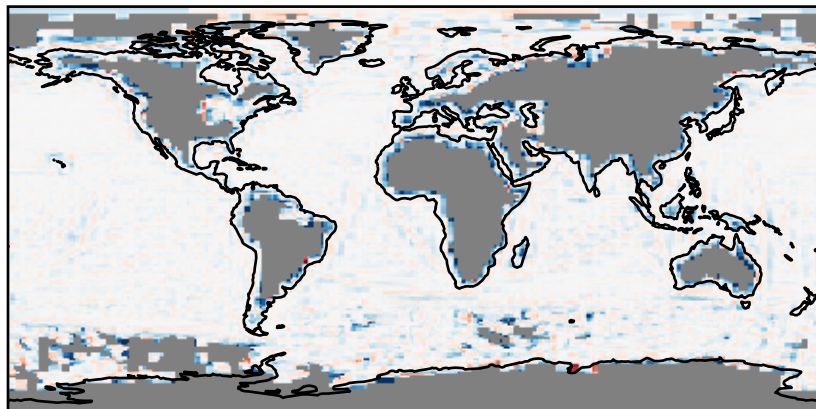
Difference (1x1.25)
Dev - Ref, Dynamic Range



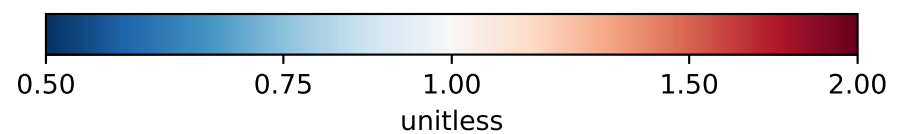
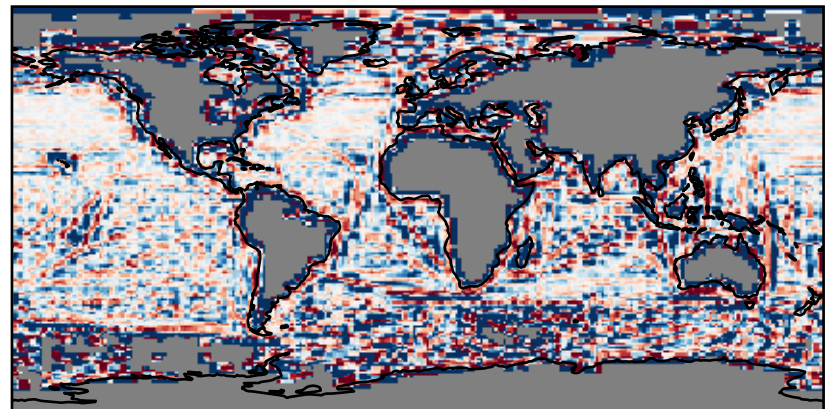
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

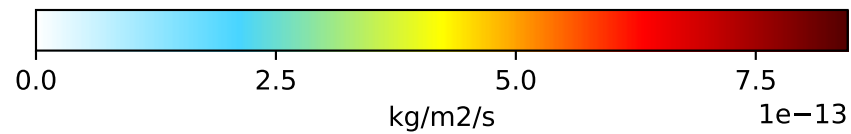
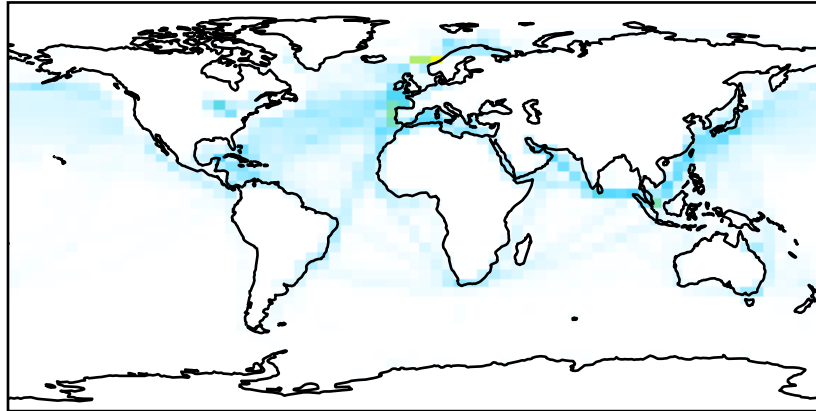


Ratio (1x1.25)
Dev/Ref, Fixed Range

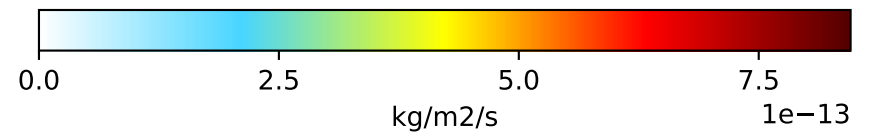
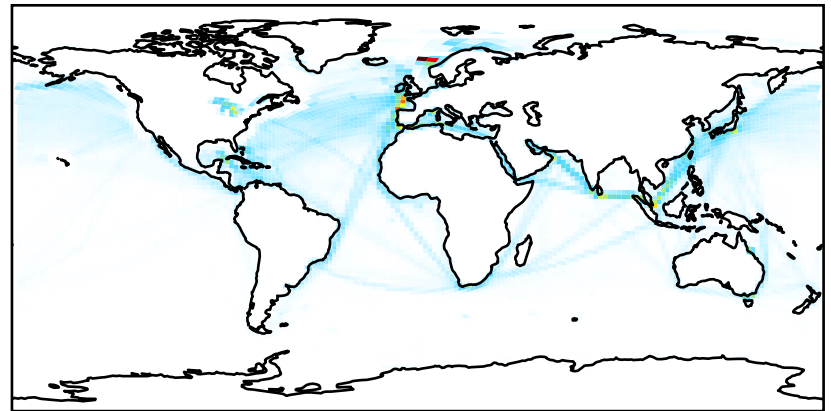


EmisSO4_Ship (Jul2019)

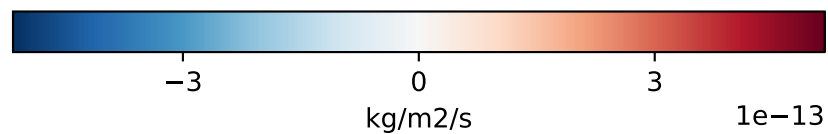
GCC_12.9.0 (Ref)
4.0x5.0



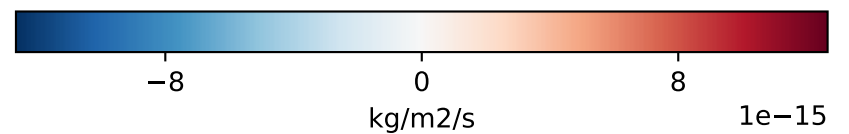
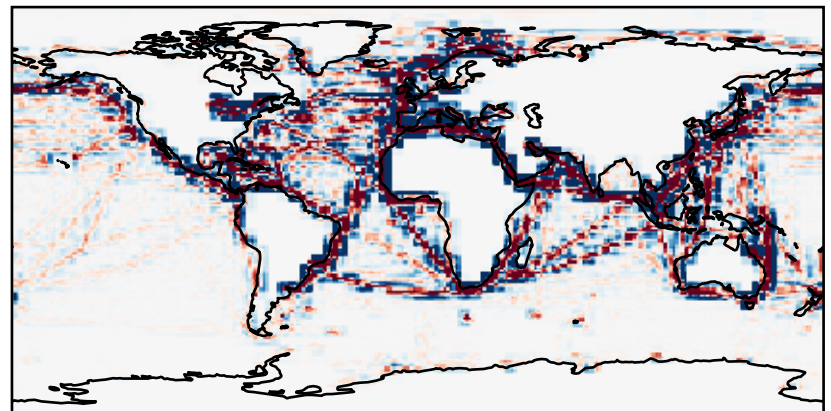
GCC_13.0.0-beta.1 (Dev)
c48



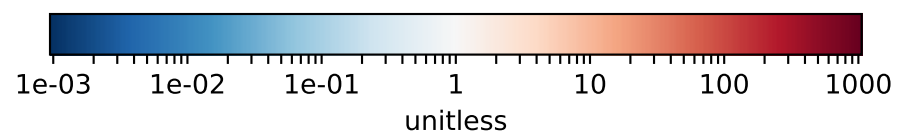
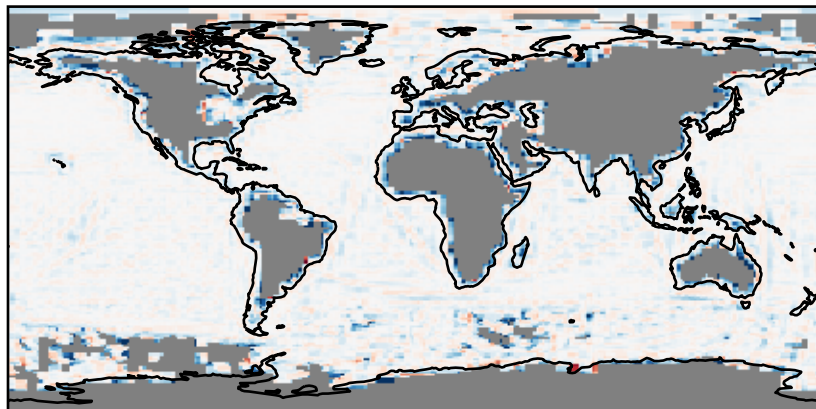
Difference (1x1.25)
Dev - Ref, Dynamic Range



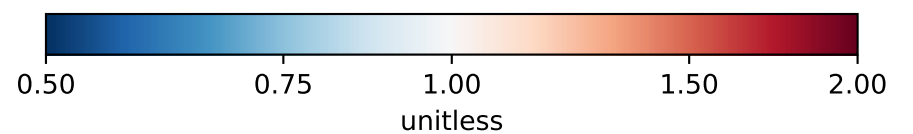
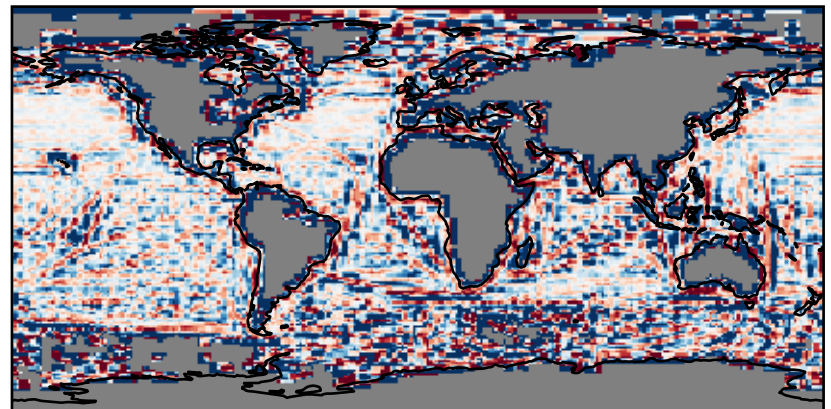
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

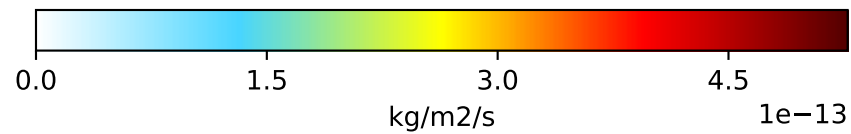
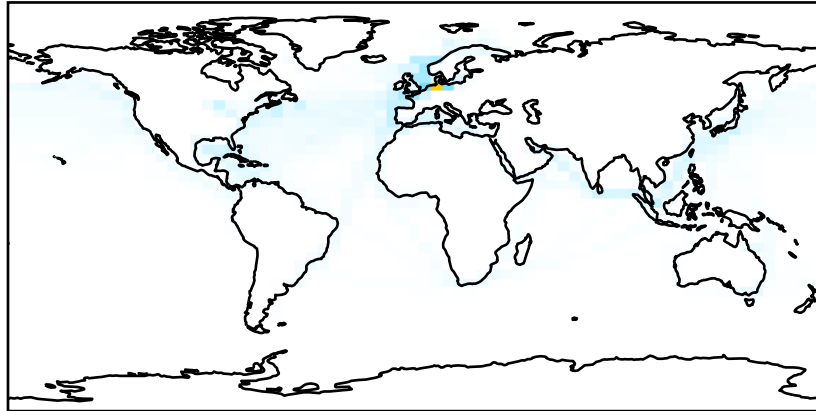


Ratio (1x1.25)
Dev/Ref, Fixed Range

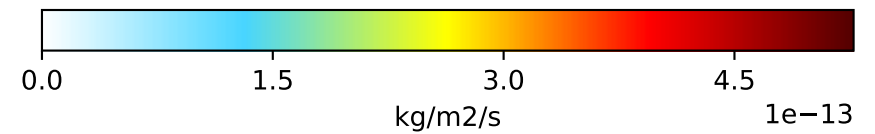
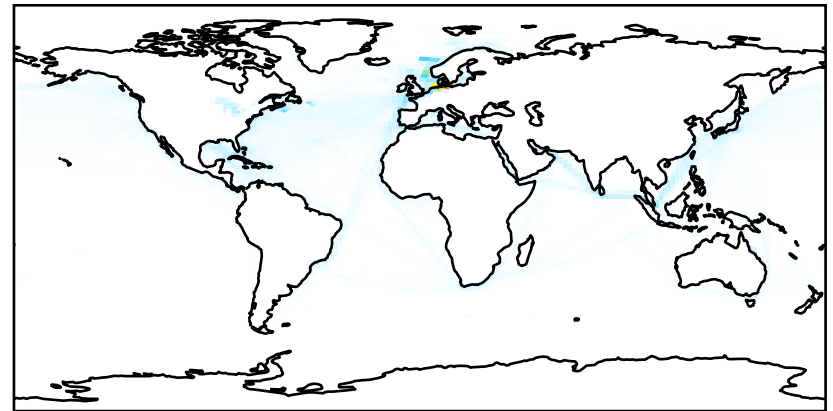


EmisSOAP_Ship (Jul2019)

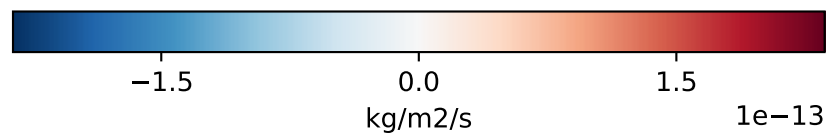
GCC_12.9.0 (Ref)
4.0x5.0



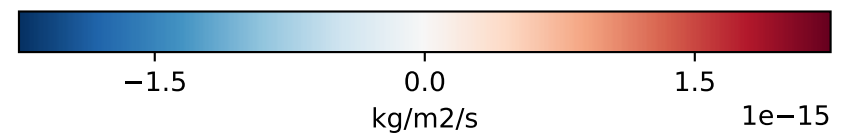
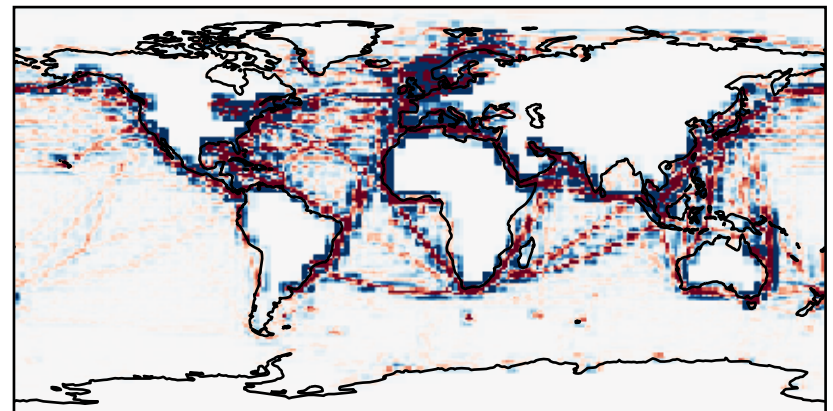
GCC_13.0.0-beta.1 (Dev)
c48



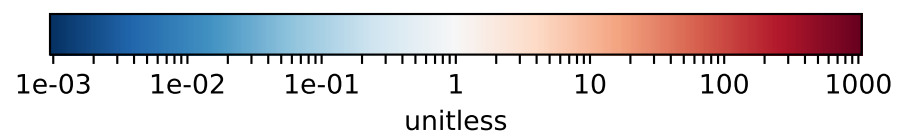
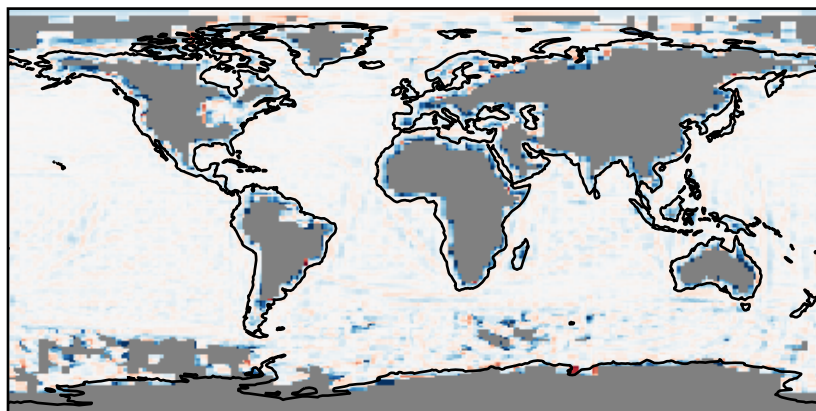
Difference (1x1.25)
Dev - Ref, Dynamic Range



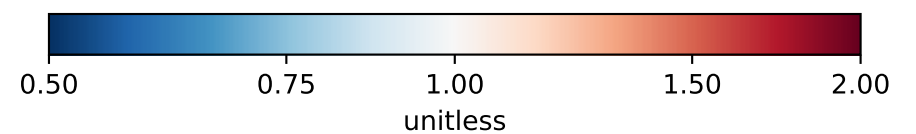
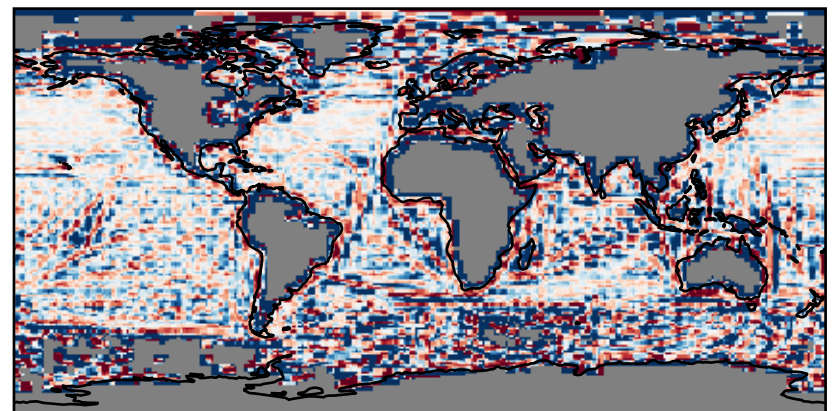
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

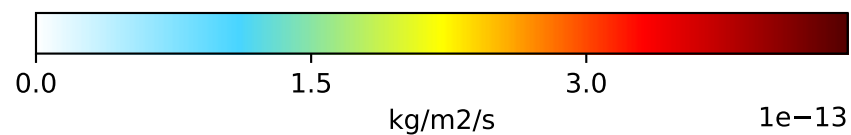
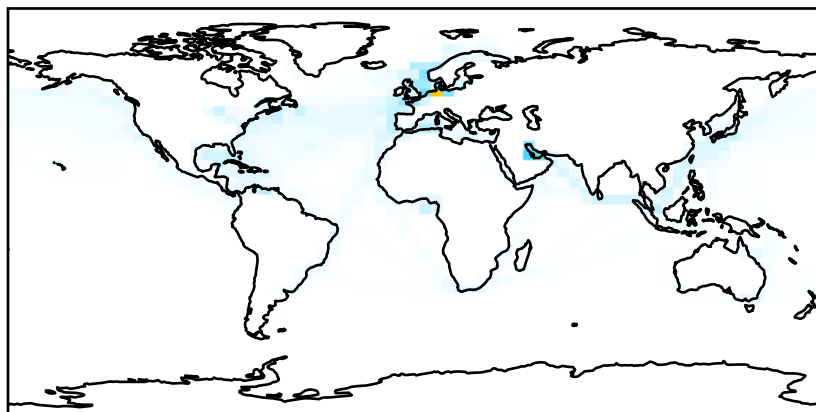


Ratio (1x1.25)
Dev/Ref, Fixed Range

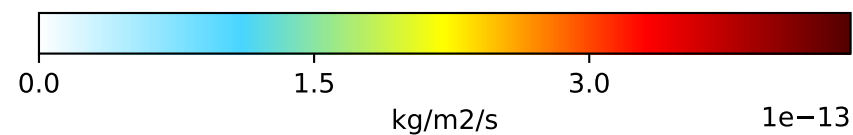
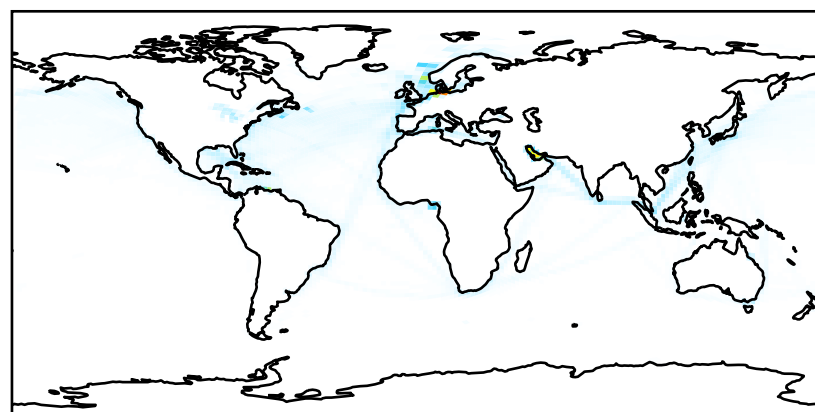


EmisTOLU_Ship (Jul2019)

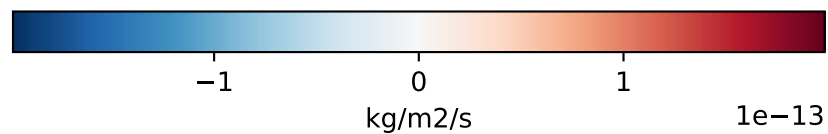
GCC_12.9.0 (Ref)
4.0x5.0



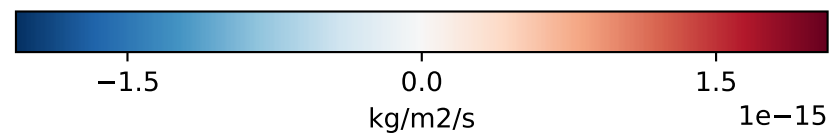
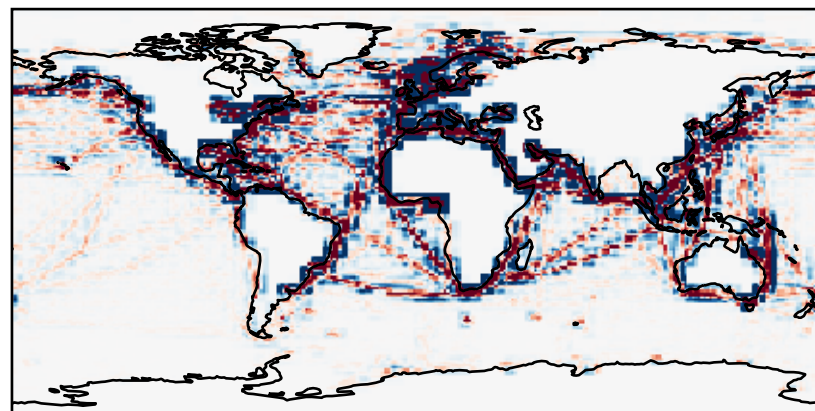
GCC_13.0.0-beta.1 (Dev)
c48



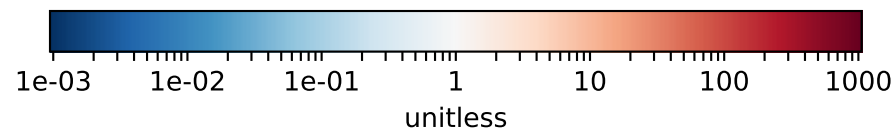
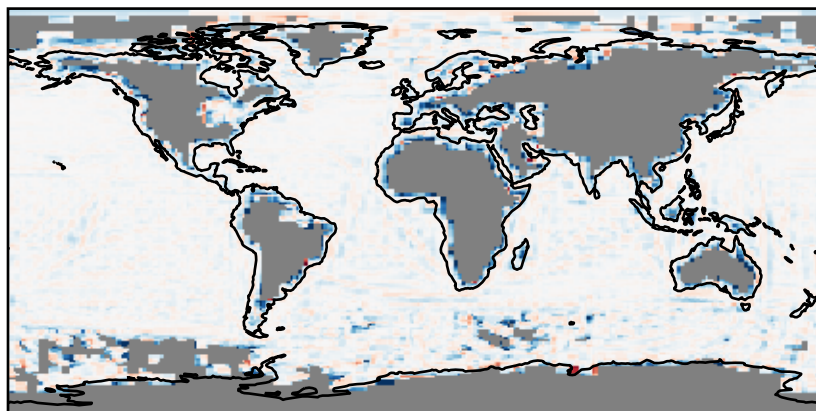
Difference (1x1.25)
Dev - Ref, Dynamic Range



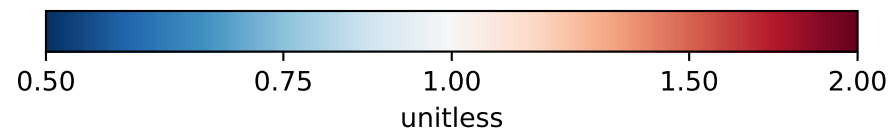
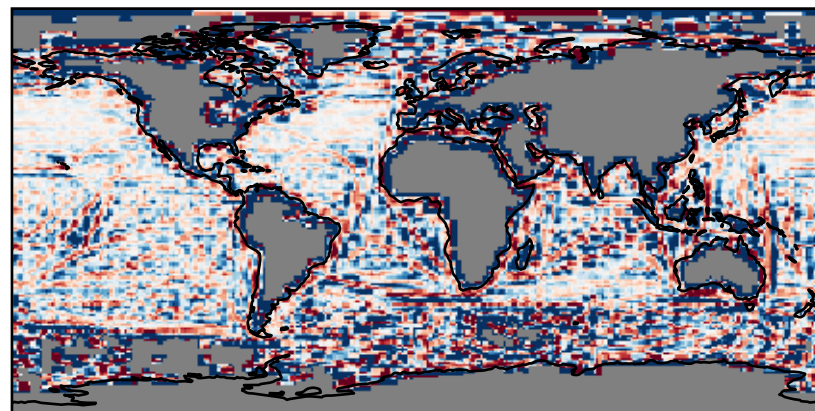
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

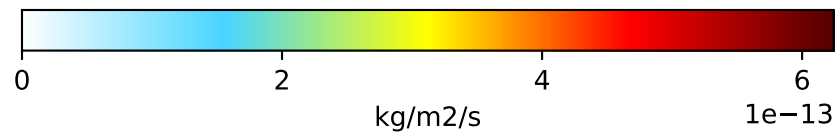
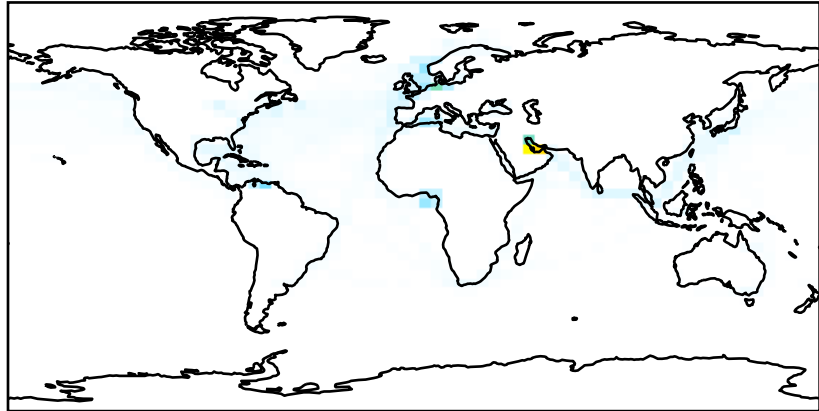


Ratio (1x1.25)
Dev/Ref, Fixed Range

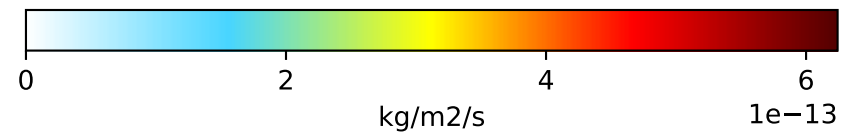
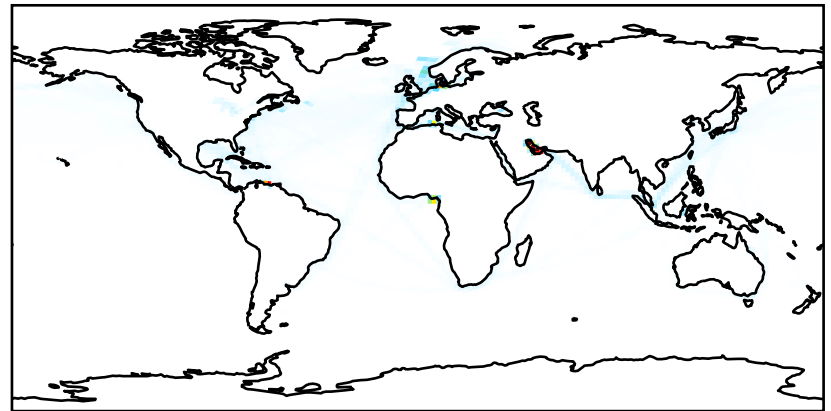


EmisXYLE_Ship (Jul2019)

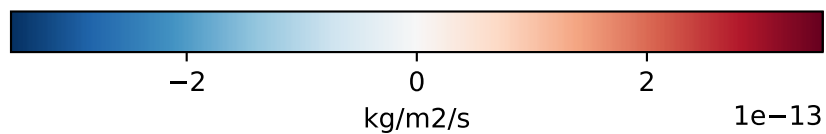
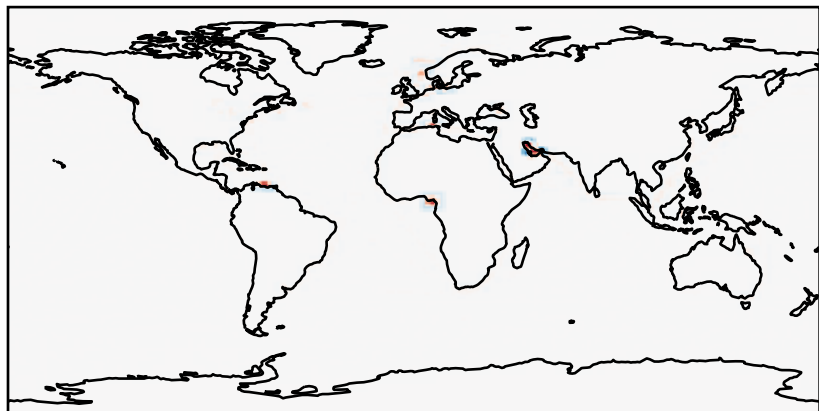
GCC_12.9.0 (Ref)
4.0x5.0



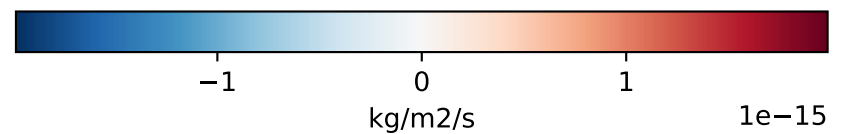
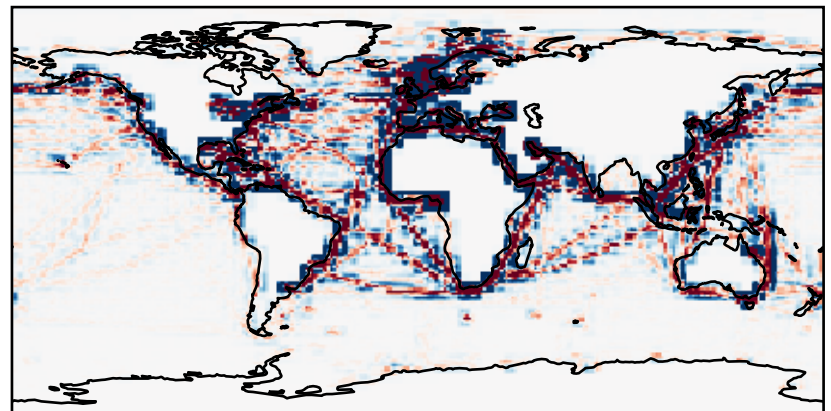
GCC_13.0.0-beta.1 (Dev)
c48



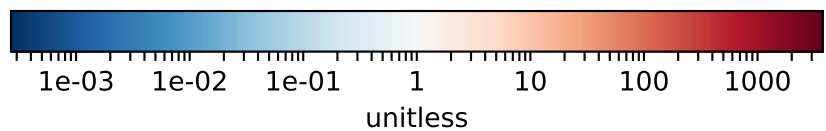
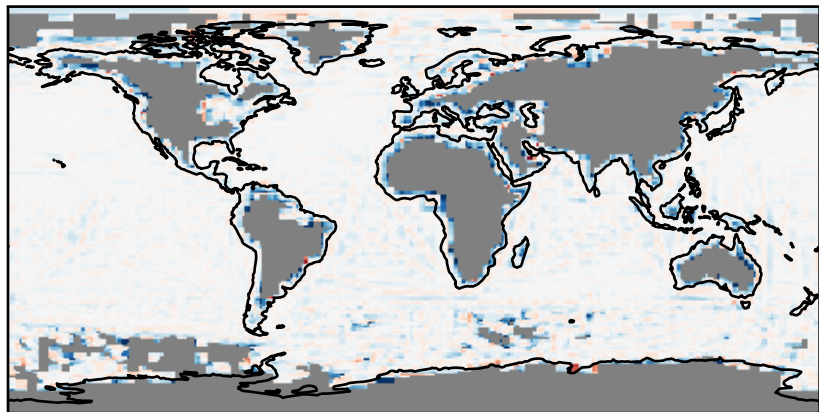
Difference (1x1.25)
Dev - Ref, Dynamic Range



Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range



Ratio (1x1.25)
Dev/Ref, Fixed Range

