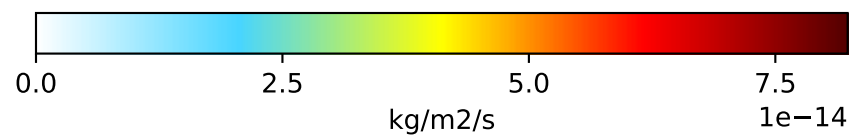
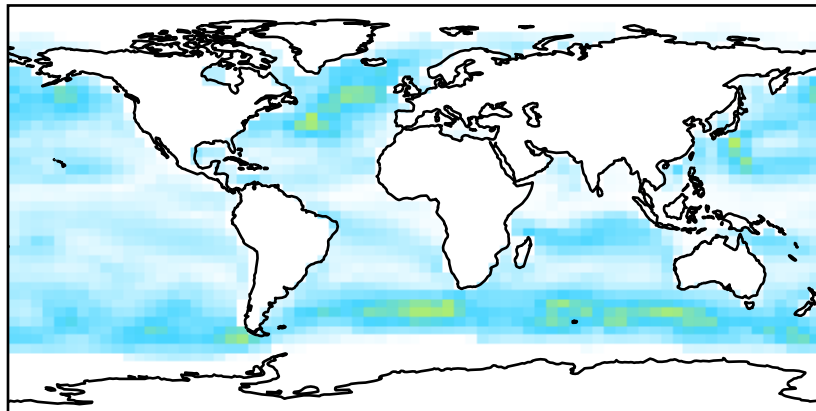
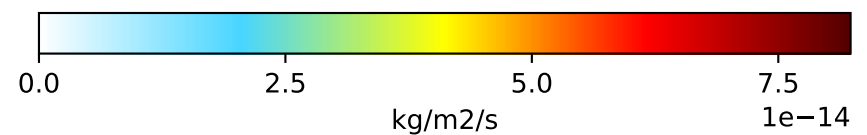
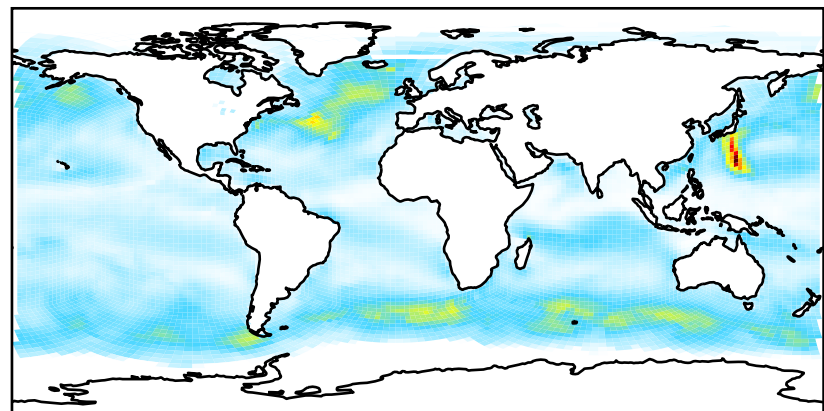


EmisBrSALA_Natural (Oct2019)

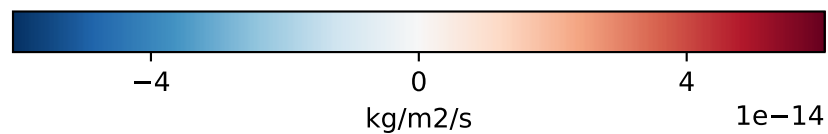
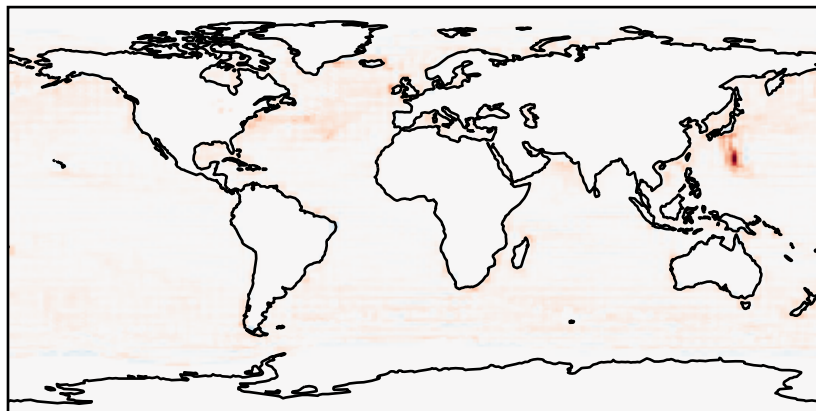
GCC_12.9.0 (Ref)
4.0x5.0



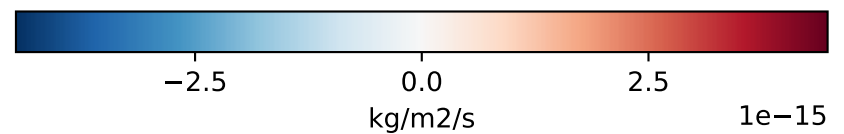
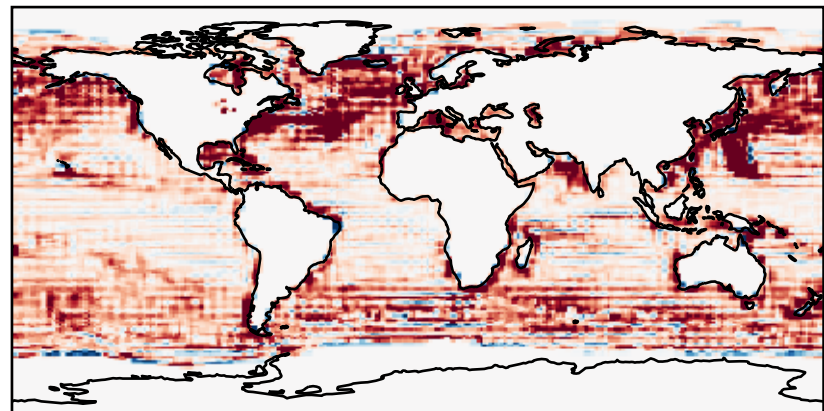
GCC_13.0.0-beta.1 (Dev)
c48



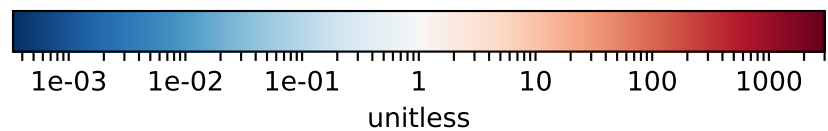
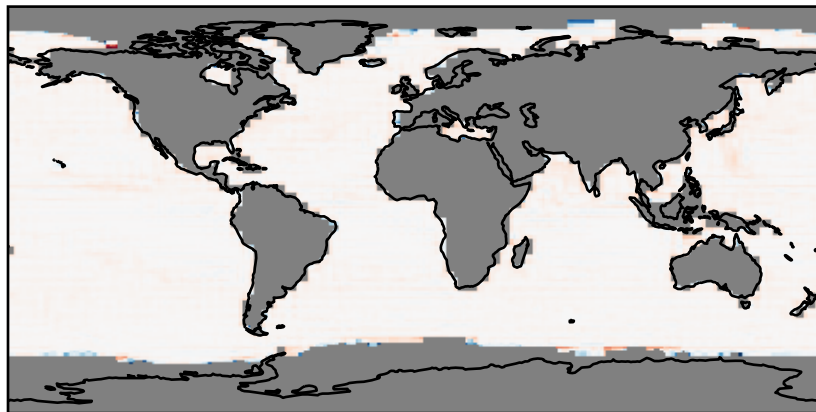
Difference (1x1.25)
Dev - Ref, Dynamic Range



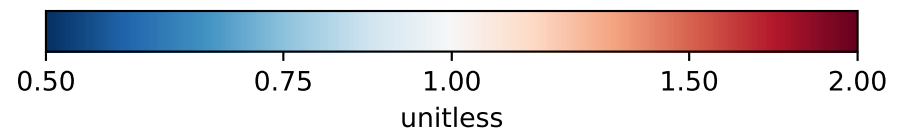
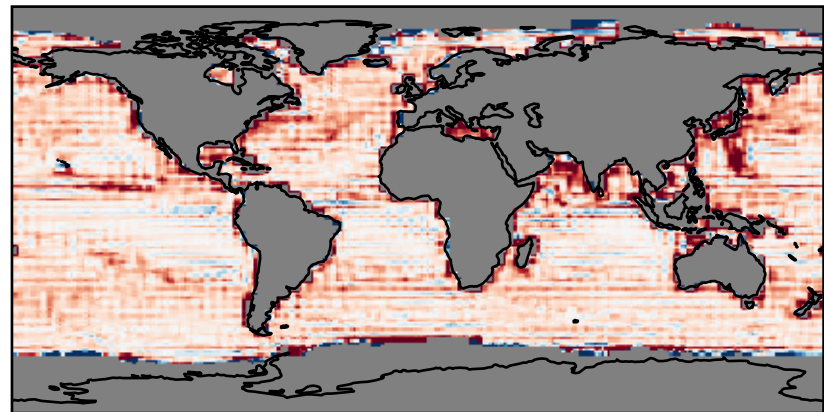
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

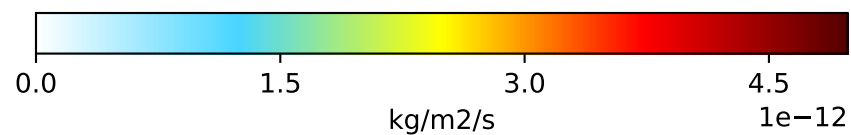
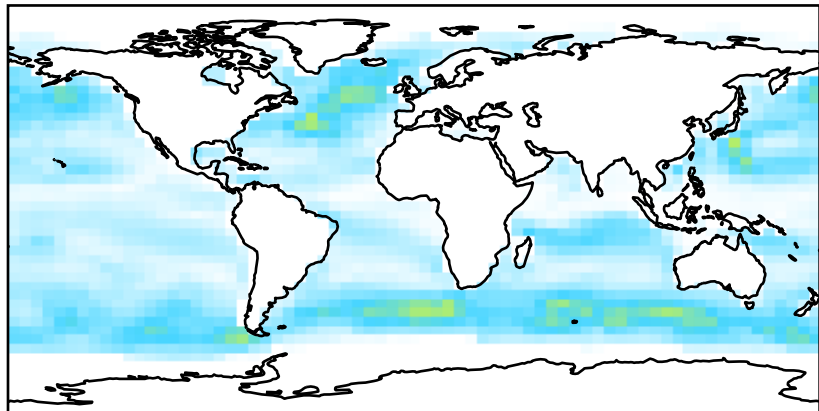


Ratio (1x1.25)
Dev/Ref, Fixed Range

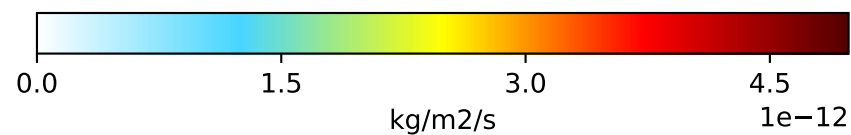
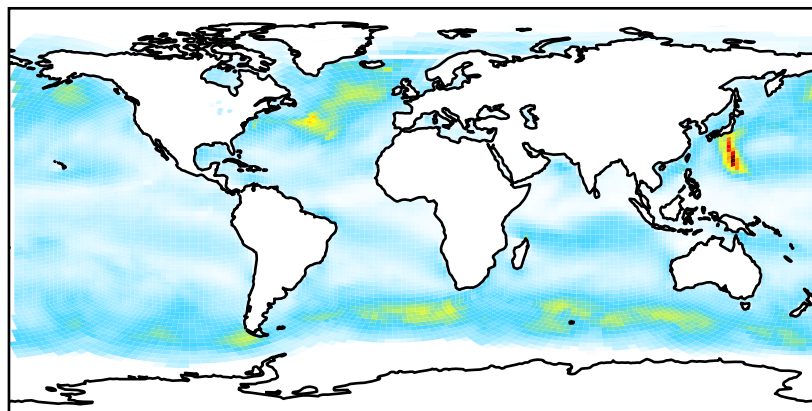


EmisBrSALC_Natural (Oct2019)

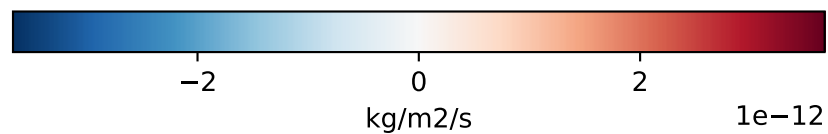
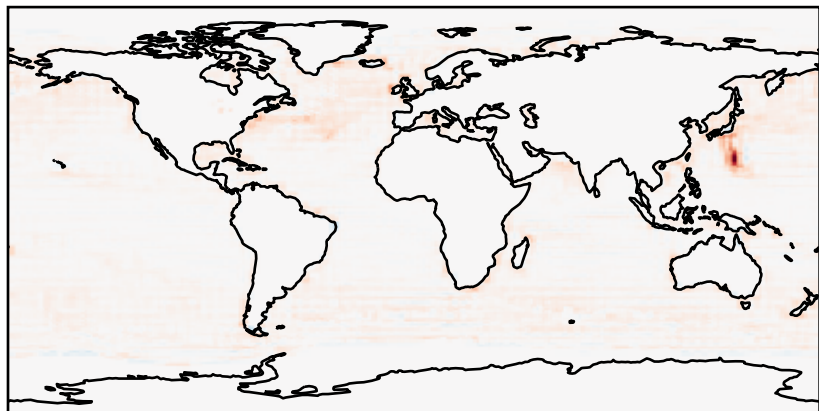
GCC_12.9.0 (Ref)
4.0x5.0



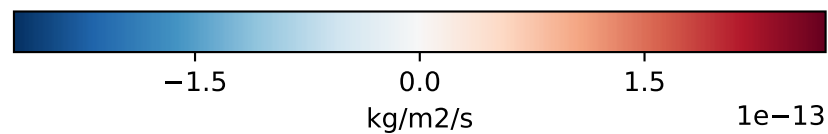
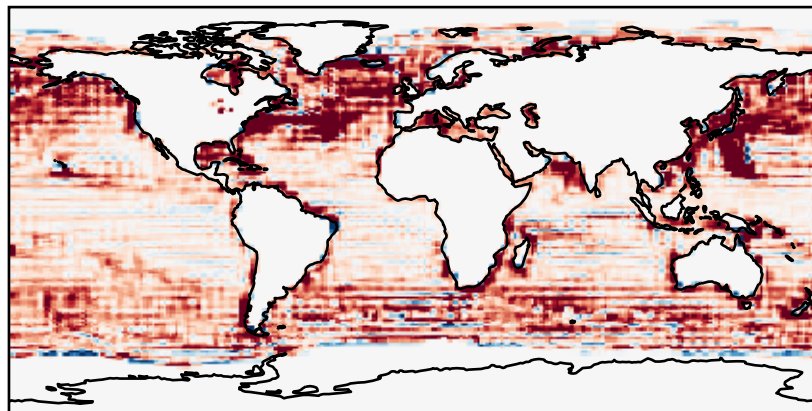
GCC_13.0.0-beta.1 (Dev)
c48



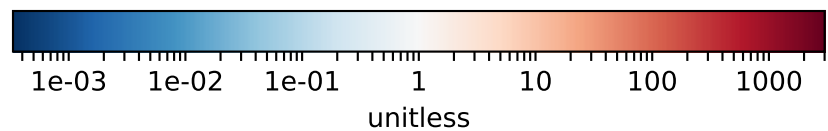
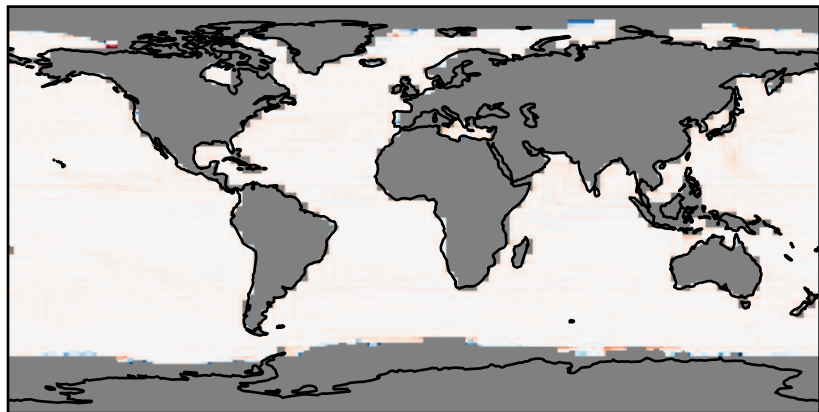
Difference (1x1.25)
Dev - Ref, Dynamic Range



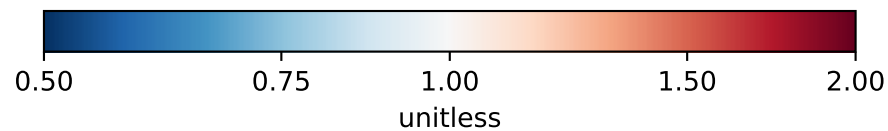
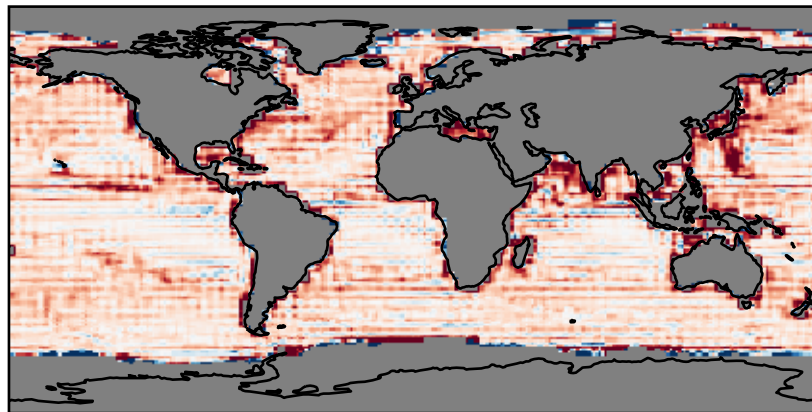
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

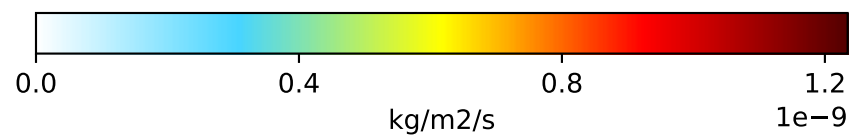
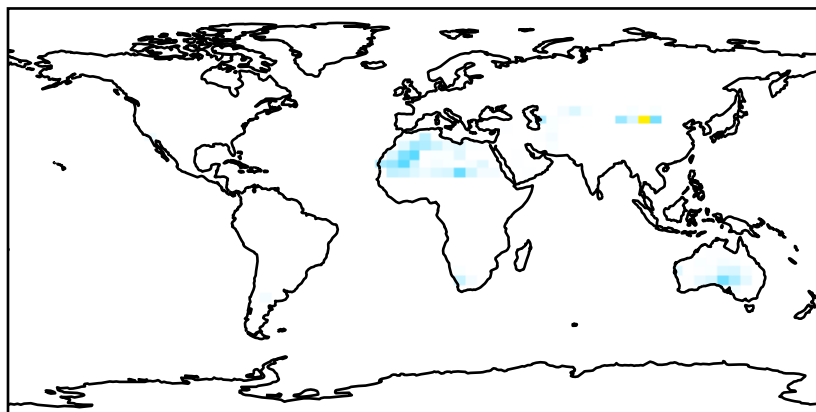


Ratio (1x1.25)
Dev/Ref, Fixed Range

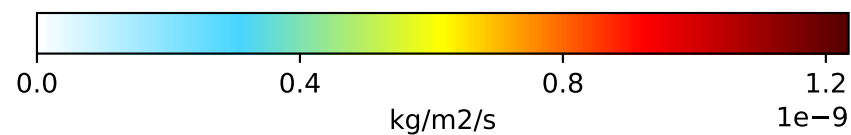
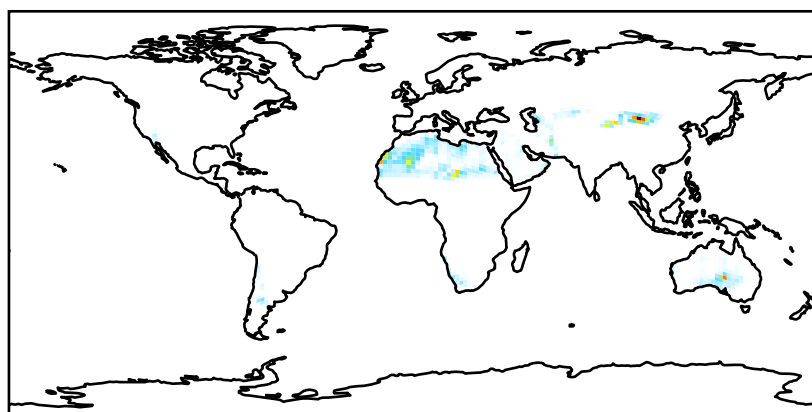


EmisDST1_Natural (Oct2019)

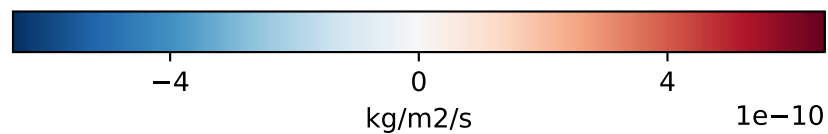
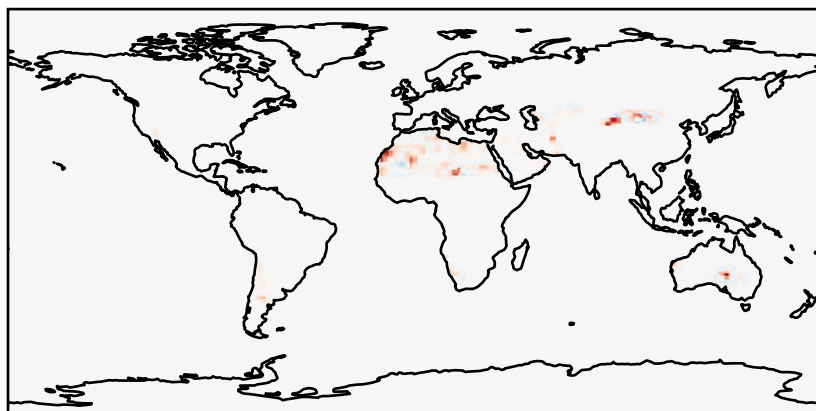
GCC_12.9.0 (Ref)
4.0x5.0



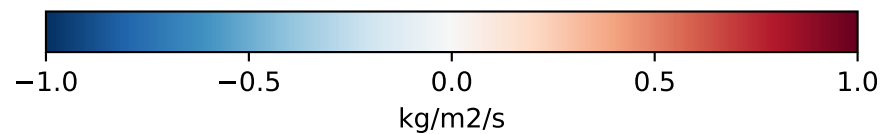
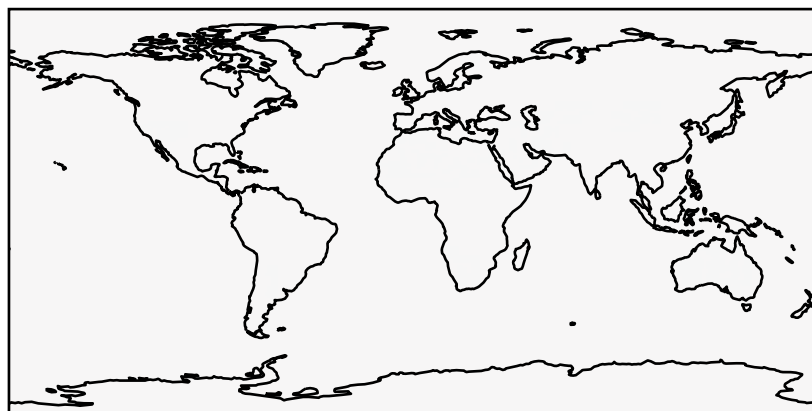
GCC_13.0.0-beta.1 (Dev)
c48



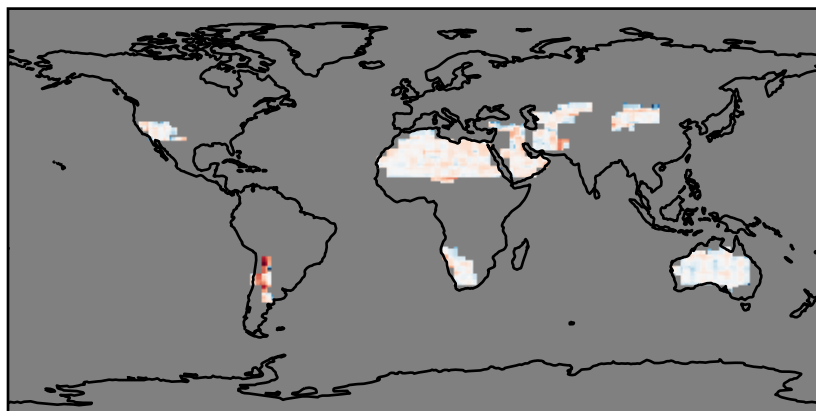
Difference (1x1.25)
Dev - Ref, Dynamic Range



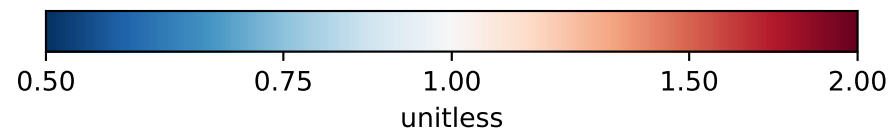
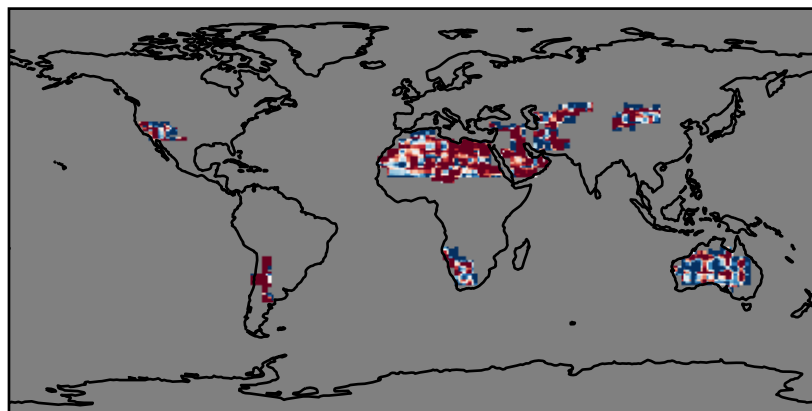
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

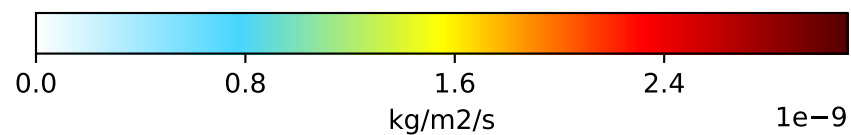
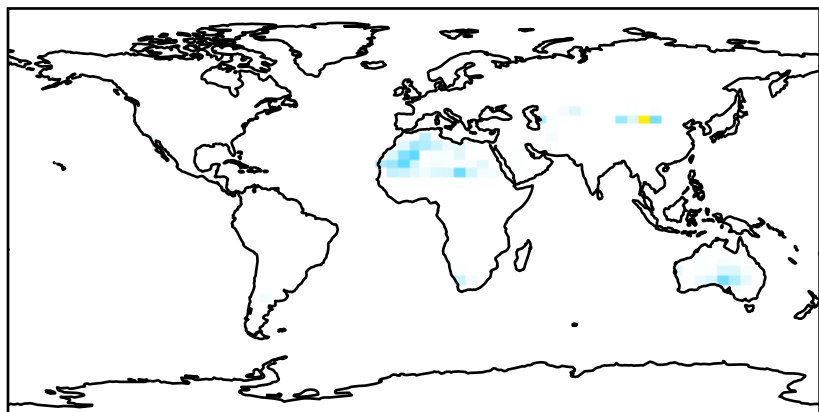


Ratio (1x1.25)
Dev/Ref, Fixed Range

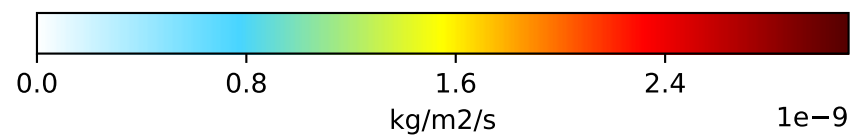
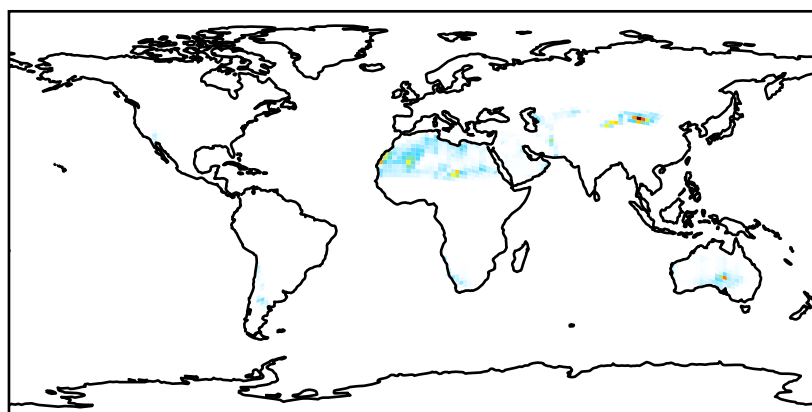


EmisDST2_Natural (Oct2019)

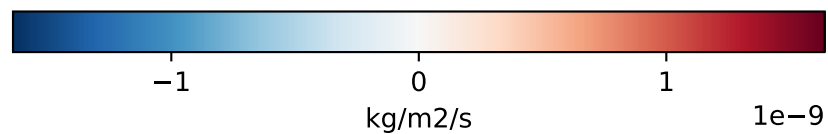
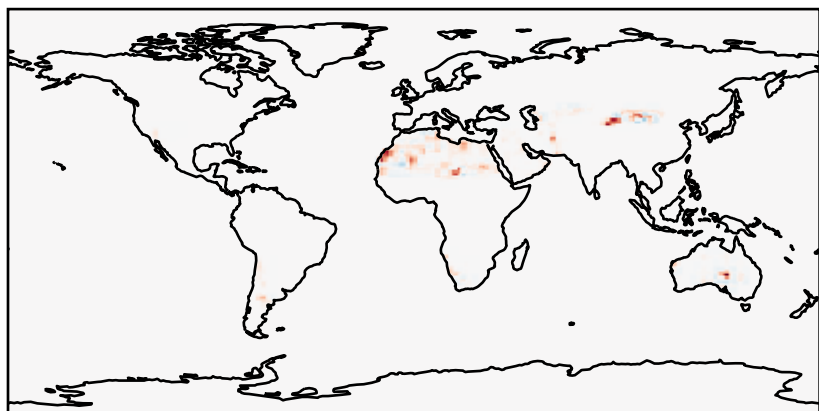
GCC_12.9.0 (Ref)
4.0x5.0



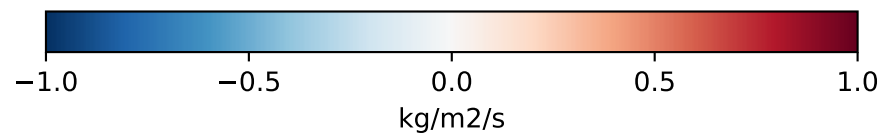
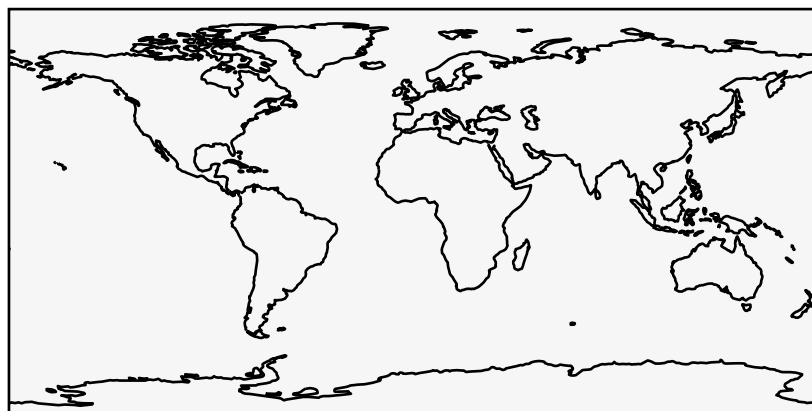
GCC_13.0.0-beta.1 (Dev)
c48



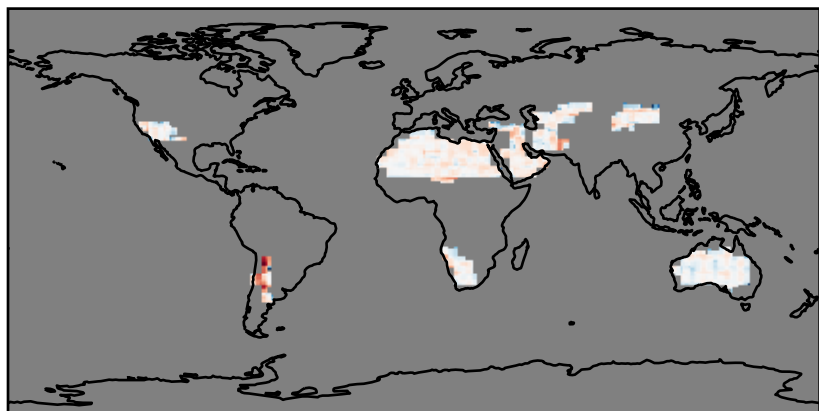
Difference (1x1.25)
Dev - Ref, Dynamic Range



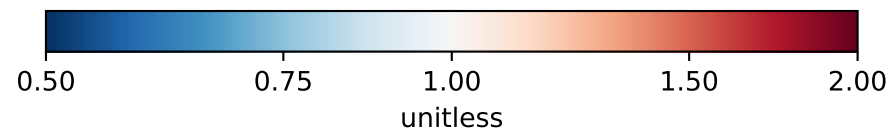
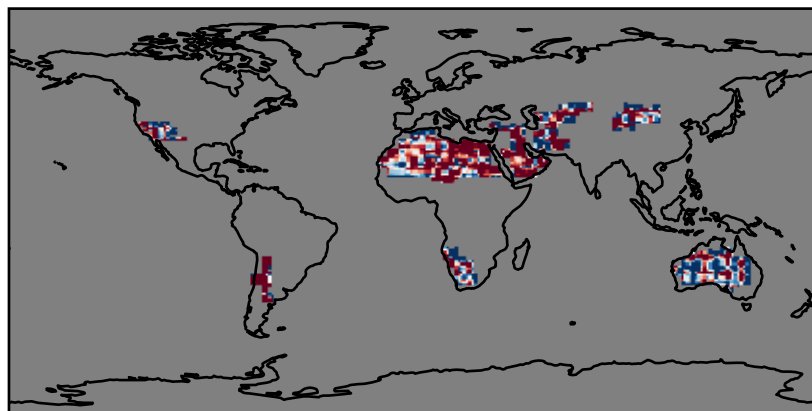
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

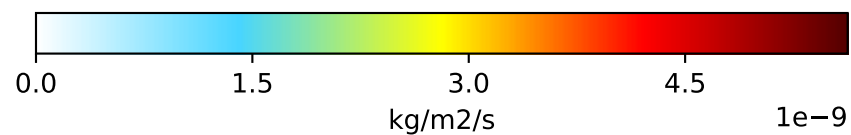
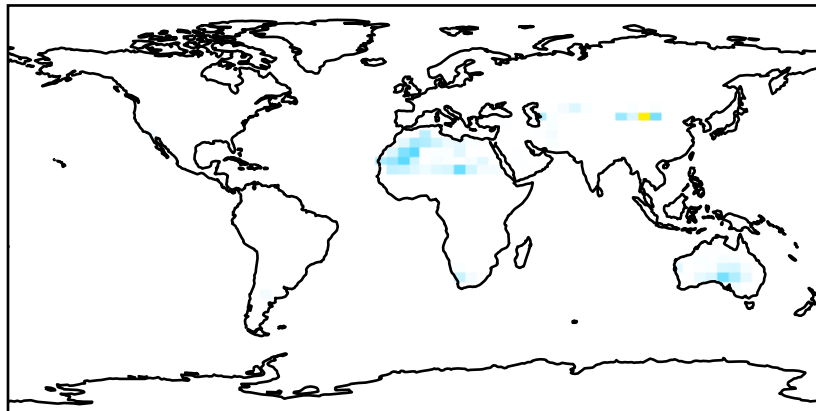


Ratio (1x1.25)
Dev/Ref, Fixed Range

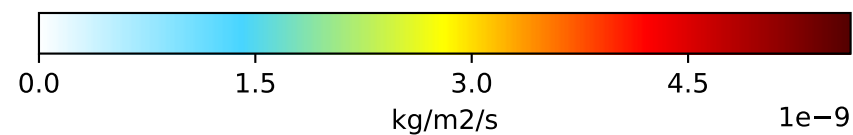
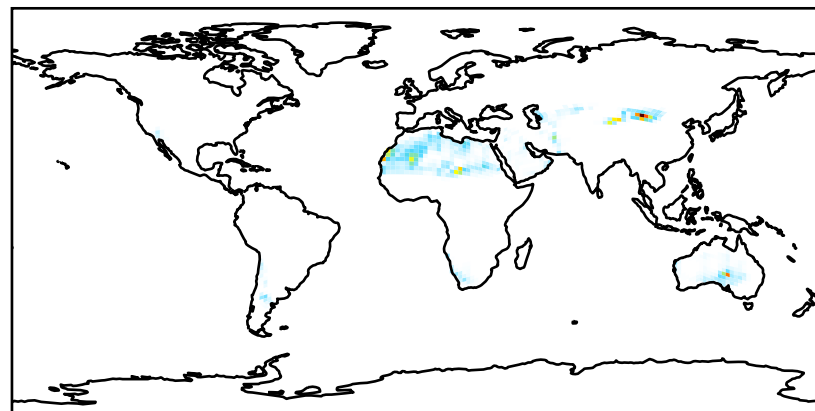


EmisDST3_Natural (Oct2019)

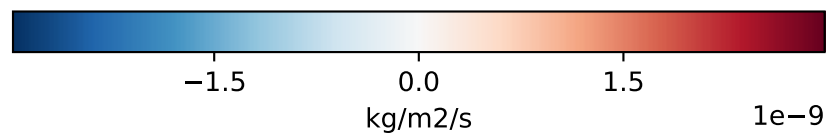
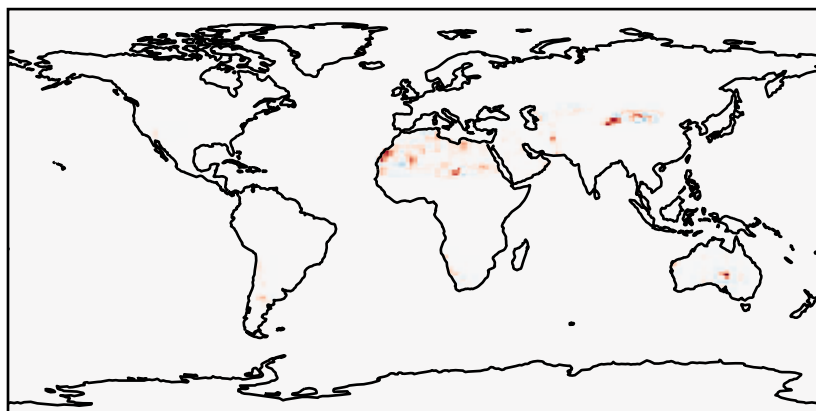
GCC_12.9.0 (Ref)
4.0x5.0



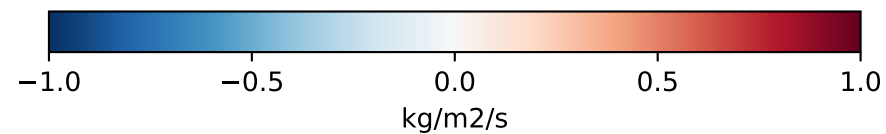
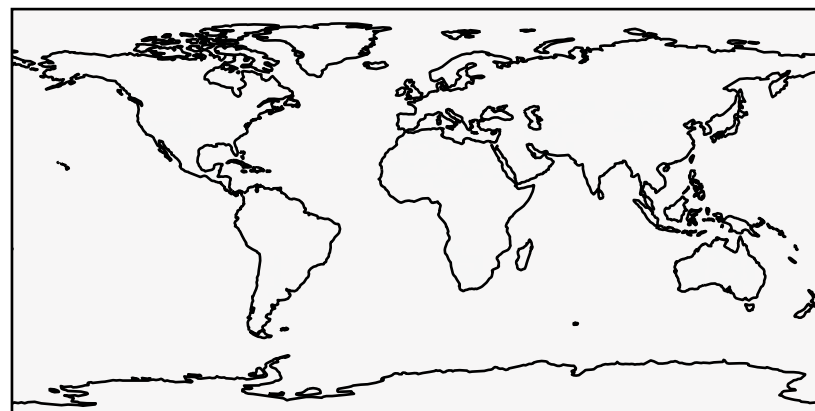
GCC_13.0.0-beta.1 (Dev)
c48



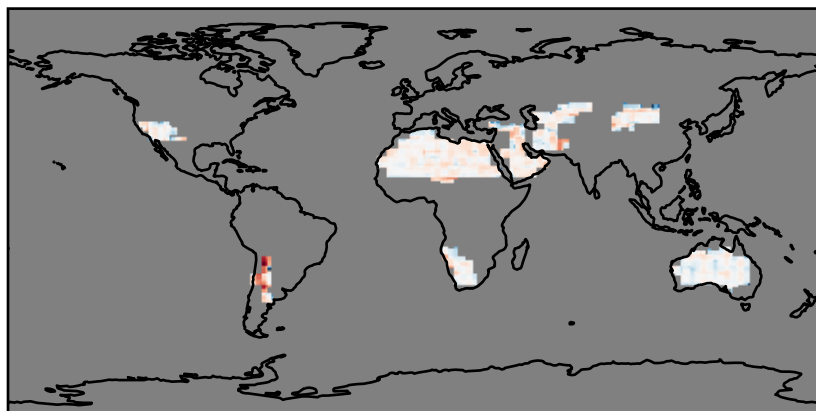
Difference (1x1.25)
Dev - Ref, Dynamic Range



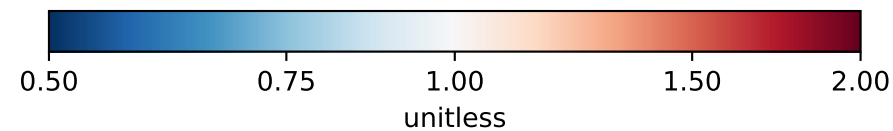
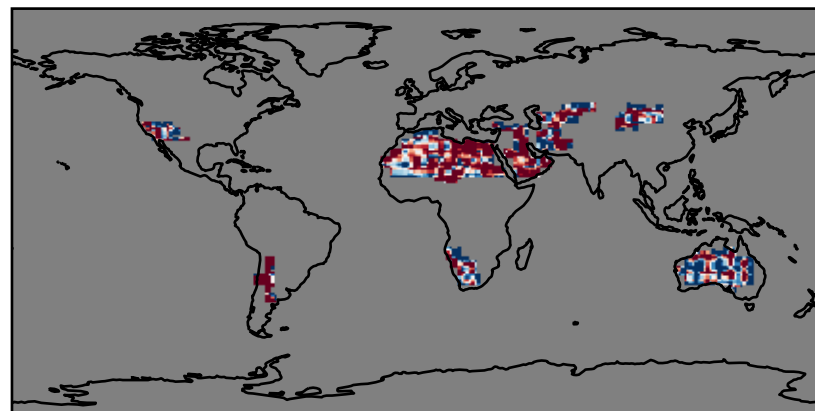
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

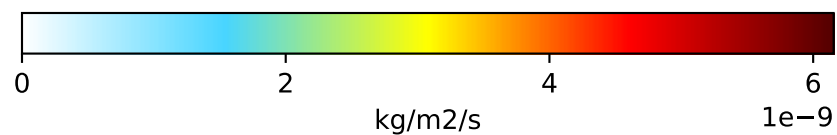
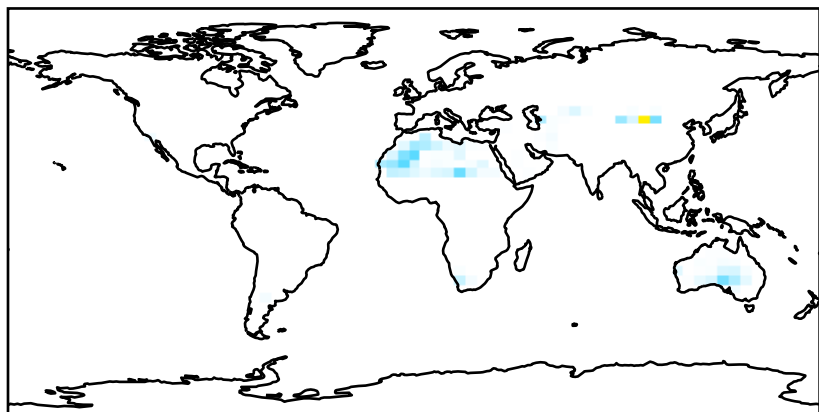


Ratio (1x1.25)
Dev/Ref, Fixed Range

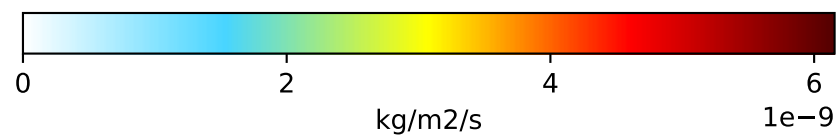
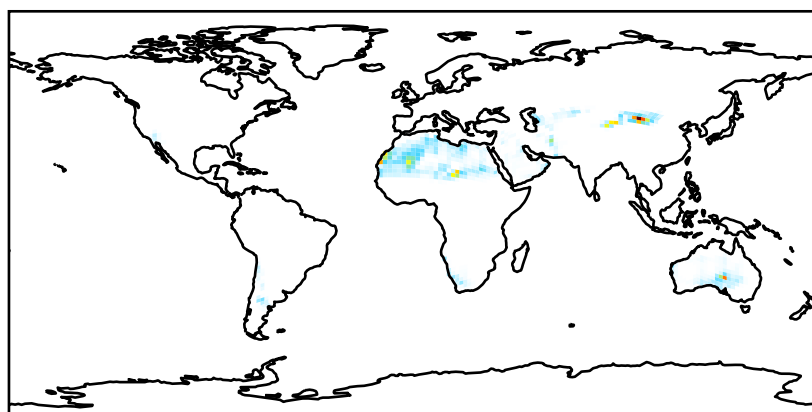


EmisDST4_Natural (Oct2019)

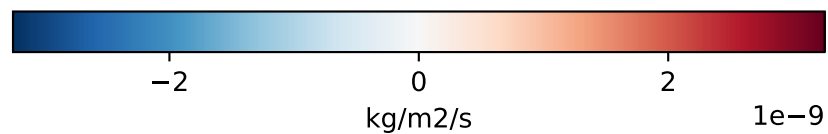
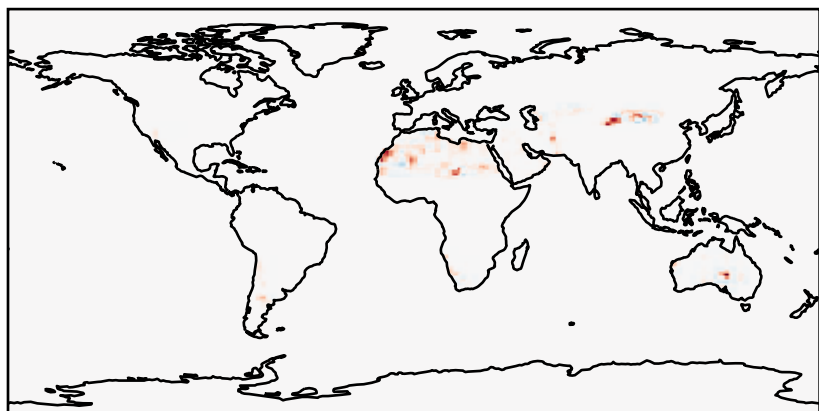
GCC_12.9.0 (Ref)
4.0x5.0



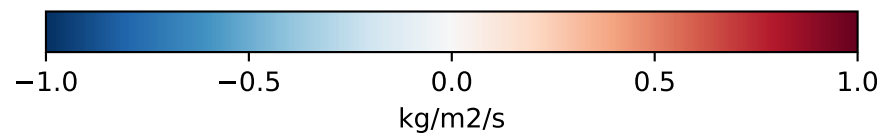
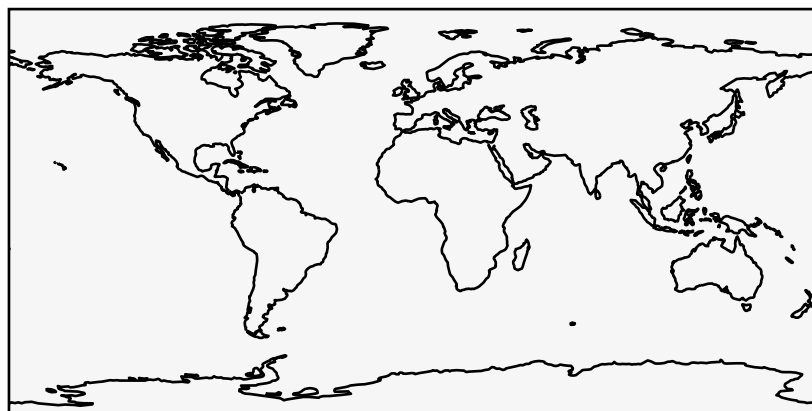
GCC_13.0.0-beta.1 (Dev)
c48



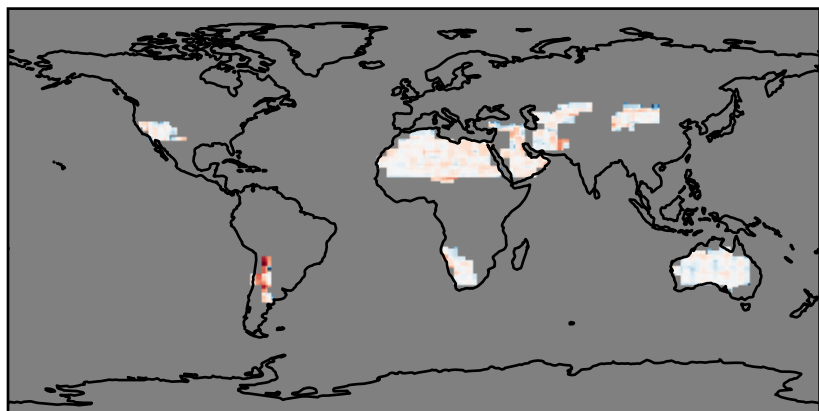
Difference (1x1.25)
Dev - Ref, Dynamic Range



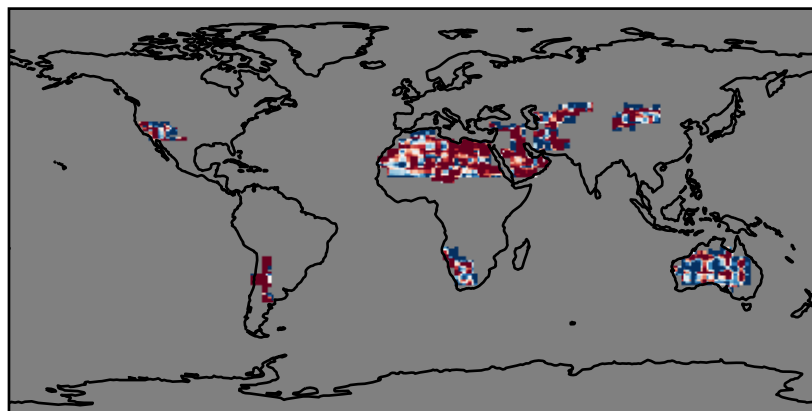
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

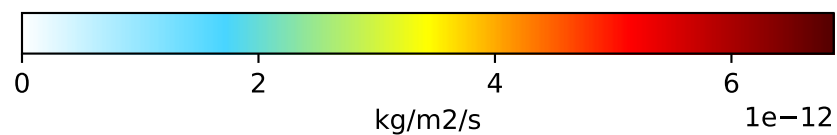
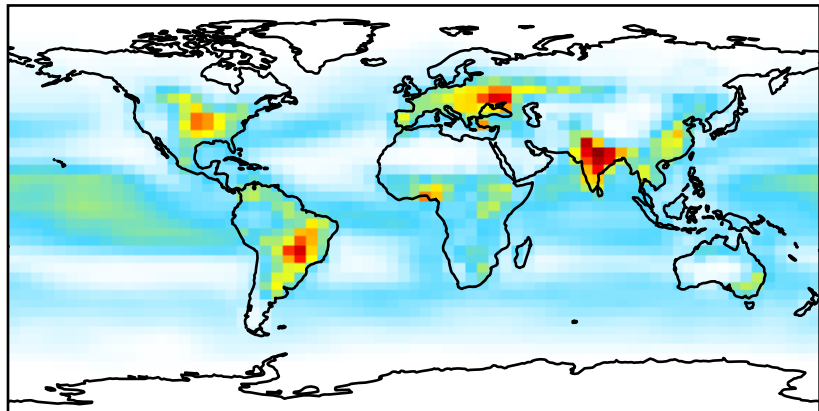


Ratio (1x1.25)
Dev/Ref, Fixed Range

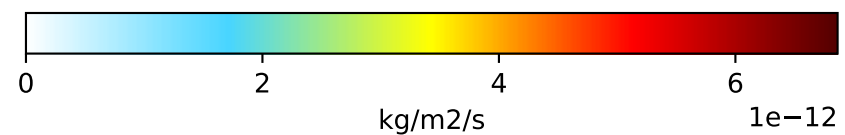
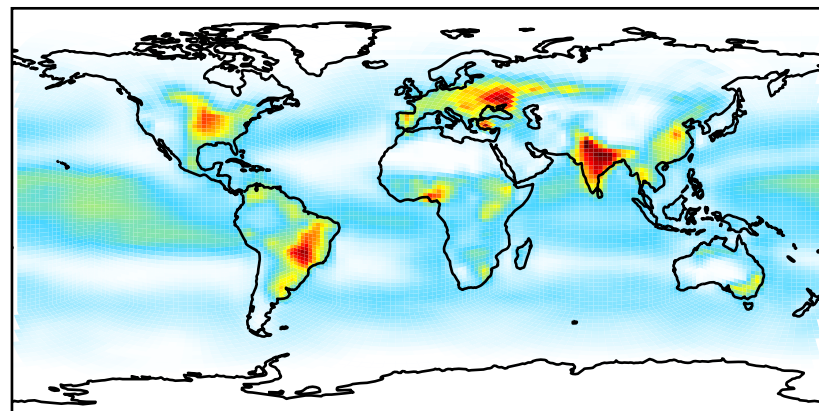


EmisNH3_Natural (Oct2019)

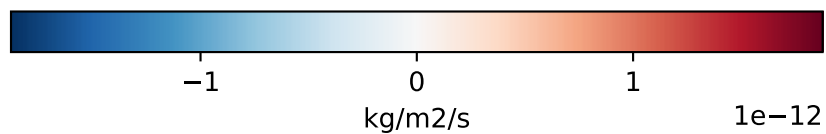
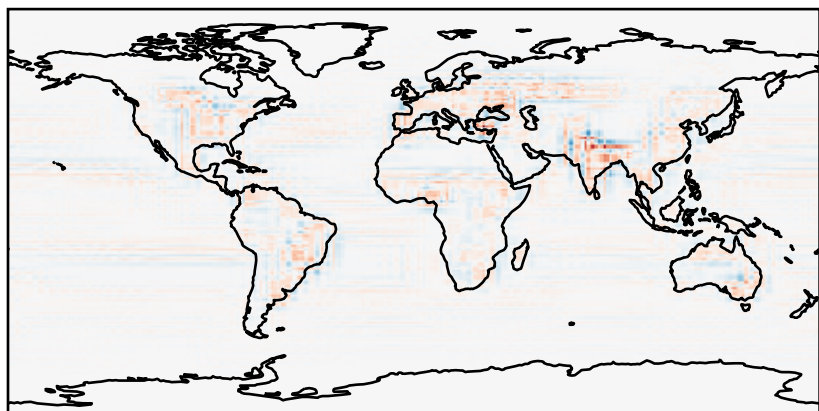
GCC_12.9.0 (Ref)
4.0x5.0



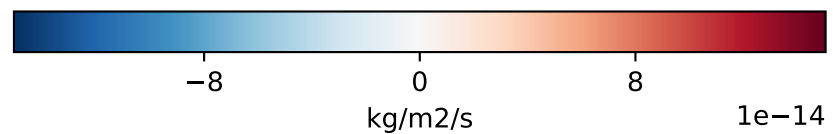
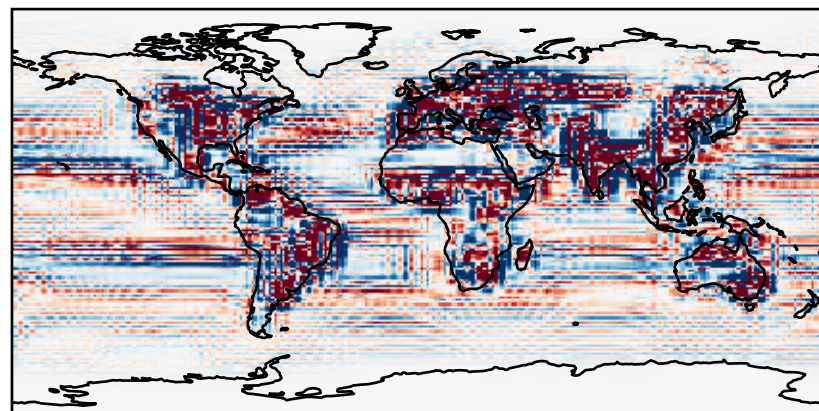
GCC_13.0.0-beta.1 (Dev)
c48



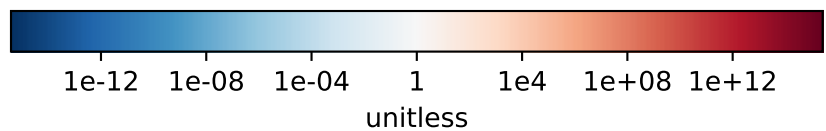
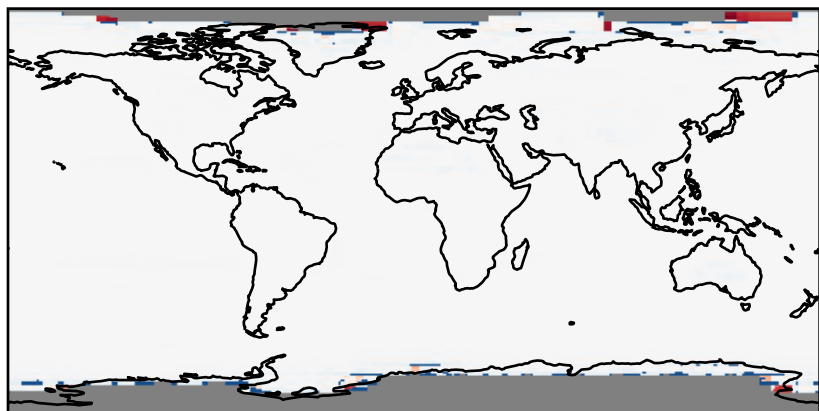
Difference (1x1.25)
Dev - Ref, Dynamic Range



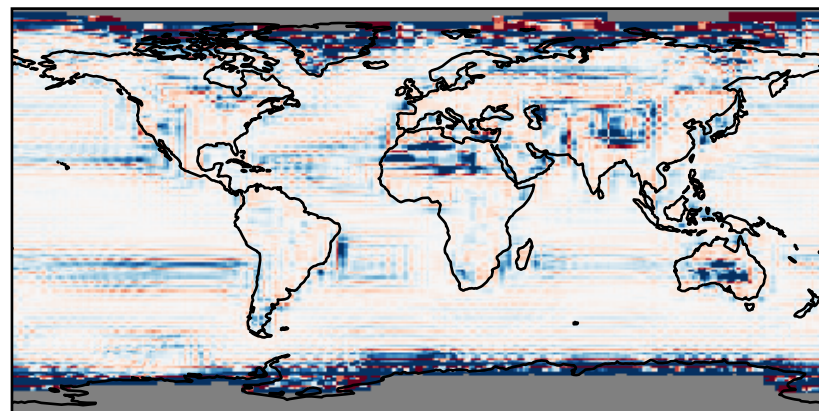
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

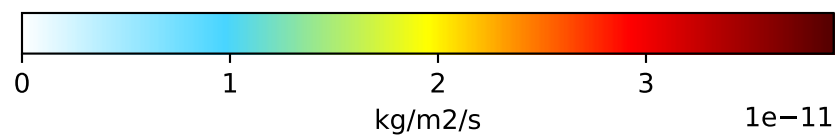
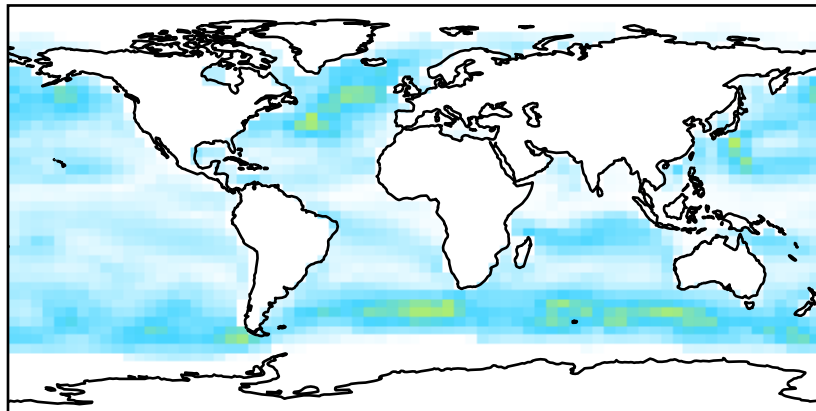


Ratio (1x1.25)
Dev/Ref, Fixed Range

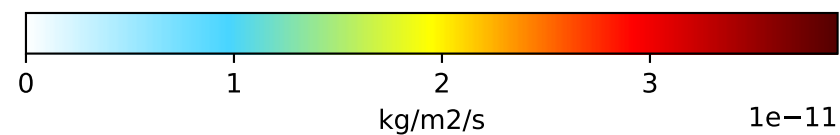
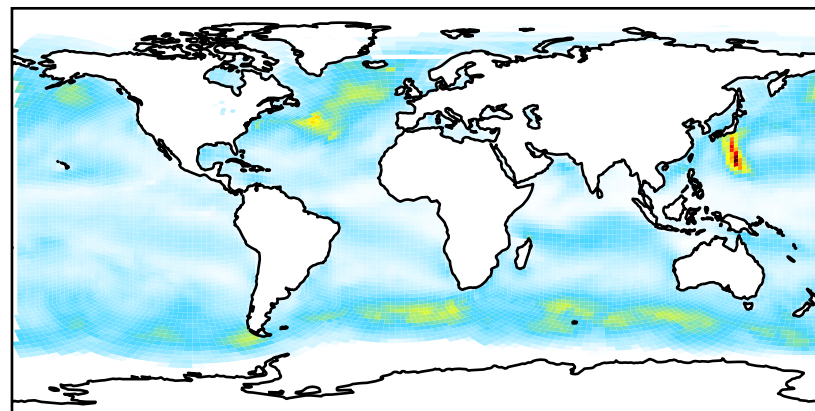


EmisSALA_Natural (Oct2019)

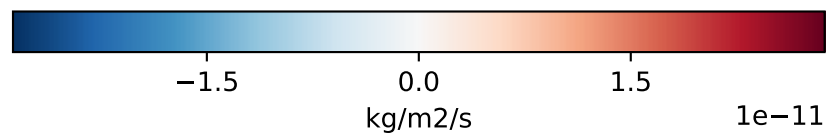
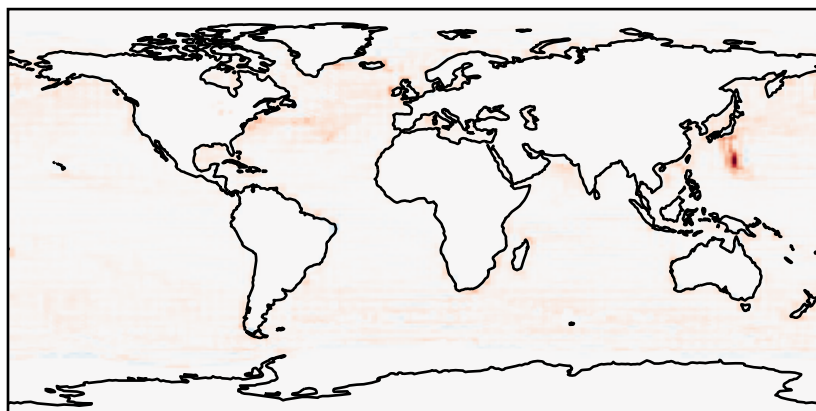
GCC_12.9.0 (Ref)
4.0x5.0



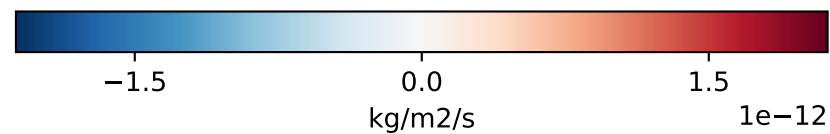
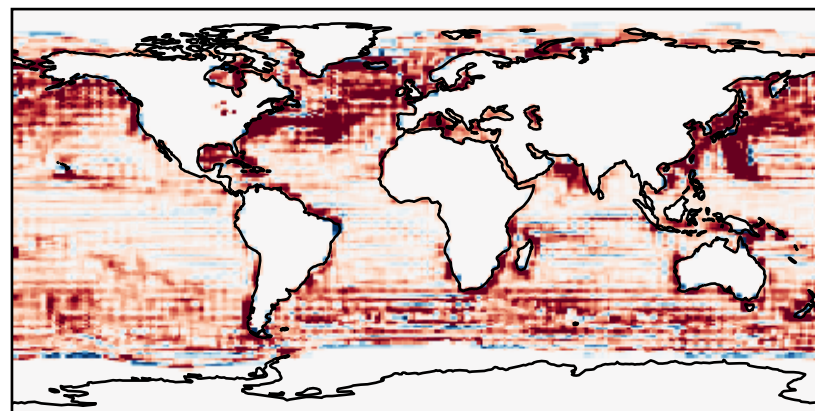
GCC_13.0.0-beta.1 (Dev)
c48



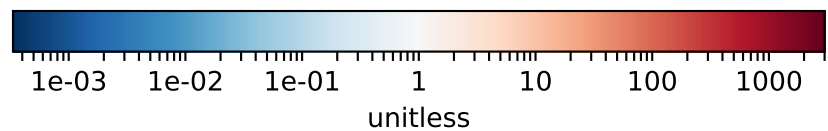
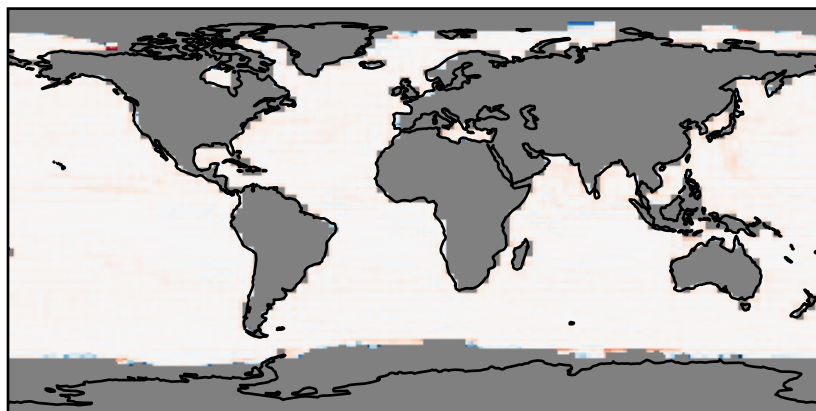
Difference (1x1.25)
Dev - Ref, Dynamic Range



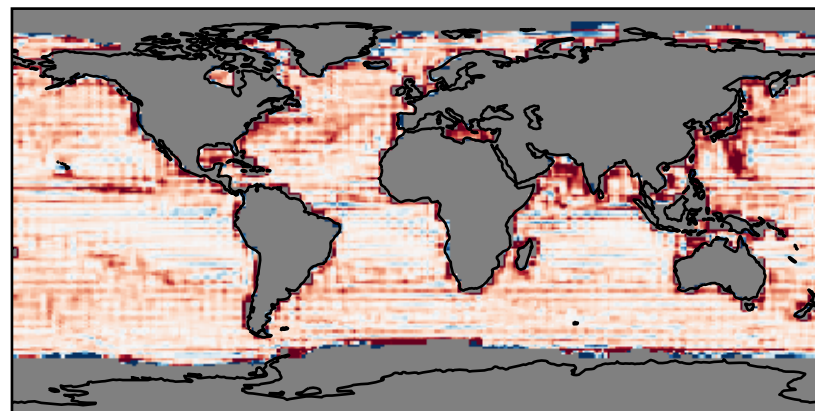
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

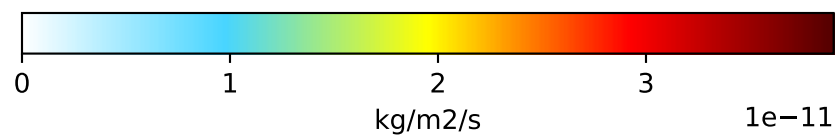
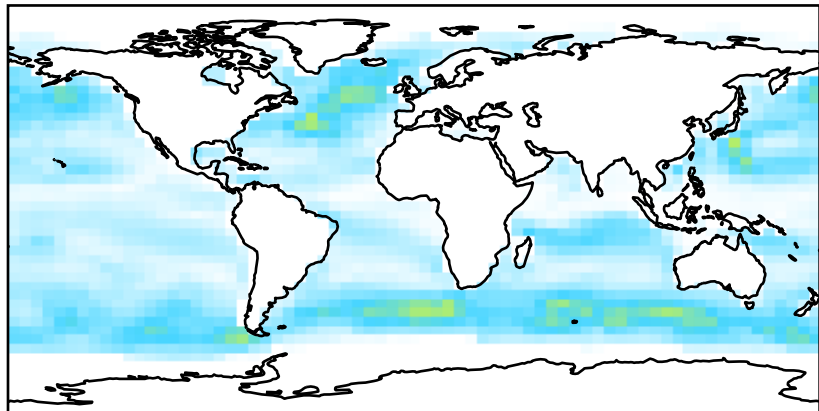


Ratio (1x1.25)
Dev/Ref, Fixed Range

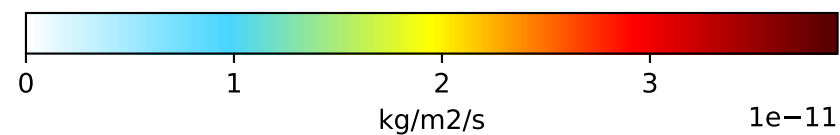
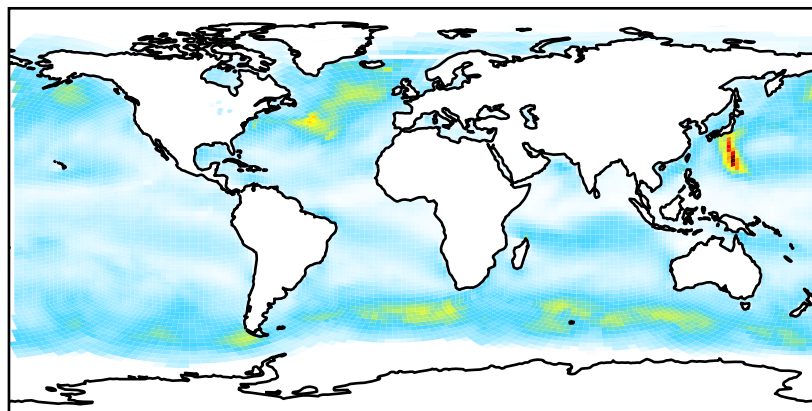


EmisSALAAL_Natural (Oct2019)

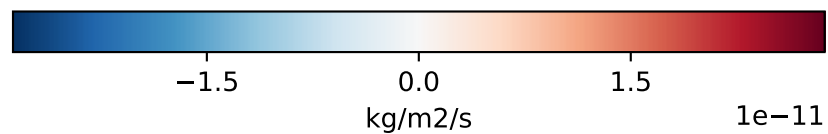
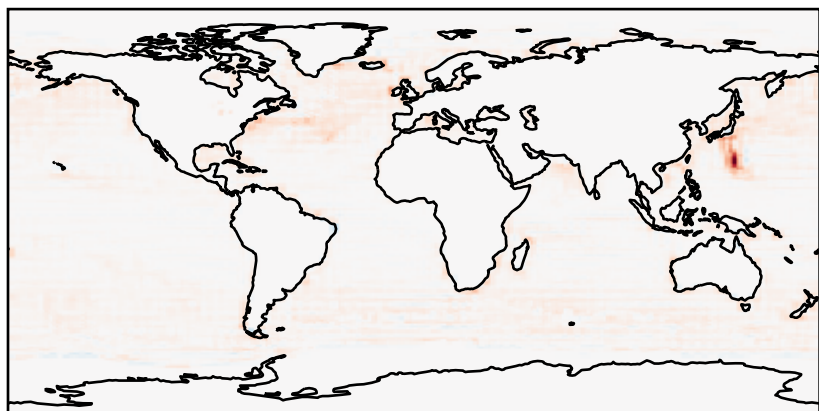
GCC_12.9.0 (Ref)
4.0x5.0



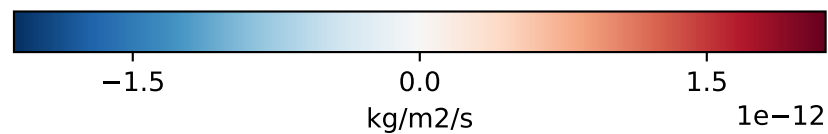
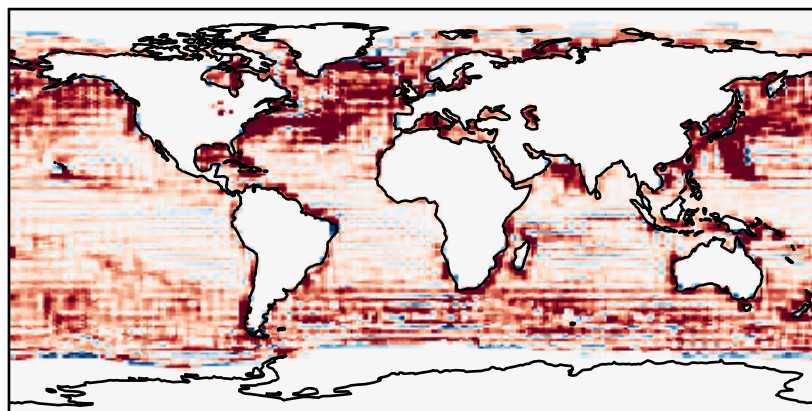
GCC_13.0.0-beta.1 (Dev)
c48



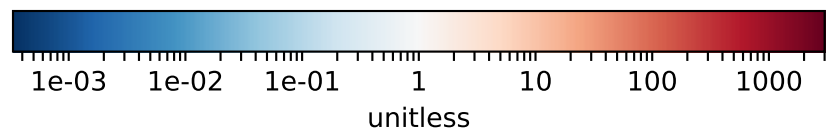
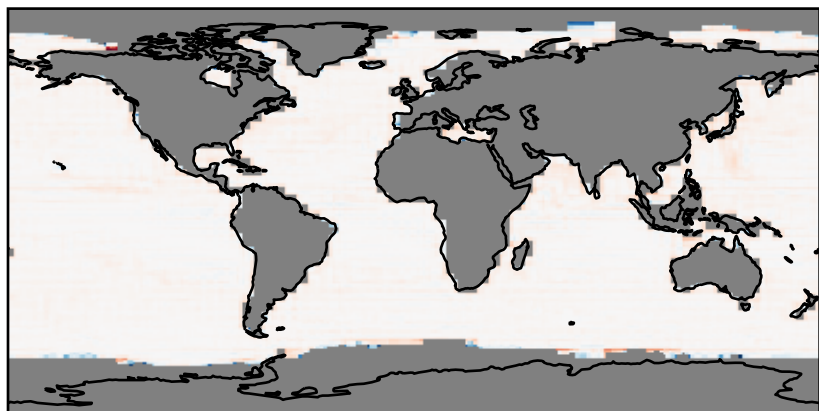
Difference (1x1.25)
Dev - Ref, Dynamic Range



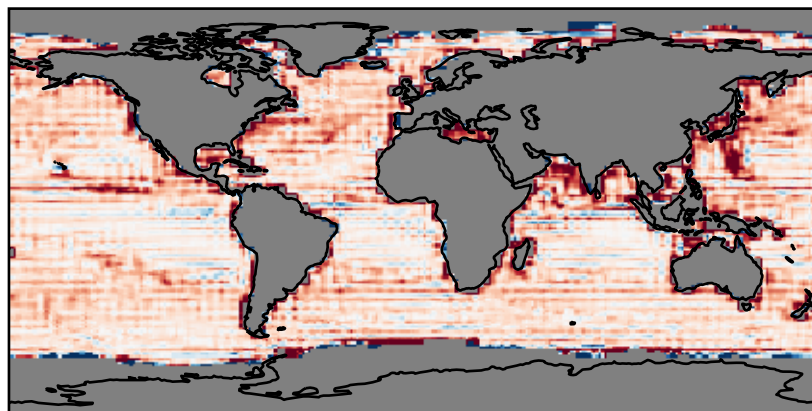
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

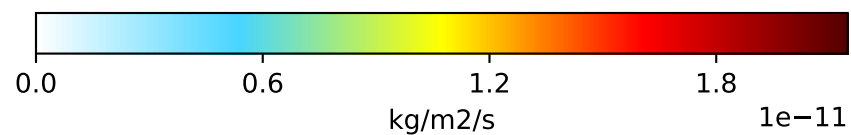
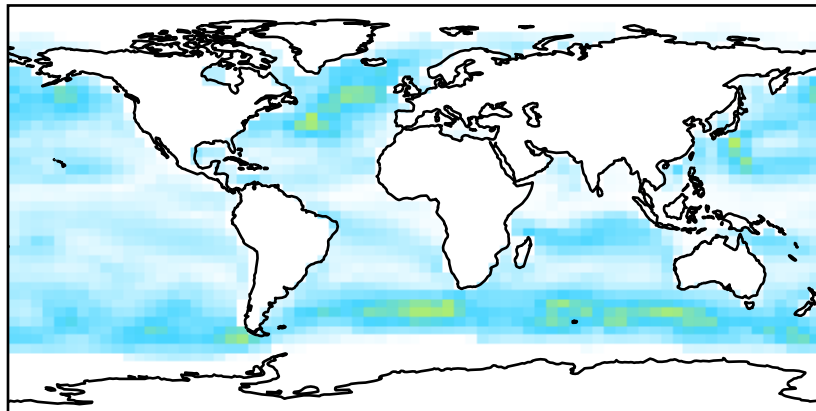


Ratio (1x1.25)
Dev/Ref, Fixed Range

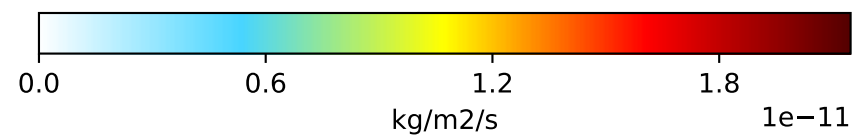
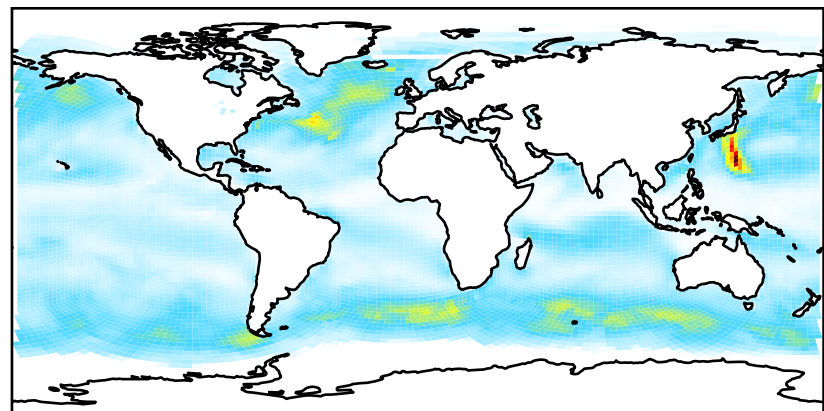


EmisSALACL_Natural (Oct2019)

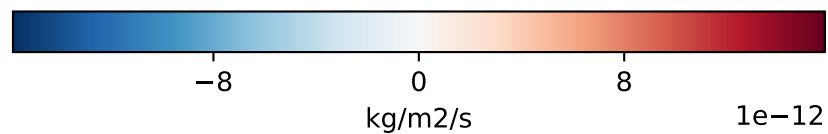
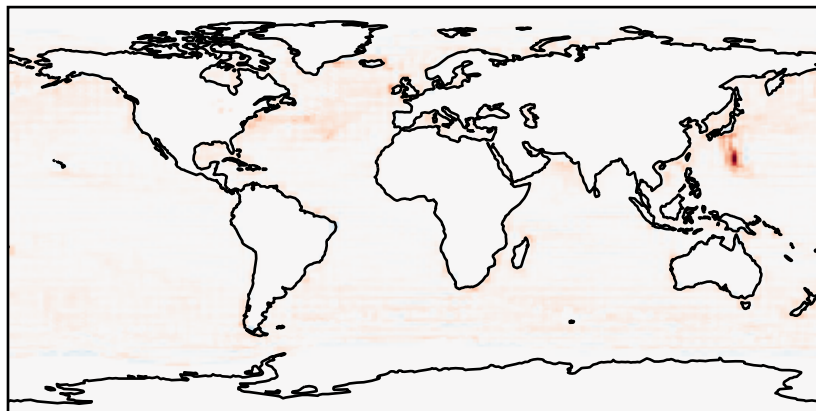
GCC_12.9.0 (Ref)
4.0x5.0



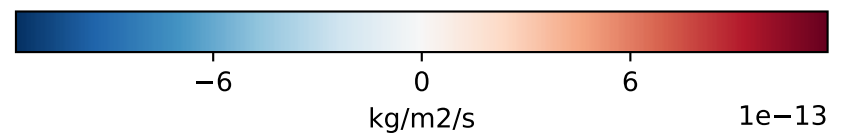
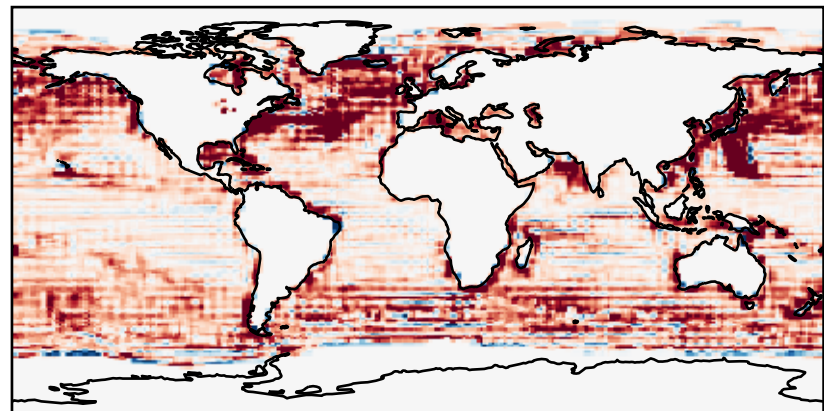
GCC_13.0.0-beta.1 (Dev)
c48



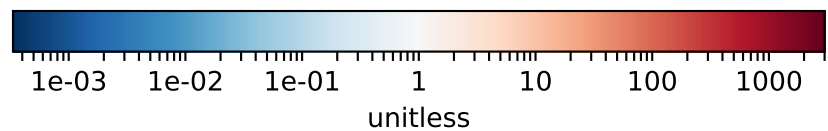
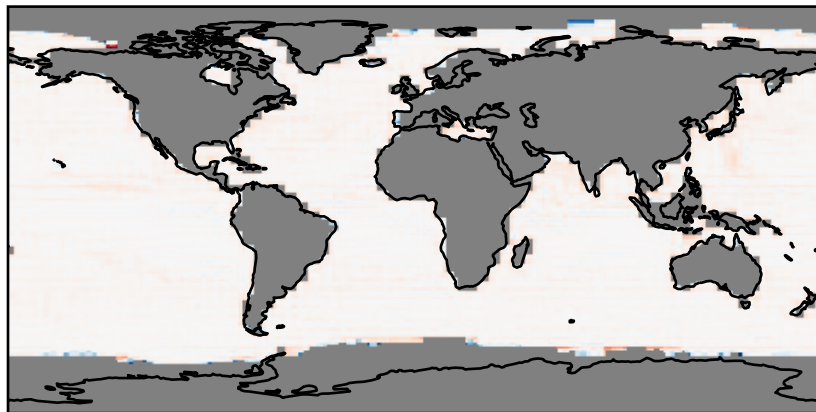
Difference (1x1.25)
Dev - Ref, Dynamic Range



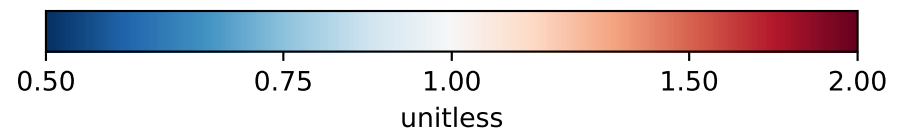
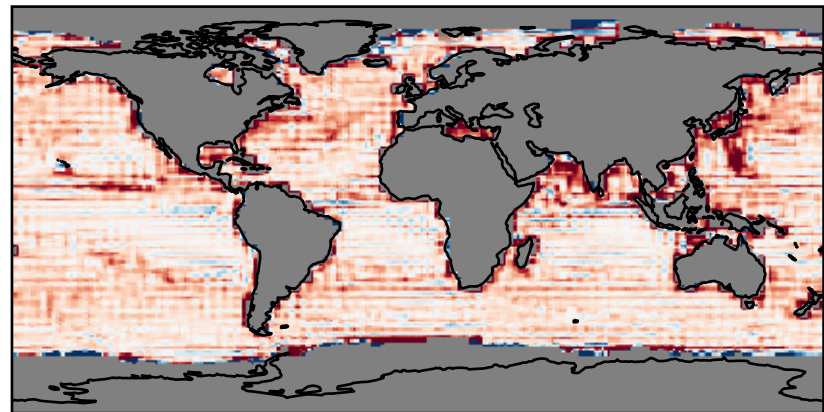
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

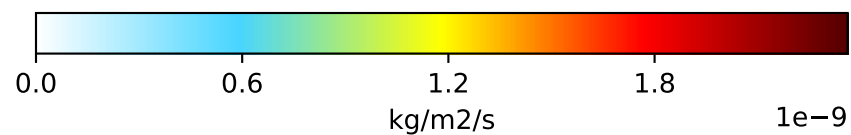
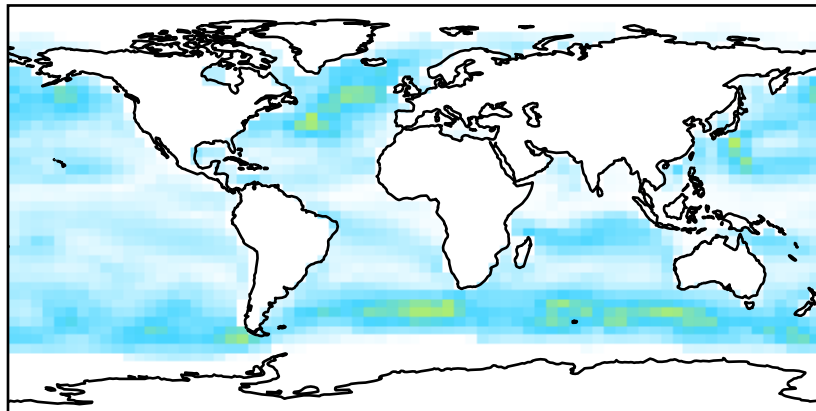


Ratio (1x1.25)
Dev/Ref, Fixed Range

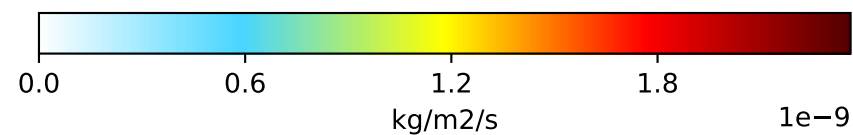
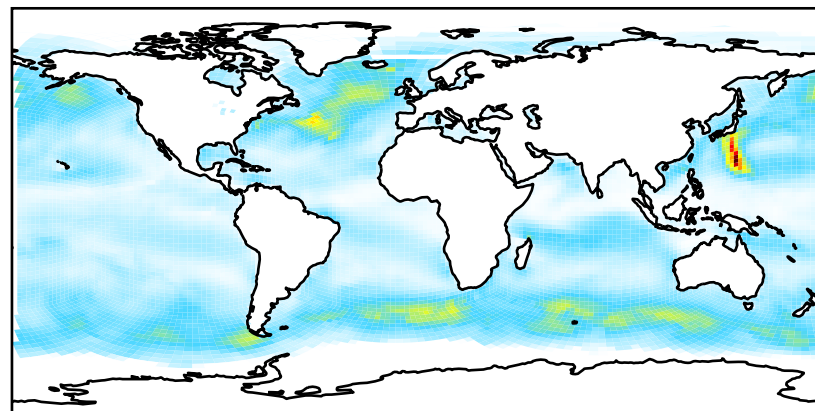


EmisSALC_Natural (Oct2019)

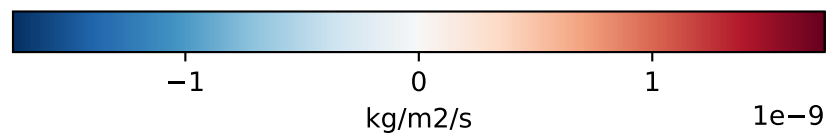
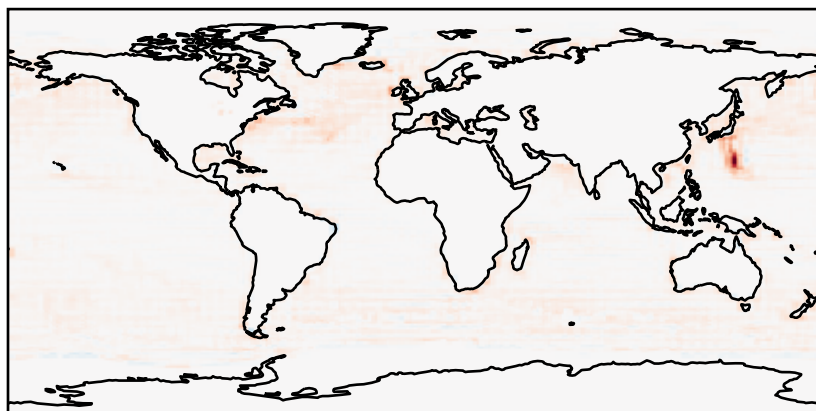
GCC_12.9.0 (Ref)
4.0x5.0



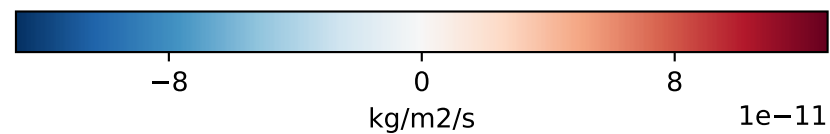
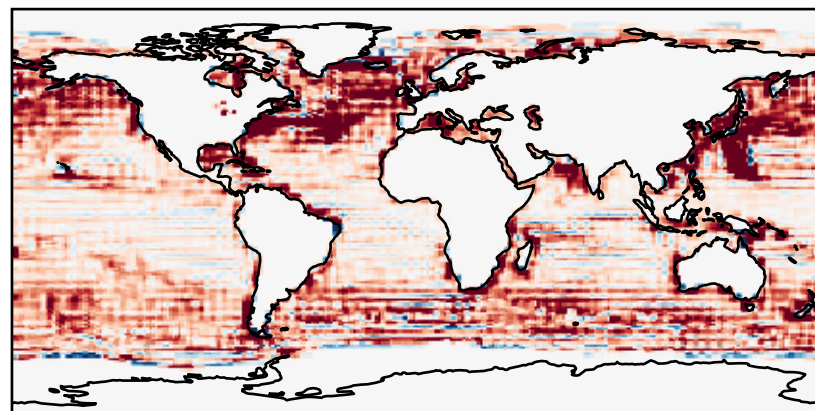
GCC_13.0.0-beta.1 (Dev)
c48



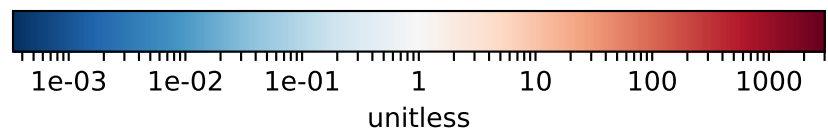
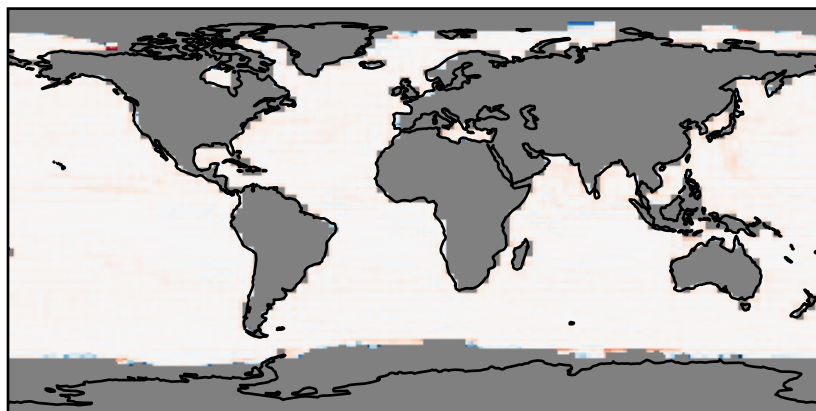
Difference (1x1.25)
Dev - Ref, Dynamic Range



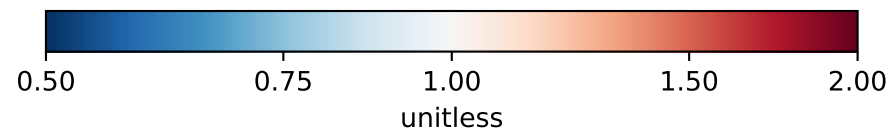
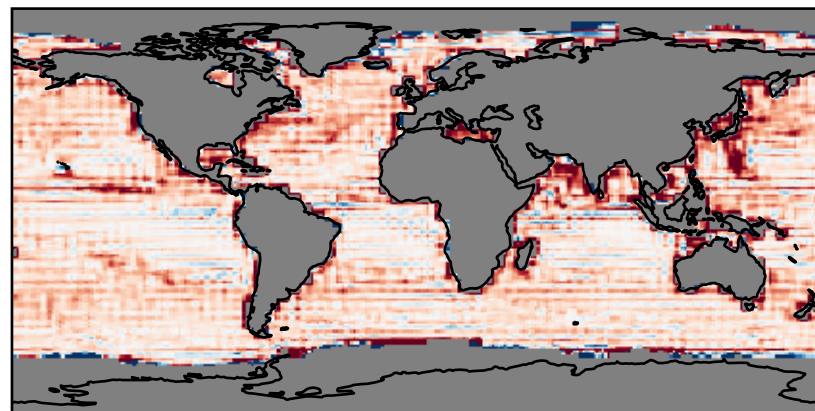
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

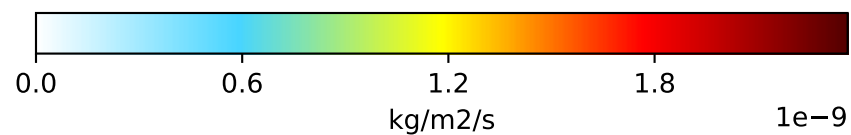
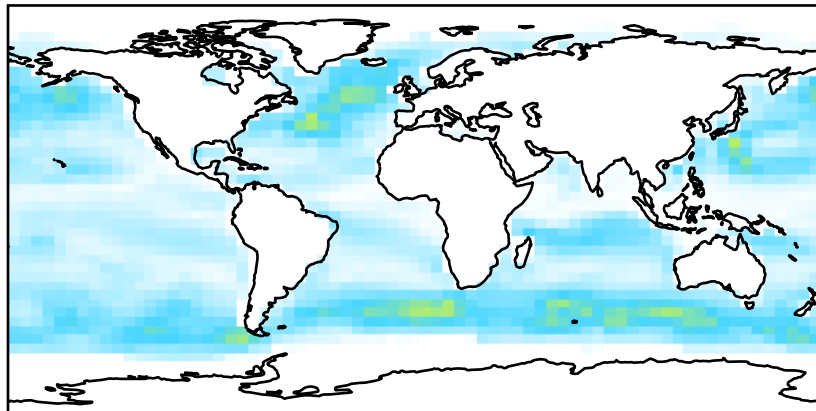


Ratio (1x1.25)
Dev/Ref, Fixed Range

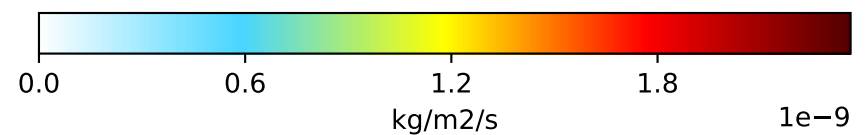
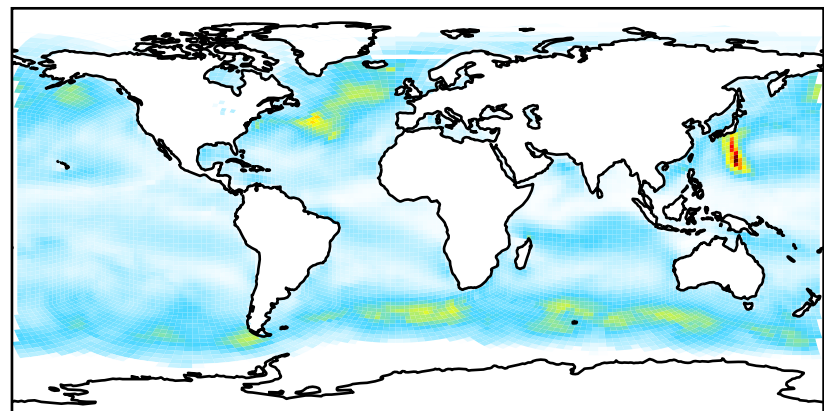


EmisSALCAL_Natural (Oct2019)

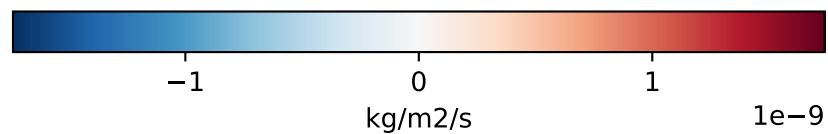
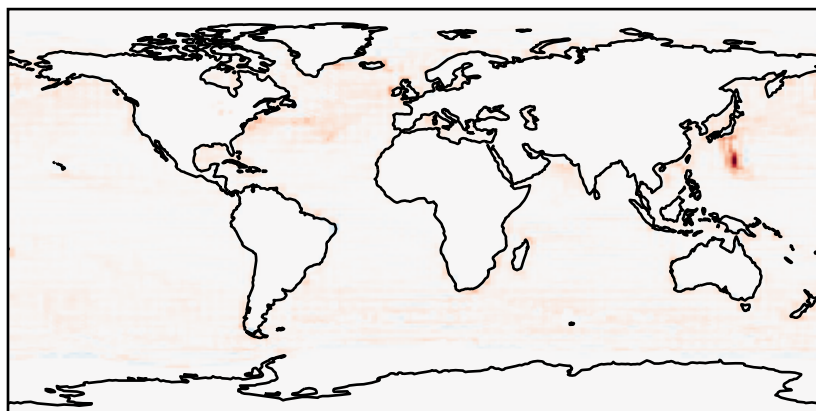
GCC_12.9.0 (Ref)
4.0x5.0



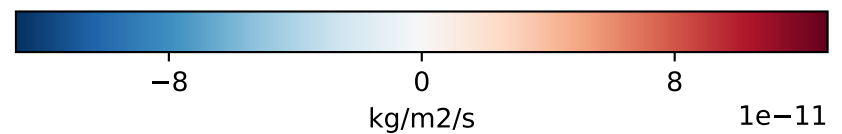
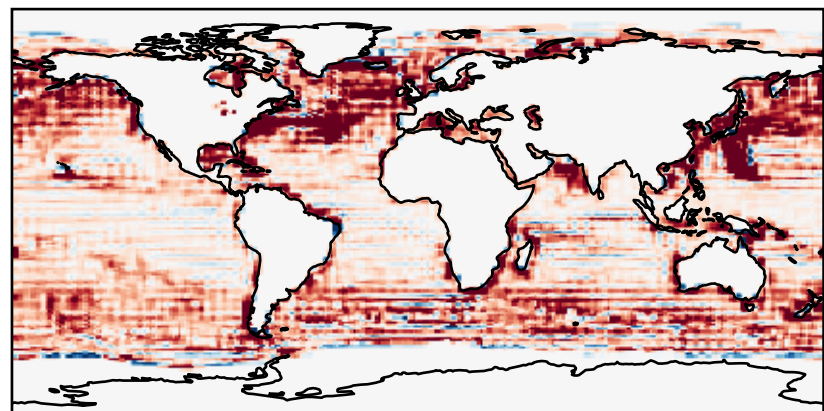
GCC_13.0.0-beta.1 (Dev)
c48



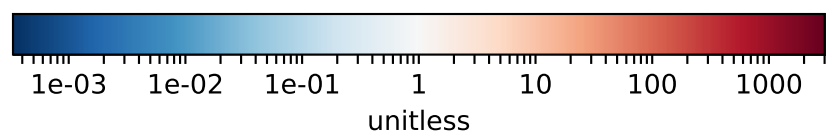
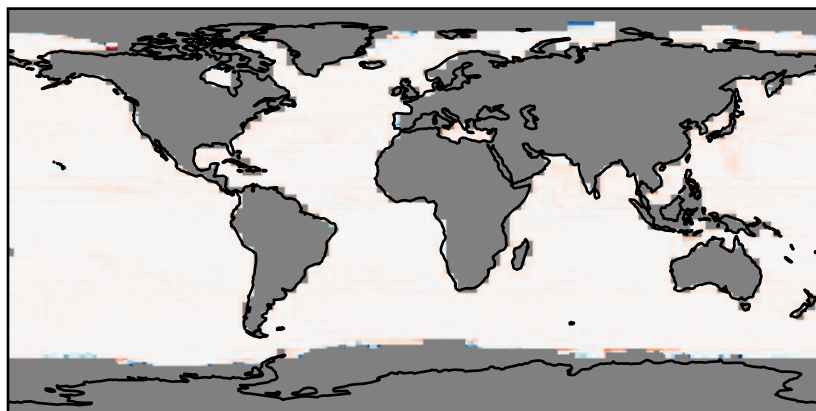
Difference (1x1.25)
Dev - Ref, Dynamic Range



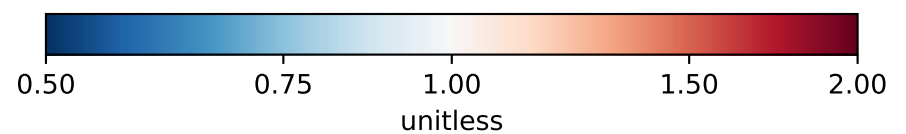
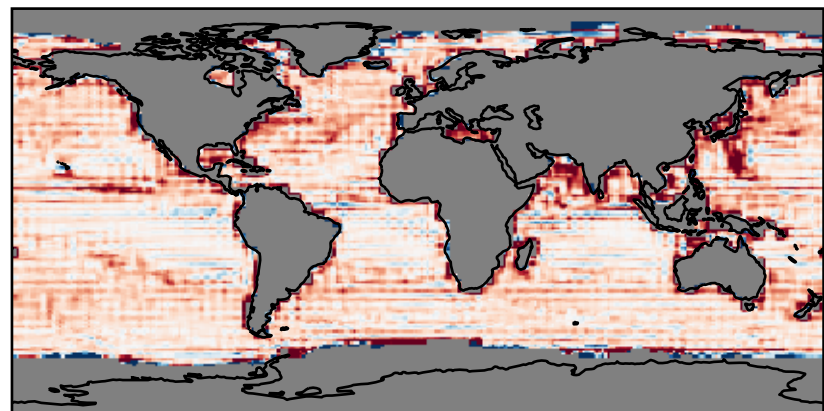
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

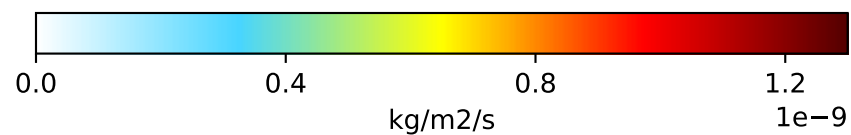
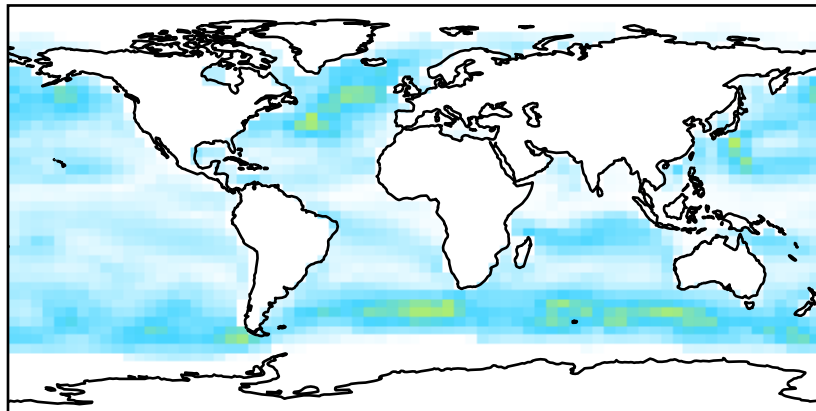


Ratio (1x1.25)
Dev/Ref, Fixed Range

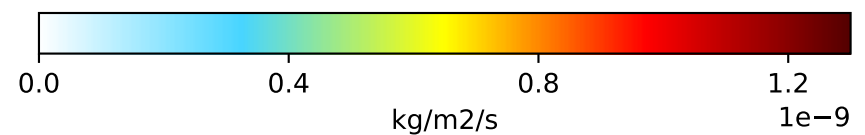
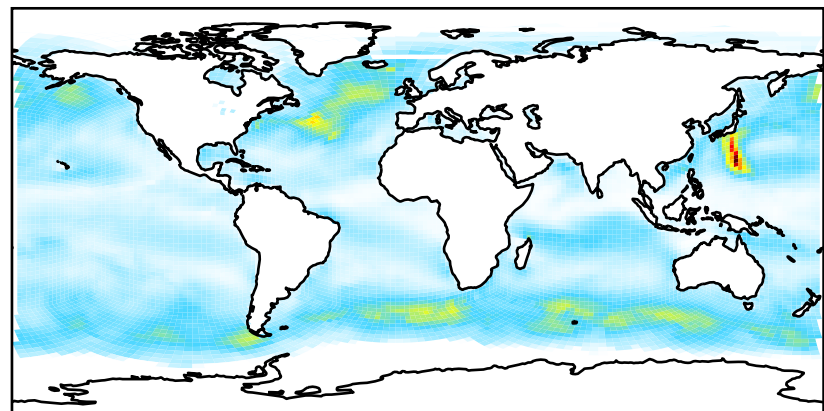


EmisSALCCL_Natural (Oct2019)

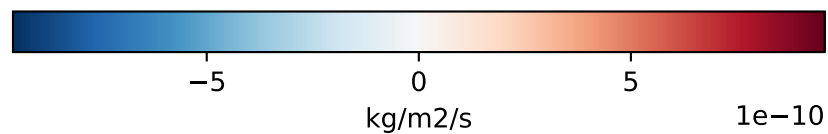
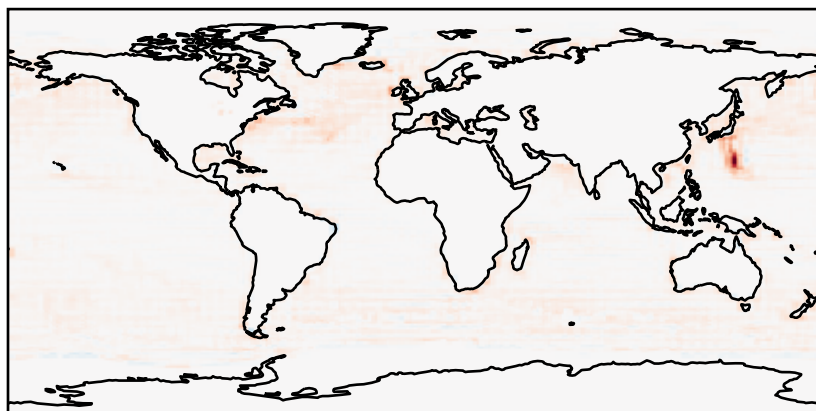
GCC_12.9.0 (Ref)
4.0x5.0



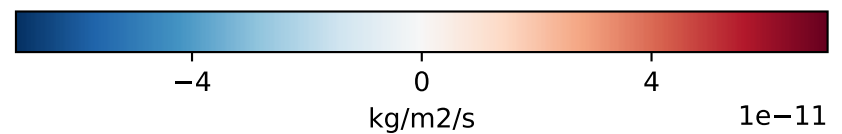
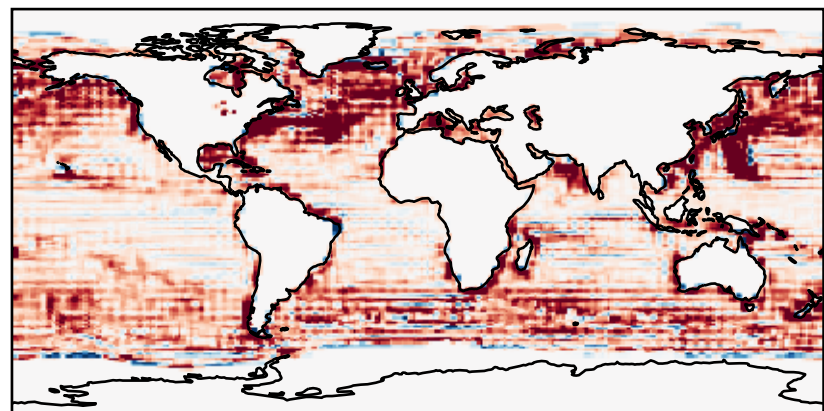
GCC_13.0.0-beta.1 (Dev)
c48



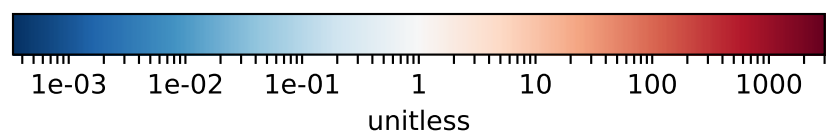
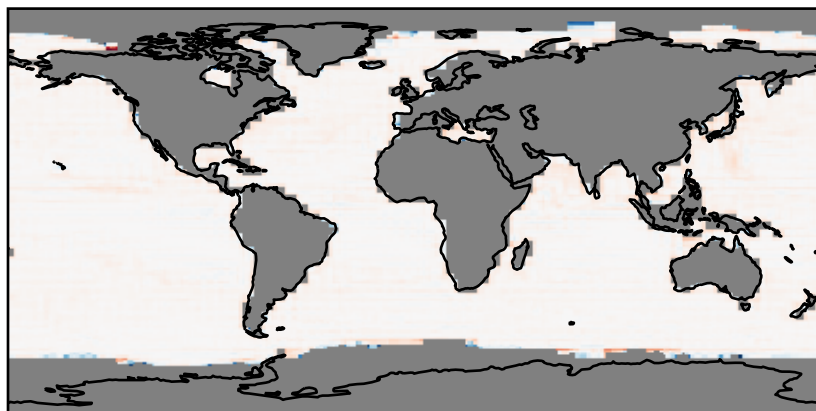
Difference (1x1.25)
Dev - Ref, Dynamic Range



Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range



Ratio (1x1.25)
Dev/Ref, Fixed Range

