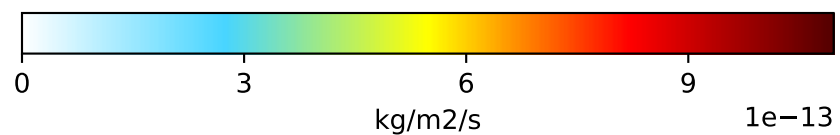
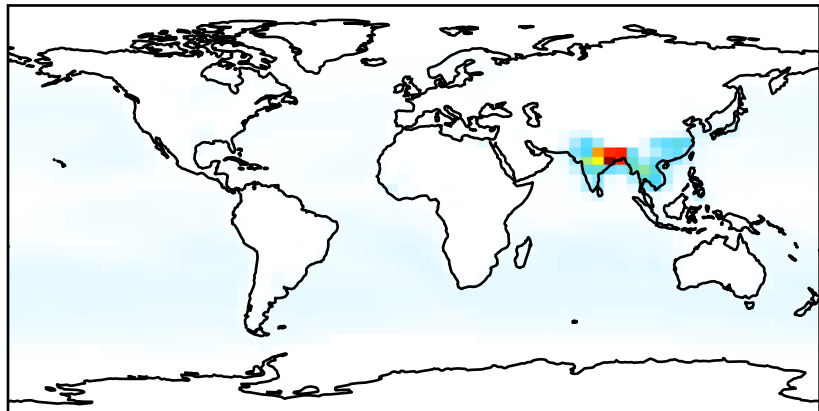
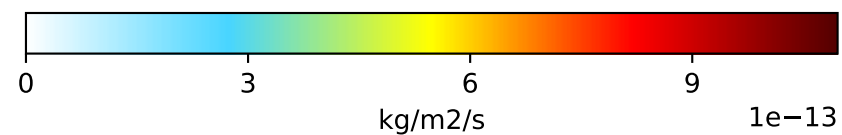
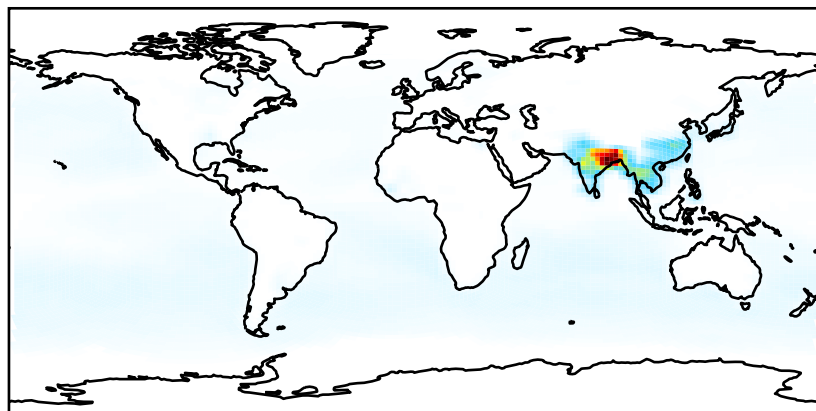


EmisCH3I_Ocean (Oct2019)

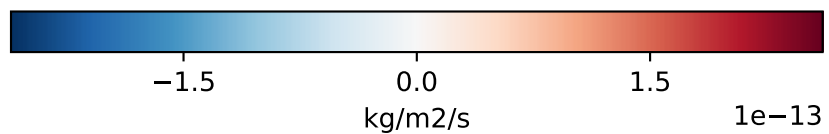
GCC_12.9.0 (Ref)
4.0x5.0



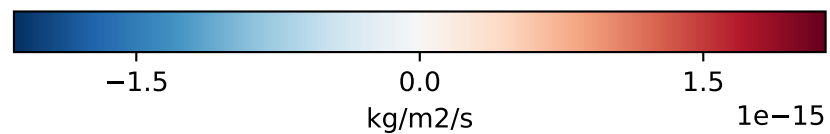
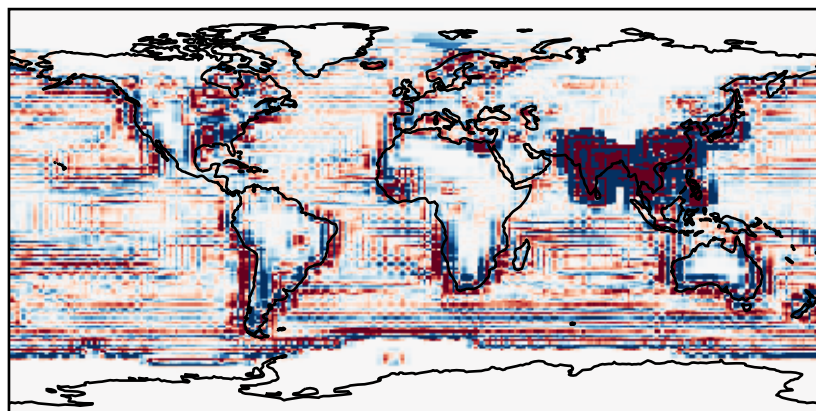
GCC_13.0.0-beta.1 (Dev)
c48



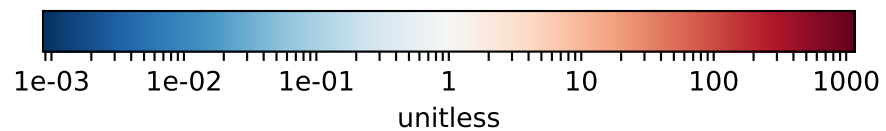
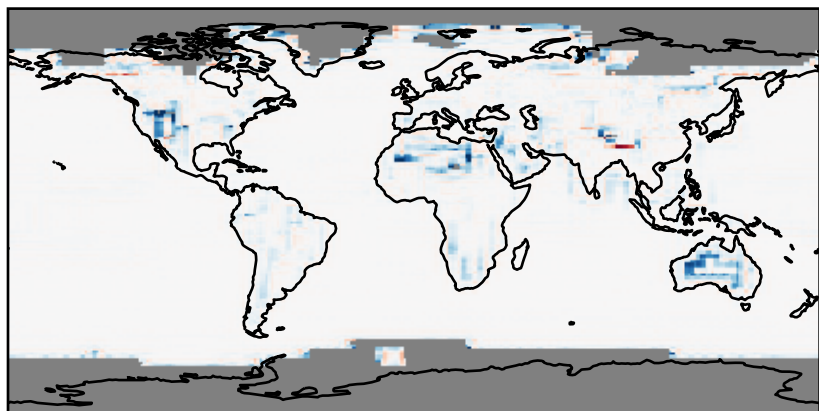
Difference (1x1.25)
Dev - Ref, Dynamic Range



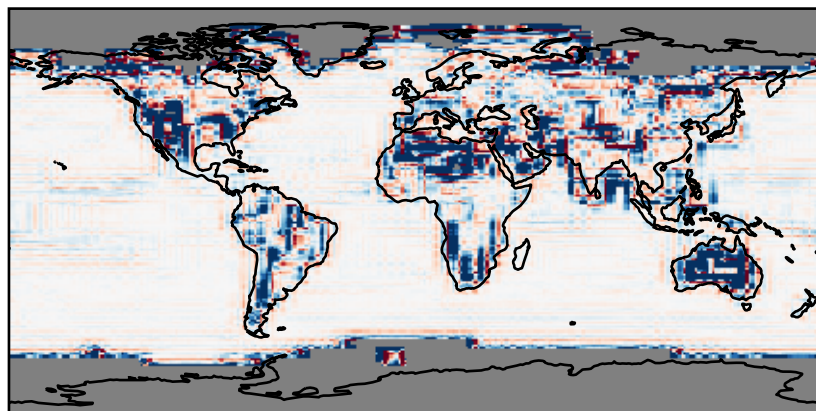
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

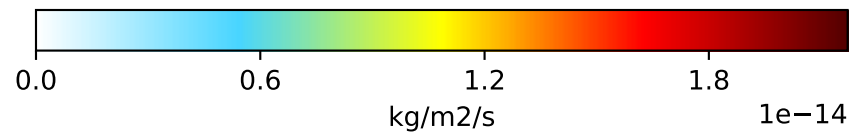
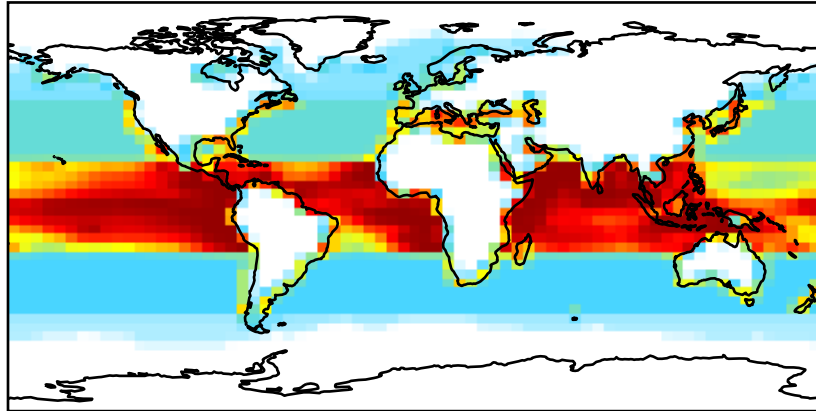


Ratio (1x1.25)
Dev/Ref, Fixed Range

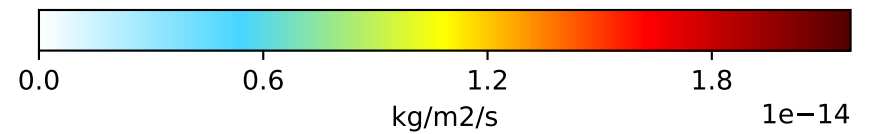
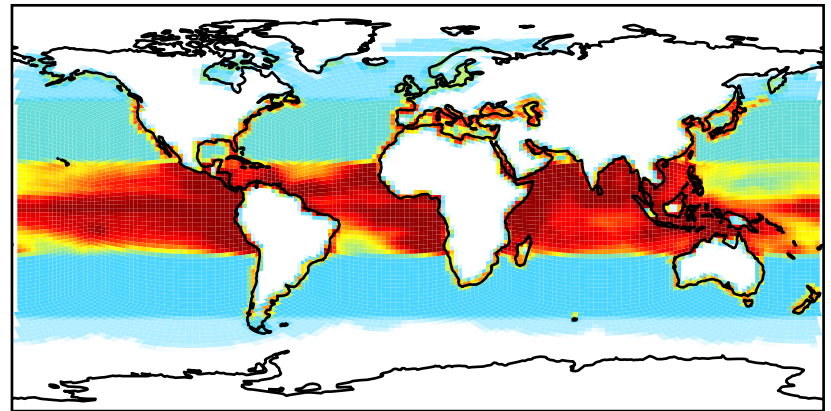


EmisCH2I2_Ocean (Oct2019)

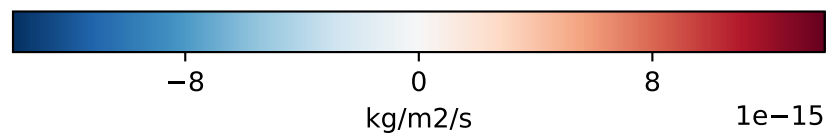
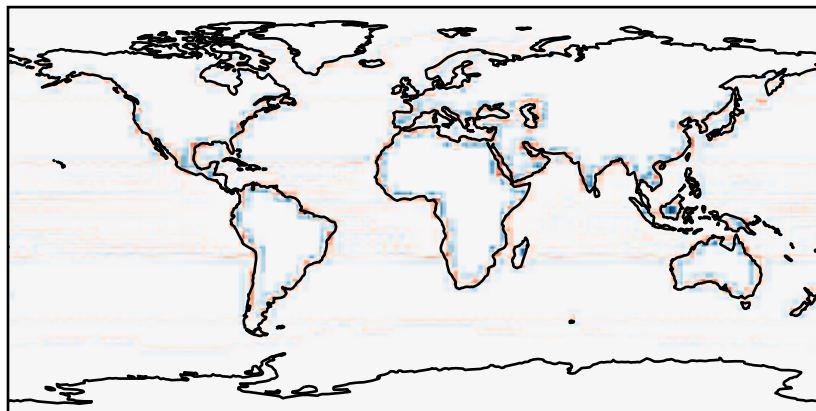
GCC_12.9.0 (Ref)
4.0x5.0



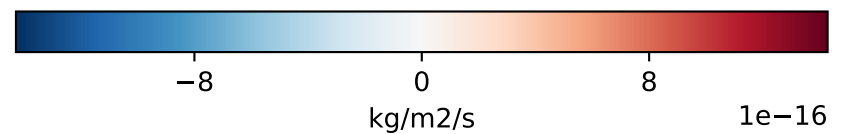
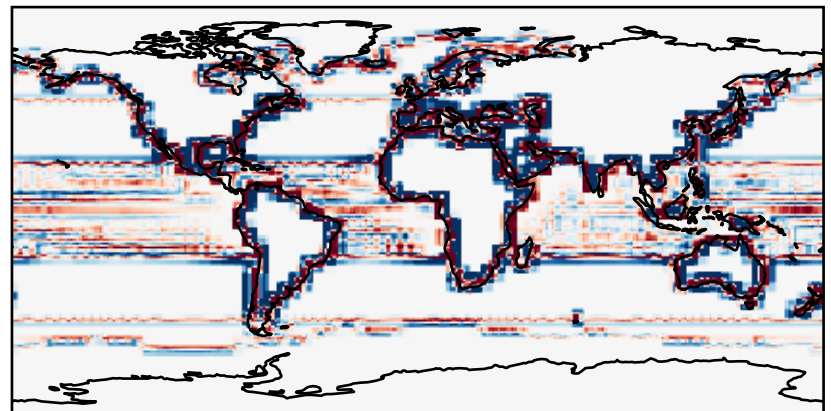
GCC_13.0.0-beta.1 (Dev)
c48



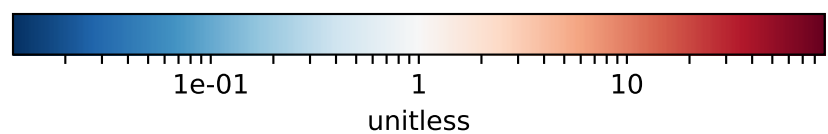
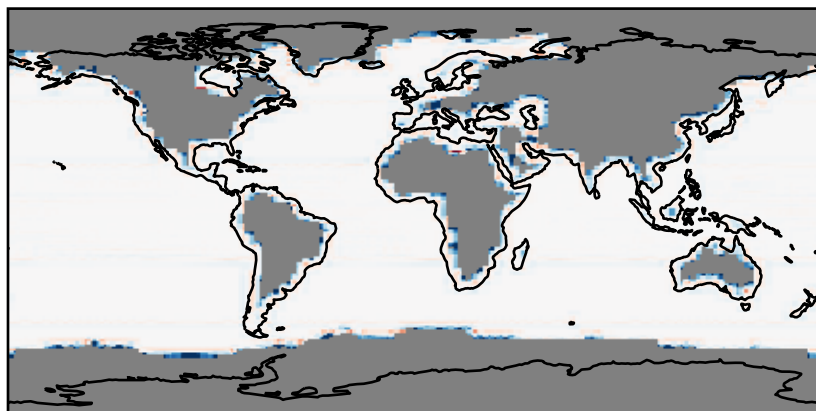
Difference (1x1.25)
Dev - Ref, Dynamic Range



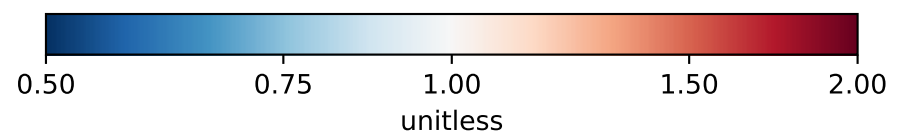
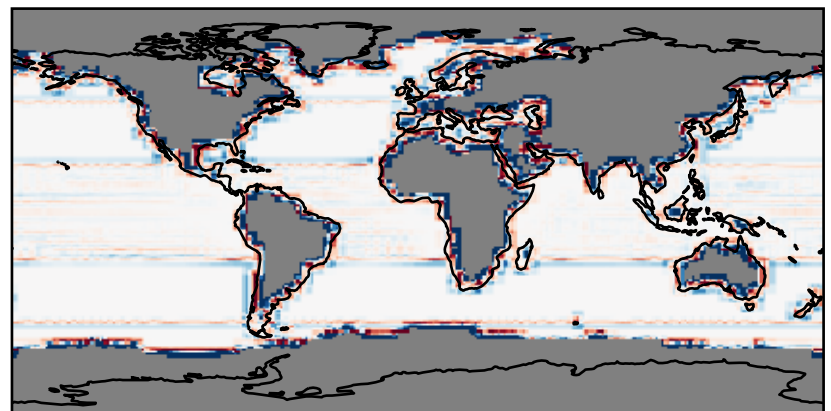
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

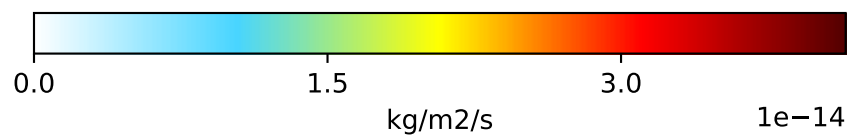
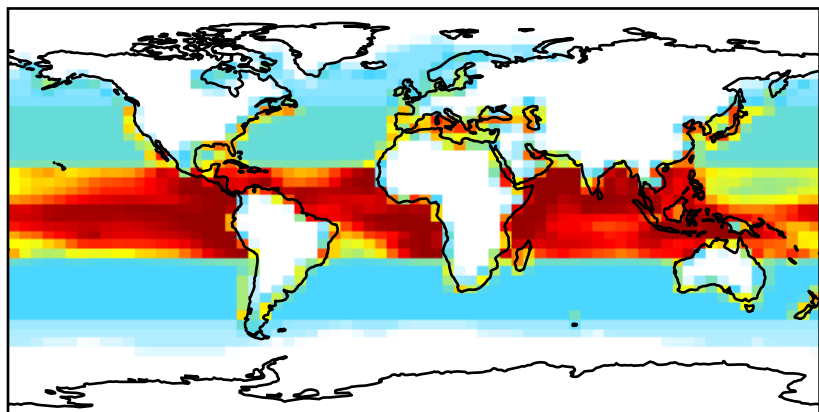


Ratio (1x1.25)
Dev/Ref, Fixed Range

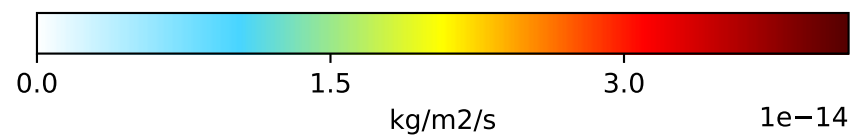
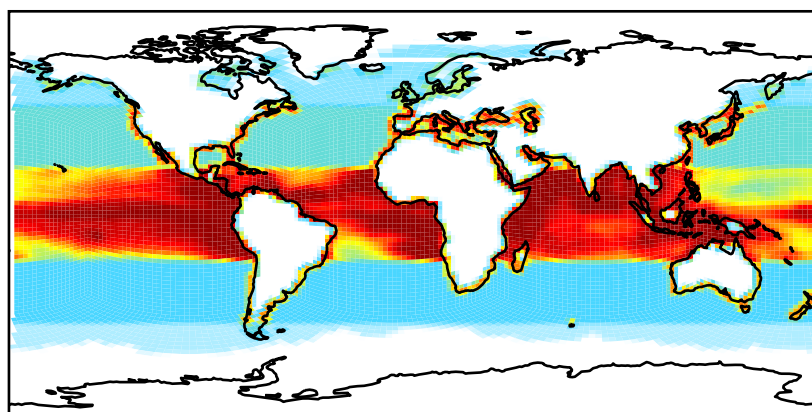


EmisCH2ICl_Ocean (Oct2019)

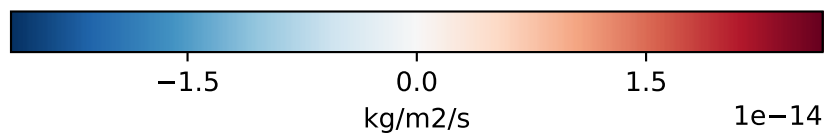
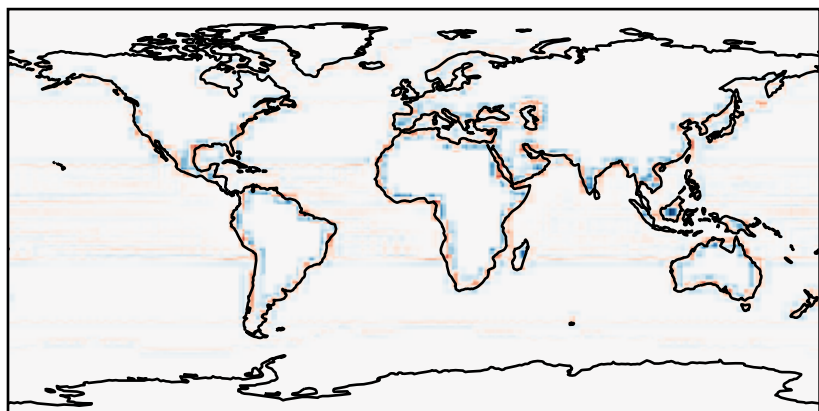
GCC_12.9.0 (Ref)
4.0x5.0



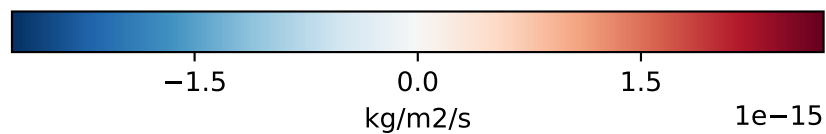
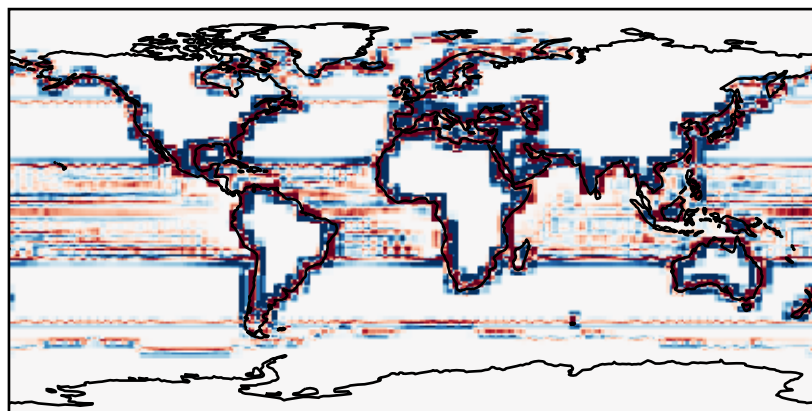
GCC_13.0.0-beta.1 (Dev)
c48



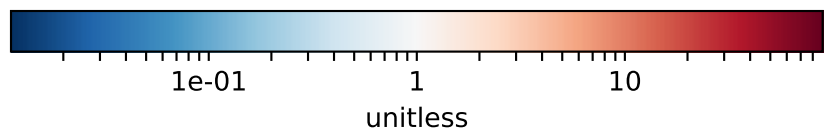
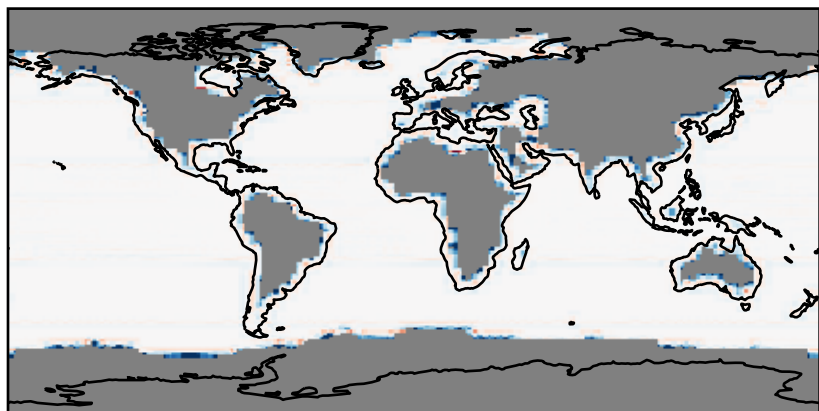
Difference (1x1.25)
Dev - Ref, Dynamic Range



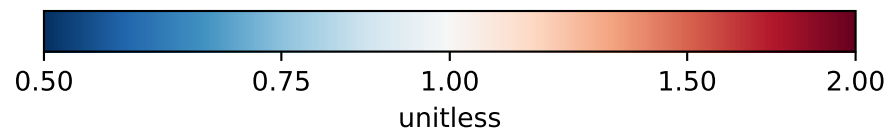
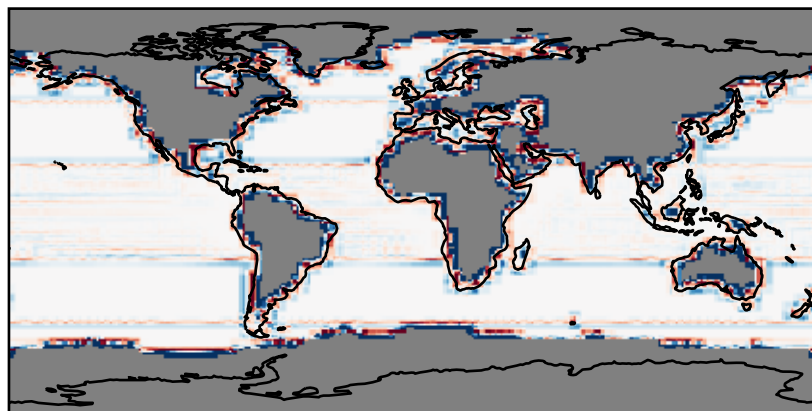
Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range

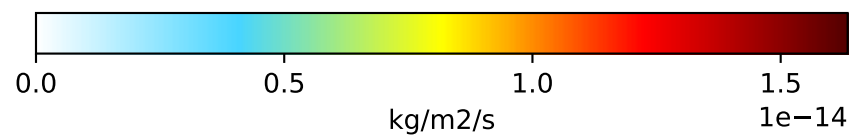
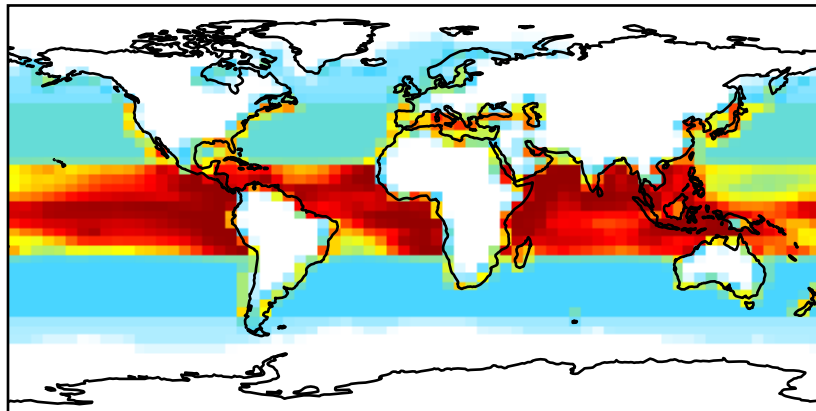


Ratio (1x1.25)
Dev/Ref, Fixed Range

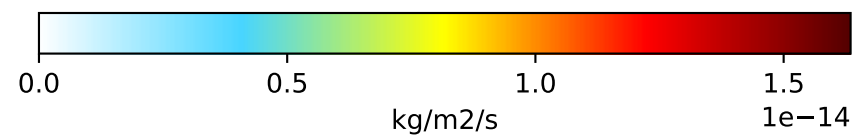
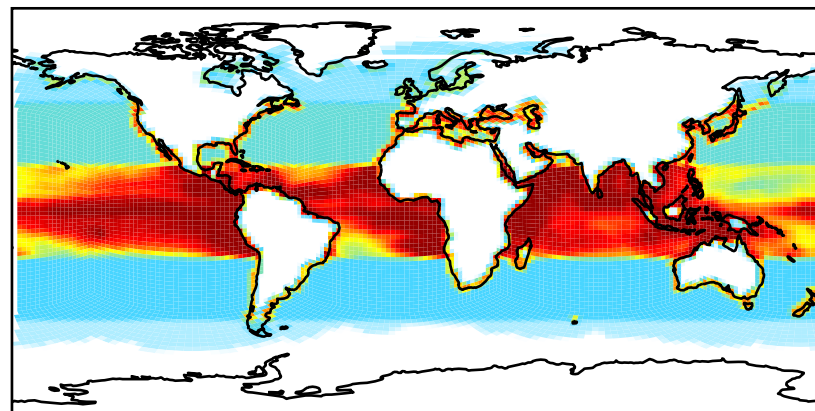


EmisCH2IBr_Ocean (Oct2019)

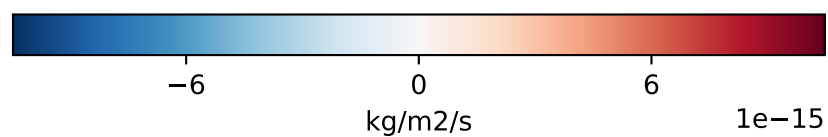
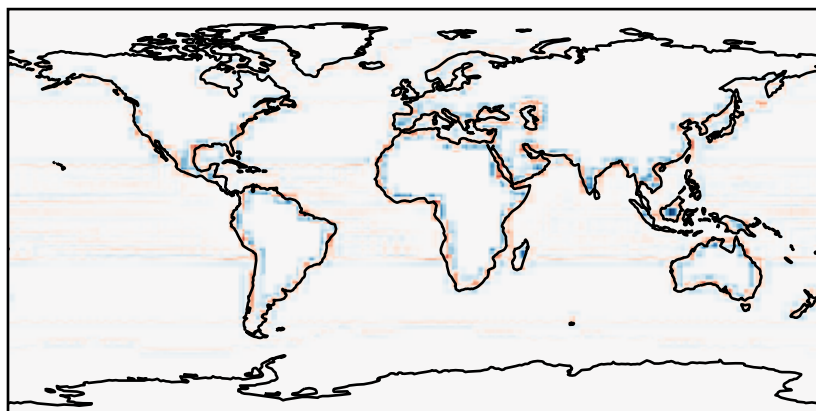
GCC_12.9.0 (Ref)
4.0x5.0



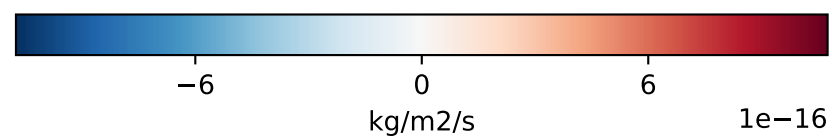
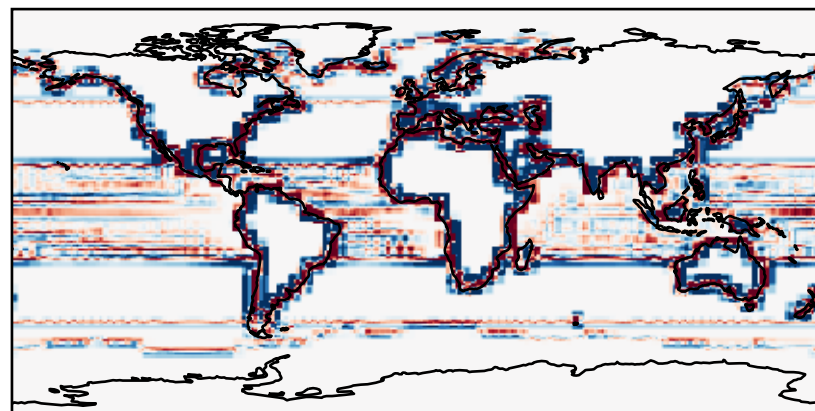
GCC_13.0.0-beta.1 (Dev)
c48



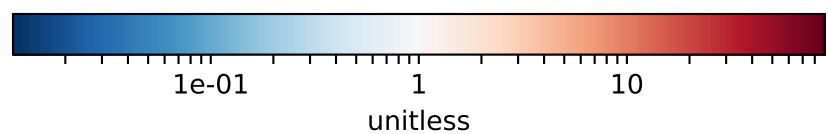
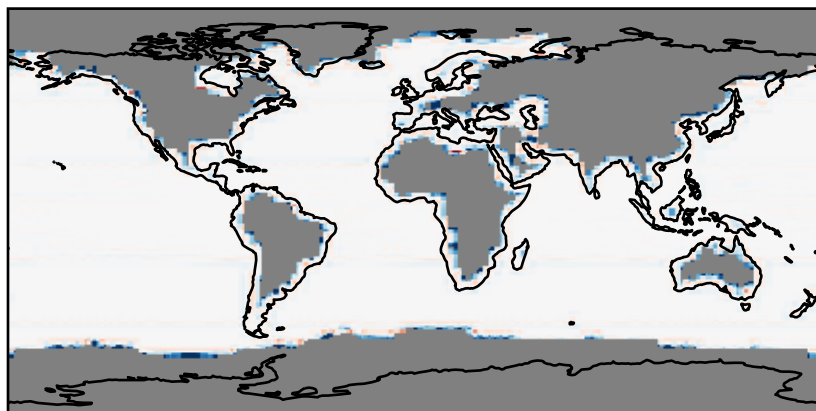
Difference (1x1.25)
Dev - Ref, Dynamic Range



Difference (1x1.25)
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)
Dev/Ref, Dynamic Range



Ratio (1x1.25)
Dev/Ref, Fixed Range

