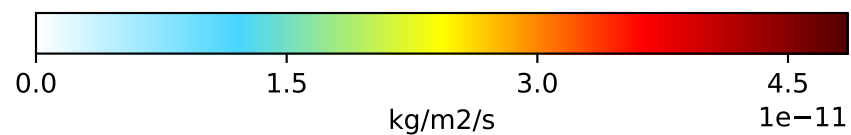
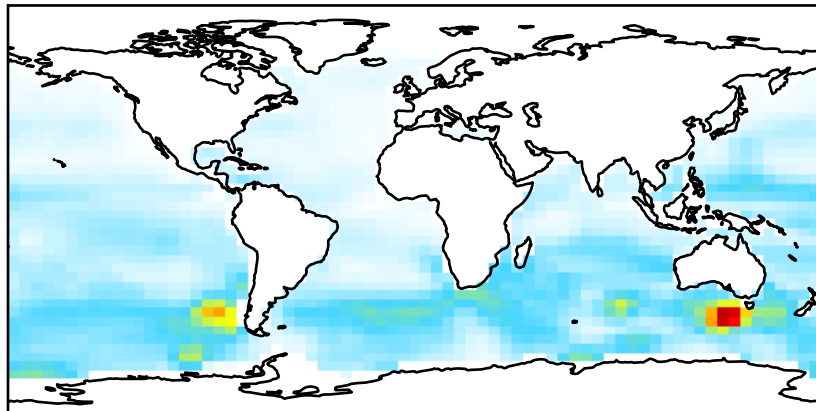
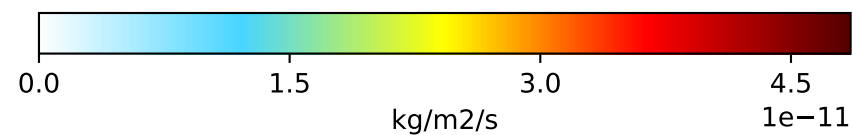
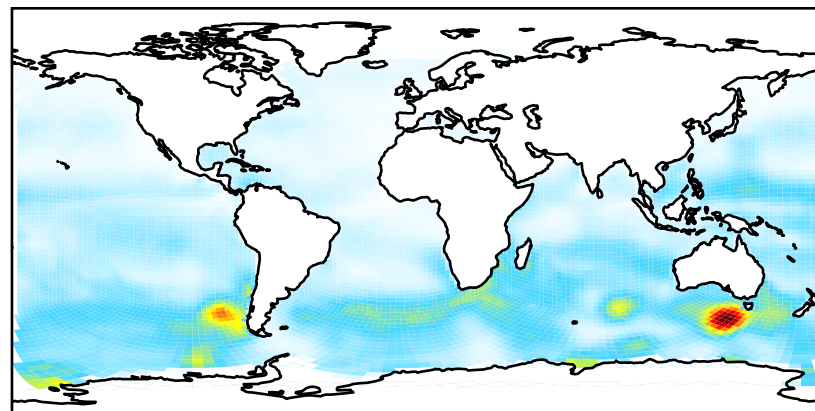


# EmisDMS\_Ocean (Jan2019)

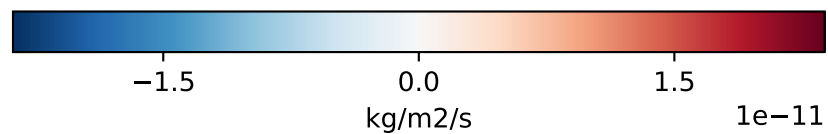
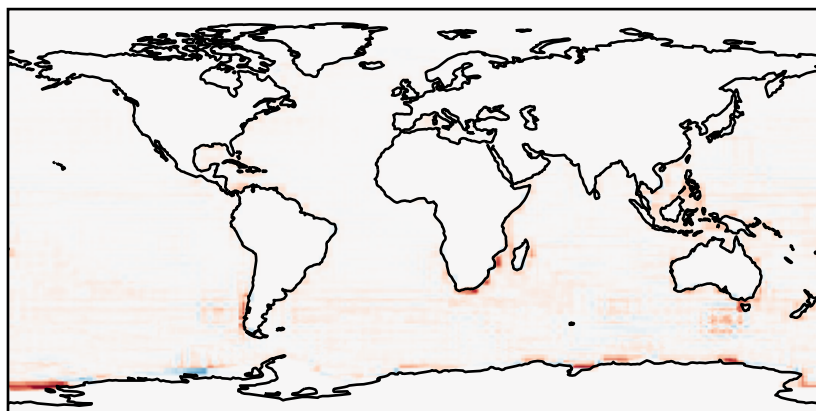
GCC\_12.9.0 (Ref)  
4.0x5.0



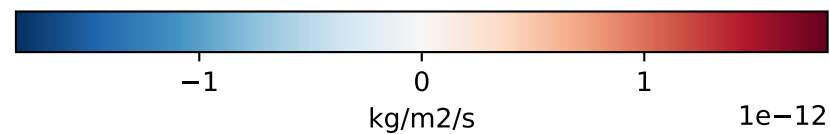
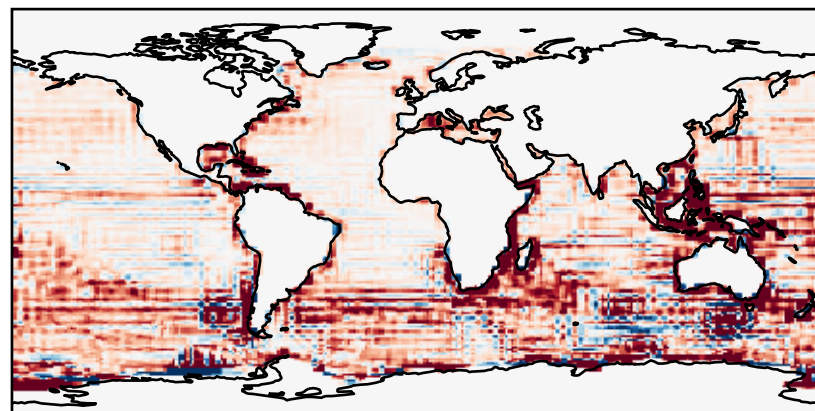
GCC\_13.0.0-beta.1 (Dev)  
c48



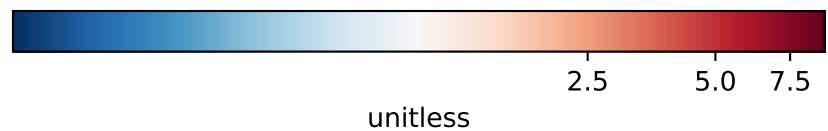
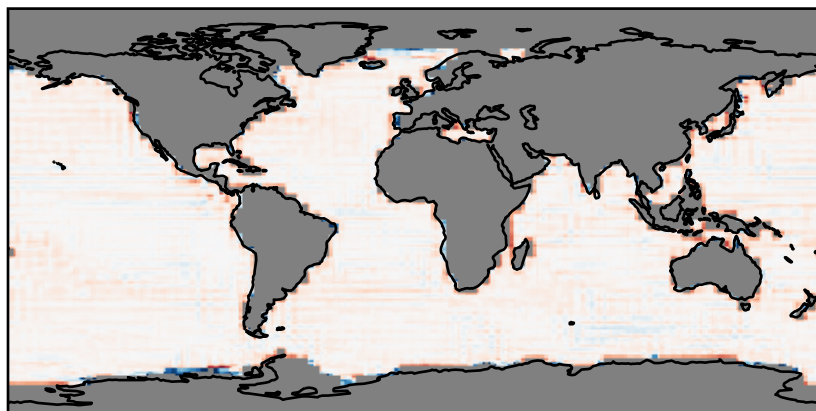
Difference (1x1.25)  
Dev - Ref, Dynamic Range



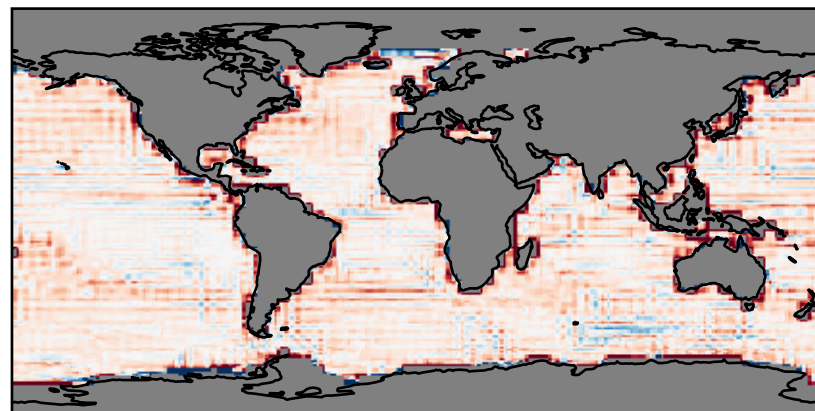
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

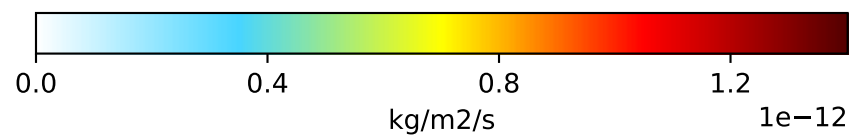
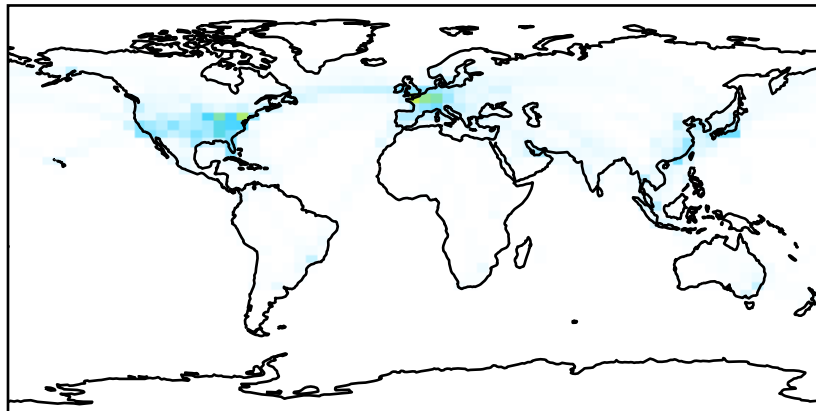


Ratio (1x1.25)  
Dev/Ref, Fixed Range

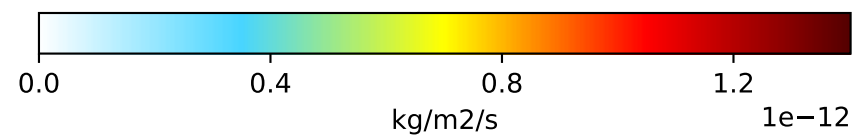
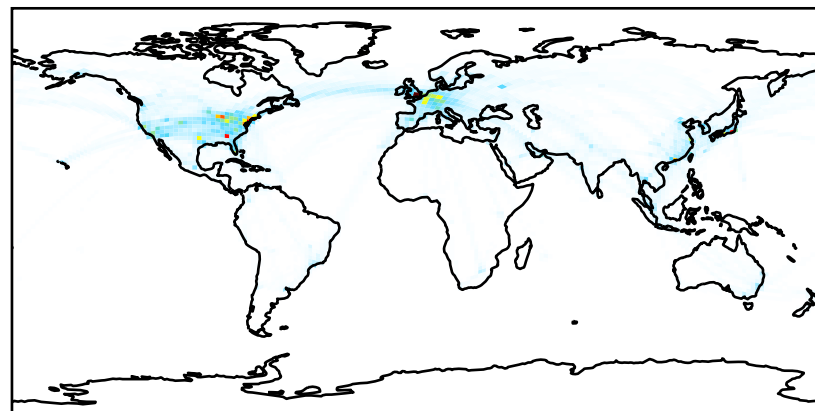


# EmisSO2\_Aircraft (Jan2019)

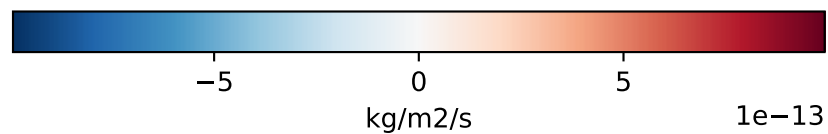
GCC\_12.9.0 (Ref)  
4.0x5.0



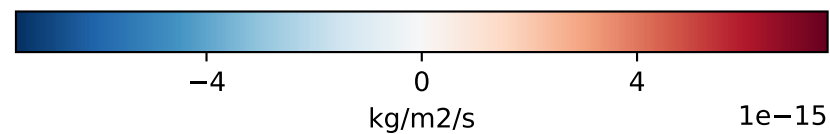
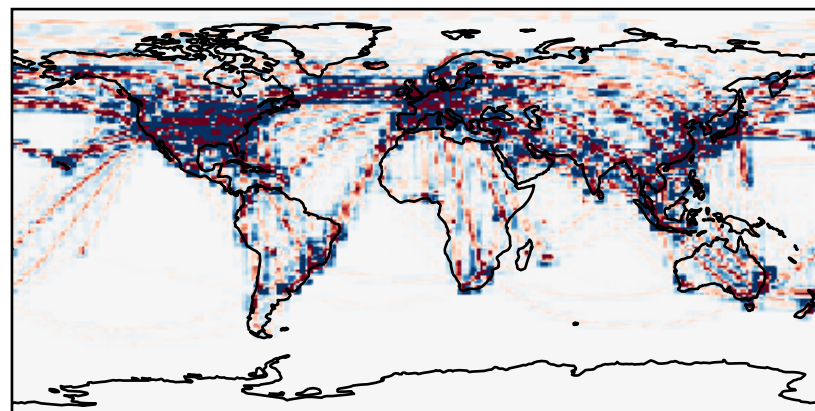
GCC\_13.0.0-beta.1 (Dev)  
c48



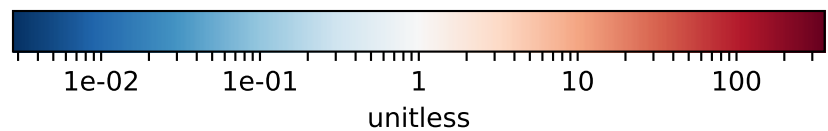
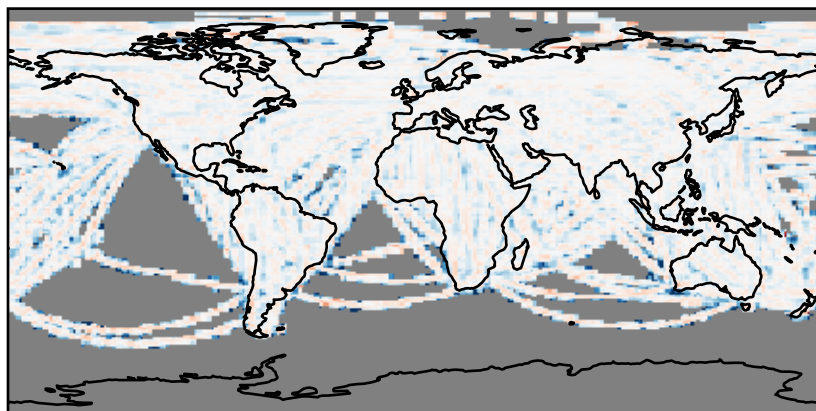
Difference (1x1.25)  
Dev - Ref, Dynamic Range



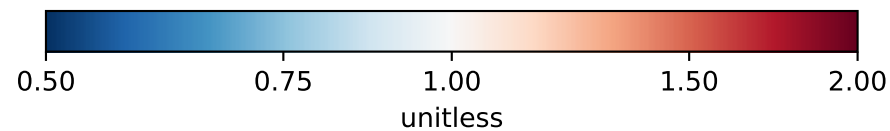
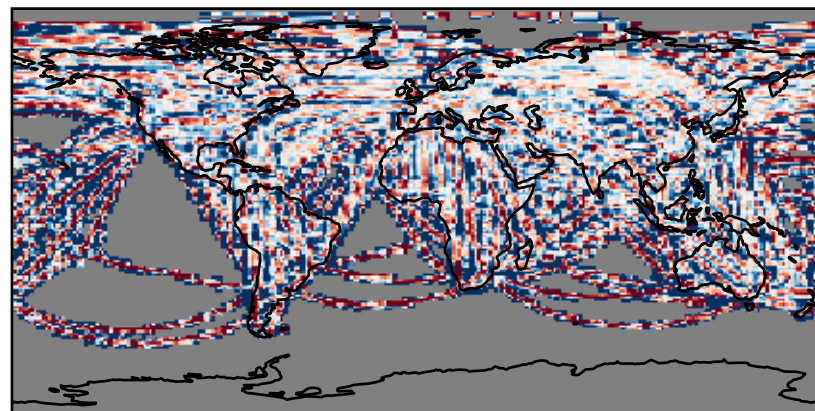
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



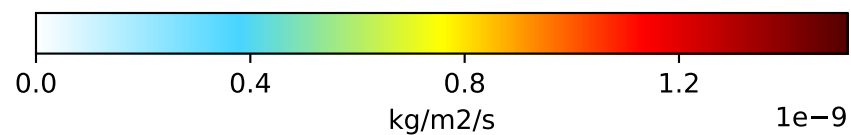
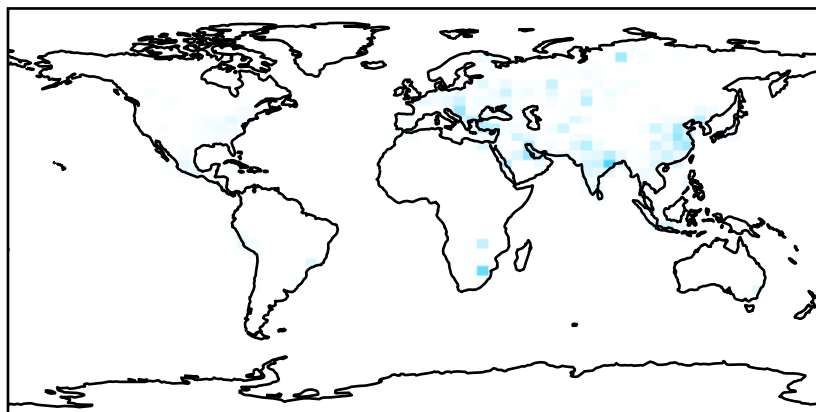
Ratio (1x1.25)  
Dev/Ref, Fixed Range



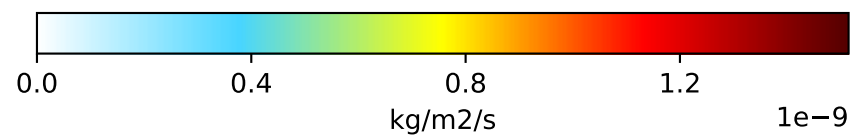
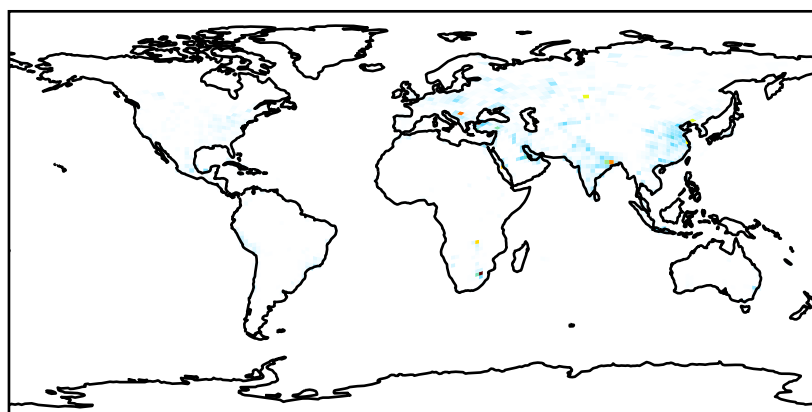


# EmisSO2\_Anthro (Jan2019)

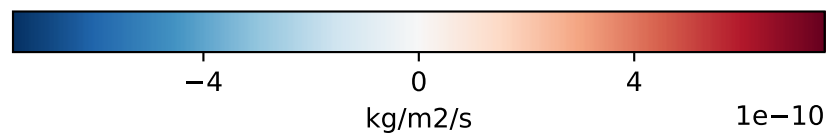
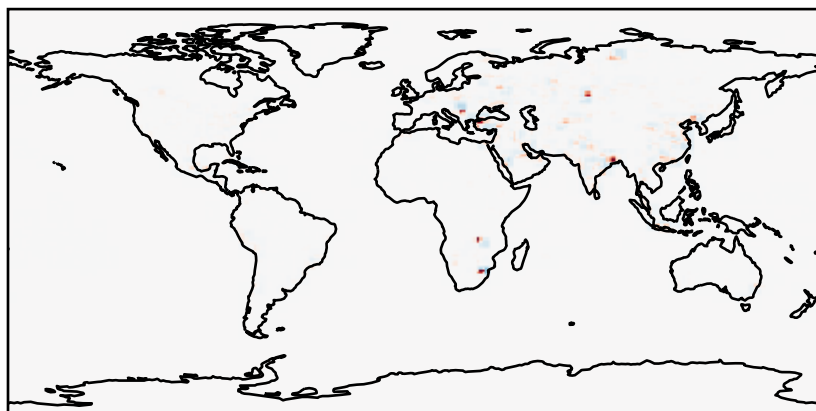
GCC\_12.9.0 (Ref)  
4.0x5.0



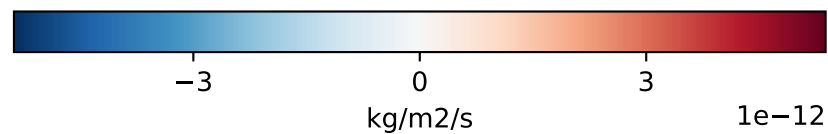
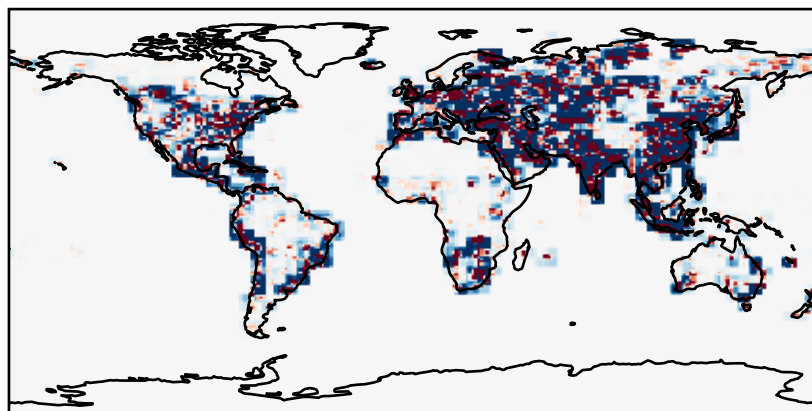
GCC\_13.0.0-beta.1 (Dev)  
c48



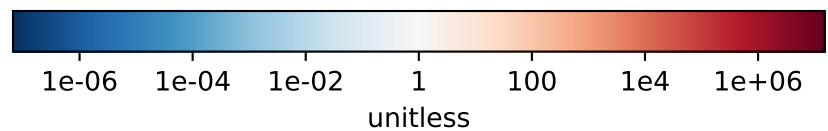
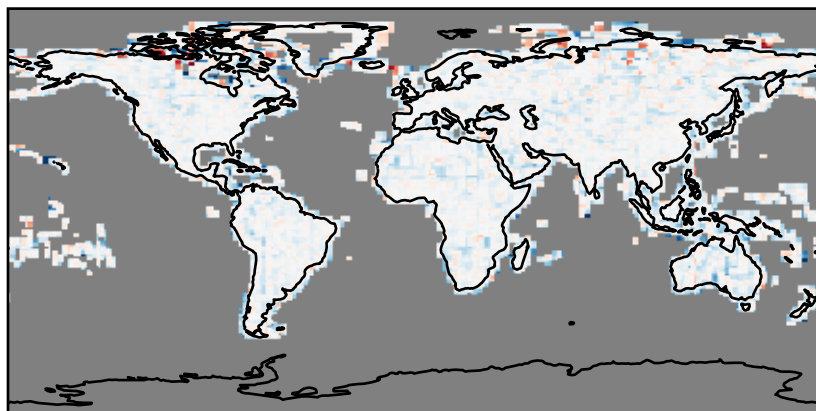
Difference (1x1.25)  
Dev - Ref, Dynamic Range



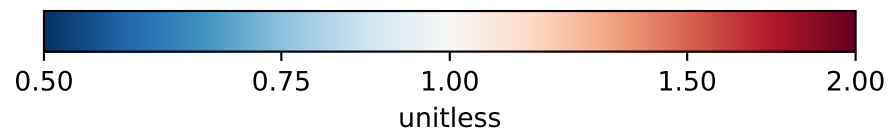
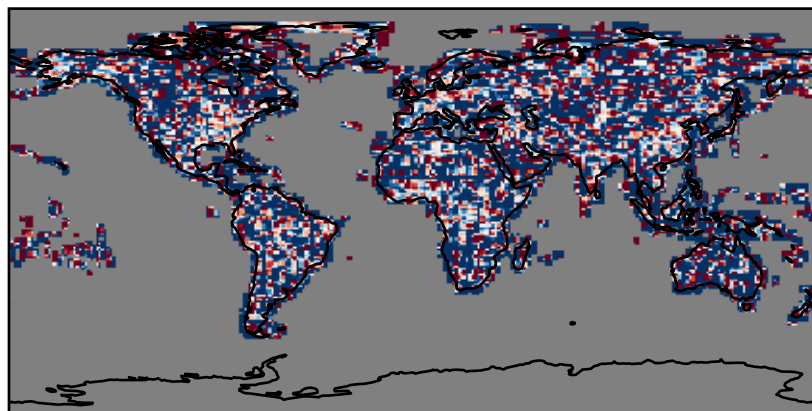
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

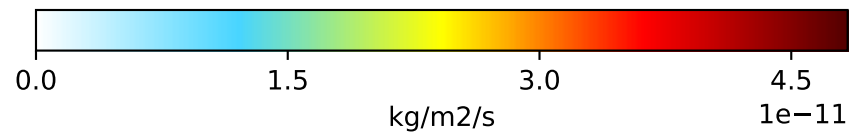
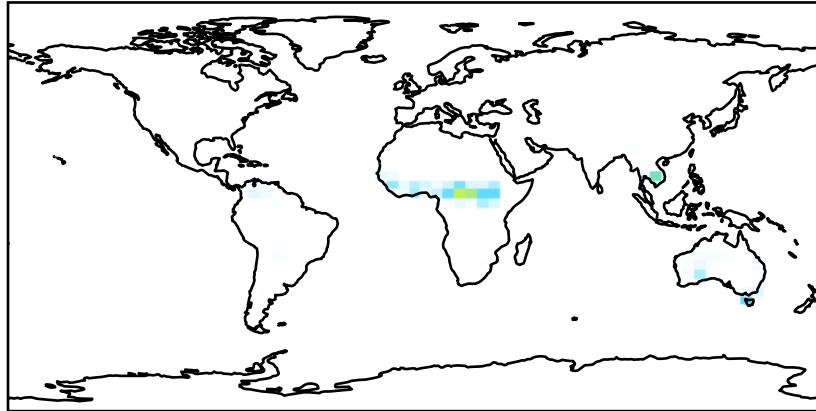


Ratio (1x1.25)  
Dev/Ref, Fixed Range

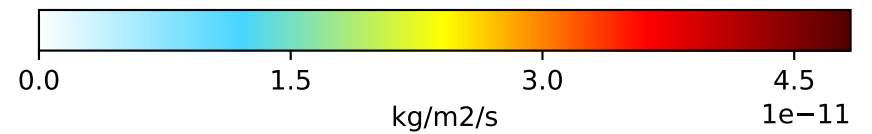
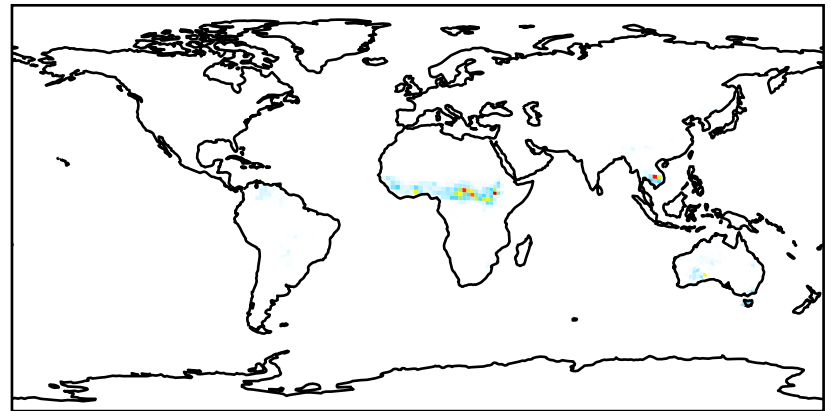


# EmisSO2\_BioBurn (Jan2019)

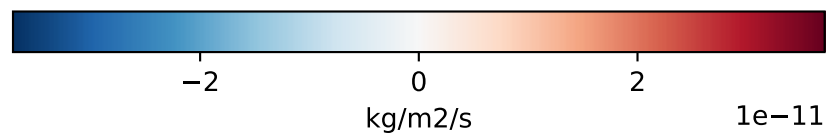
GCC\_12.9.0 (Ref)  
4.0x5.0



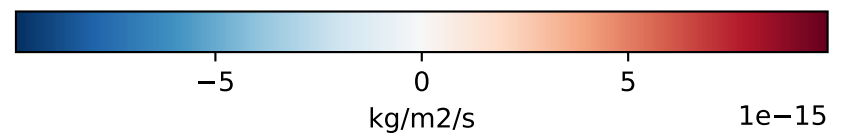
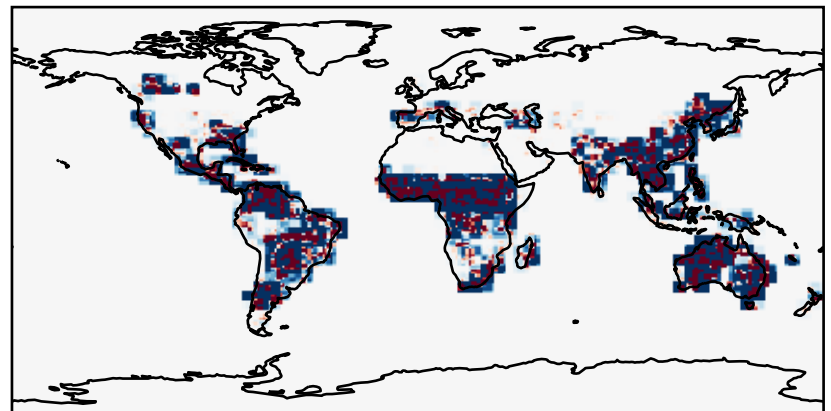
GCC\_13.0.0-beta.1 (Dev)  
c48



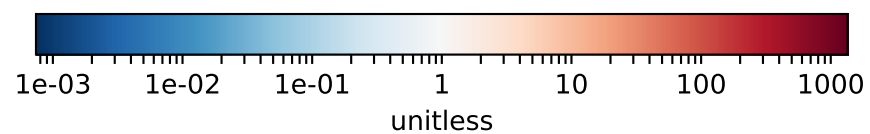
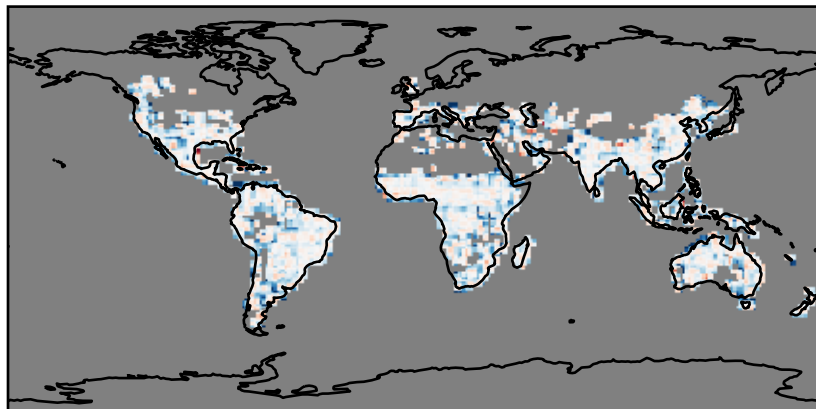
Difference (1x1.25)  
Dev - Ref, Dynamic Range



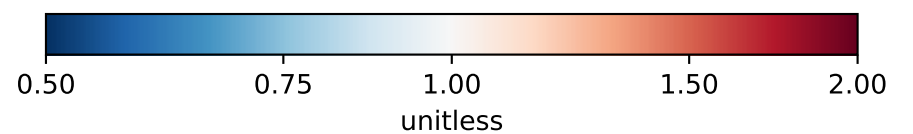
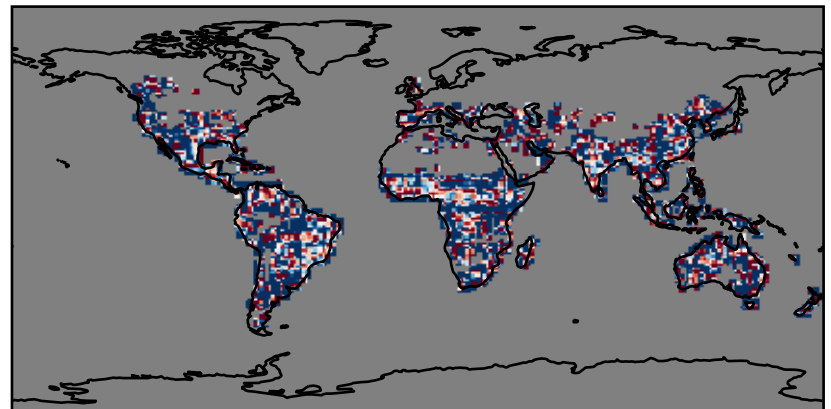
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



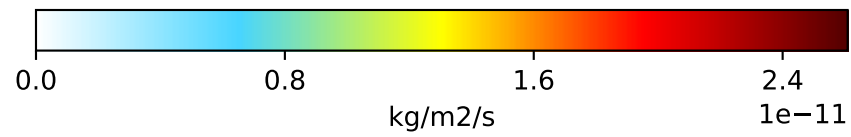
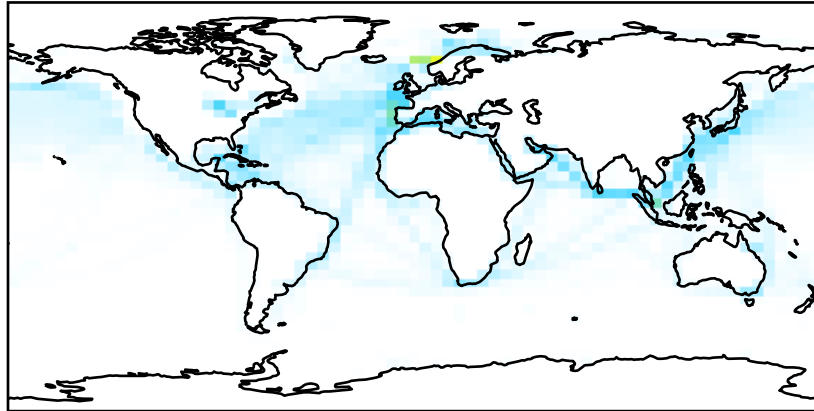
Ratio (1x1.25)  
Dev/Ref, Fixed Range



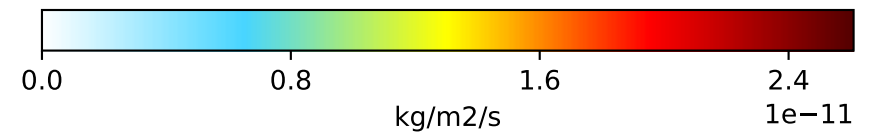
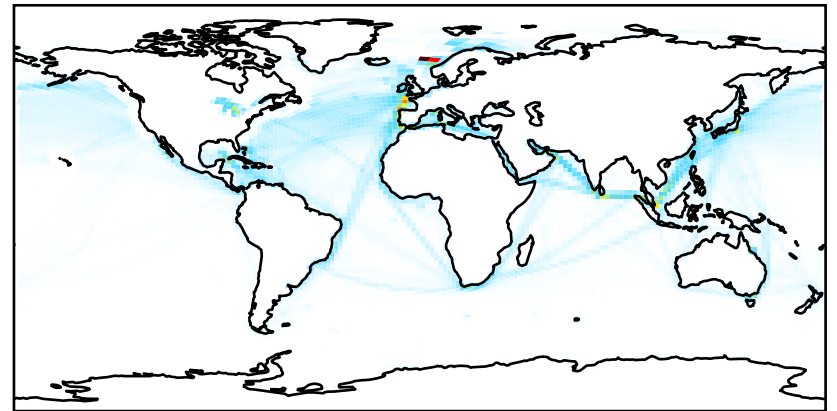


# EmisSO2\_Ship (Jan2019)

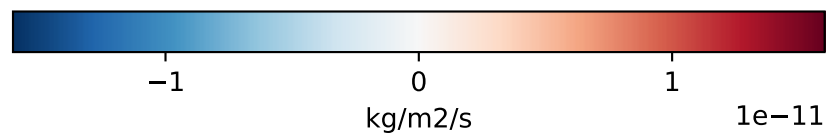
GCC\_12.9.0 (Ref)  
4.0x5.0



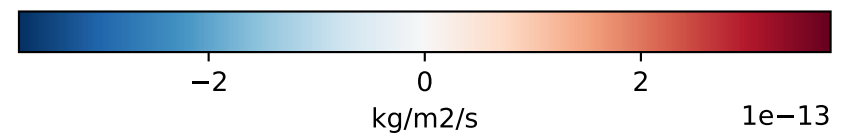
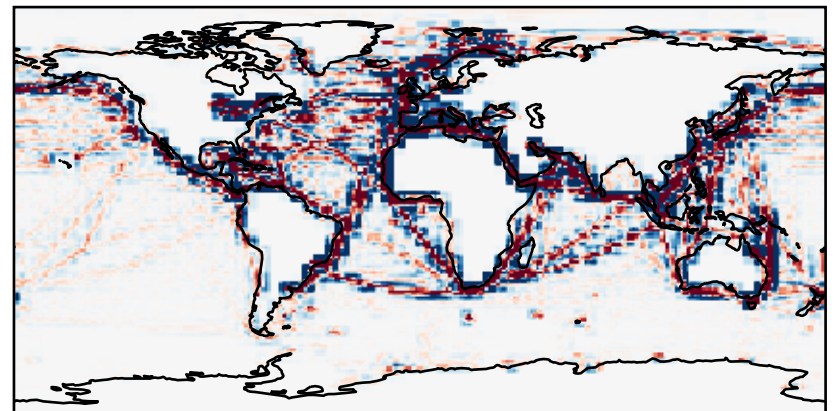
GCC\_13.0.0-beta.1 (Dev)  
c48



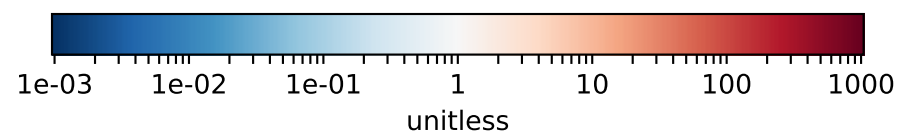
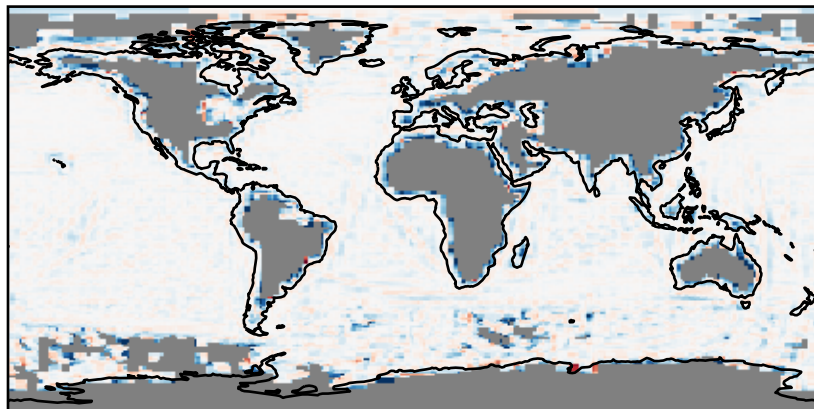
Difference (1x1.25)  
Dev - Ref, Dynamic Range



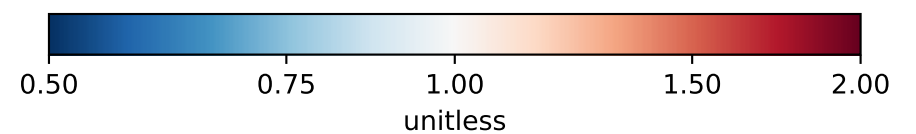
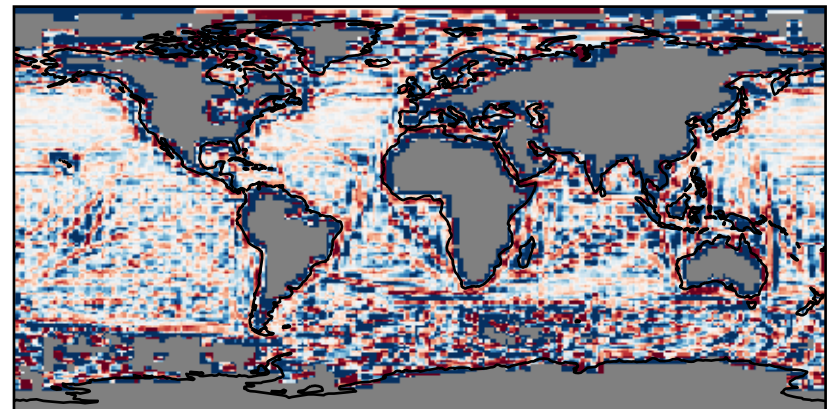
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

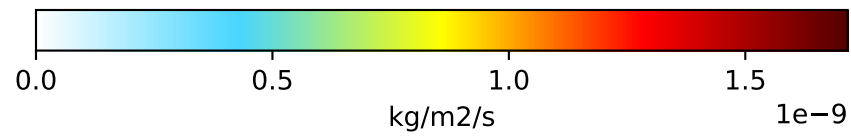
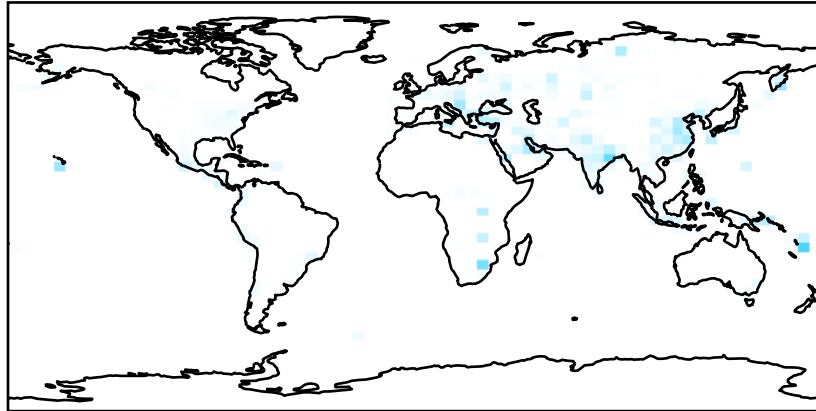


Ratio (1x1.25)  
Dev/Ref, Fixed Range

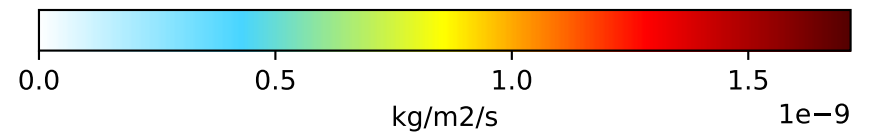
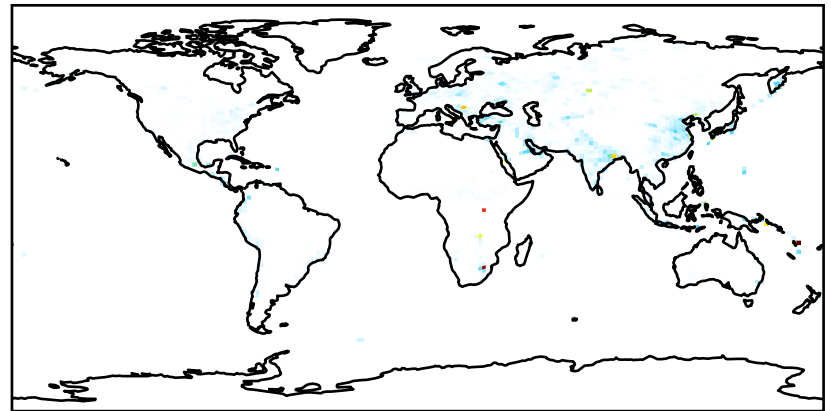


# EmisSO2\_Total (Jan2019)

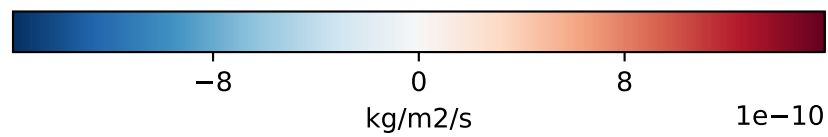
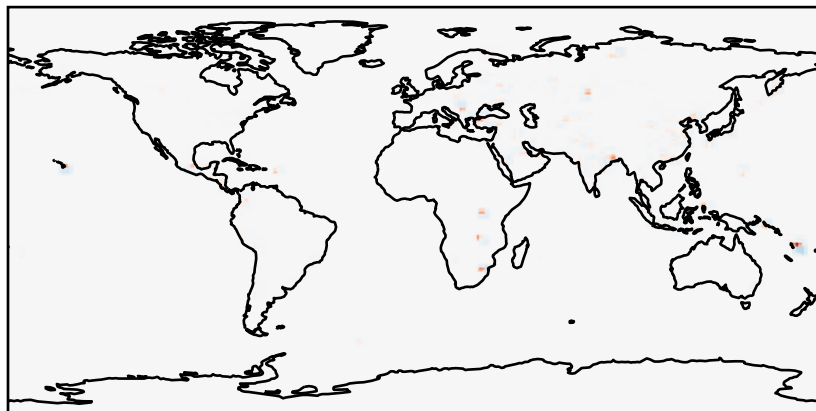
GCC\_12.9.0 (Ref)  
4.0x5.0



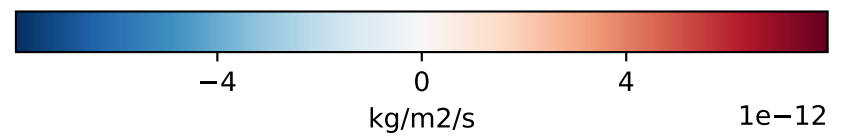
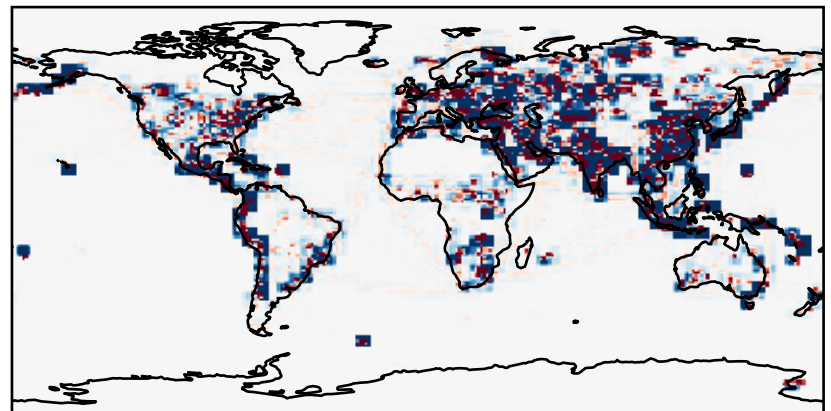
GCC\_13.0.0-beta.1 (Dev)  
c48



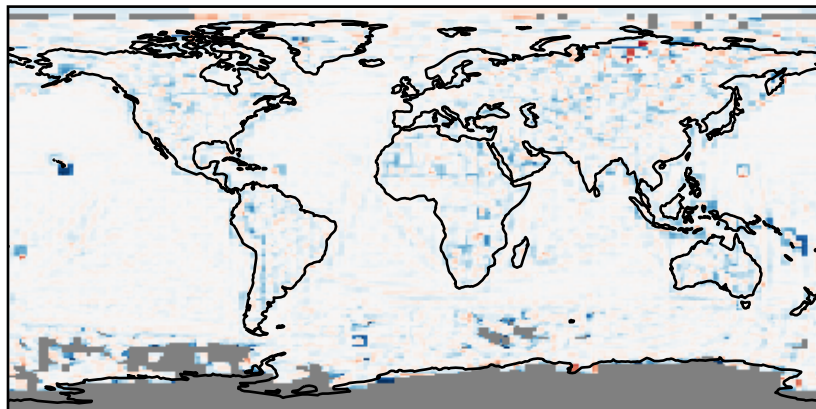
Difference (1x1.25)  
Dev - Ref, Dynamic Range



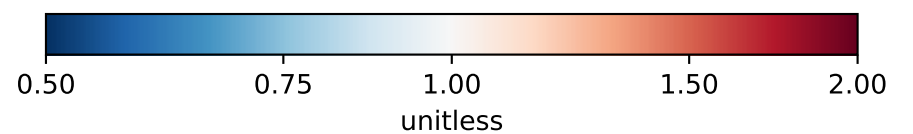
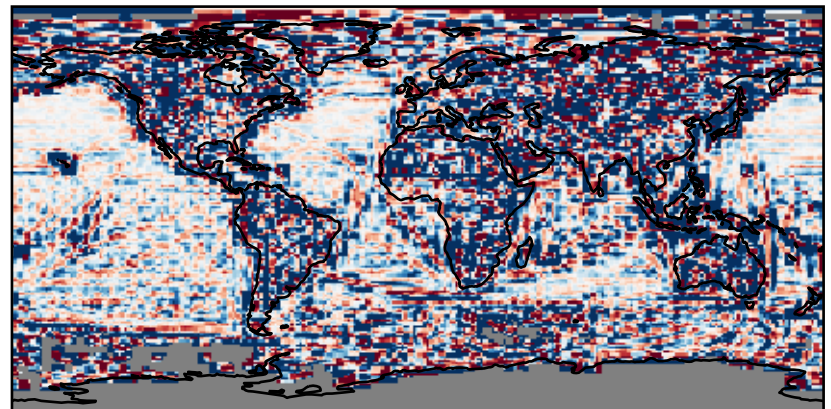
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

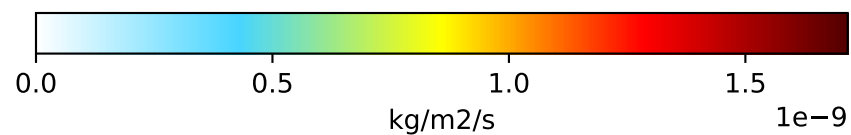
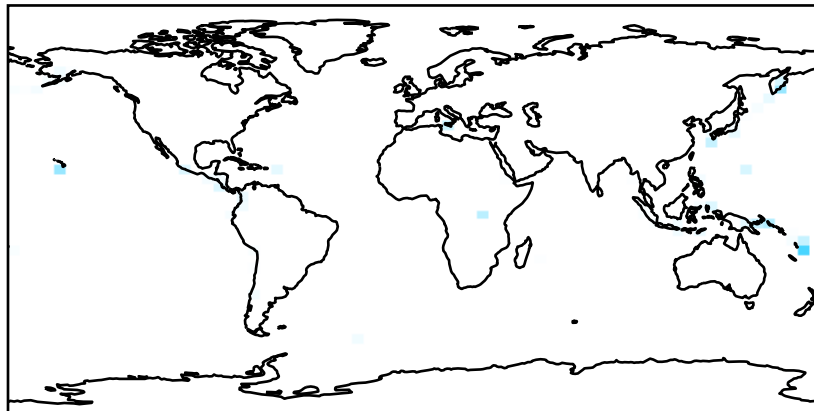


Ratio (1x1.25)  
Dev/Ref, Fixed Range

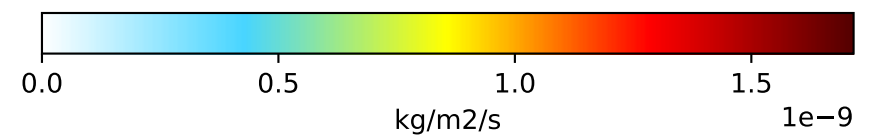
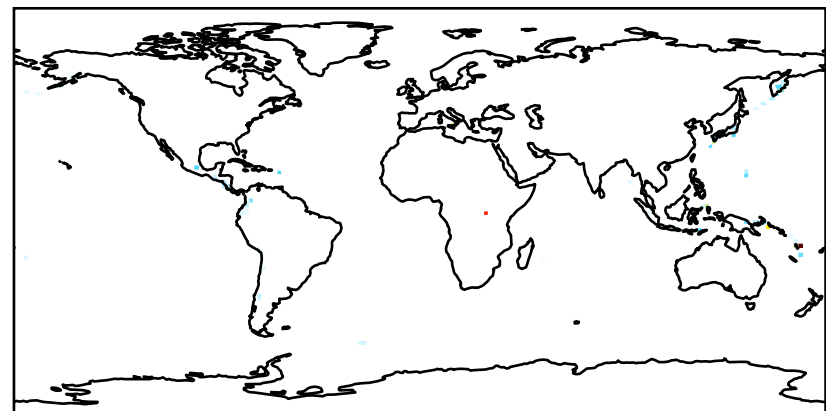


# EmisSO2\_VolcDegas (Jan2019)

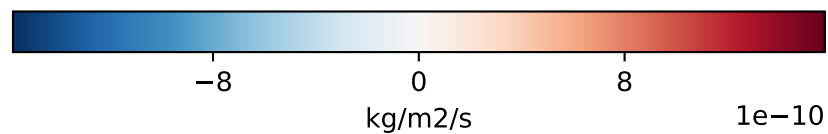
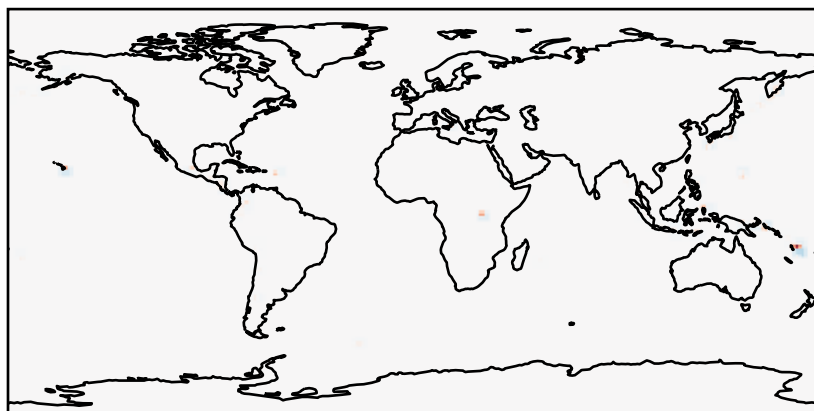
GCC\_12.9.0 (Ref)  
4.0x5.0



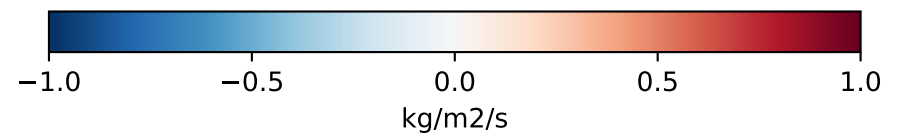
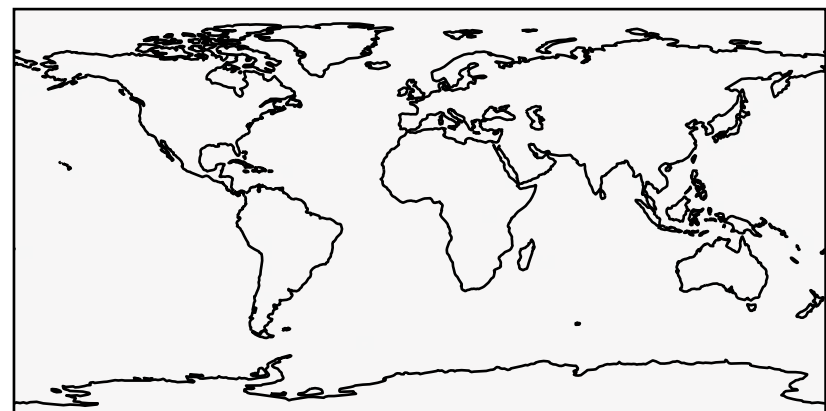
GCC\_13.0.0-beta.1 (Dev)  
c48



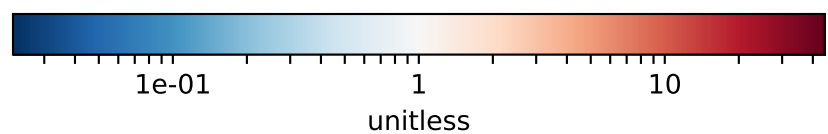
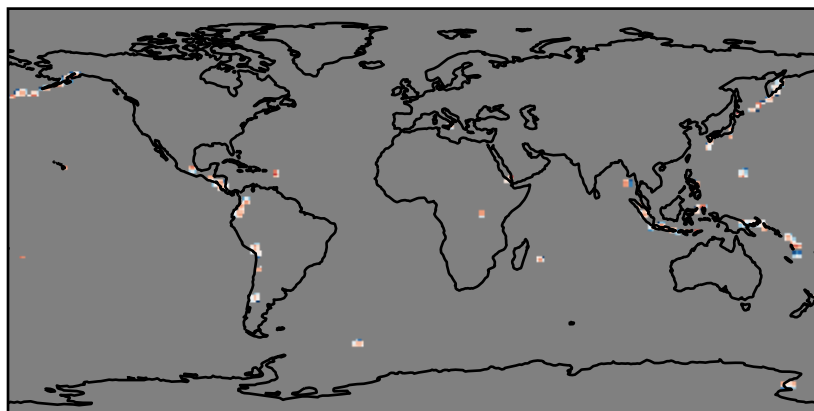
Difference (1x1.25)  
Dev - Ref, Dynamic Range



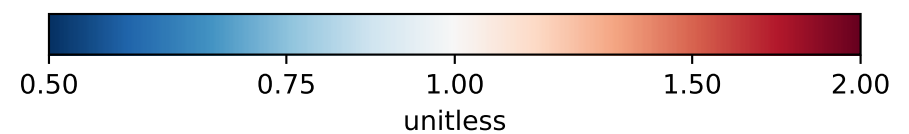
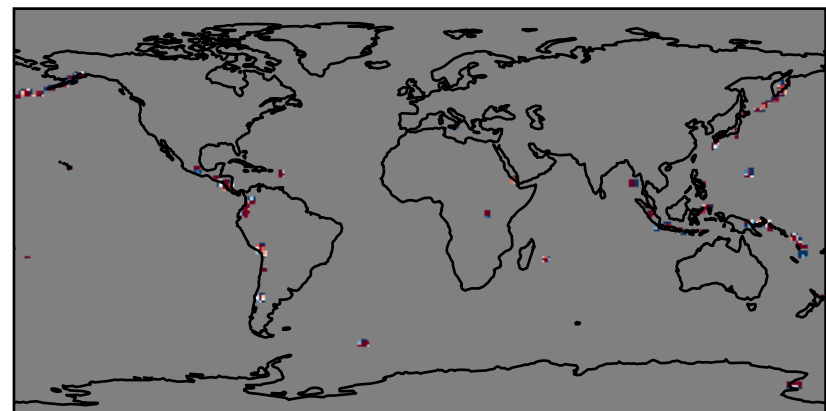
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



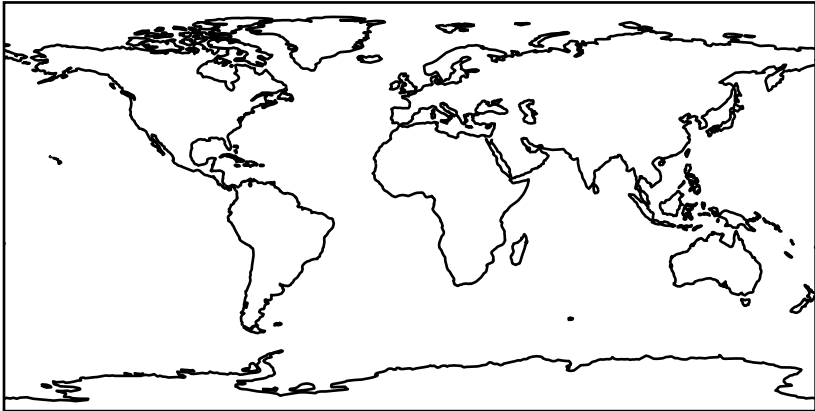
Ratio (1x1.25)  
Dev/Ref, Fixed Range





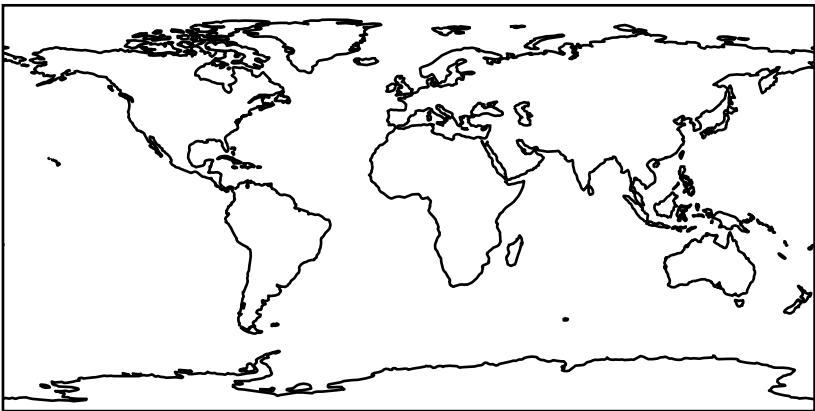
# EmisSO2\_VolcErupt (Jan2019)

GCC\_12.9.0 (Ref)  
4.0x5.0



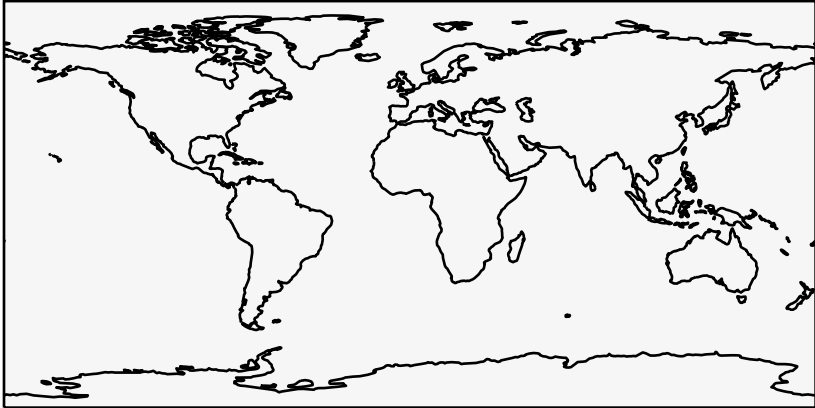
Zero throughout domain  
kg/m2/s

GCC\_13.0.0-beta.1 (Dev)  
c48



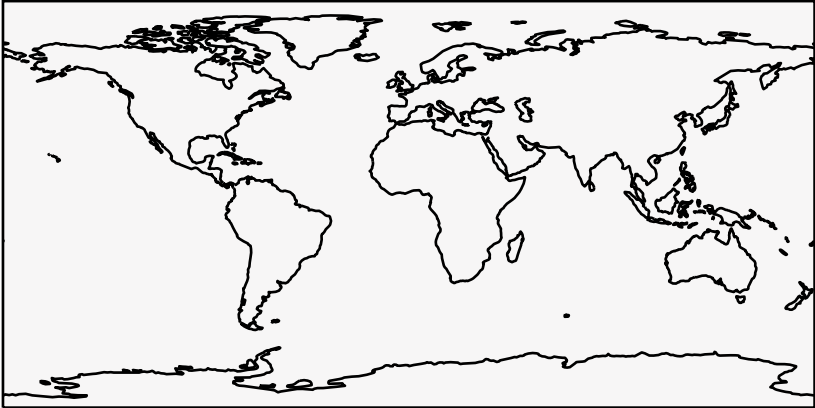
Zero throughout domain  
kg/m2/s

Difference (1x1.25)  
Dev - Ref, Dynamic Range



Zero throughout domain  
kg/m2/s

Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Zero throughout domain  
kg/m2/s

Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

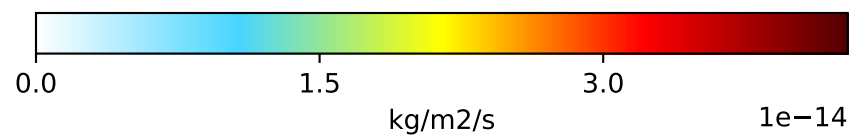
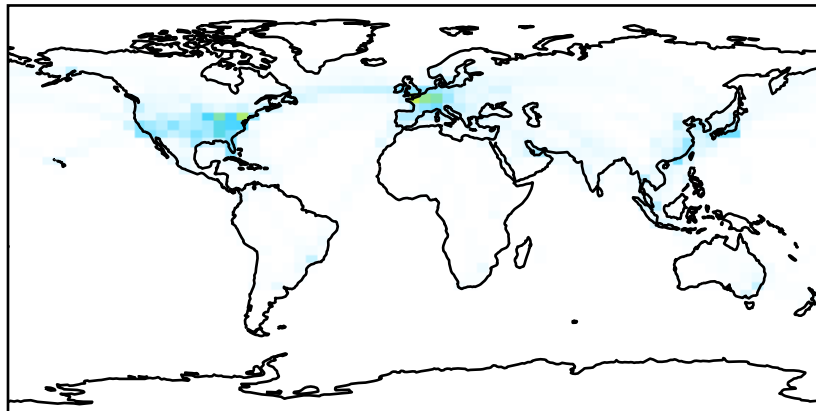
Ratio (1x1.25)  
Dev/Ref, Fixed Range



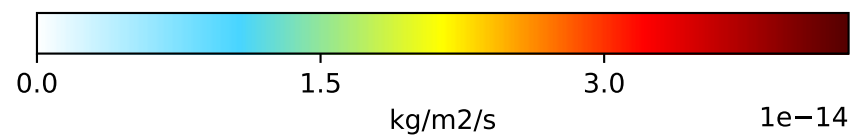
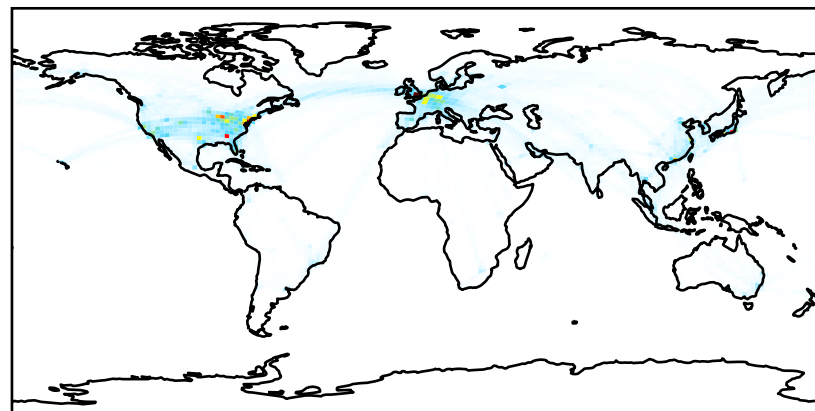
Undefined throughout domain  
unitless

# EmisSO4\_Aircraft (Jan2019)

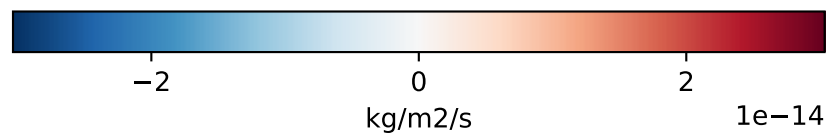
GCC\_12.9.0 (Ref)  
4.0x5.0



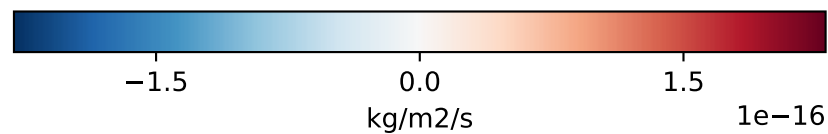
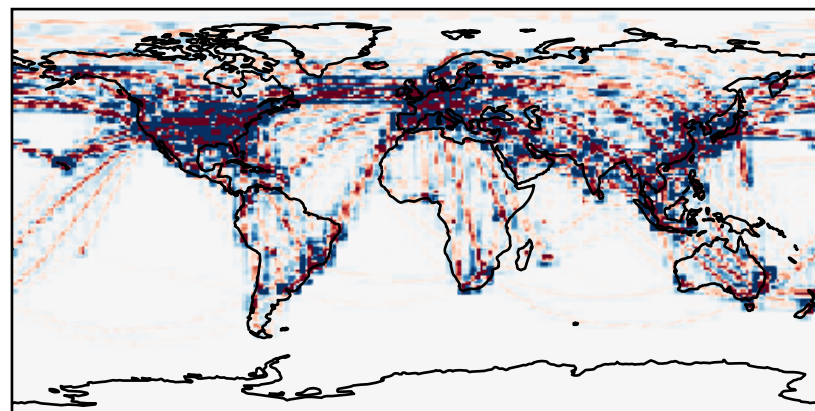
GCC\_13.0.0-beta.1 (Dev)  
c48



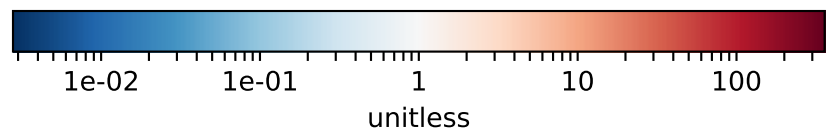
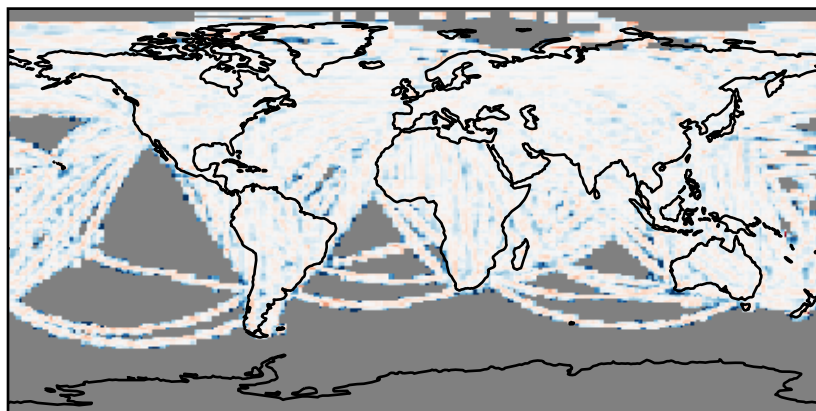
Difference (1x1.25)  
Dev - Ref, Dynamic Range



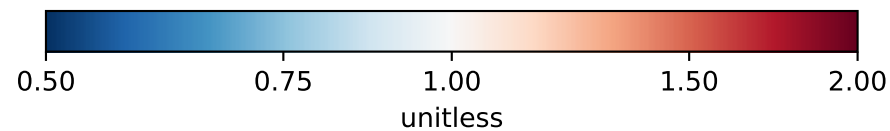
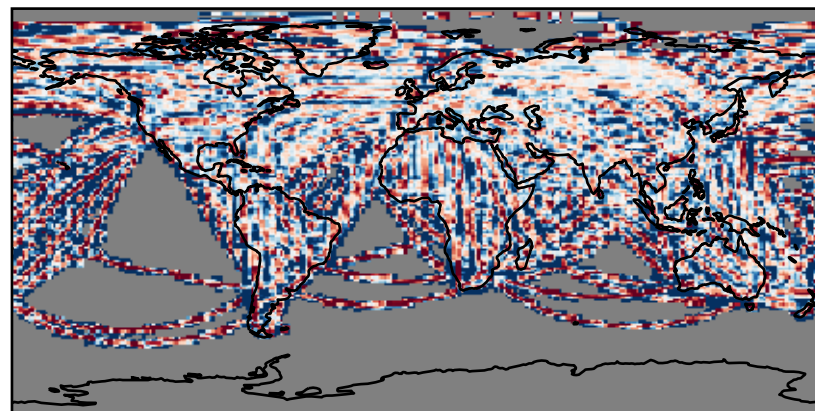
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

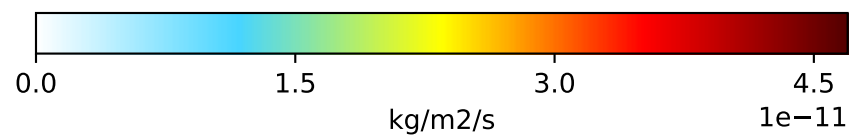
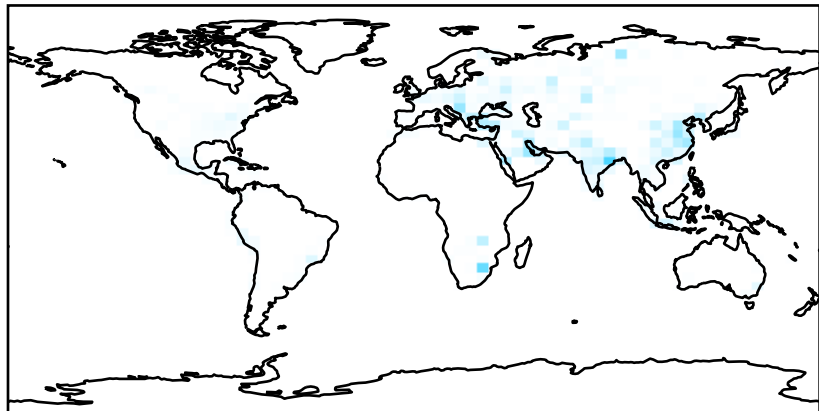


Ratio (1x1.25)  
Dev/Ref, Fixed Range

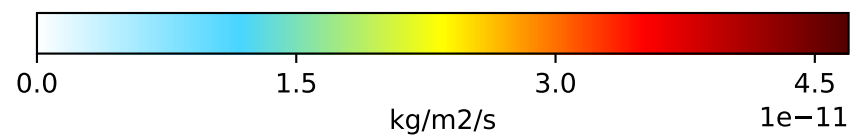
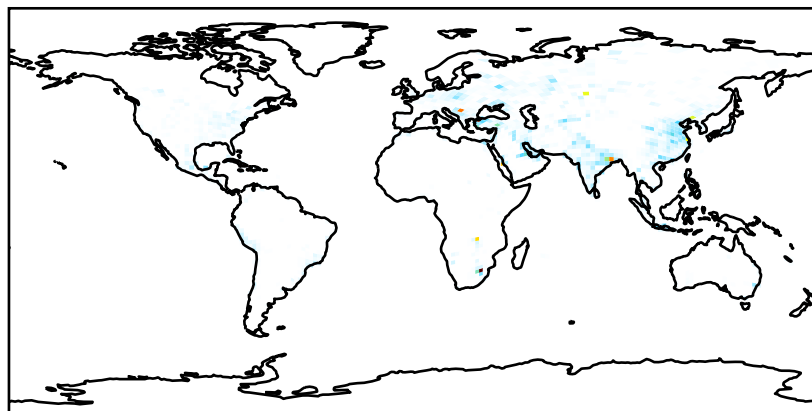


# EmisSO4\_Anthro (Jan2019)

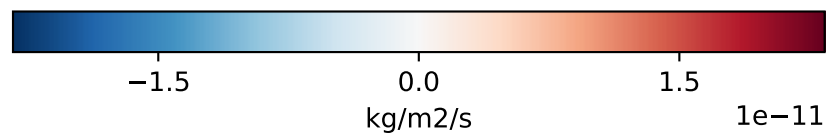
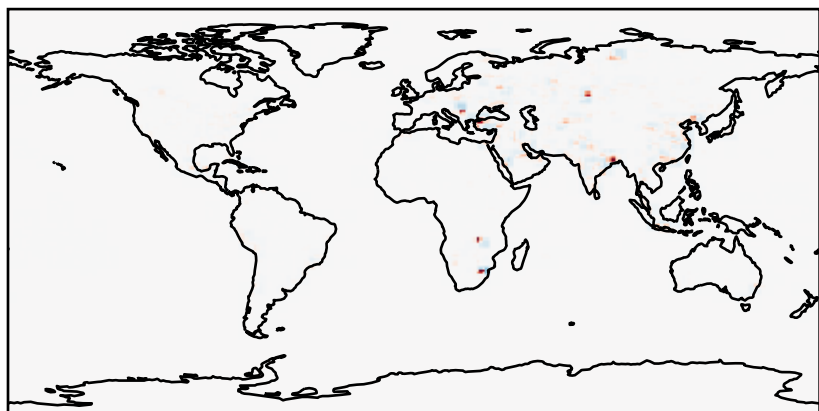
GCC\_12.9.0 (Ref)  
4.0x5.0



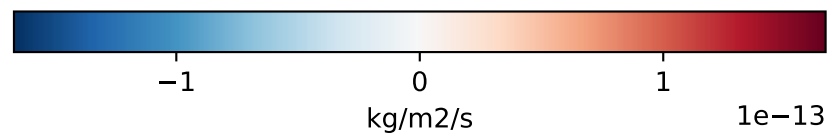
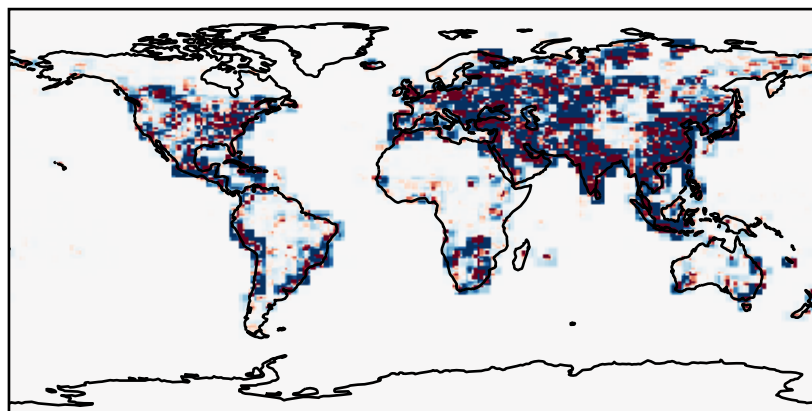
GCC\_13.0.0-beta.1 (Dev)  
c48



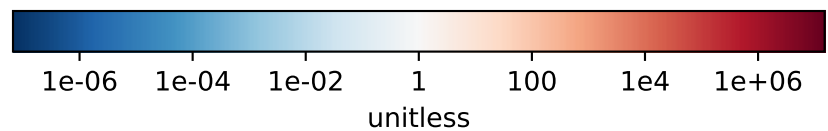
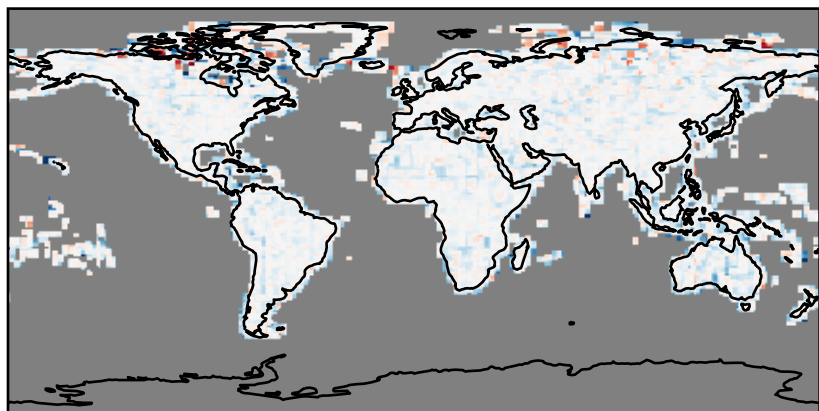
Difference (1x1.25)  
Dev - Ref, Dynamic Range



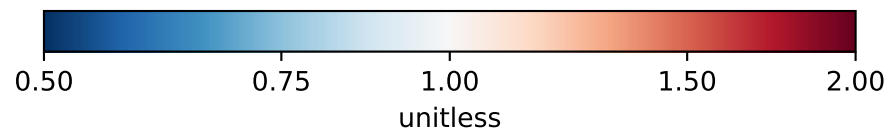
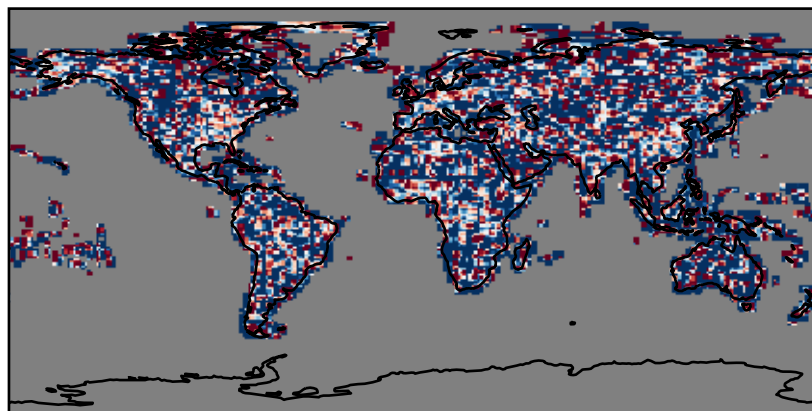
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



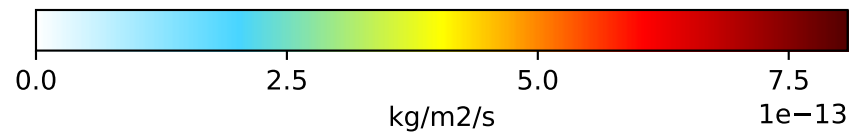
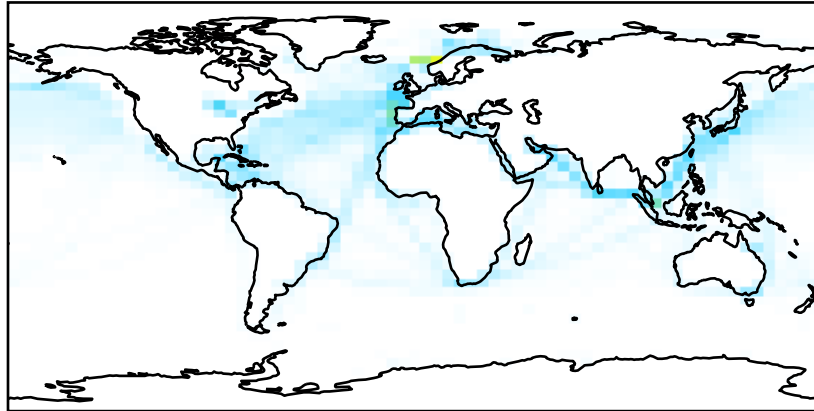
Ratio (1x1.25)  
Dev/Ref, Fixed Range



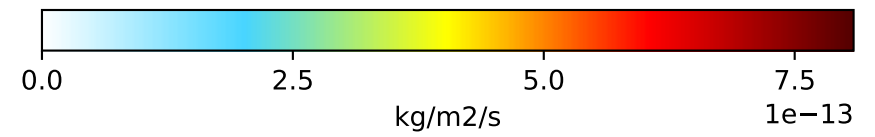
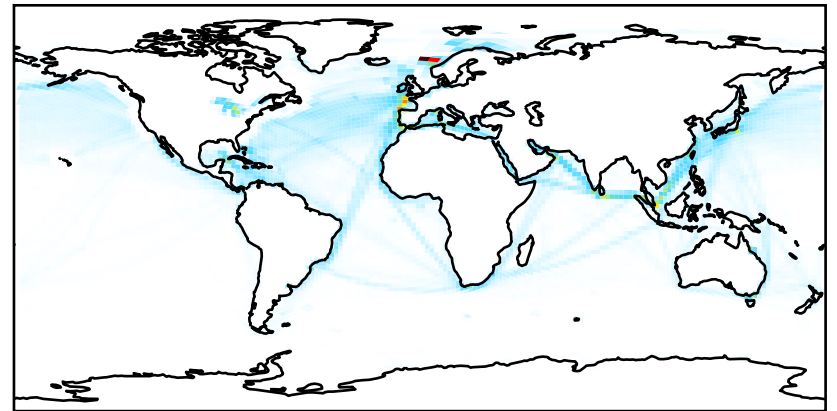


# EmisSO4\_Ship (Jan2019)

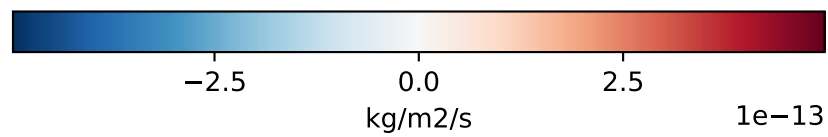
GCC\_12.9.0 (Ref)  
4.0x5.0



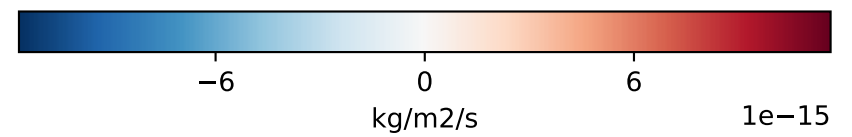
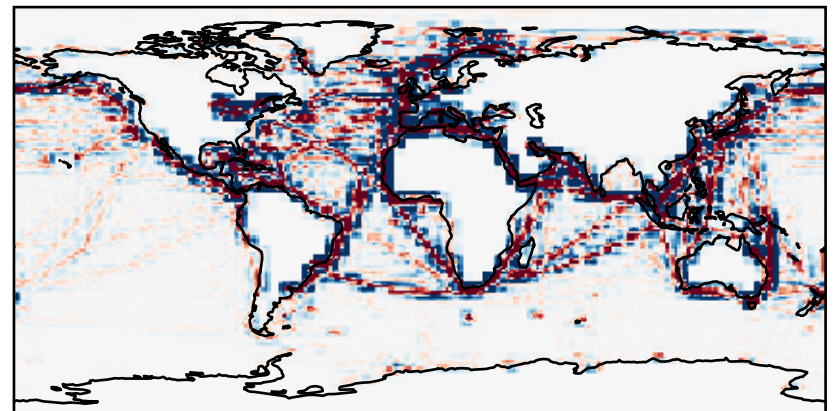
GCC\_13.0.0-beta.1 (Dev)  
c48



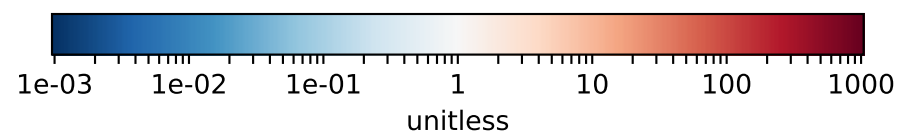
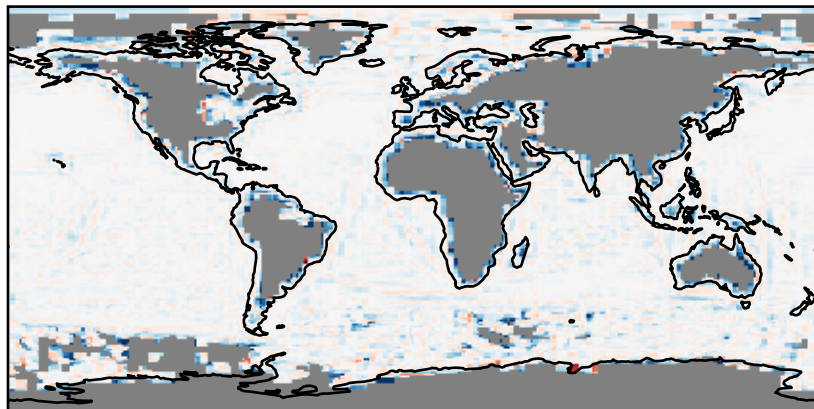
Difference (1x1.25)  
Dev - Ref, Dynamic Range



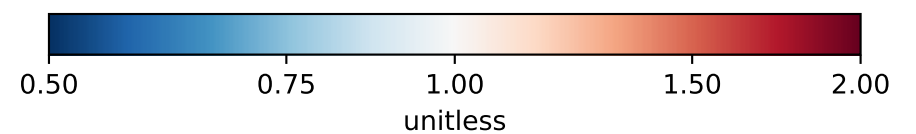
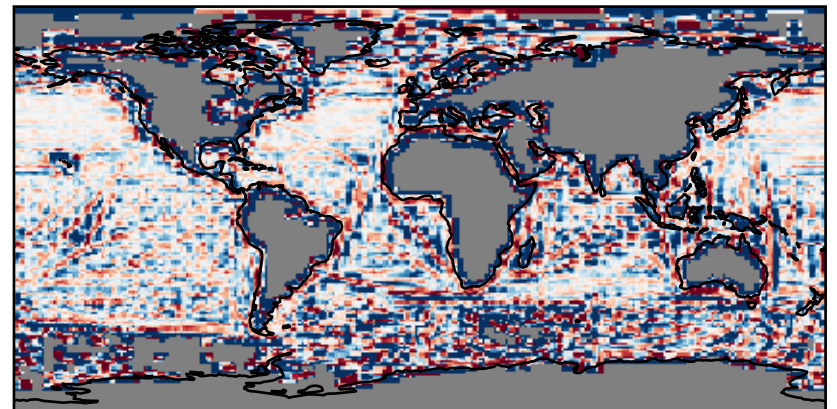
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

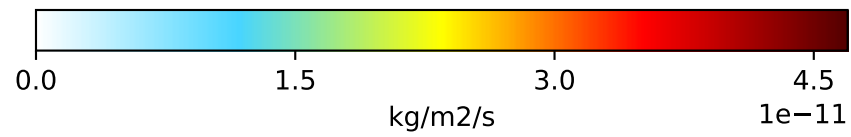
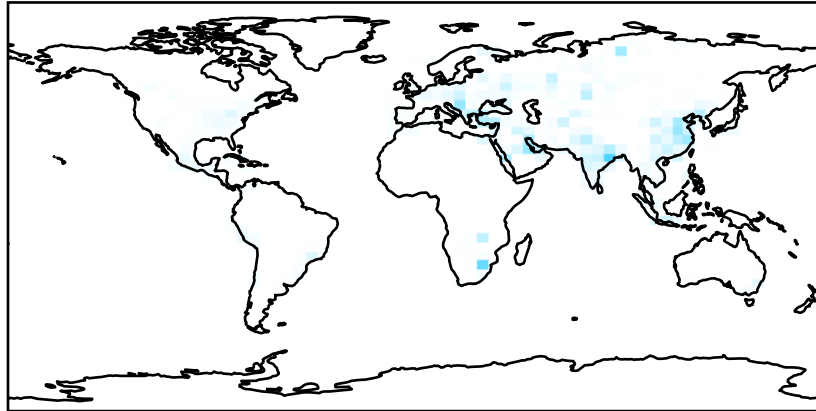


Ratio (1x1.25)  
Dev/Ref, Fixed Range

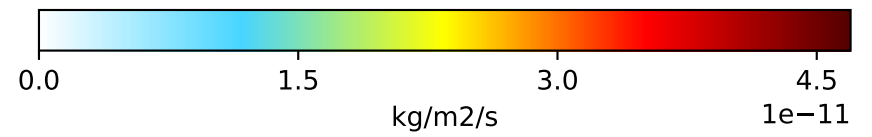
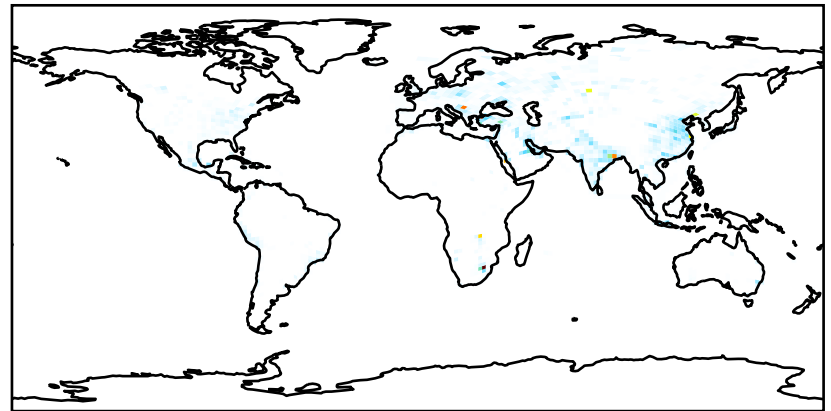


# EmisSO4\_Total (Jan2019)

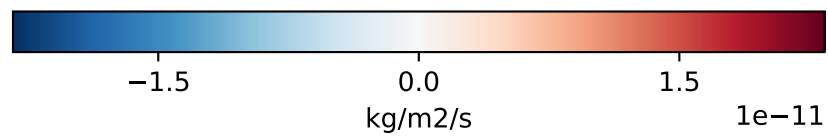
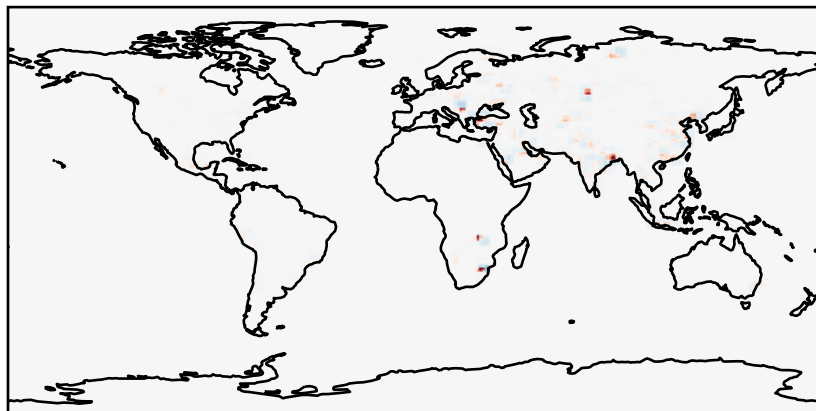
GCC\_12.9.0 (Ref)  
4.0x5.0



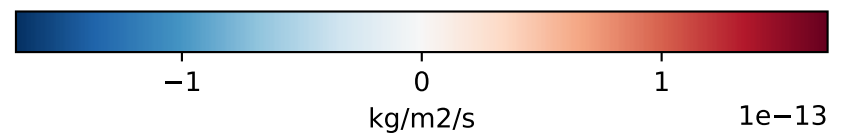
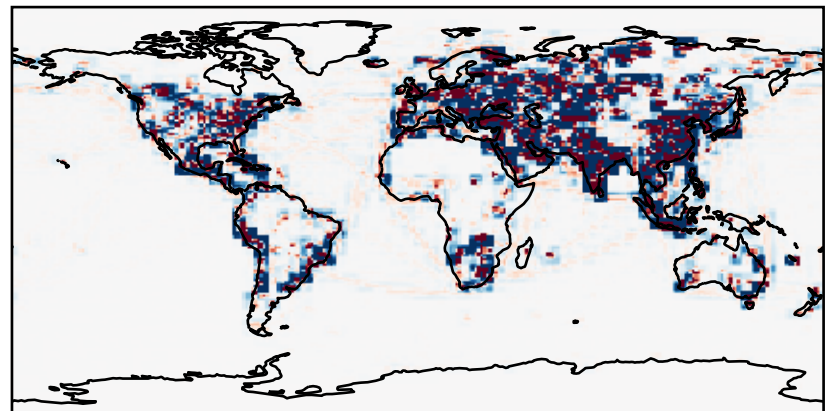
GCC\_13.0.0-beta.1 (Dev)  
c48



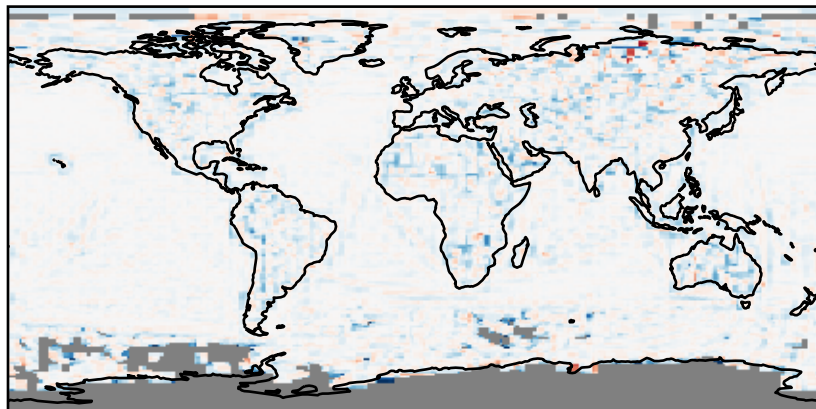
Difference (1x1.25)  
Dev - Ref, Dynamic Range



Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Ratio (1x1.25)  
Dev/Ref, Fixed Range

