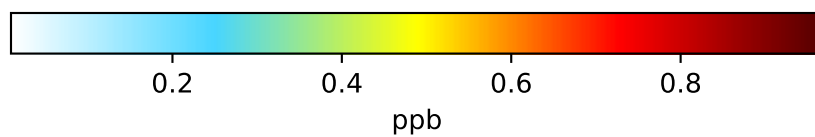
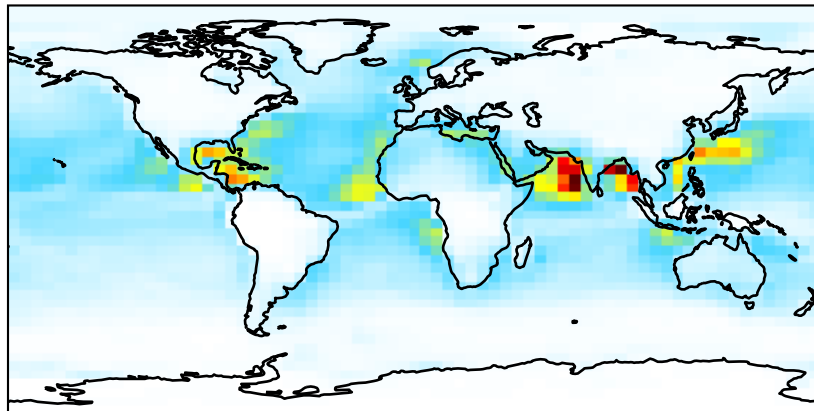
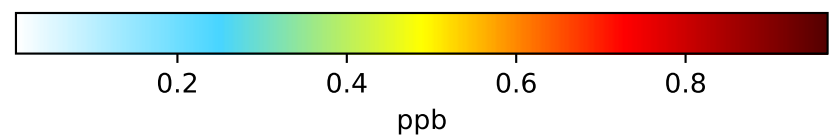
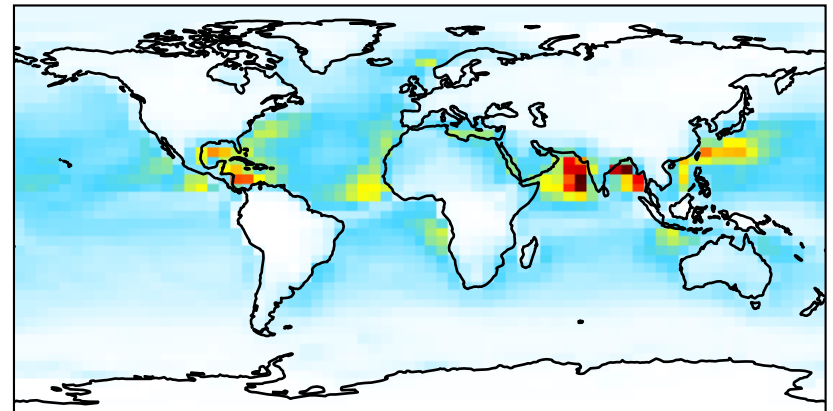


# SpeciesConc\_Cly (Apr2019)

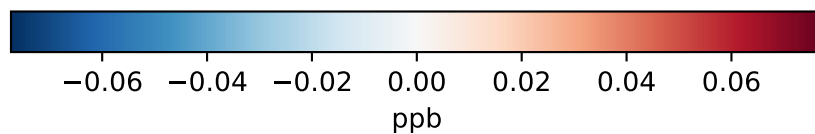
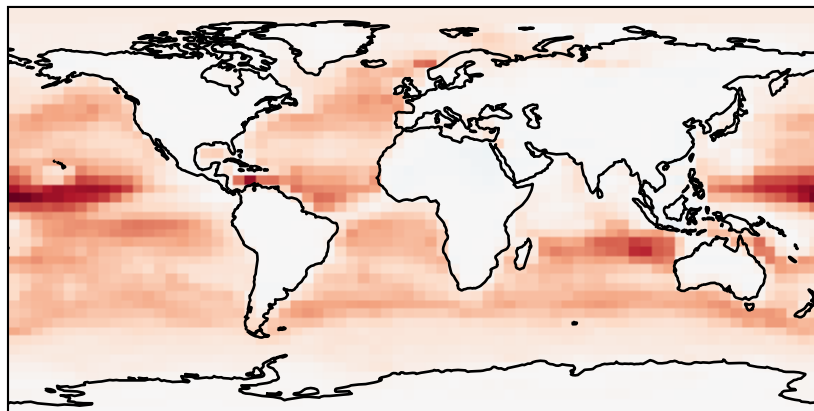
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



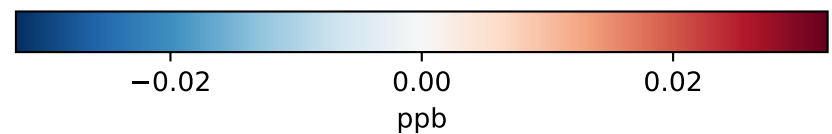
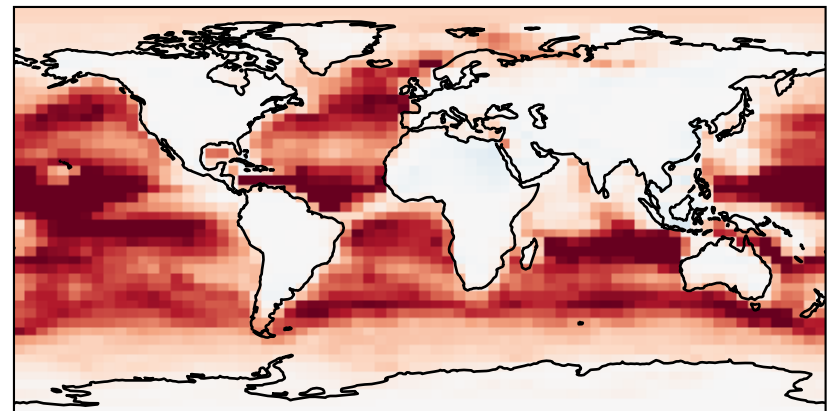
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



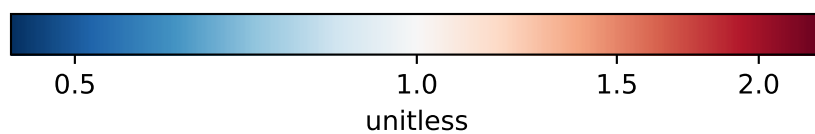
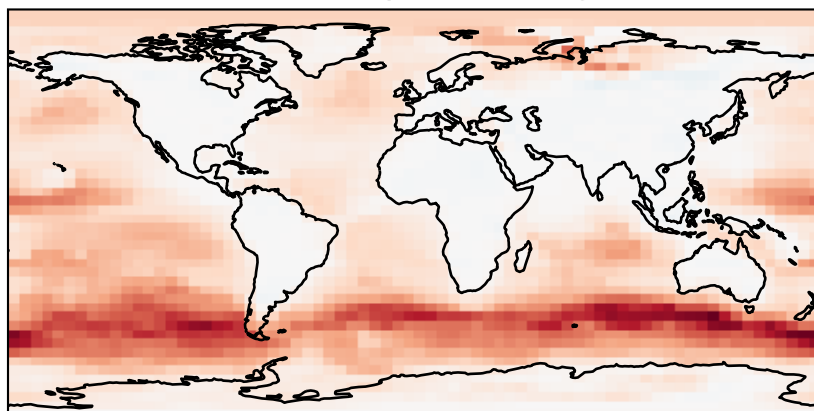
Difference  
Dev - Ref, Dynamic Range



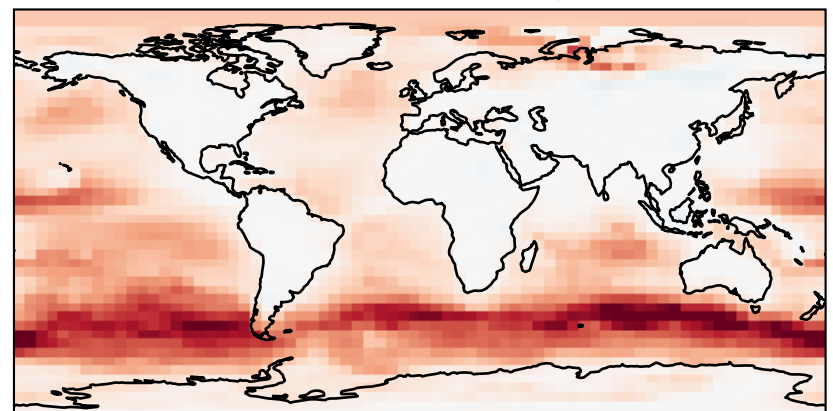
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

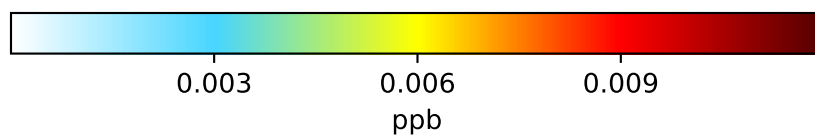
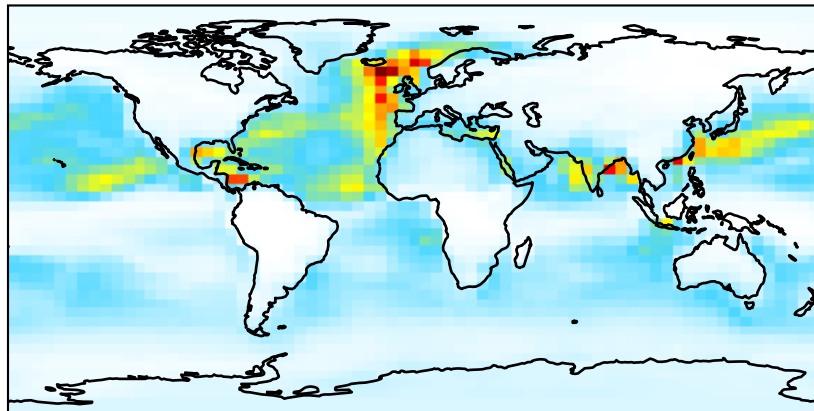


Ratio  
Dev/Ref, Fixed Range

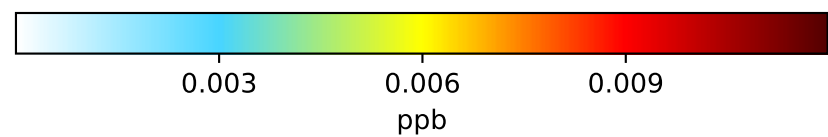
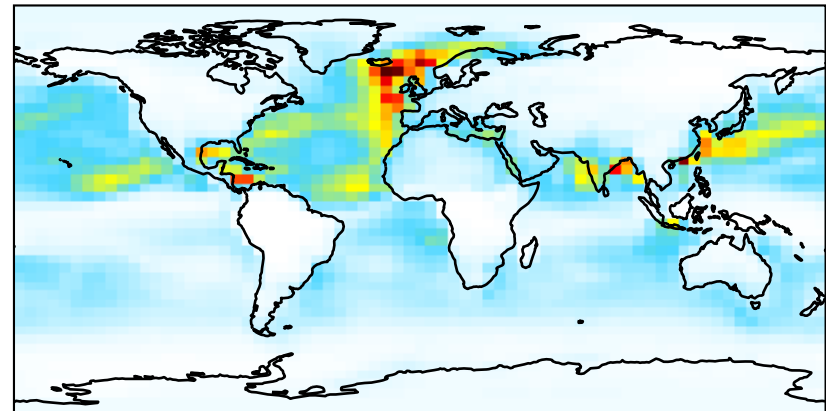


# SpeciesConc\_ClOx (Apr2019)

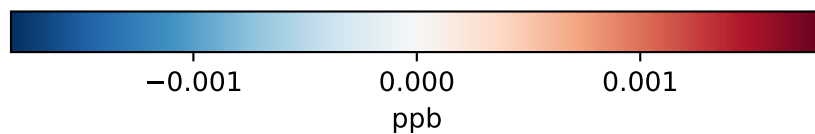
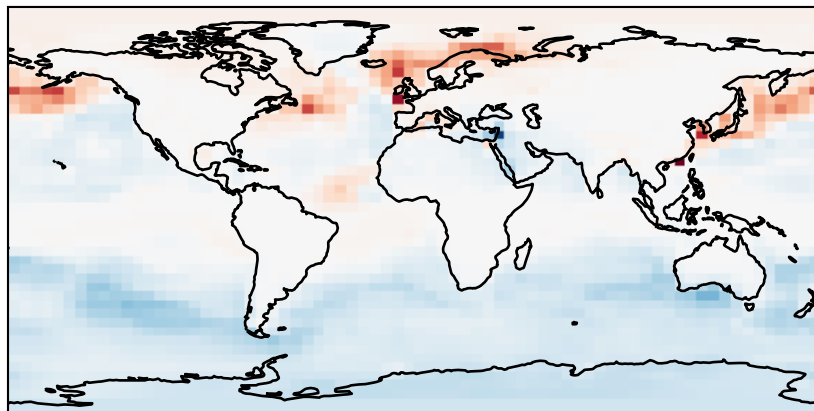
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



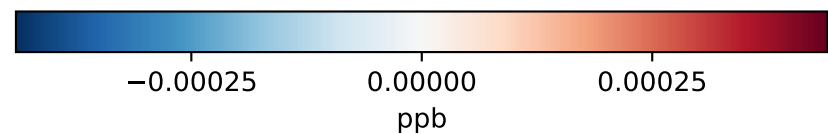
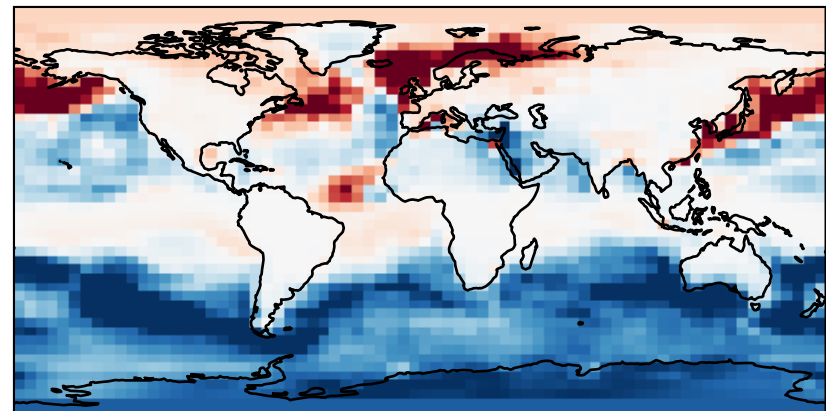
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



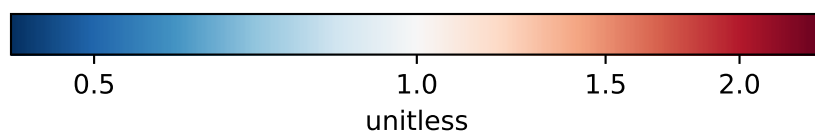
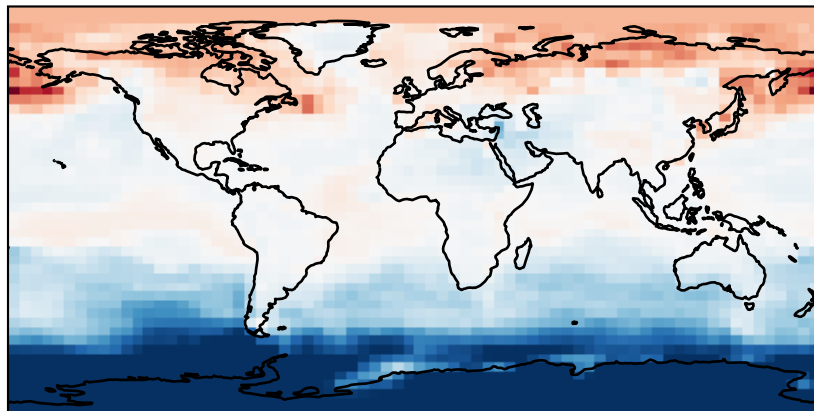
Difference  
Dev - Ref, Dynamic Range



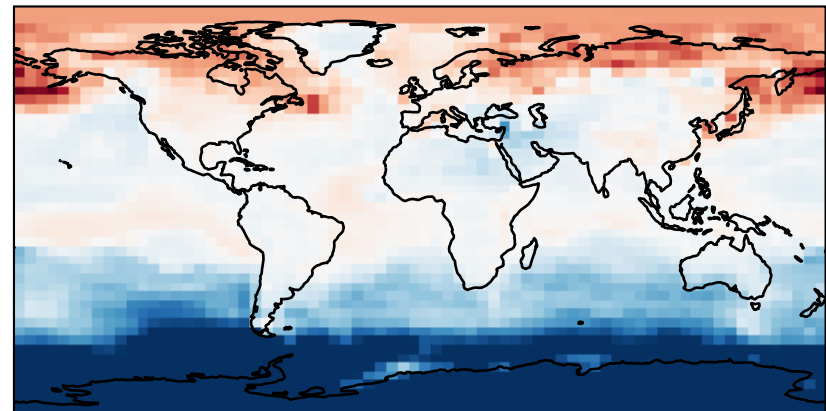
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

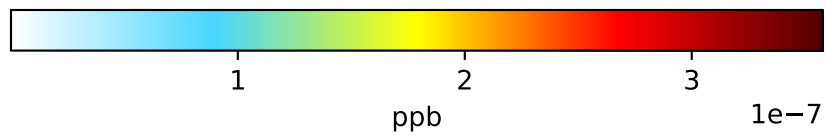
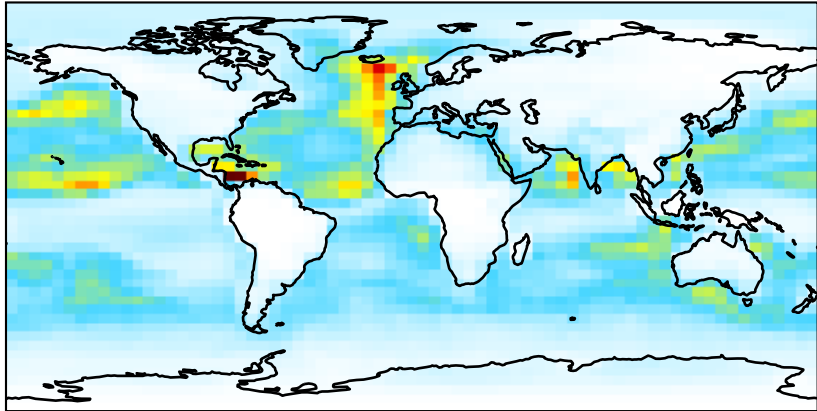


Ratio  
Dev/Ref, Fixed Range

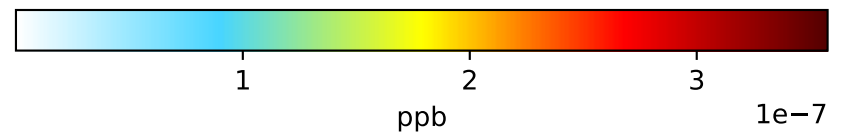
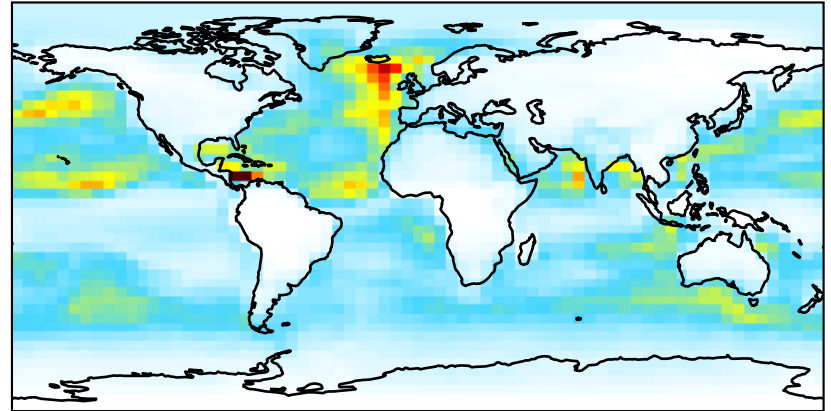


# SpeciesConc\_Cl (Apr2019)

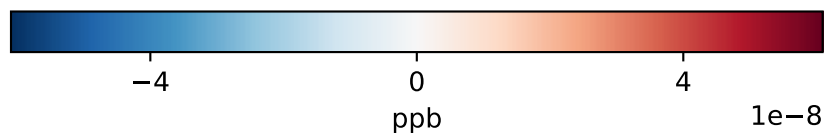
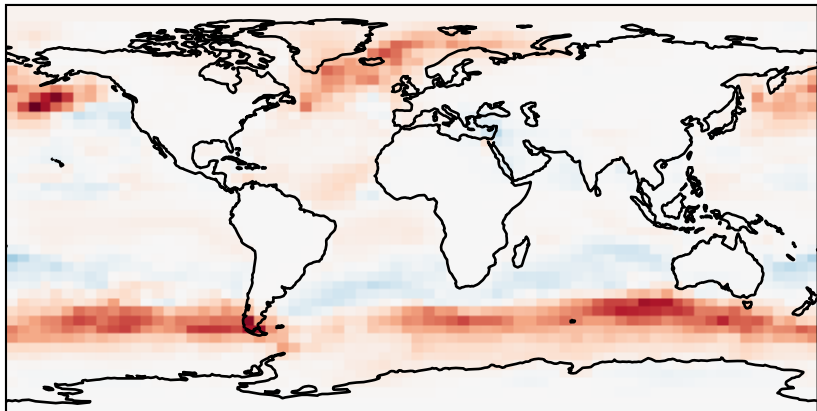
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



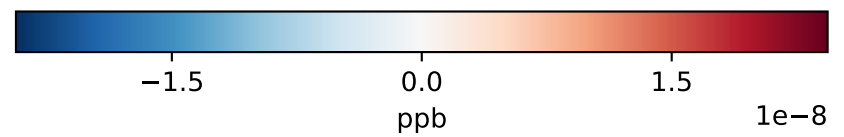
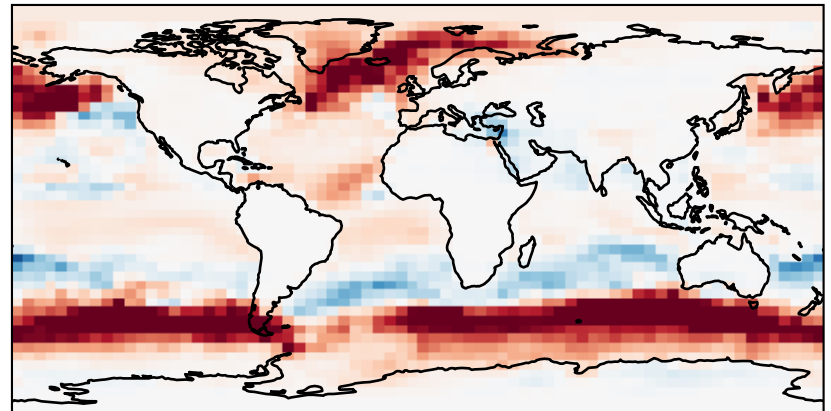
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



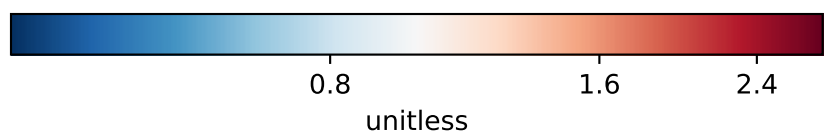
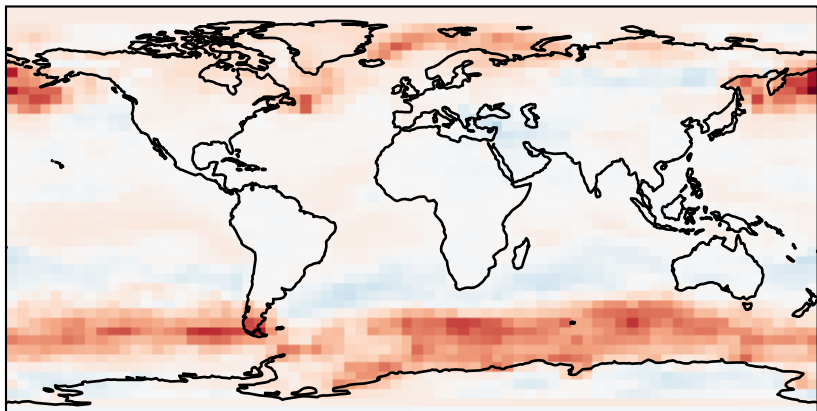
Difference  
Dev - Ref, Dynamic Range



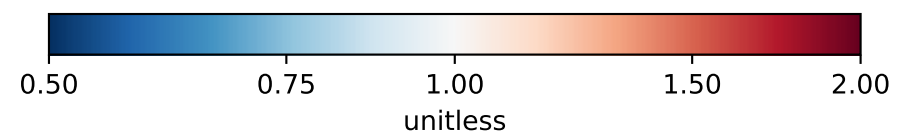
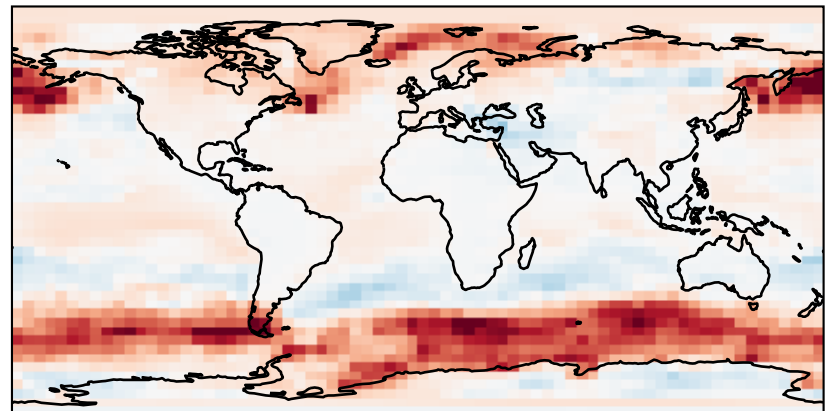
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

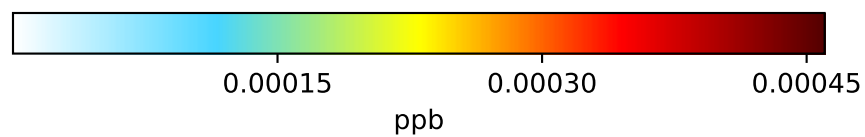
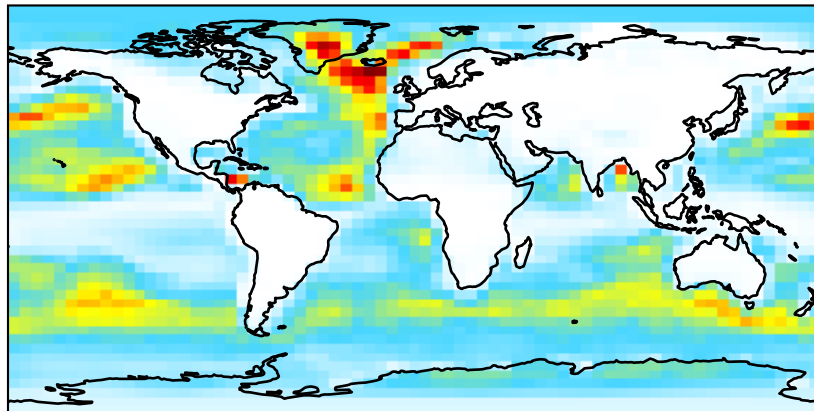


Ratio  
Dev/Ref, Fixed Range

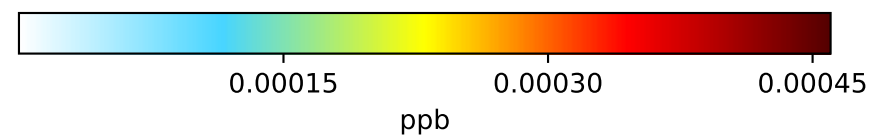
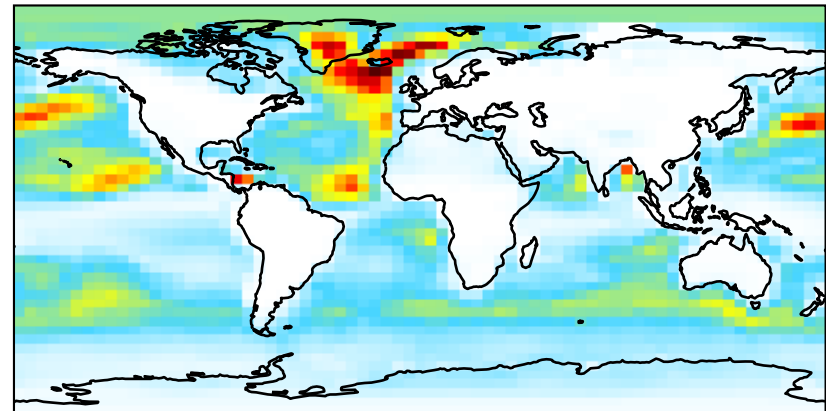


# SpeciesConc\_ClO (Apr2019)

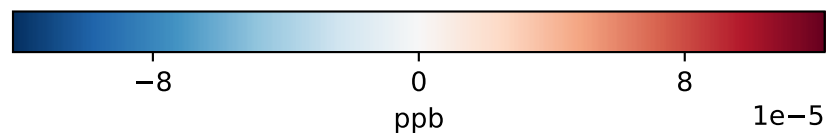
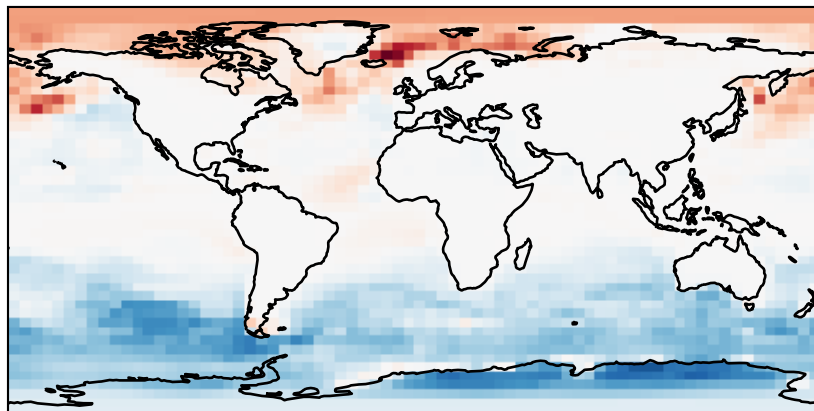
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



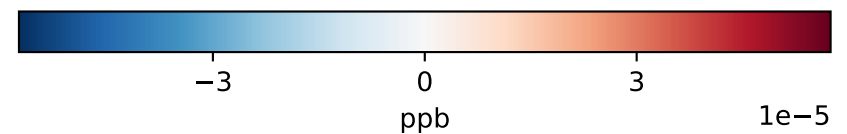
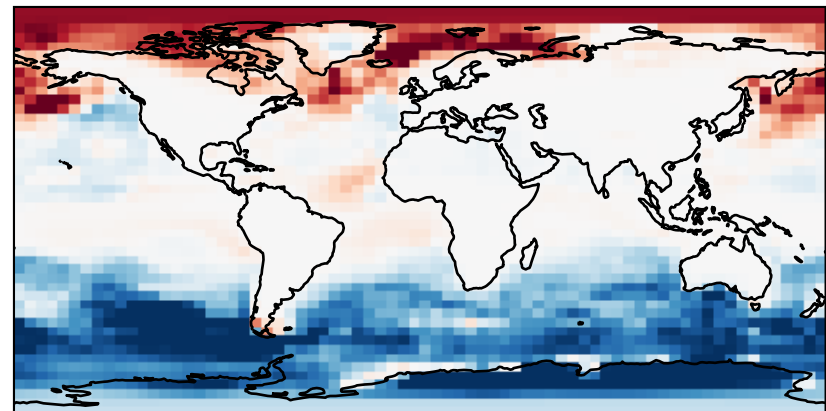
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



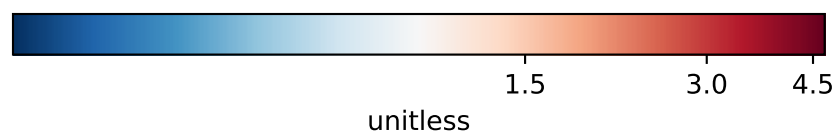
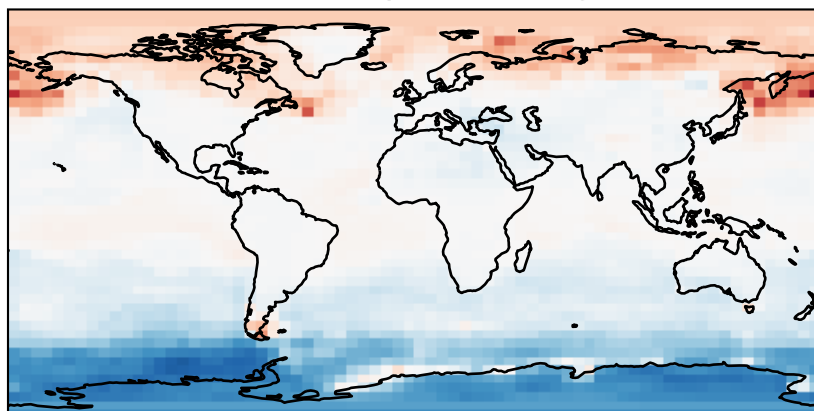
Difference  
Dev - Ref, Dynamic Range



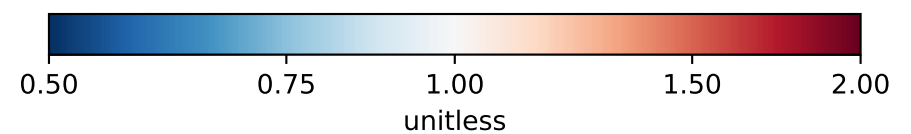
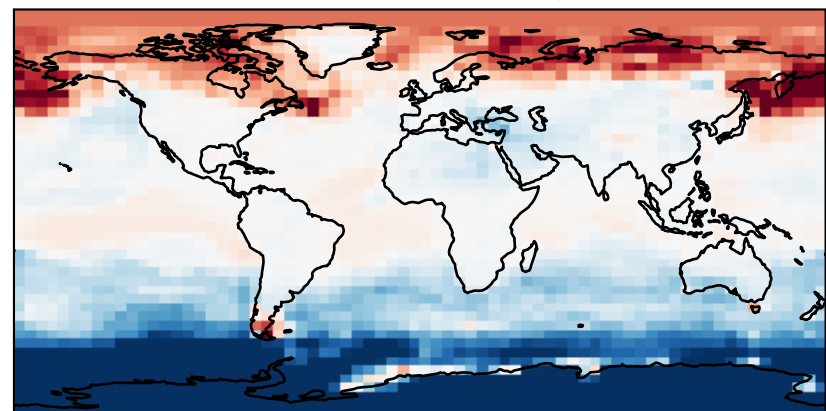
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



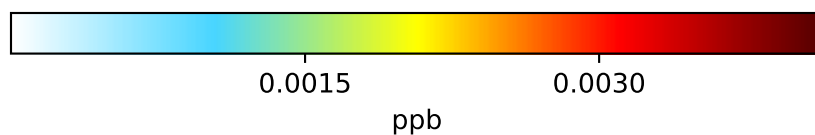
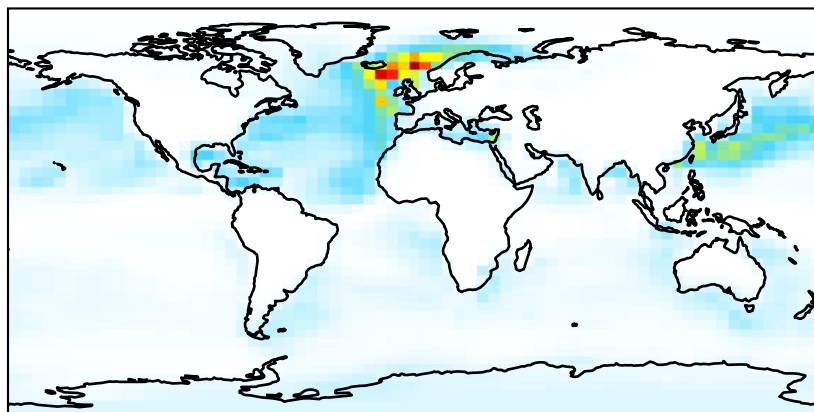
Ratio  
Dev/Ref, Fixed Range



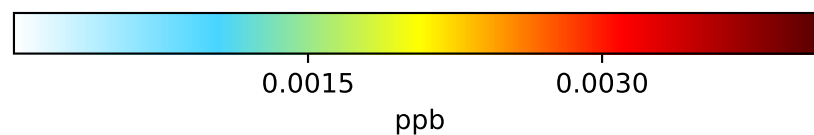
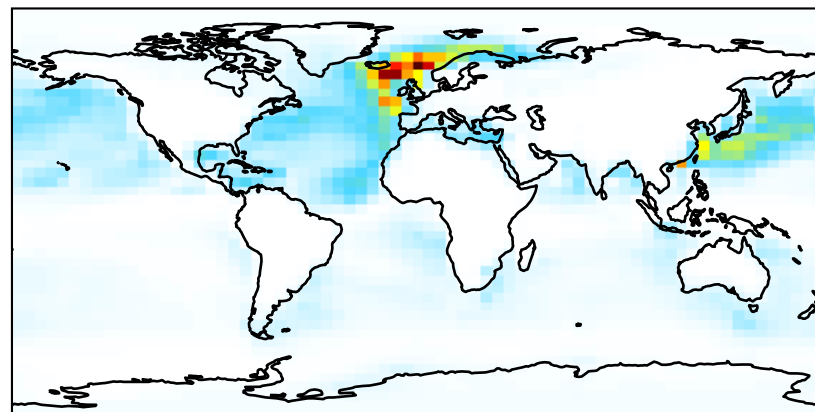


# SpeciesConc\_Cl2 (Apr2019)

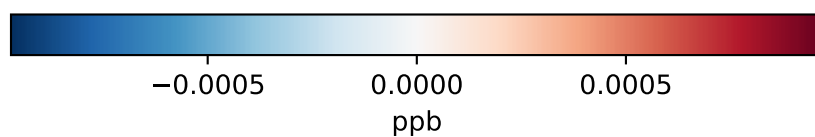
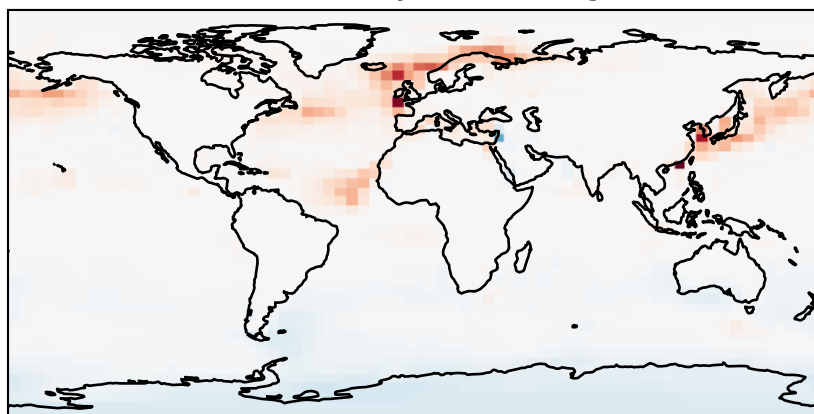
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



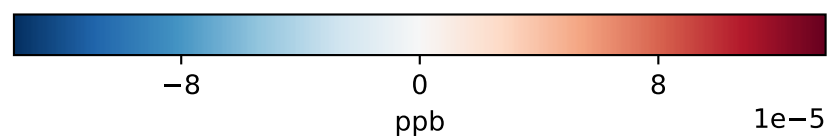
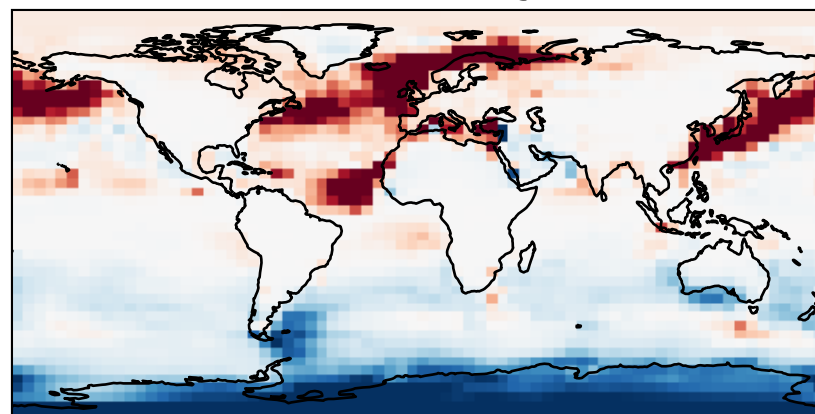
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



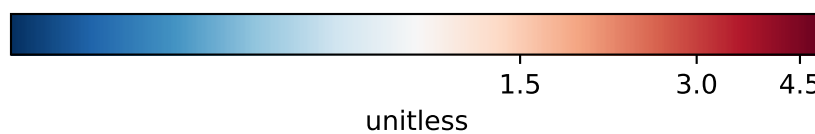
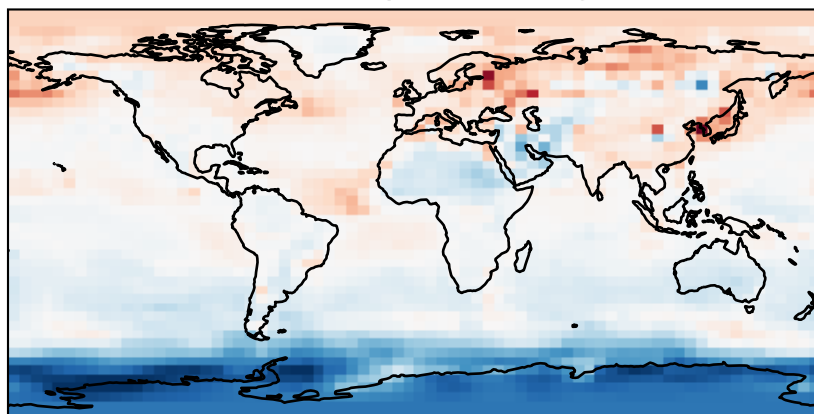
Difference  
Dev - Ref, Dynamic Range



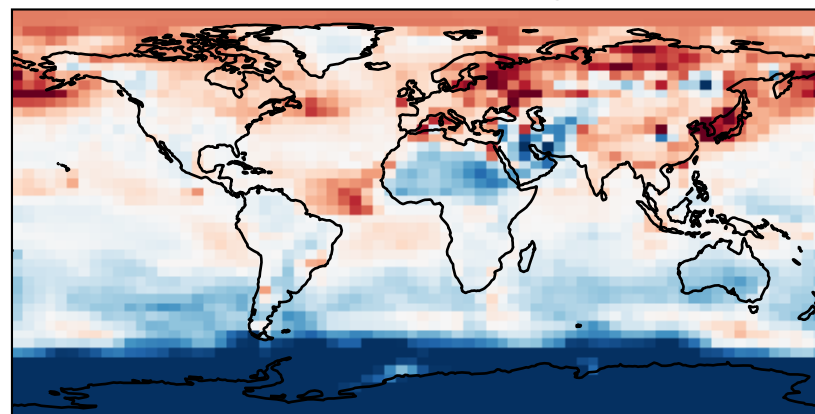
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

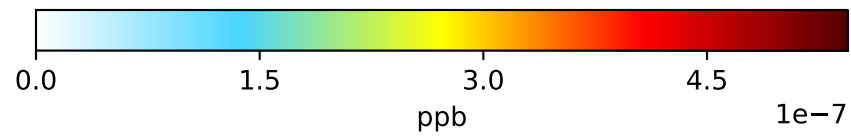
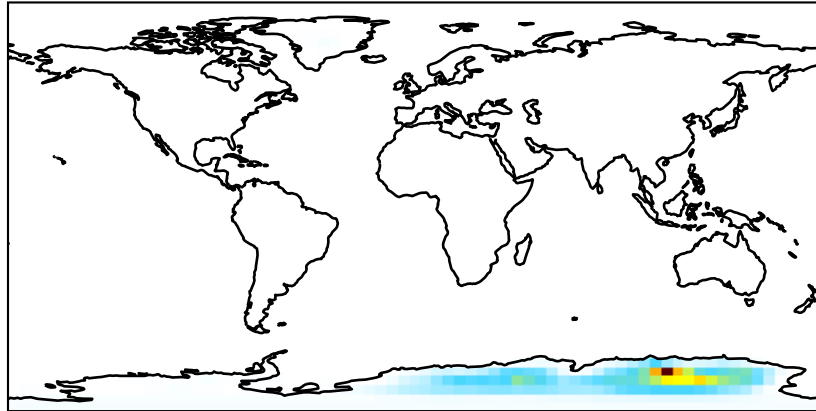


Ratio  
Dev/Ref, Fixed Range

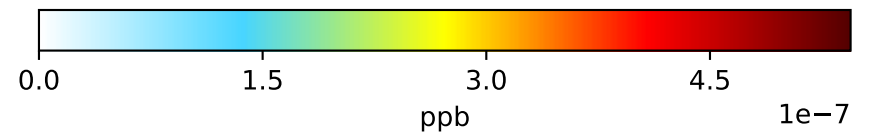
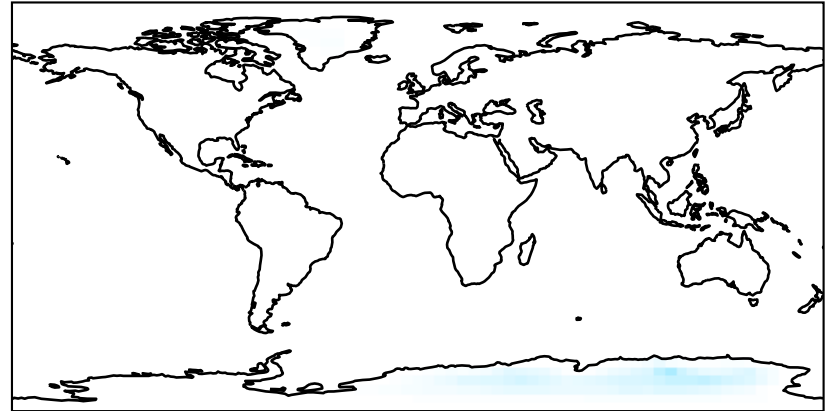


# SpeciesConc\_Cl202 (Apr2019)

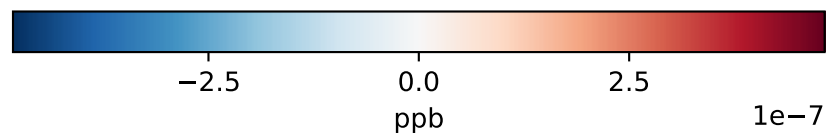
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



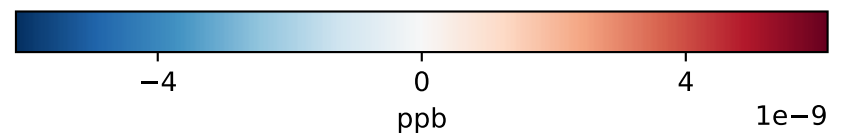
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



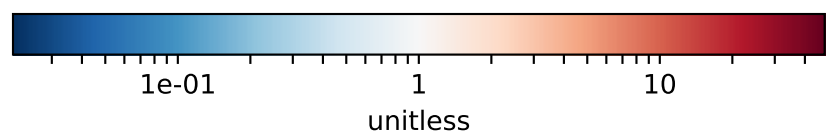
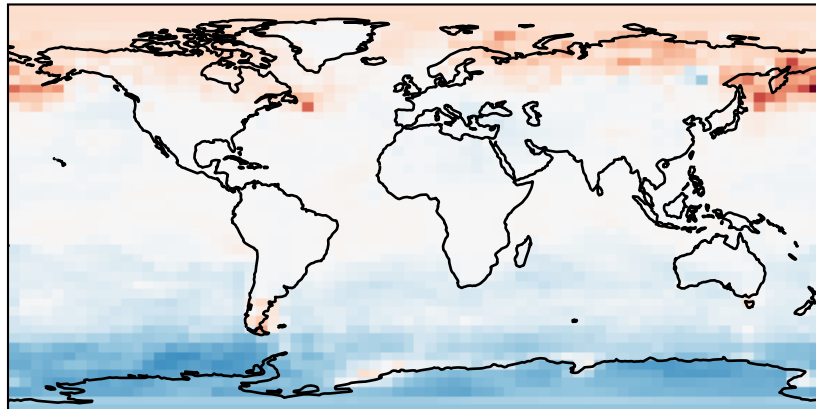
Difference  
Dev - Ref, Dynamic Range



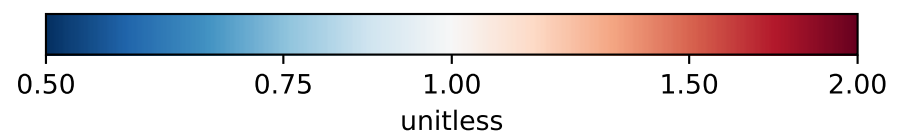
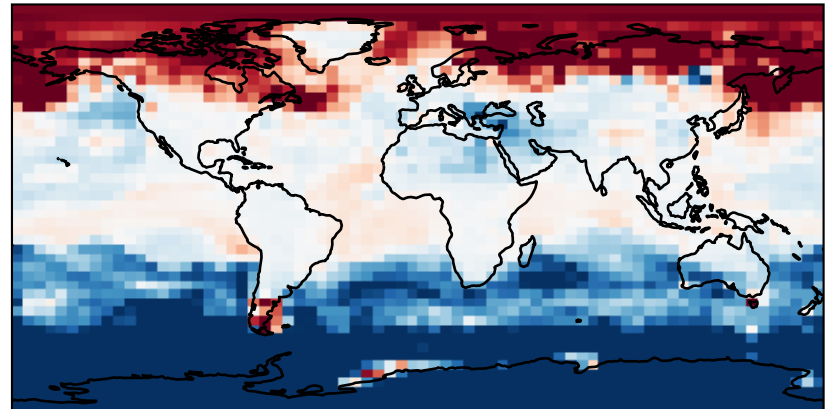
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

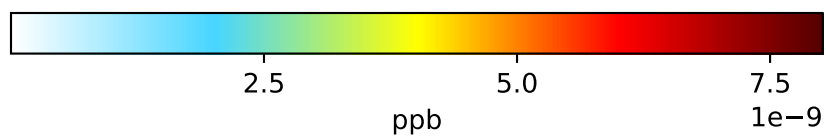
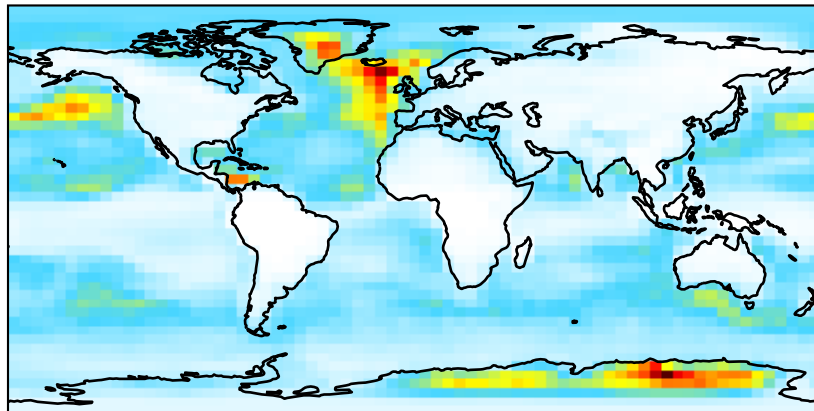


Ratio  
Dev/Ref, Fixed Range

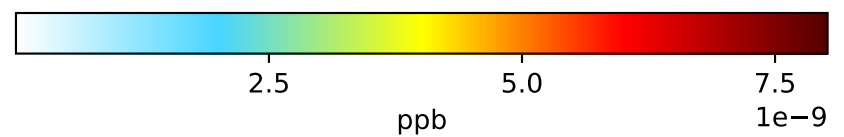
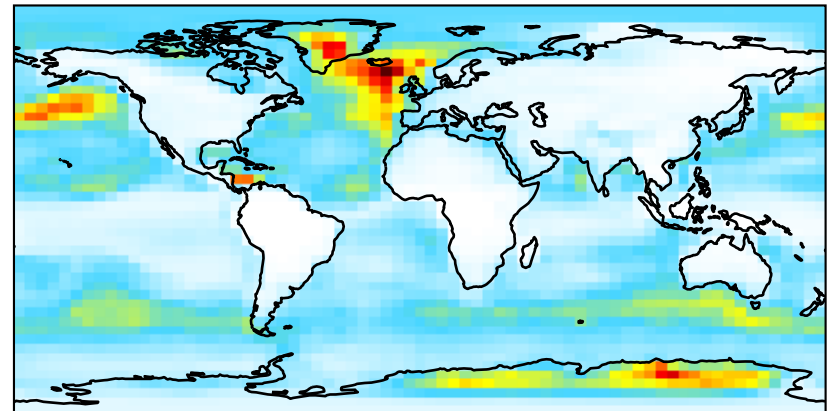


# SpeciesConc\_C100 (Apr2019)

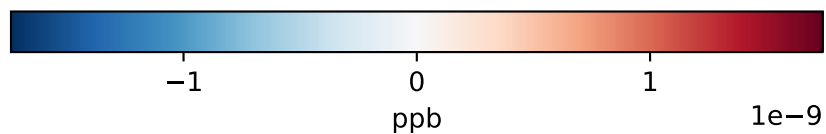
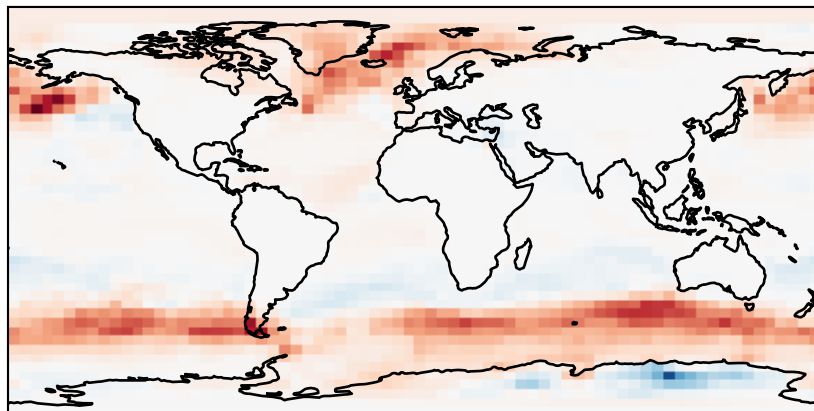
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



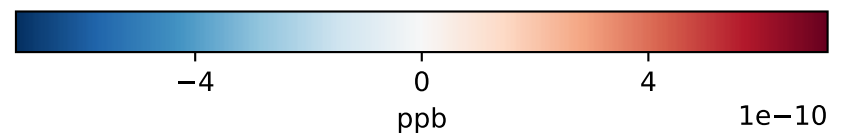
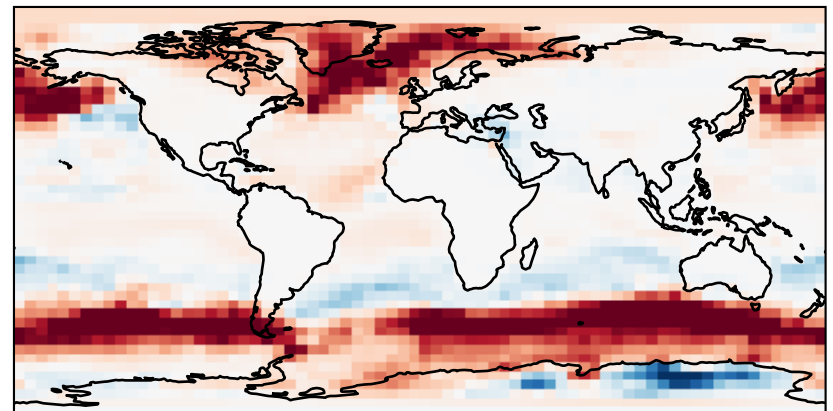
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



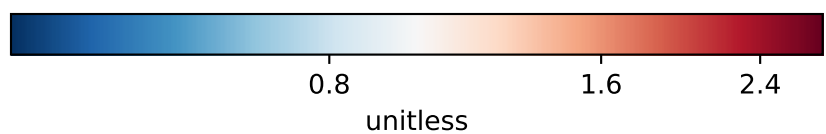
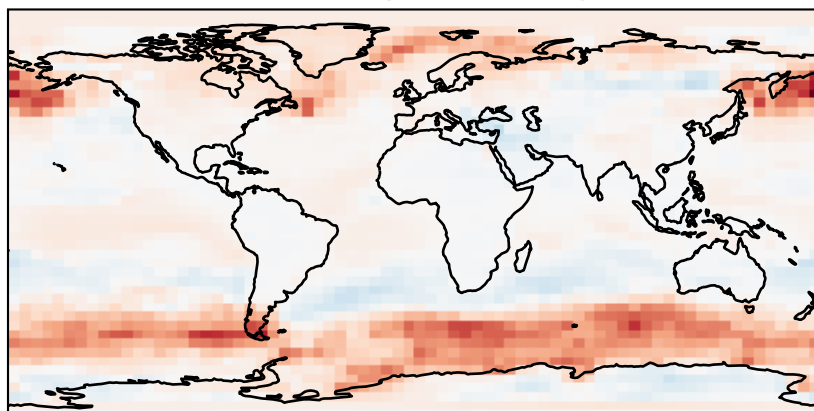
Difference  
Dev - Ref, Dynamic Range



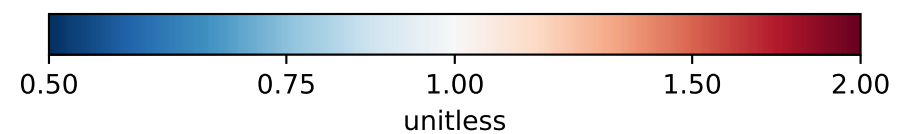
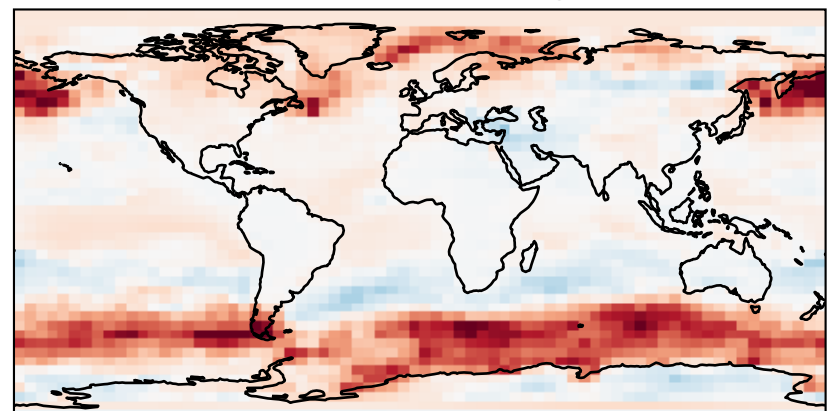
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

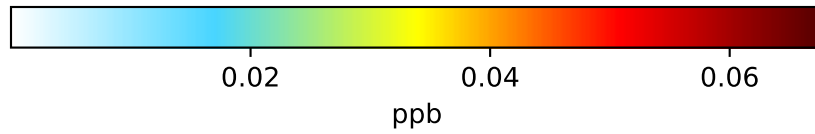
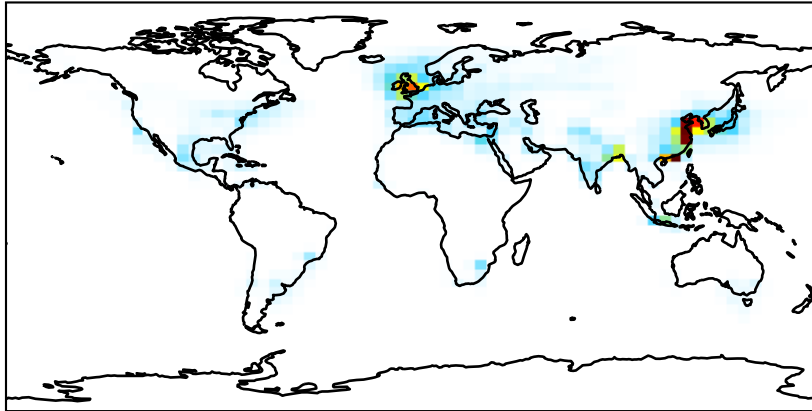


Ratio  
Dev/Ref, Fixed Range

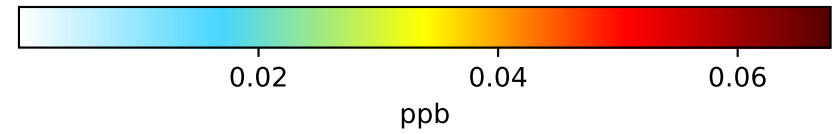
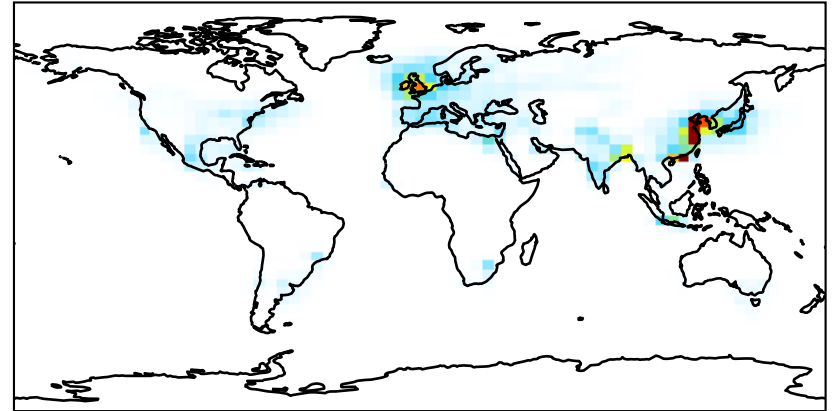


# SpeciesConc\_CINO2 (Apr2019)

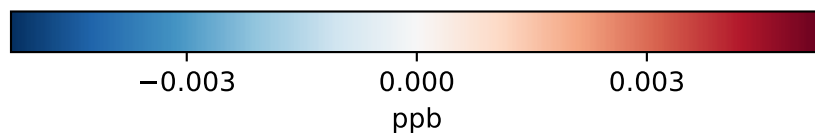
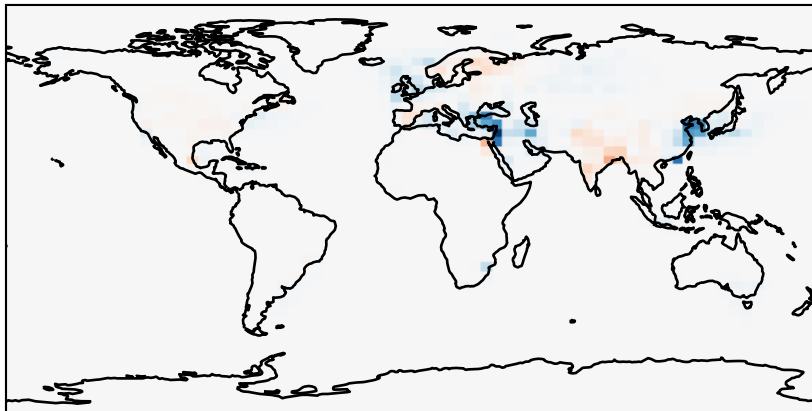
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



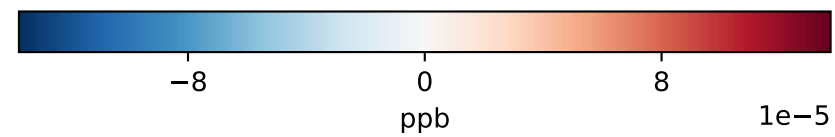
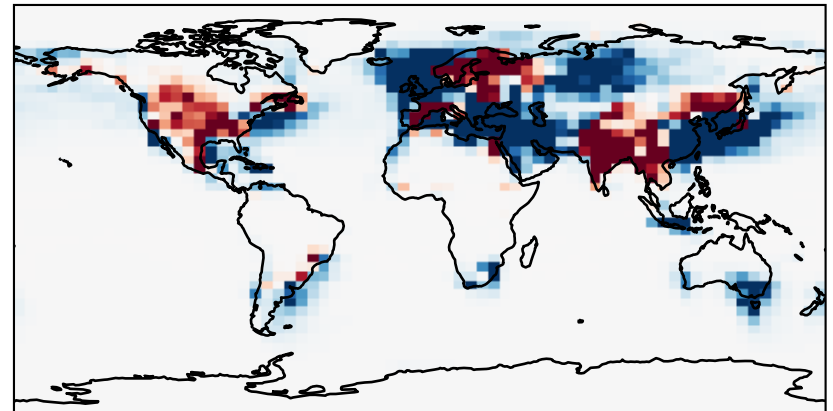
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



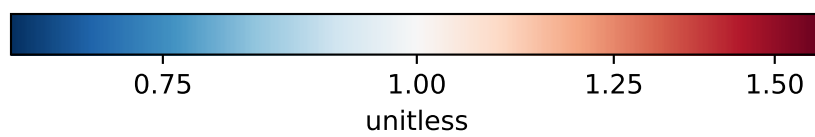
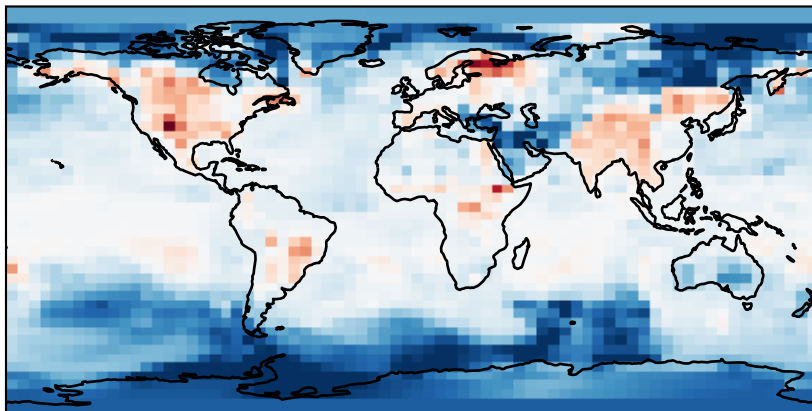
Difference  
Dev - Ref, Dynamic Range



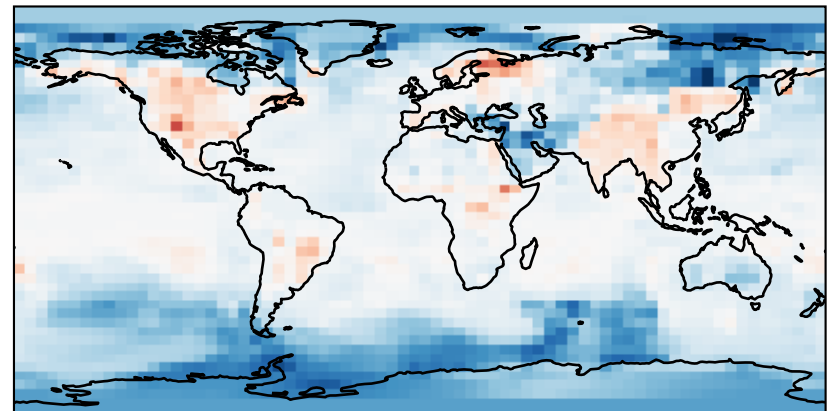
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



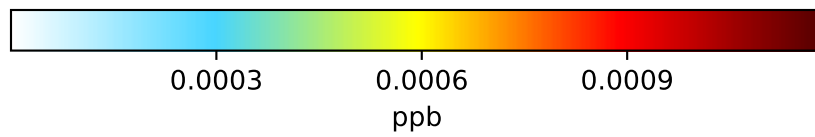
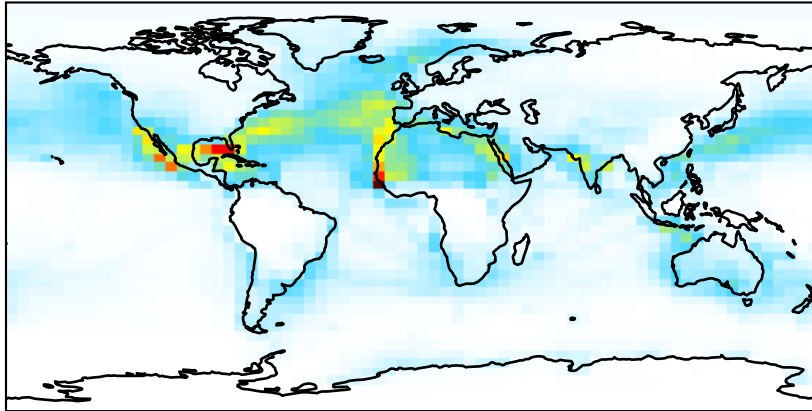
Ratio  
Dev/Ref, Fixed Range



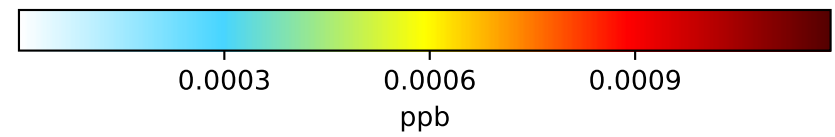
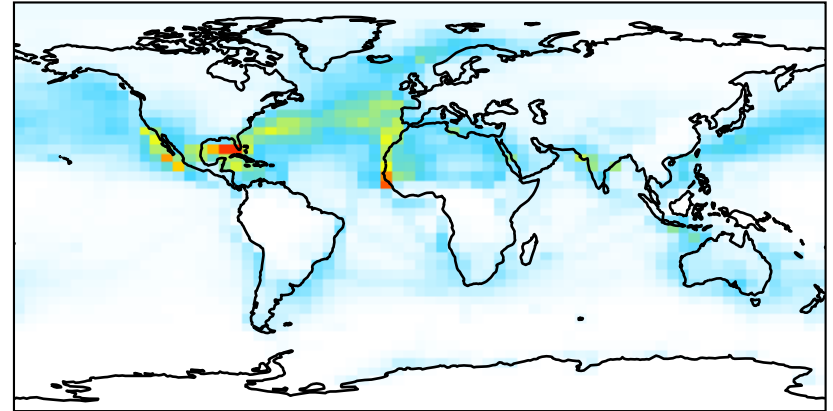


# SpeciesConc\_CINO3 (Apr2019)

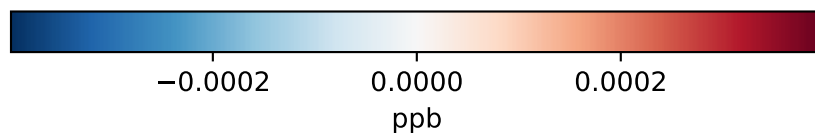
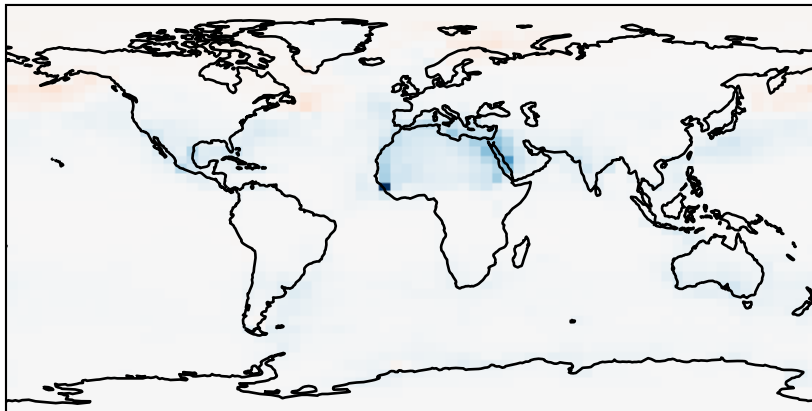
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



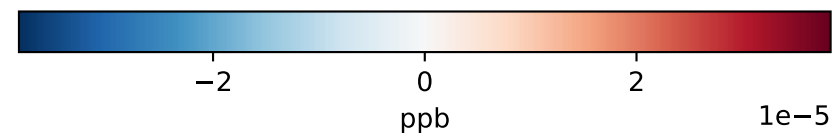
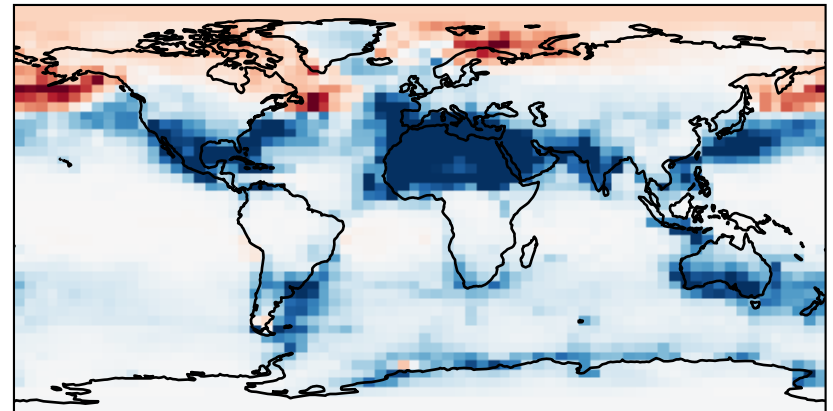
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



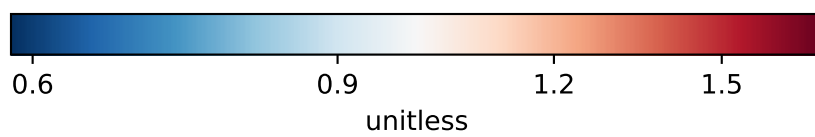
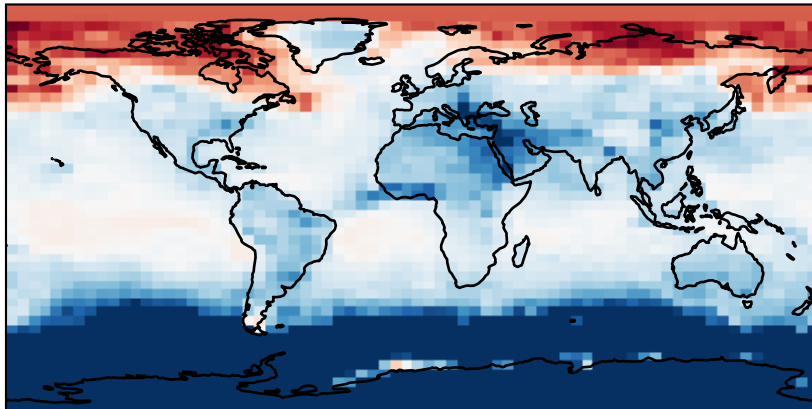
Difference  
Dev - Ref, Dynamic Range



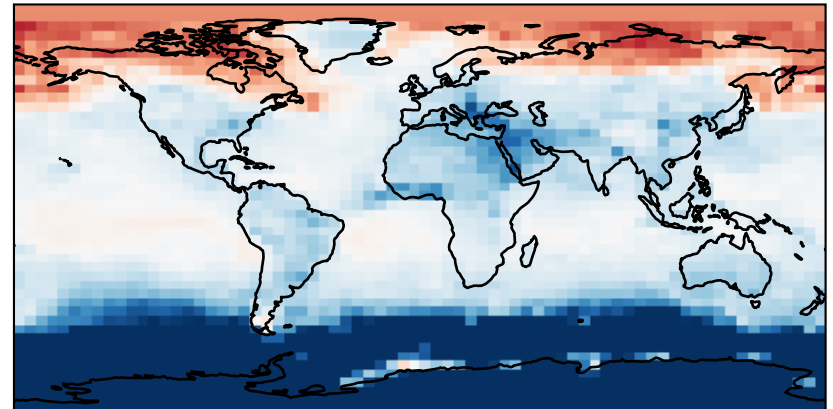
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

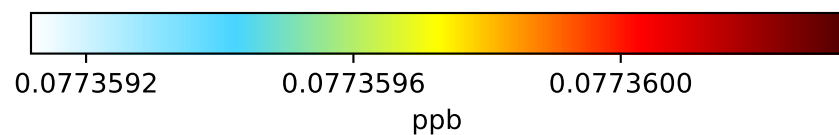
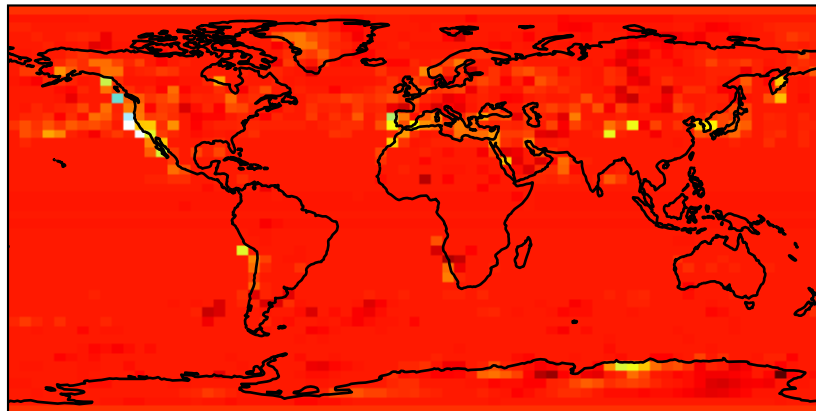


Ratio  
Dev/Ref, Fixed Range

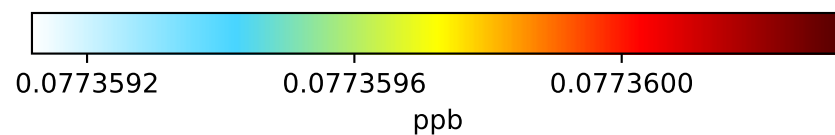
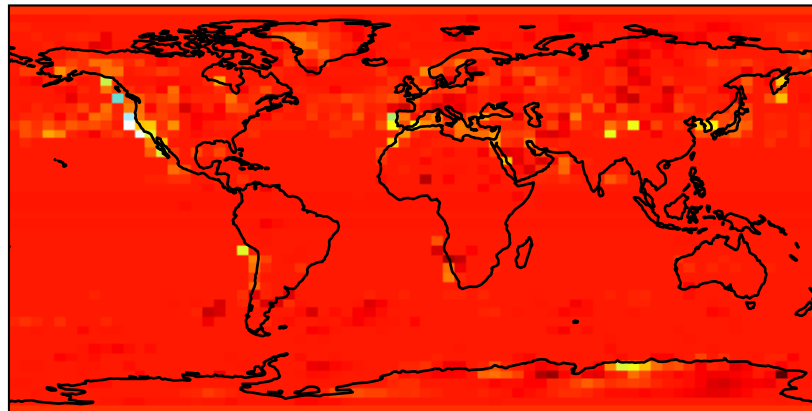


# SpeciesConc\_CCl4 (Apr2019)

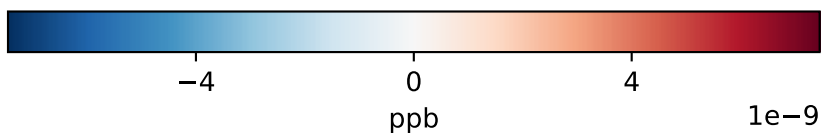
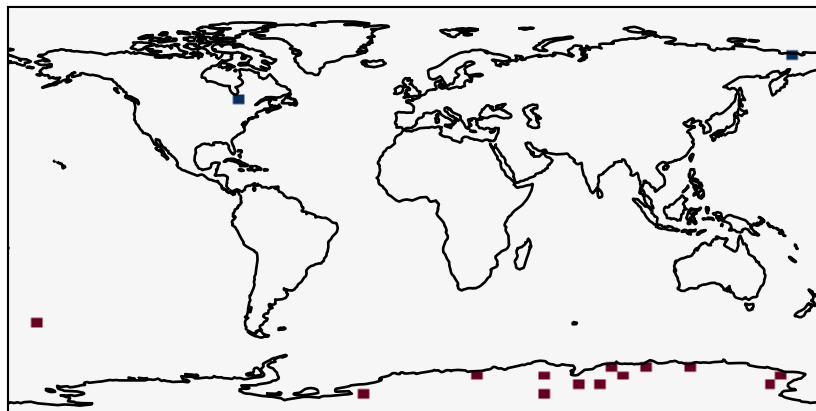
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



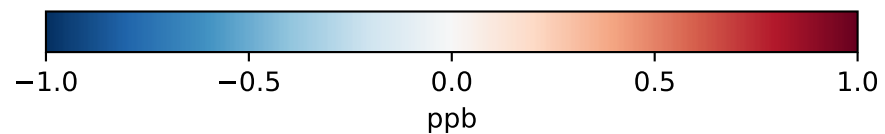
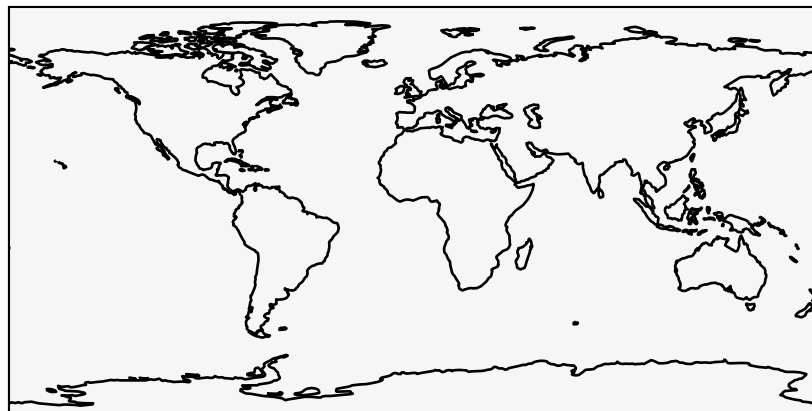
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



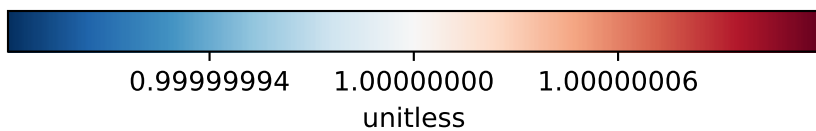
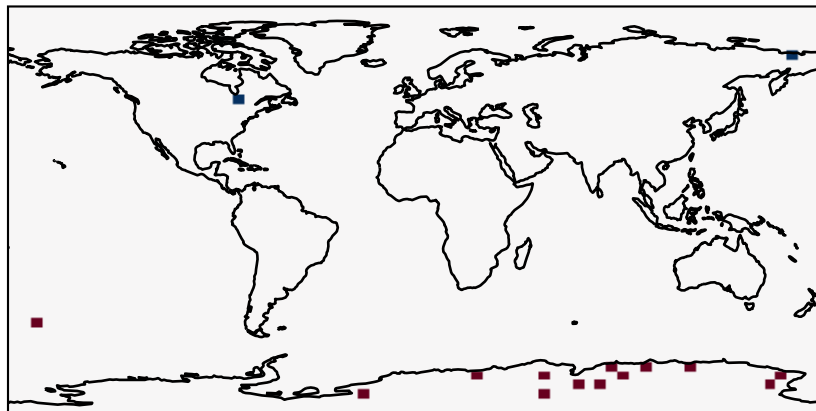
Difference  
Dev - Ref, Dynamic Range



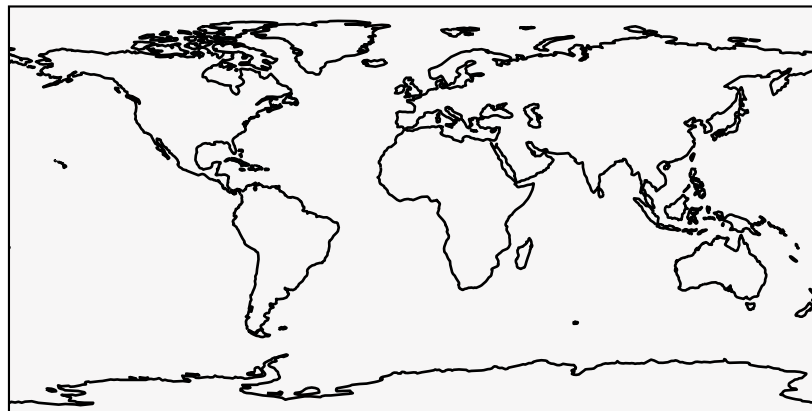
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

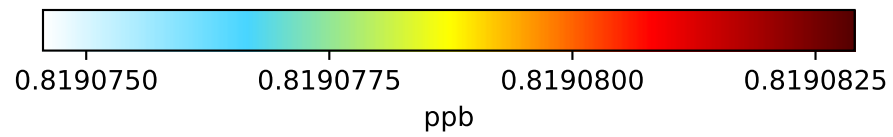
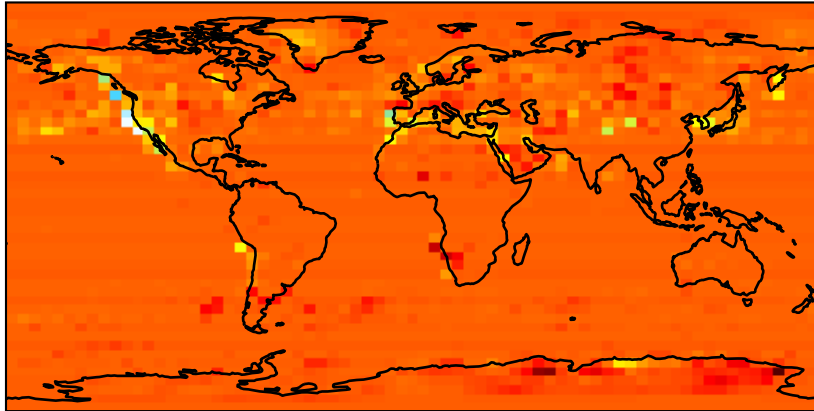


Ratio  
Dev/Ref, Fixed Range

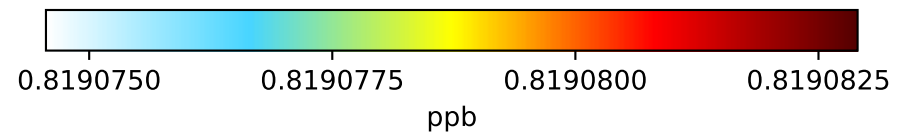
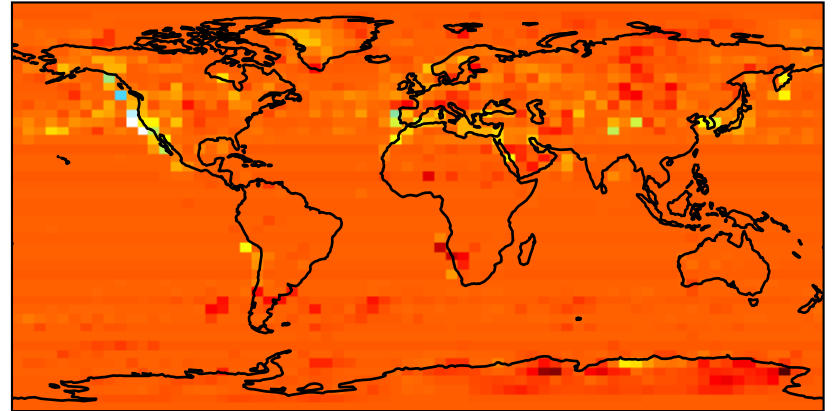


# SpeciesConc\_CFCs (Apr2019)

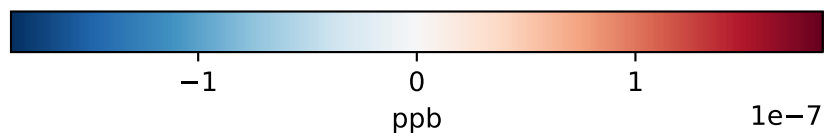
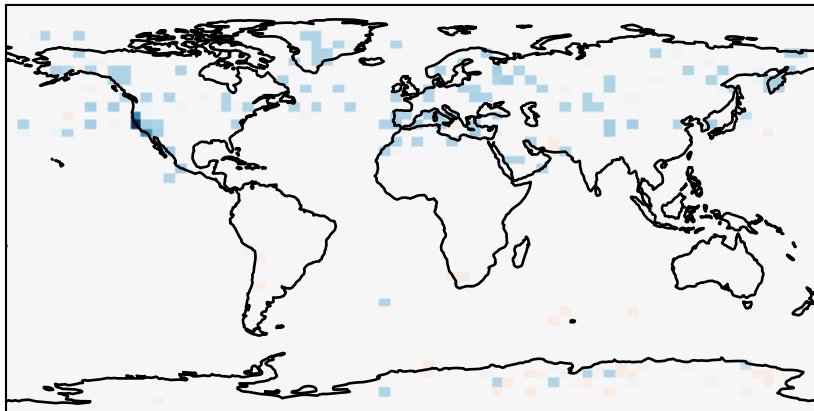
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



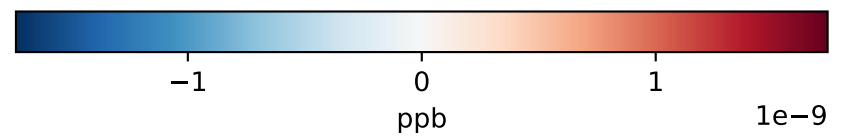
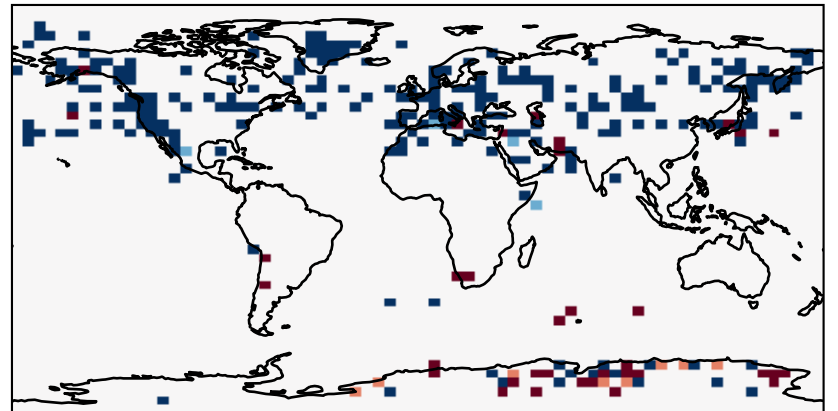
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



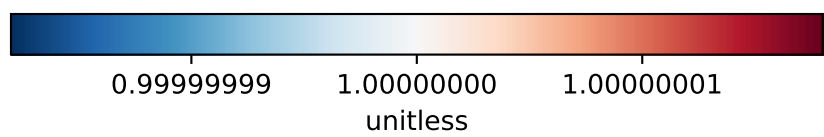
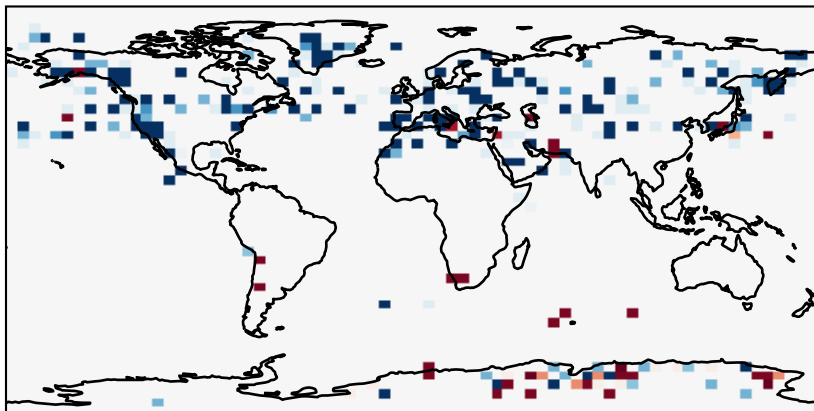
Difference  
Dev - Ref, Dynamic Range



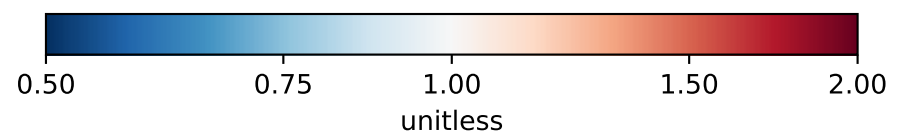
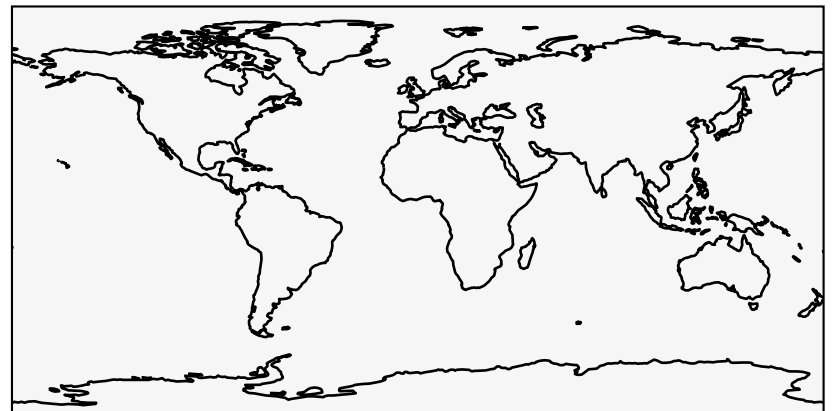
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

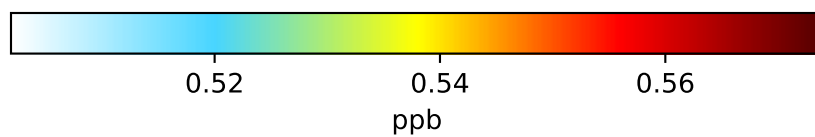
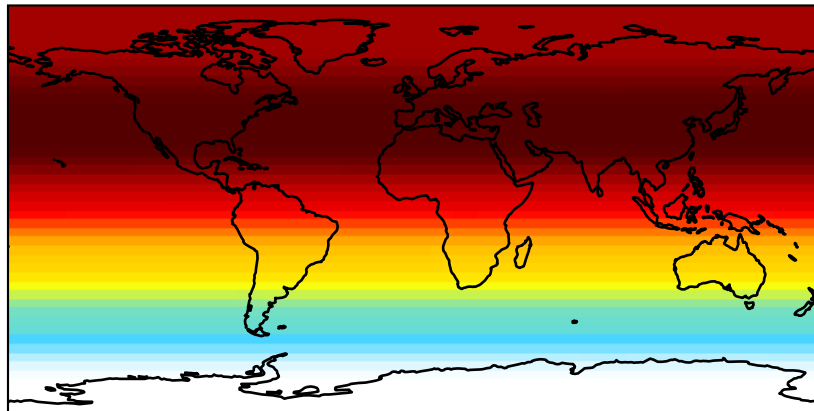


Ratio  
Dev/Ref, Fixed Range

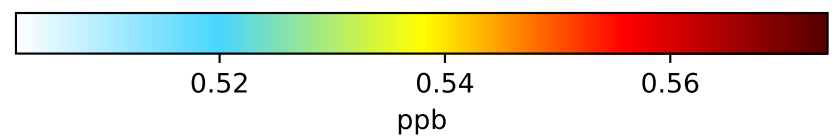
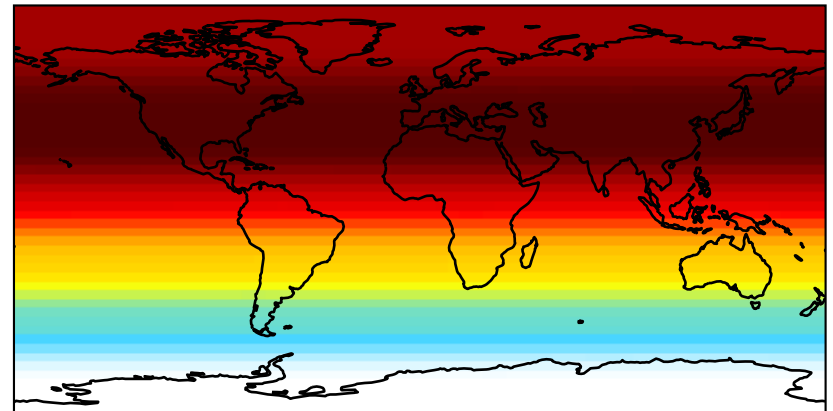


# SpeciesConc\_CH3Cl (Apr2019)

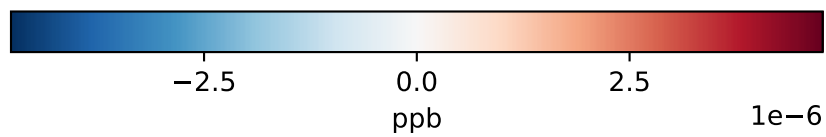
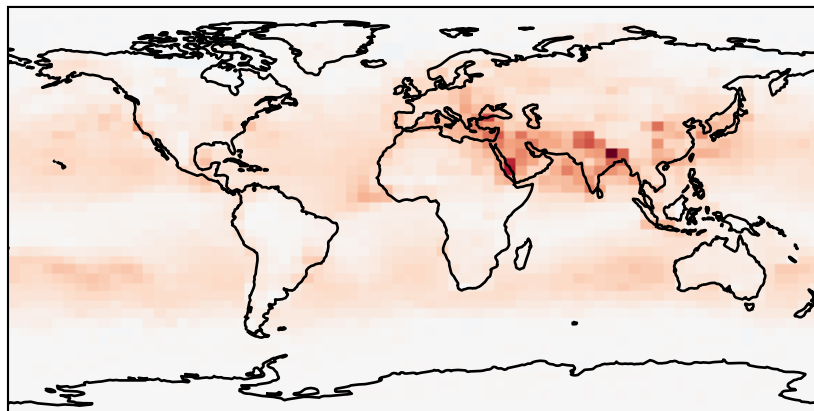
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



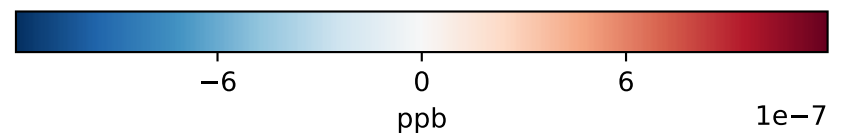
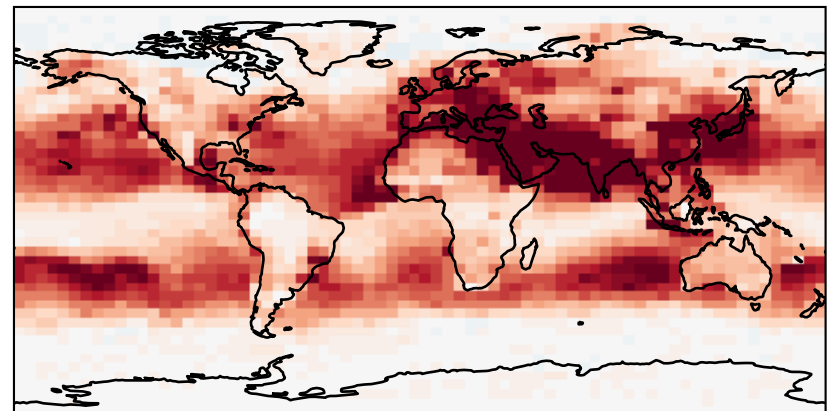
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



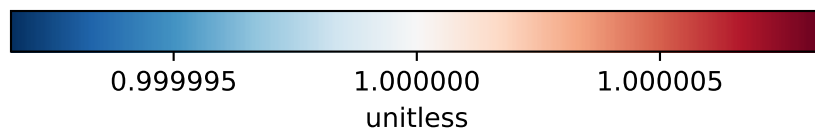
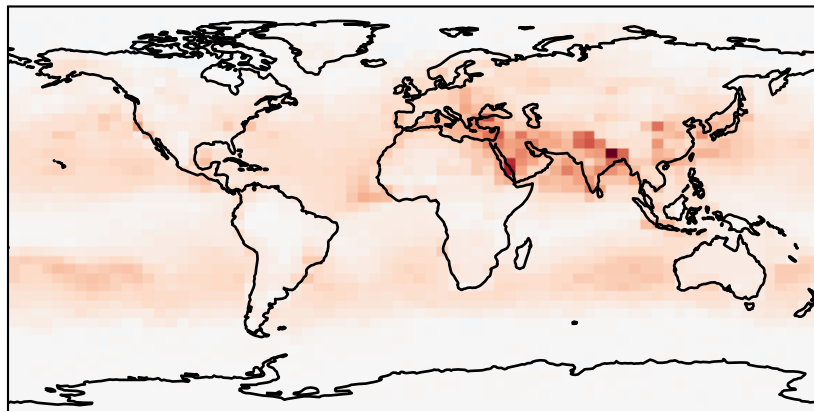
Difference  
Dev - Ref, Dynamic Range



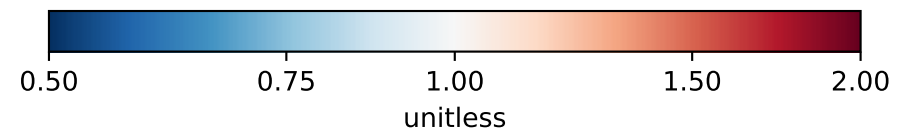
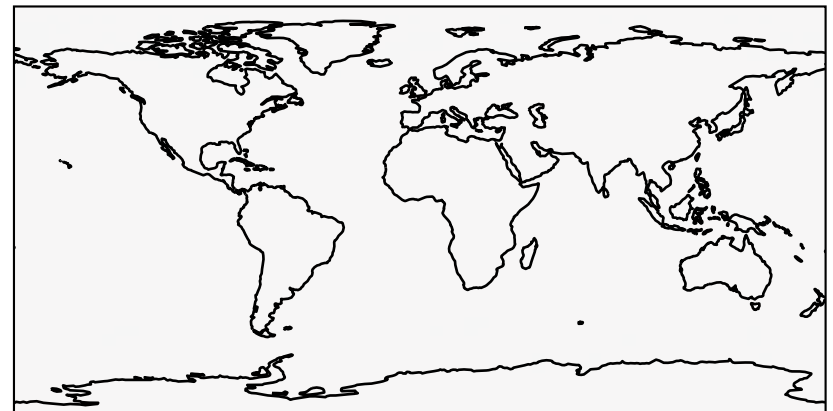
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



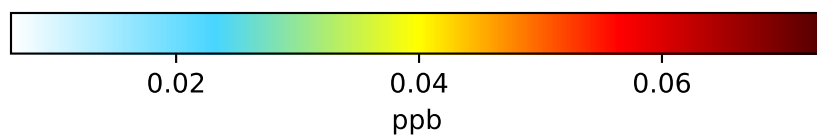
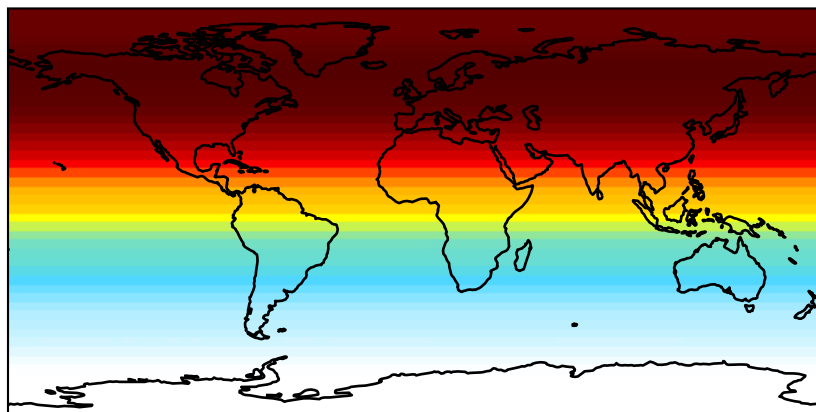
Ratio  
Dev/Ref, Fixed Range



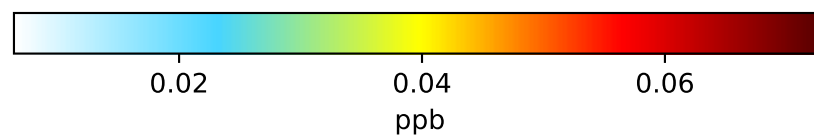
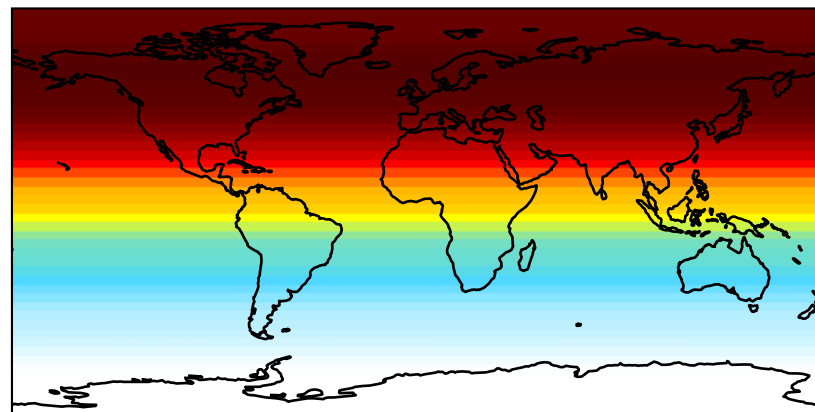


# SpeciesConc\_CH2Cl2 (Apr2019)

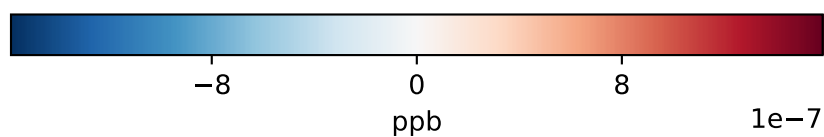
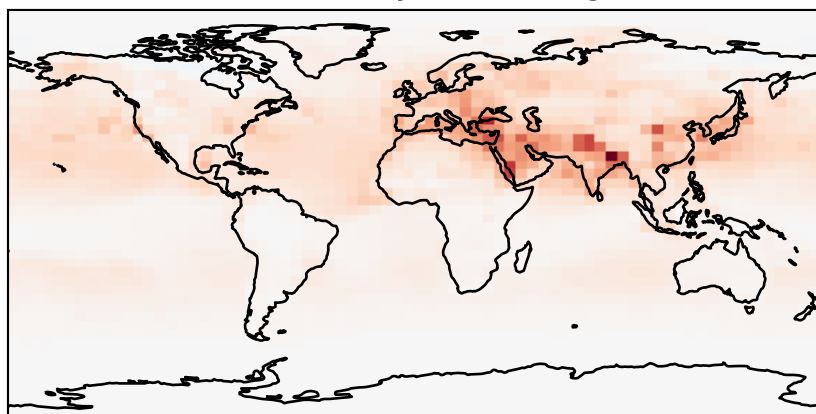
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



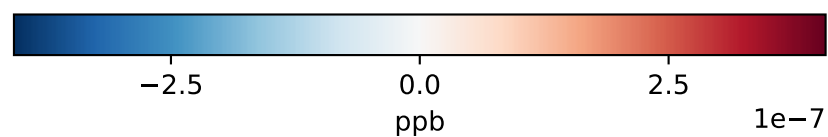
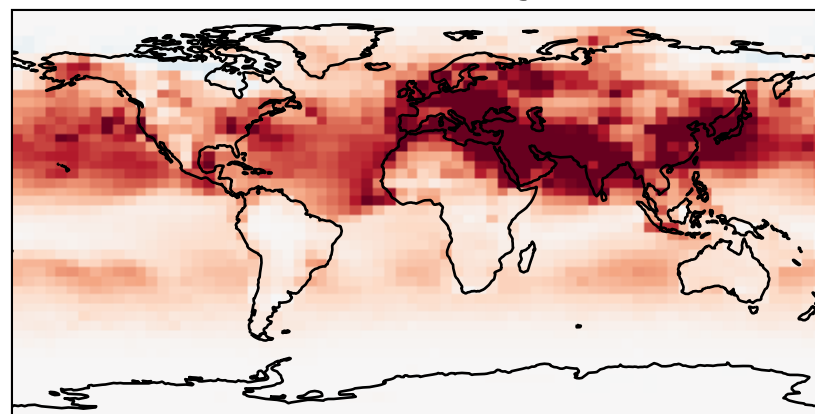
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



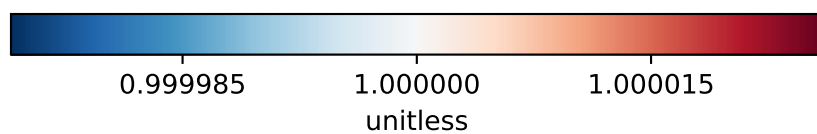
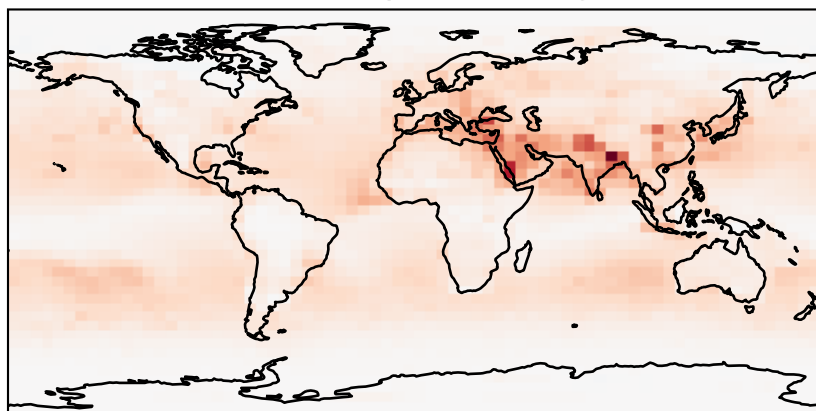
Difference  
Dev - Ref, Dynamic Range



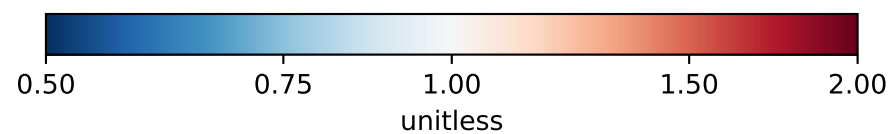
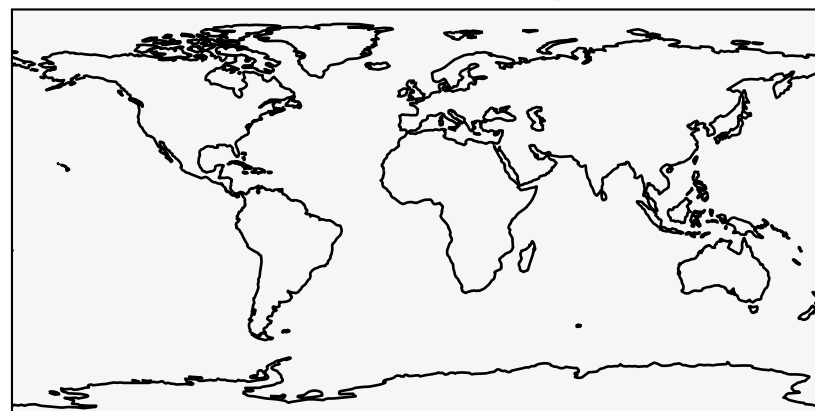
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

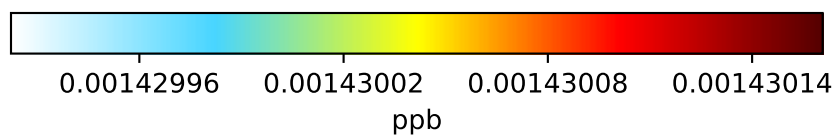
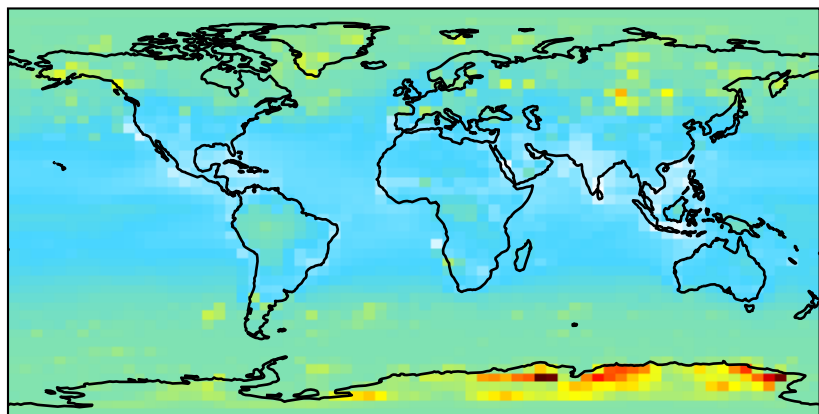


Ratio  
Dev/Ref, Fixed Range

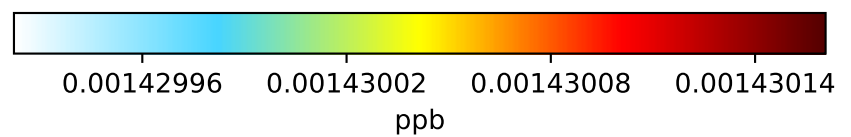
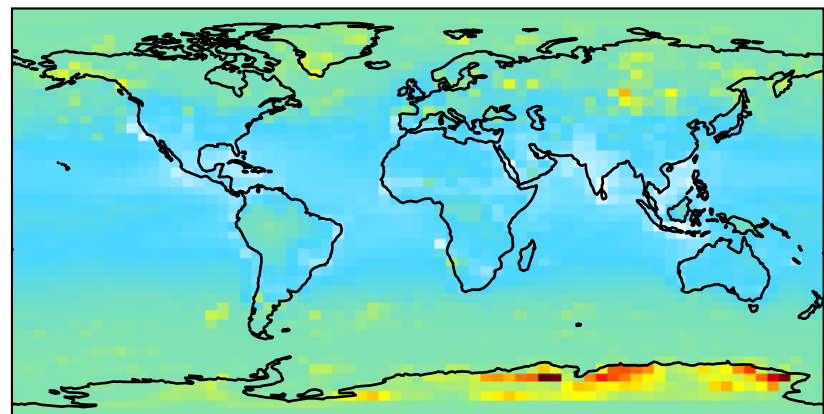


# SpeciesConc\_CH3CCl3 (Apr2019)

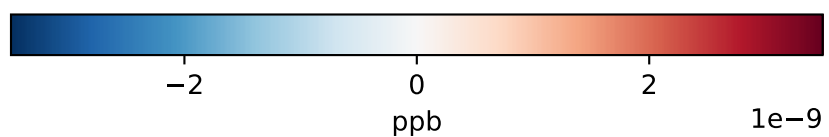
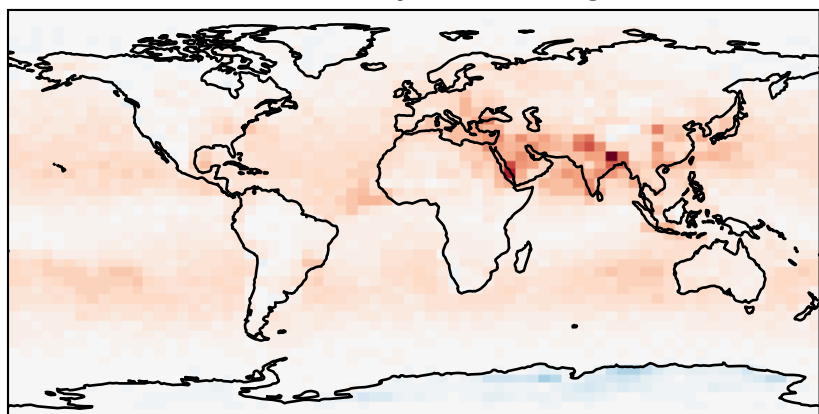
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



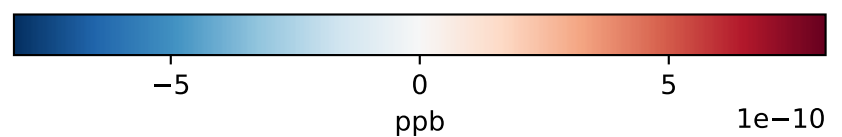
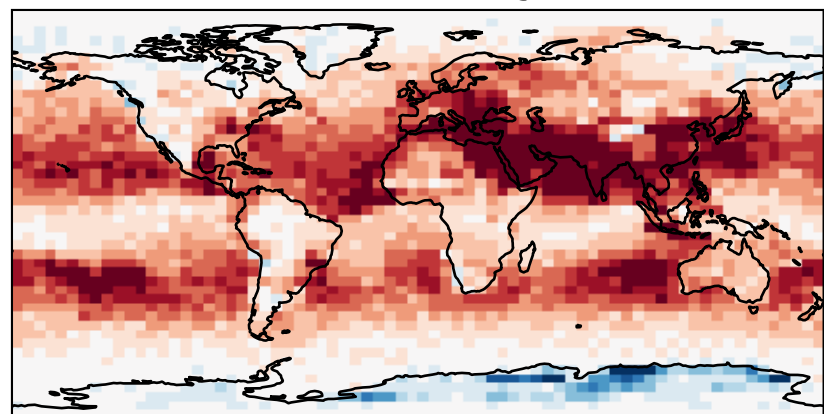
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



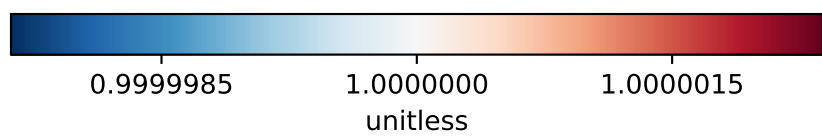
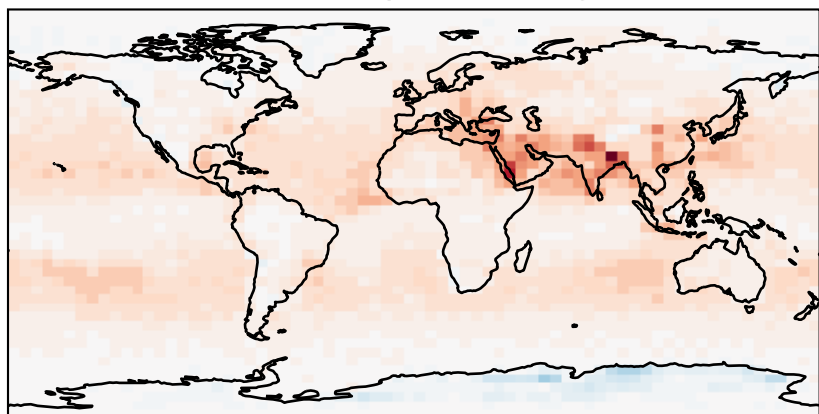
Difference  
Dev - Ref, Dynamic Range



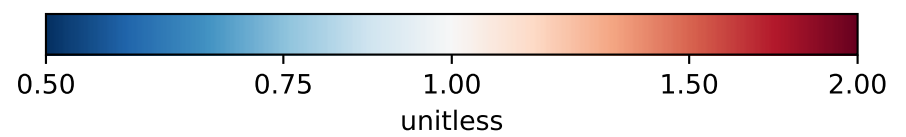
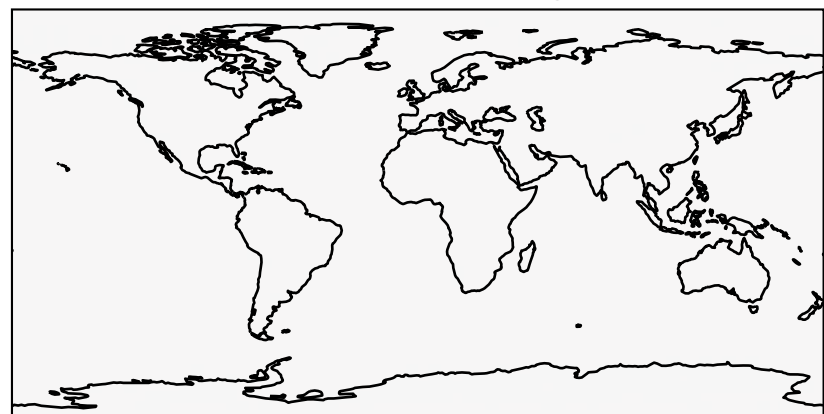
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

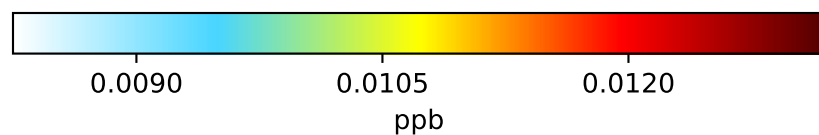
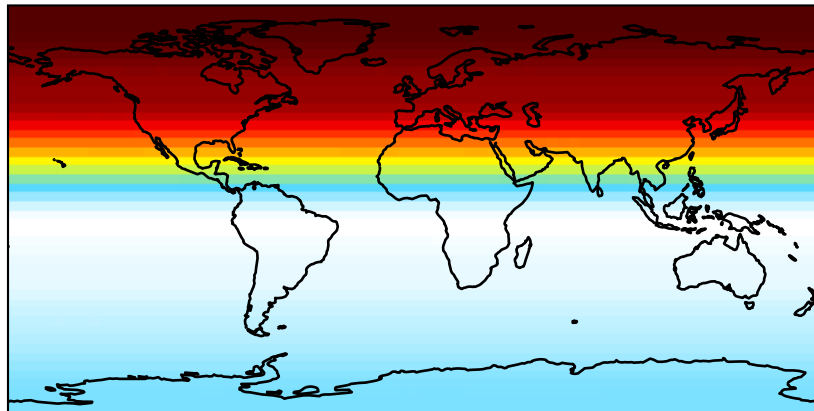


Ratio  
Dev/Ref, Fixed Range

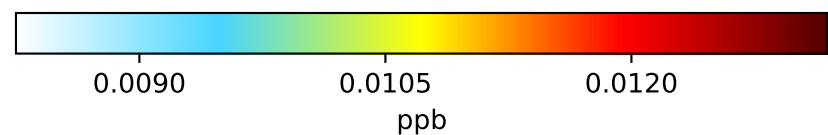
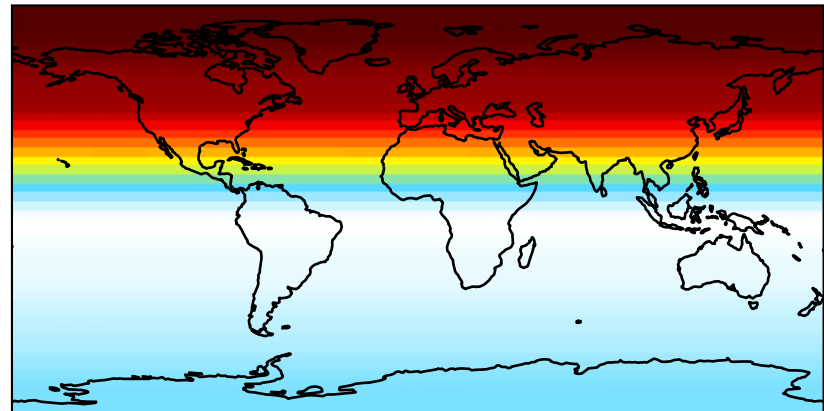


# SpeciesConc\_CHCl3 (Apr2019)

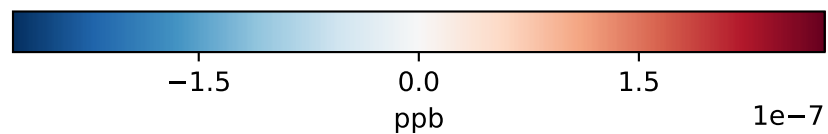
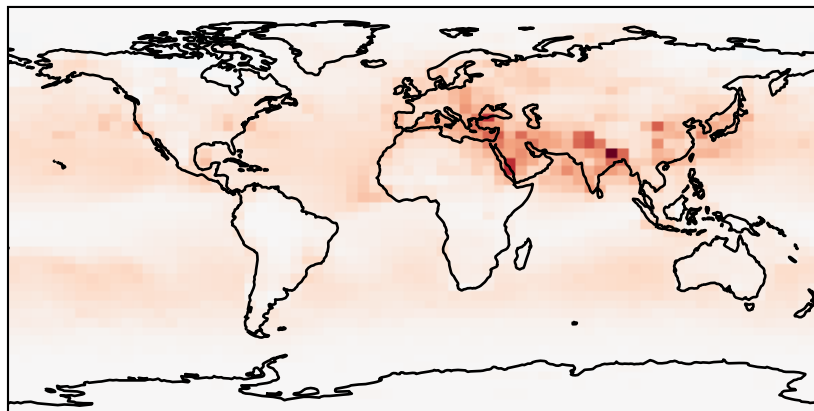
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



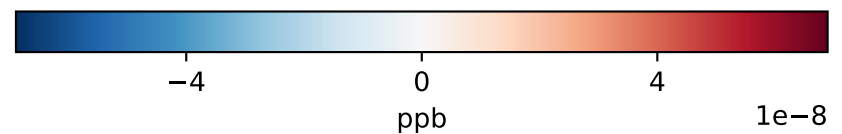
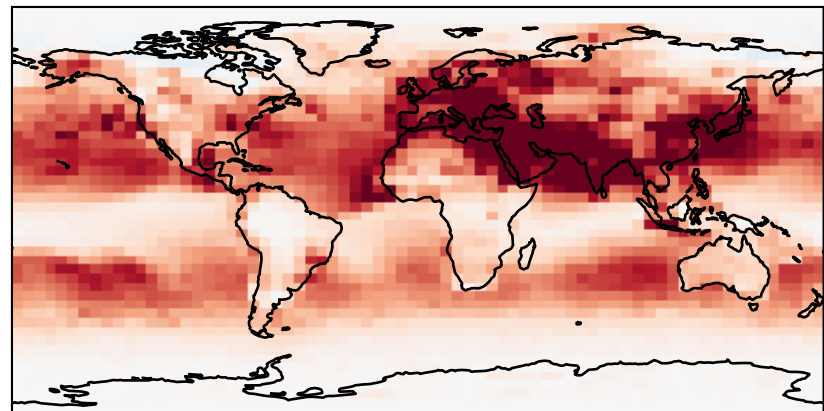
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



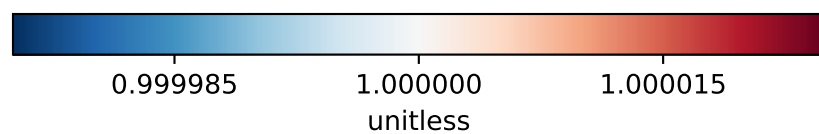
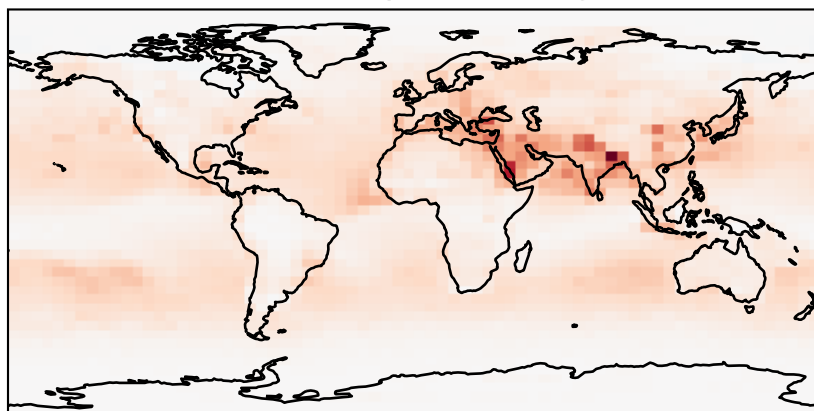
Difference  
Dev - Ref, Dynamic Range



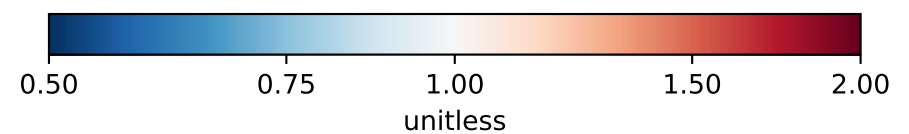
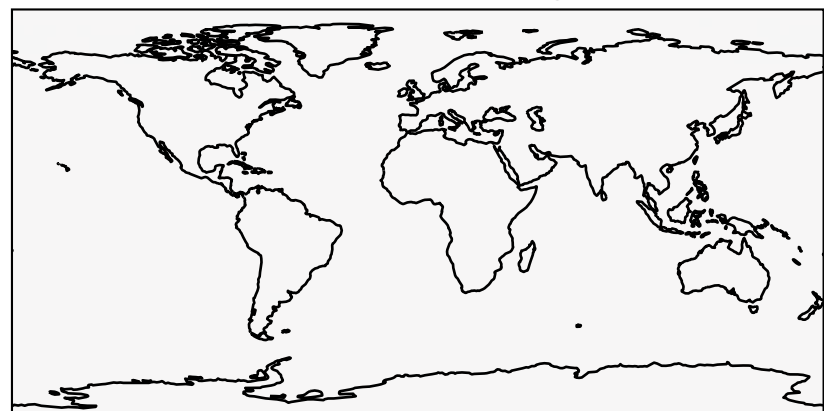
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

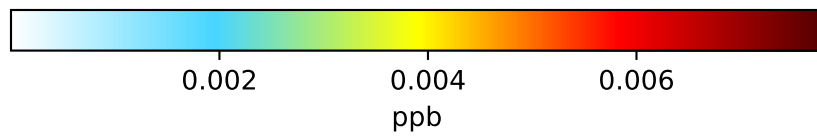
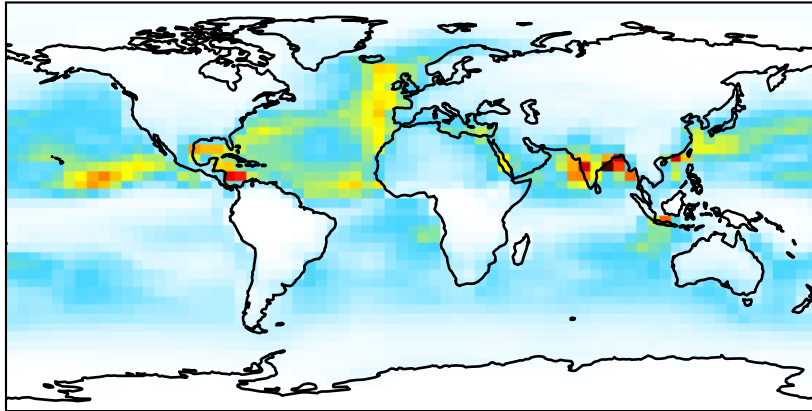


Ratio  
Dev/Ref, Fixed Range

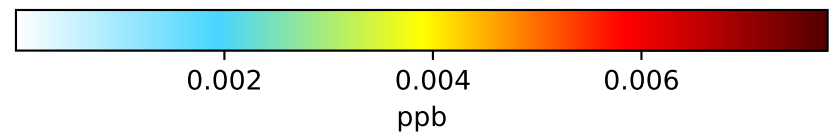
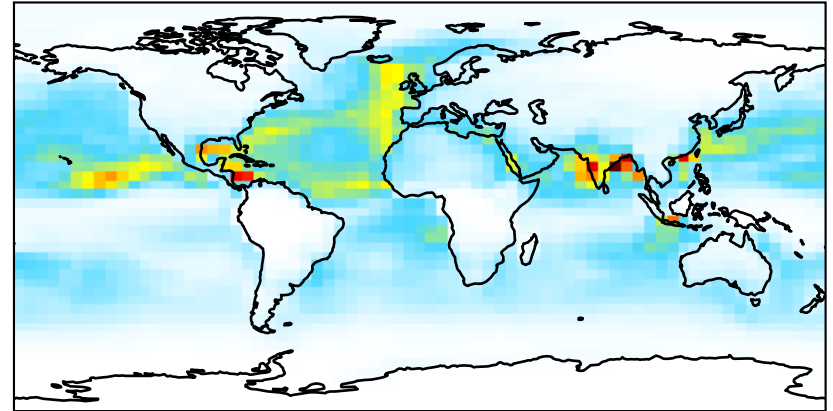


# SpeciesConc\_HOCl (Apr2019)

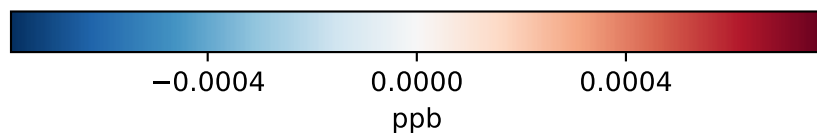
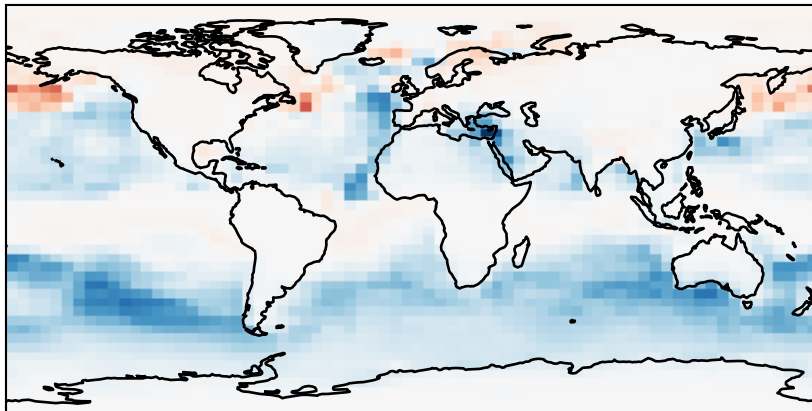
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



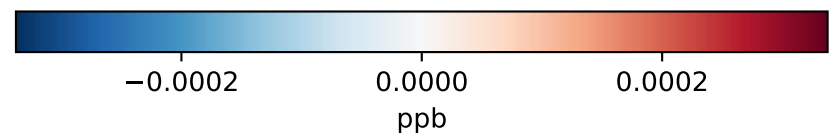
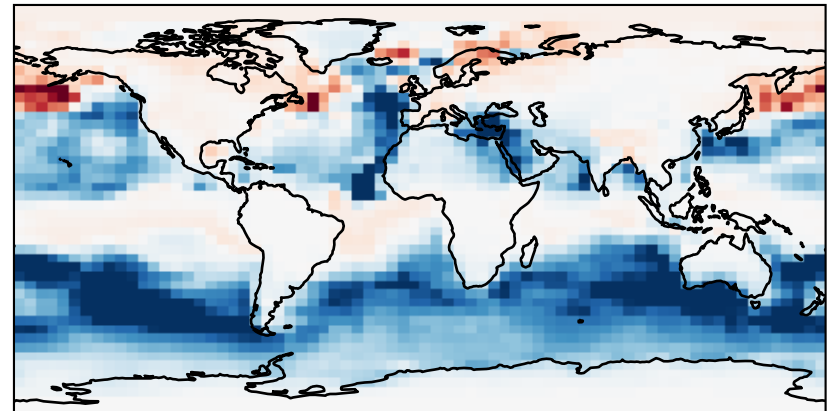
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



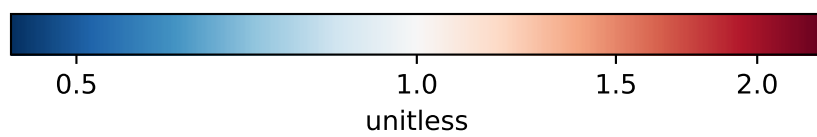
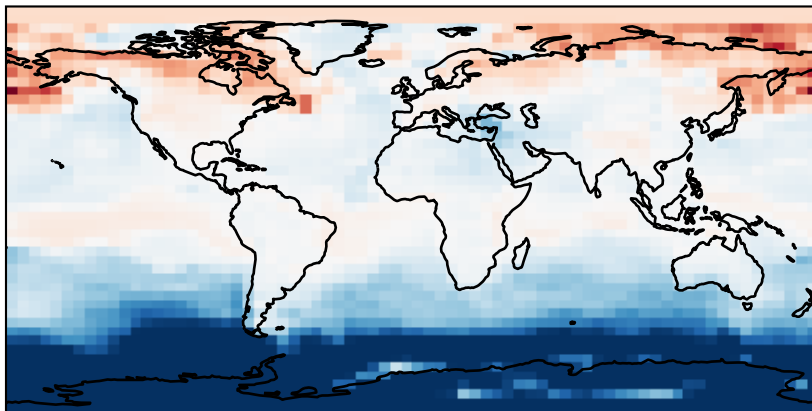
Difference  
Dev - Ref, Dynamic Range



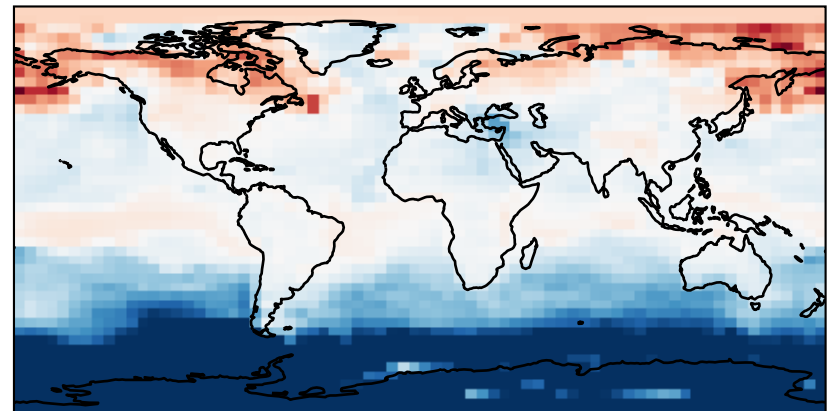
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



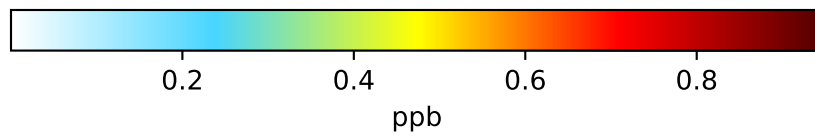
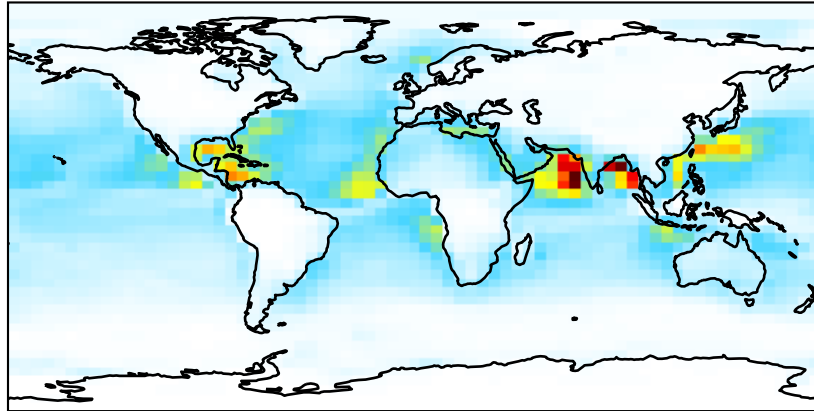
Ratio  
Dev/Ref, Fixed Range



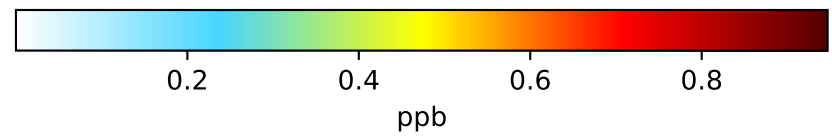
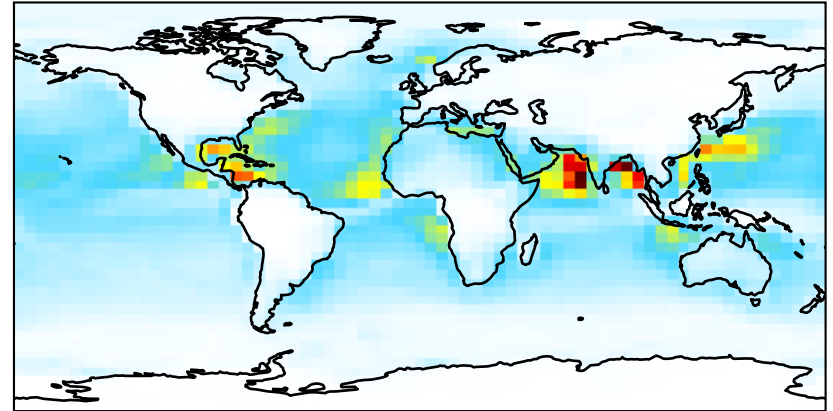


# SpeciesConc\_HCl (Apr2019)

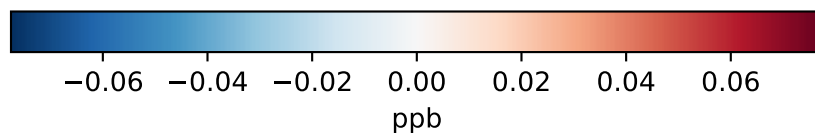
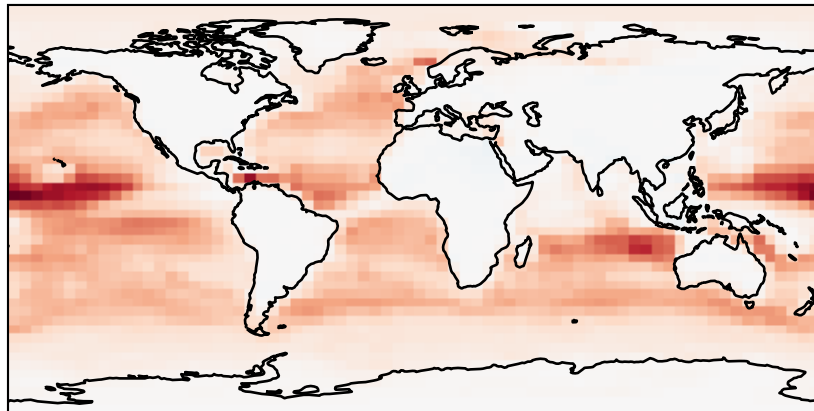
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



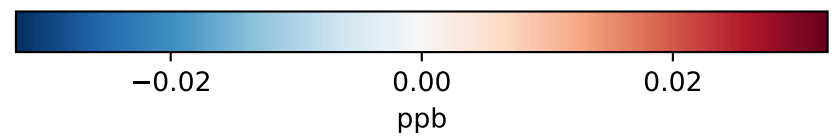
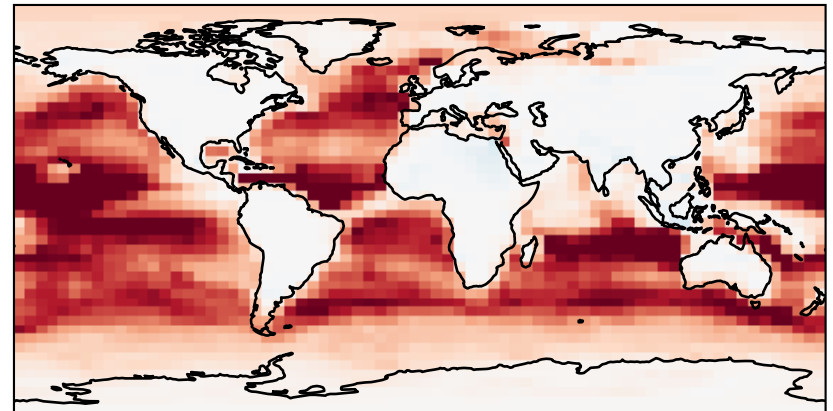
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



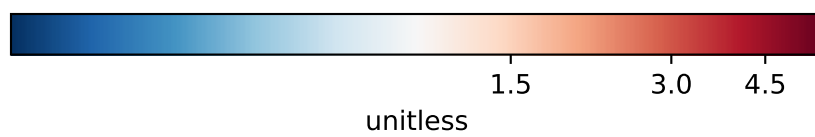
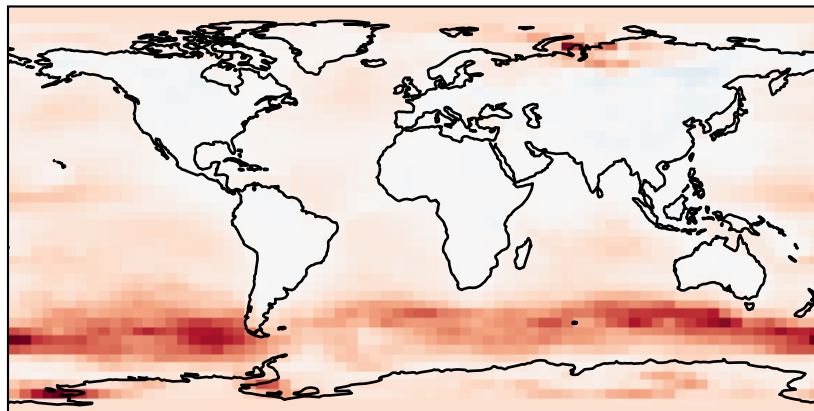
Difference  
Dev - Ref, Dynamic Range



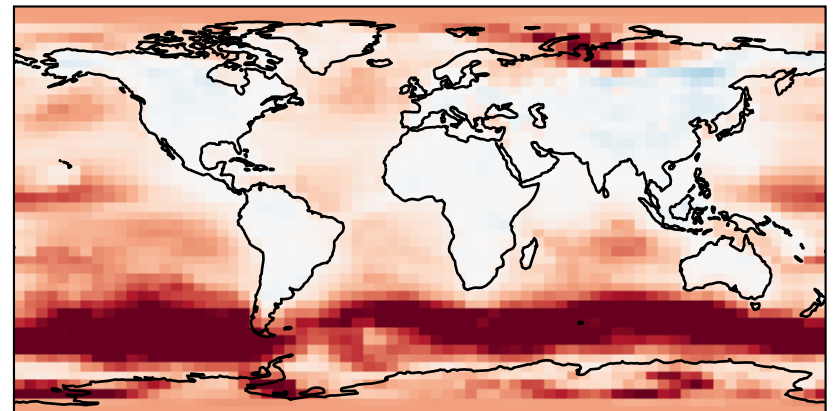
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

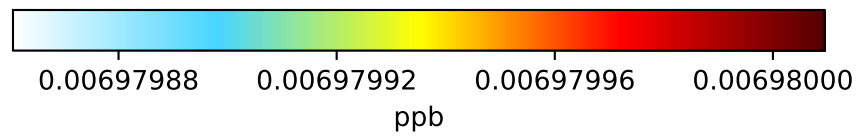
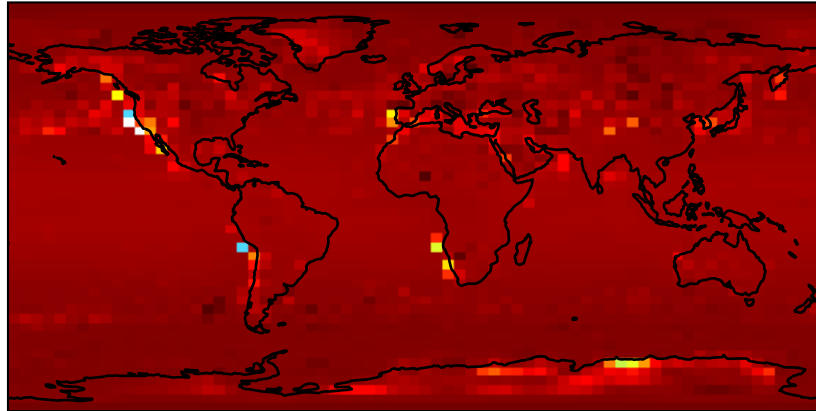


Ratio  
Dev/Ref, Fixed Range

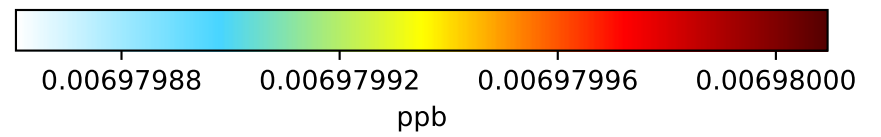
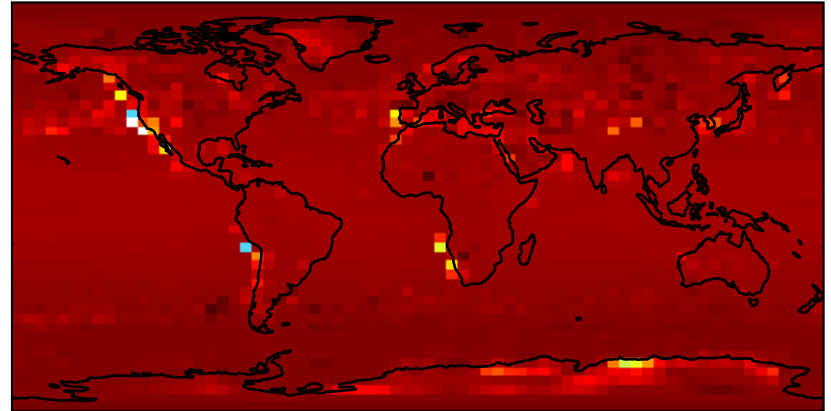


# SpeciesConc\_Halons (Apr2019)

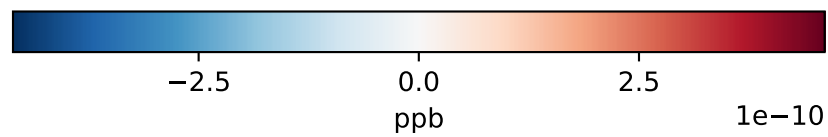
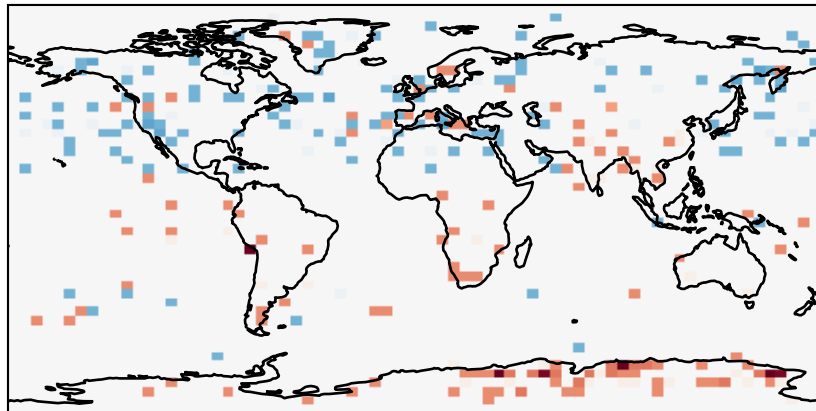
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



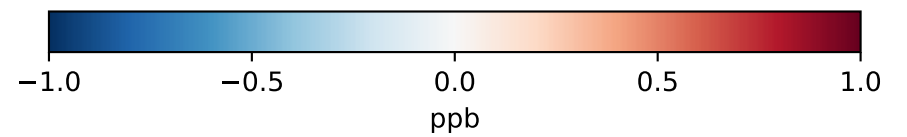
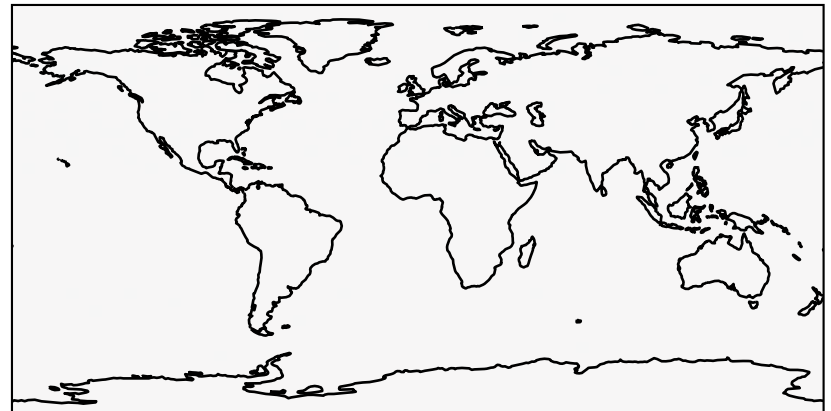
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



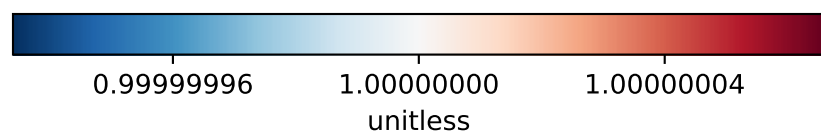
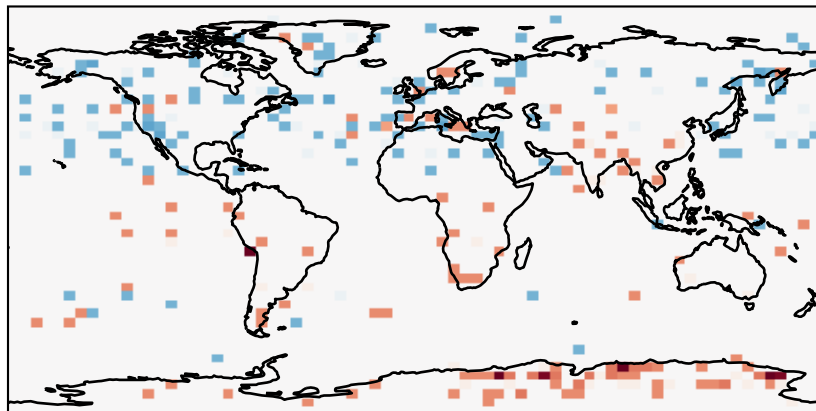
Difference  
Dev - Ref, Dynamic Range



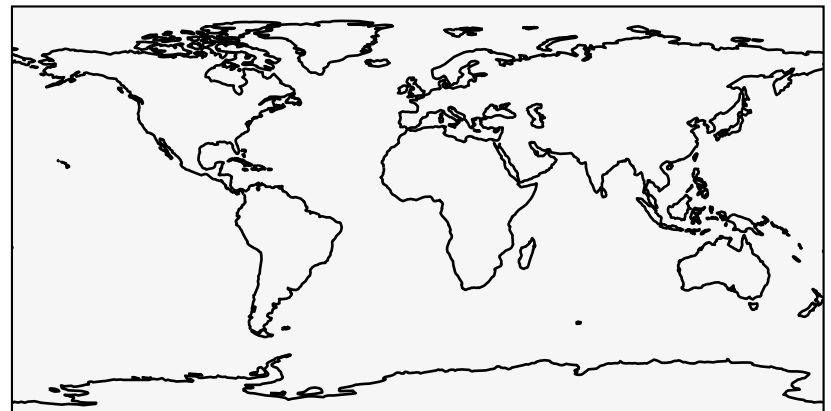
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

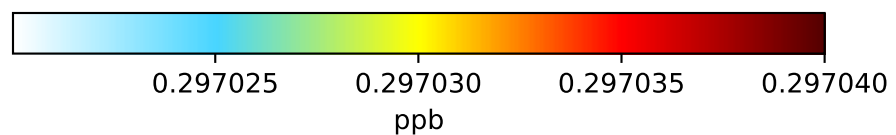
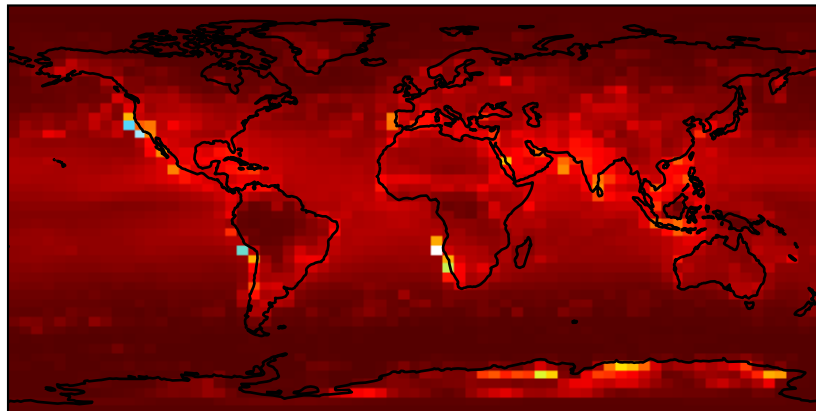


Ratio  
Dev/Ref, Fixed Range

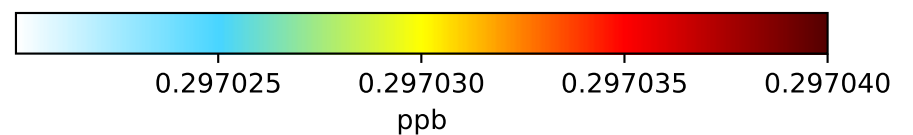
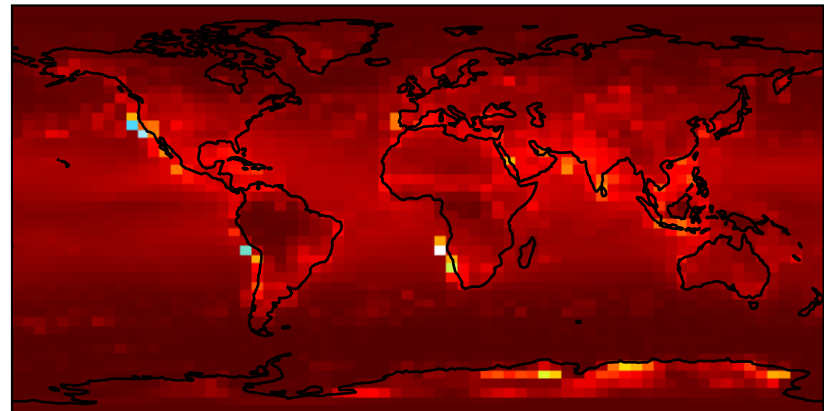


# SpeciesConc\_HCFCs (Apr2019)

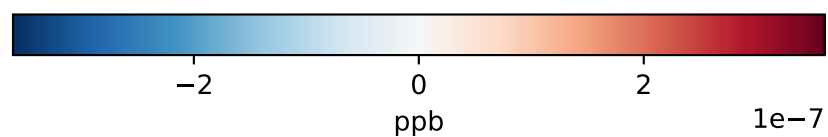
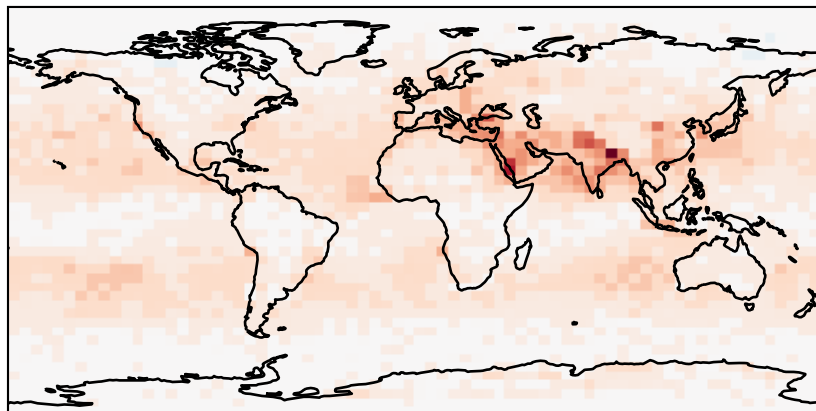
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



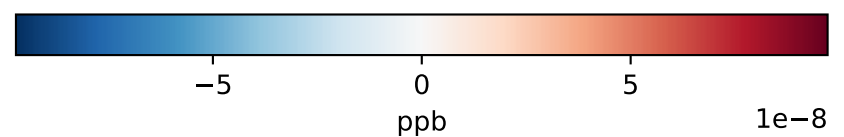
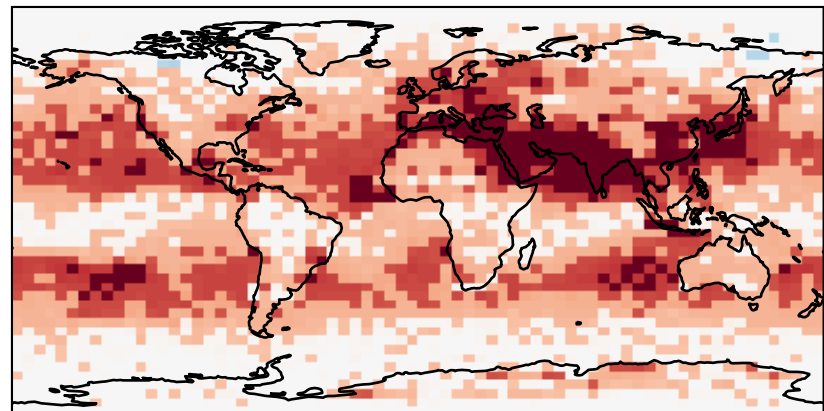
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



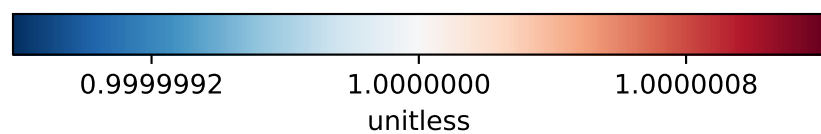
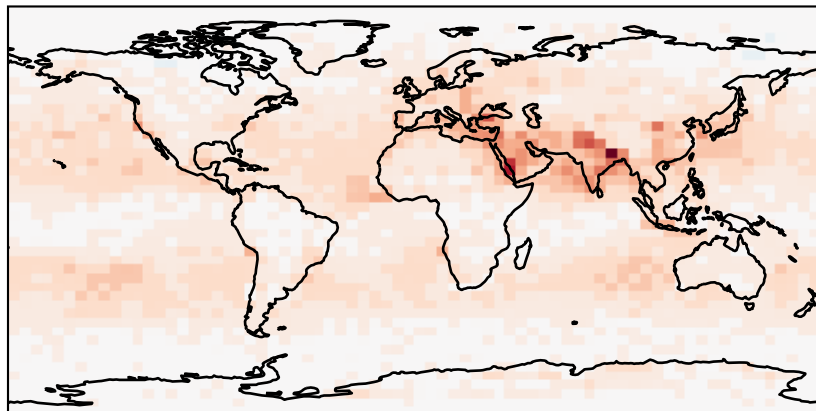
Difference  
Dev - Ref, Dynamic Range



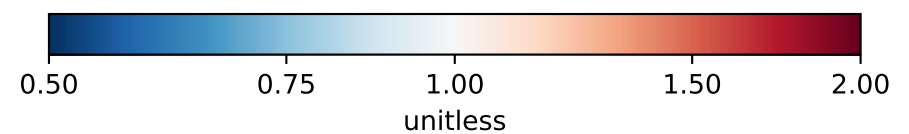
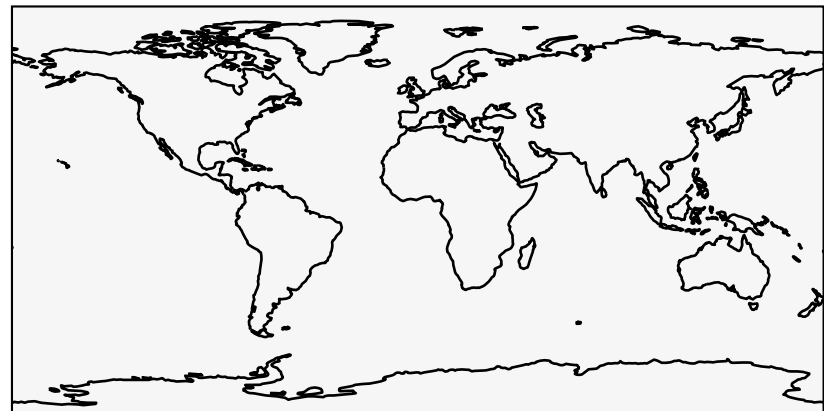
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

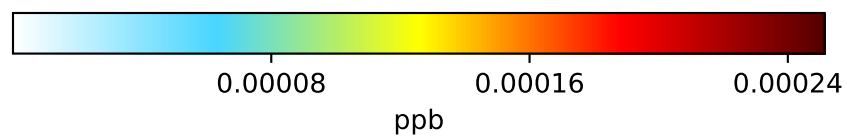
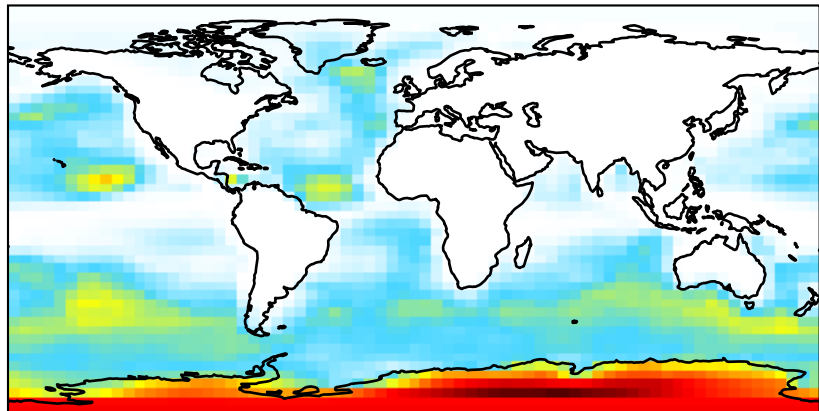


Ratio  
Dev/Ref, Fixed Range

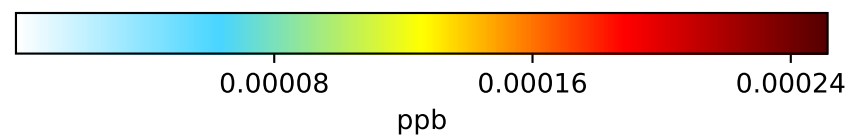
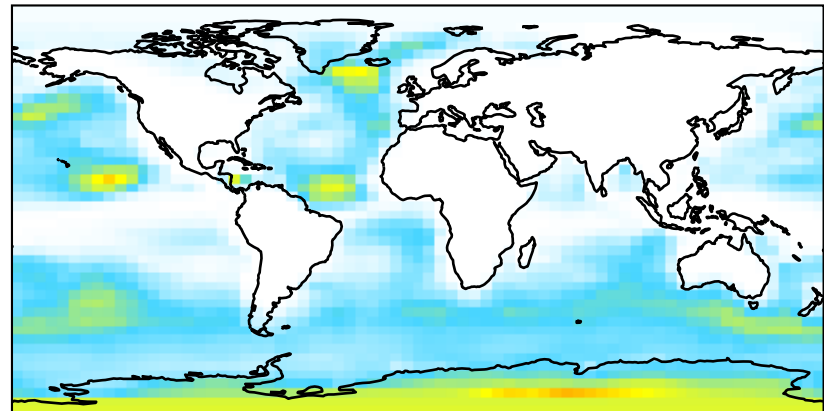


# SpeciesConc\_OClO (Apr2019)

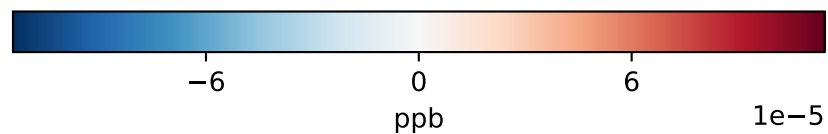
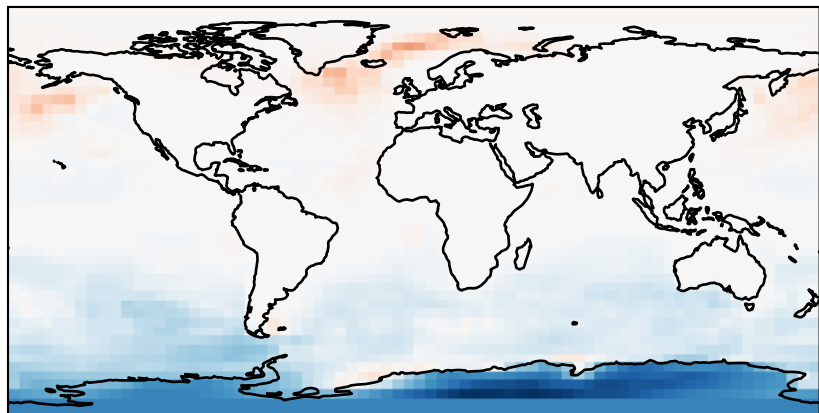
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



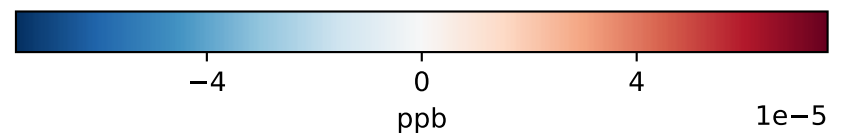
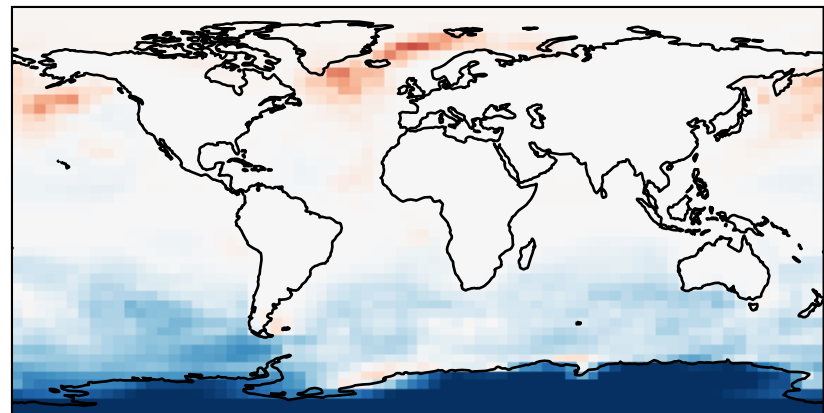
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



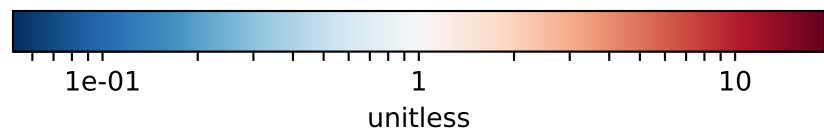
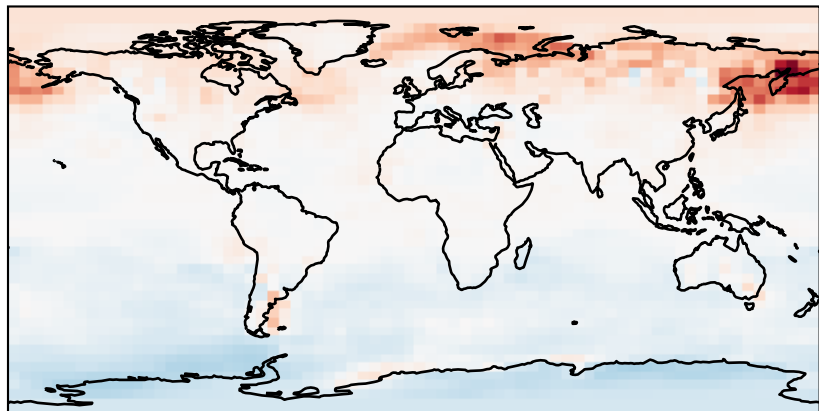
Difference  
Dev - Ref, Dynamic Range



Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

