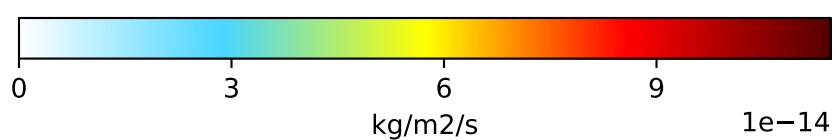
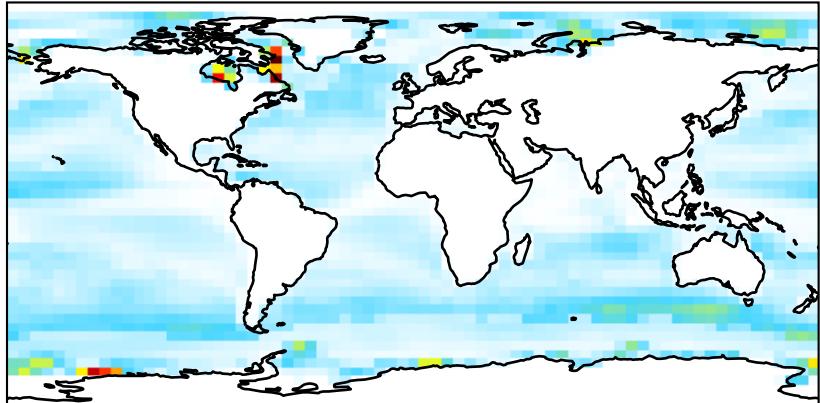
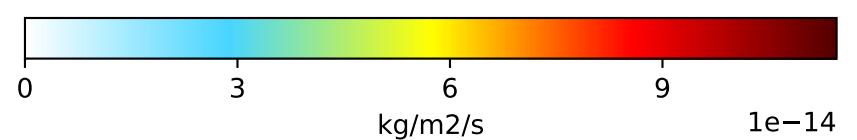
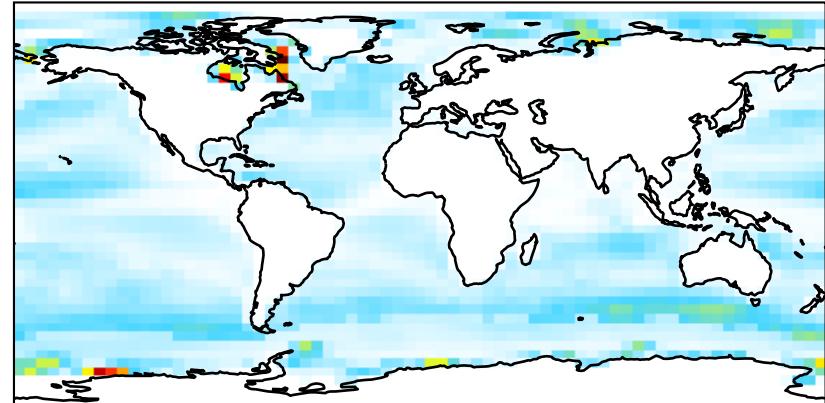


EmisBrSALA_Natural (Apr2019)

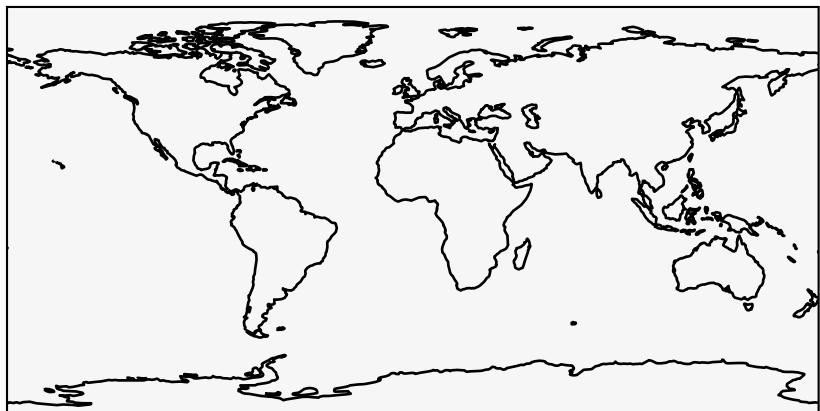
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



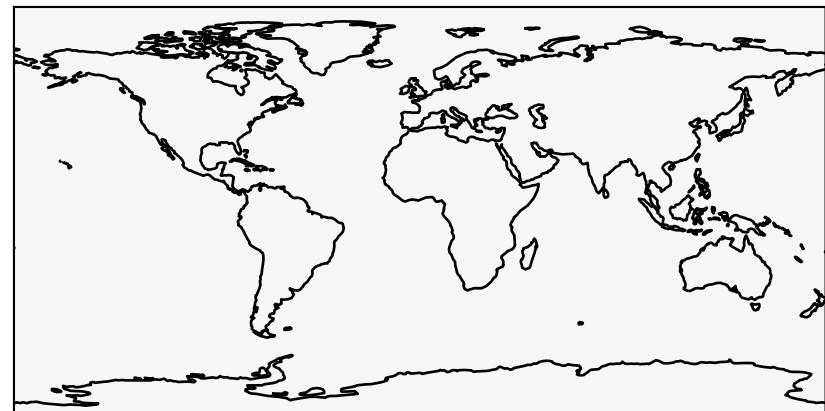
GCC_13.4.0-rc.1 (Dev)
4.0x5.0



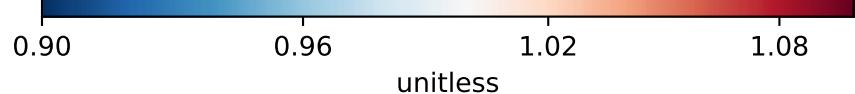
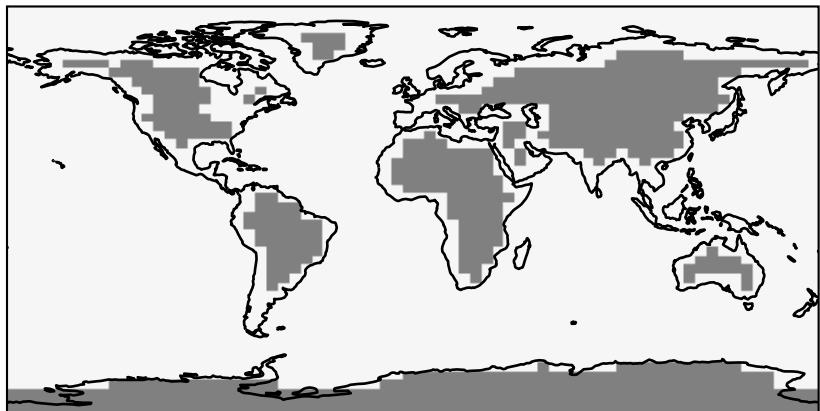
Difference
Dev - Ref, Dynamic Range



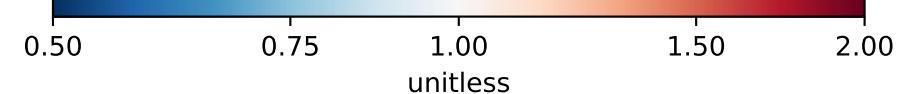
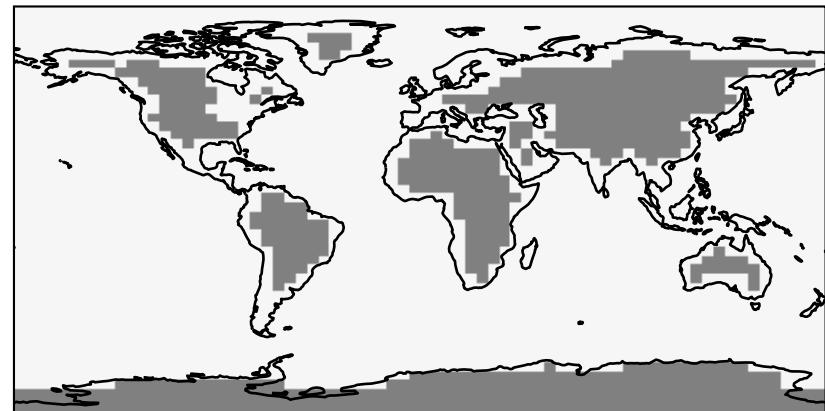
Difference
Dev - Ref, Restricted Range [5%, 95%]



Ratio
Dev/Ref, Dynamic Range

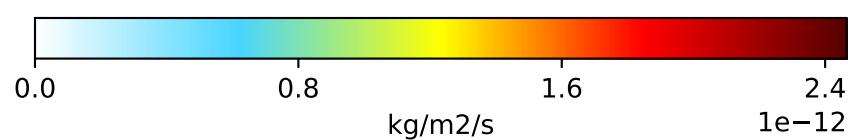
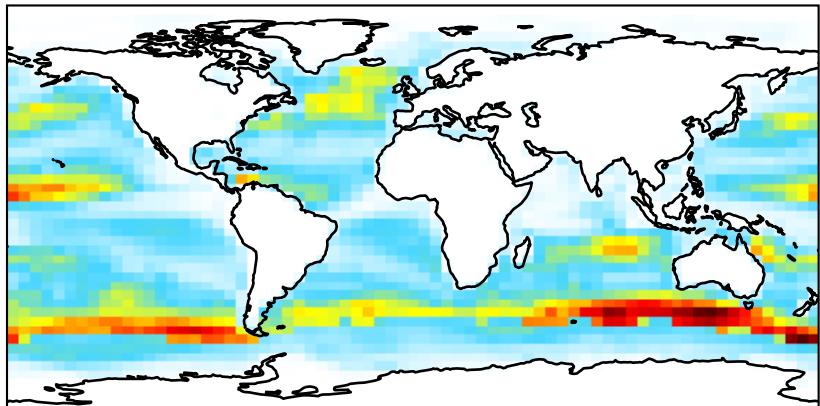


Ratio
Dev/Ref, Fixed Range

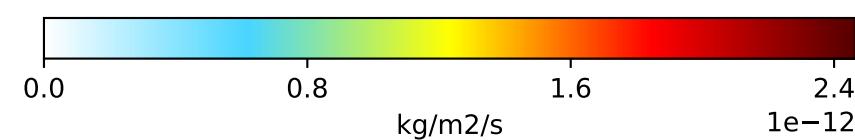
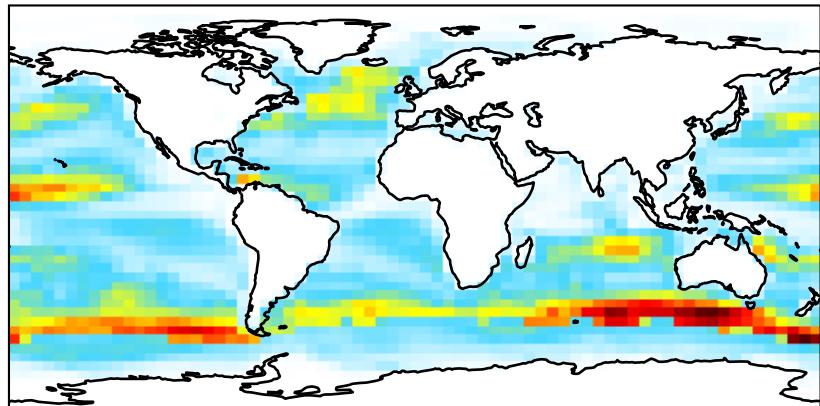


EmisBrSALC_Natural (Apr2019)

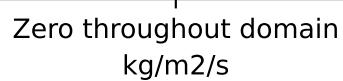
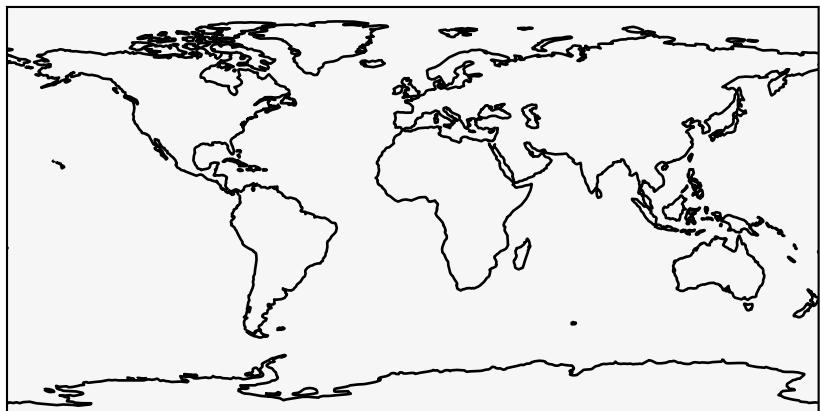
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



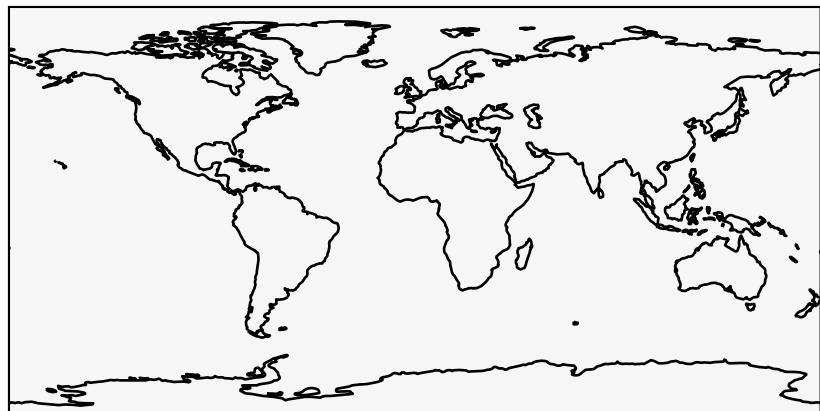
GCC_13.4.0-rc.1 (Dev)
4.0x5.0



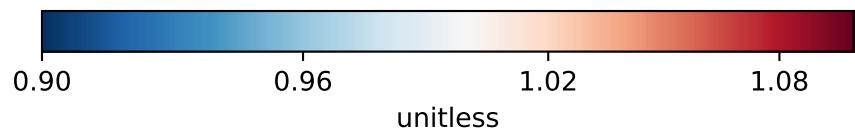
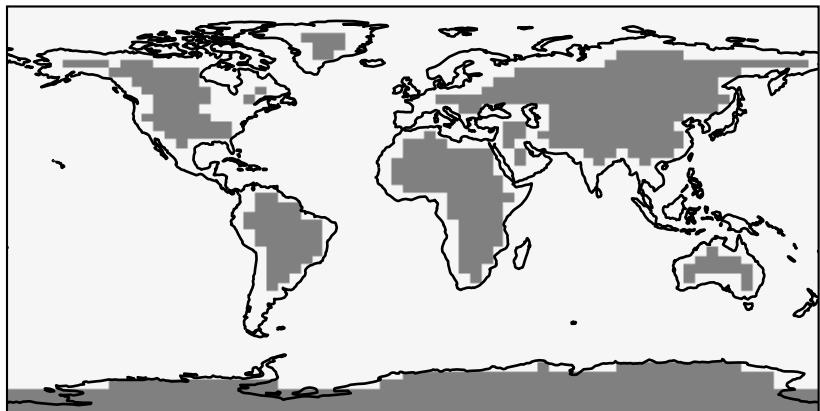
Difference
Dev - Ref, Dynamic Range



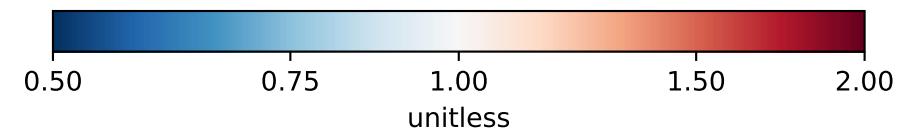
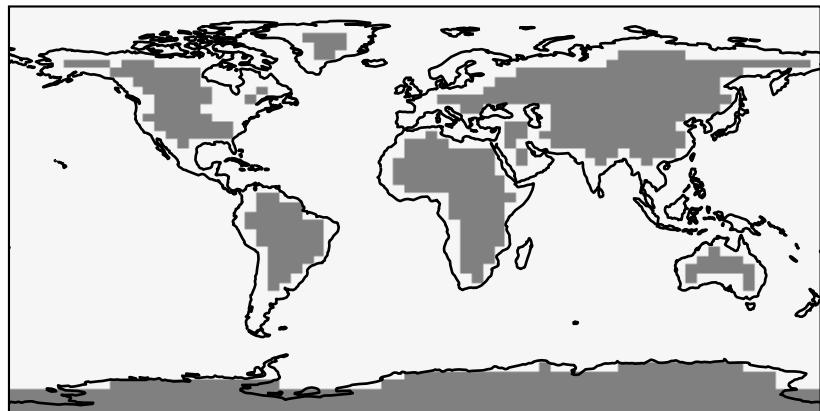
Difference
Dev - Ref, Restricted Range [5%, 95%]



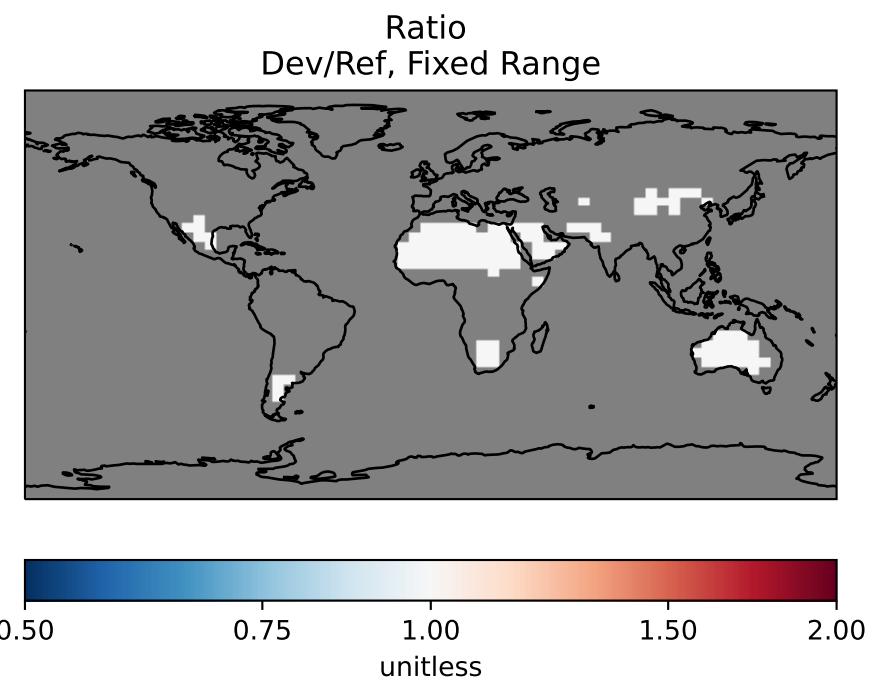
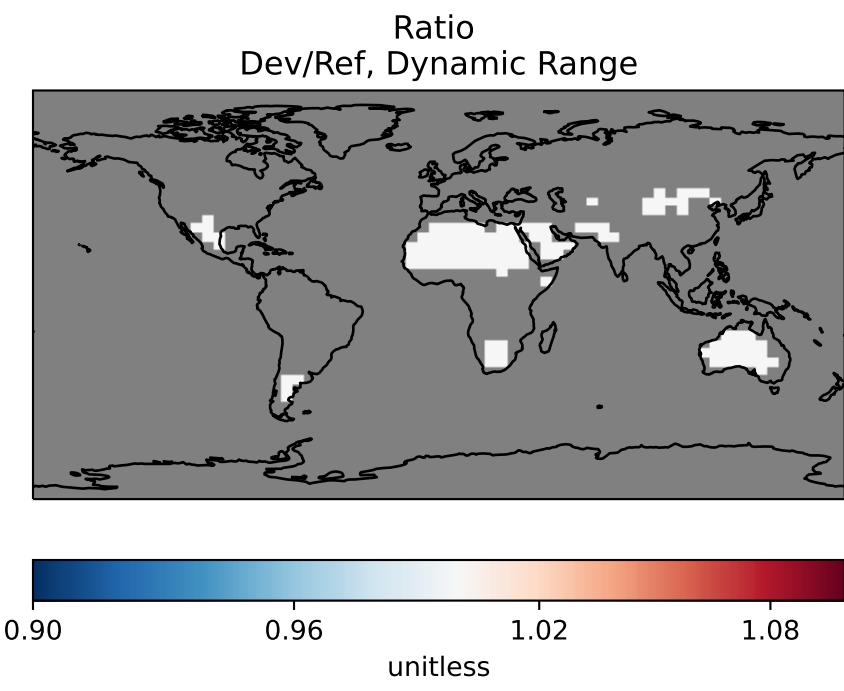
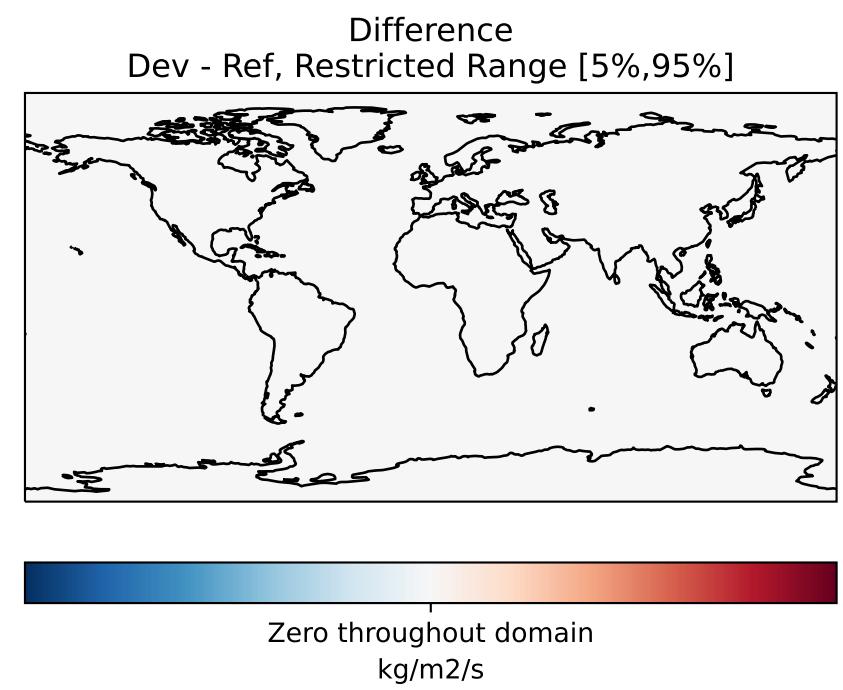
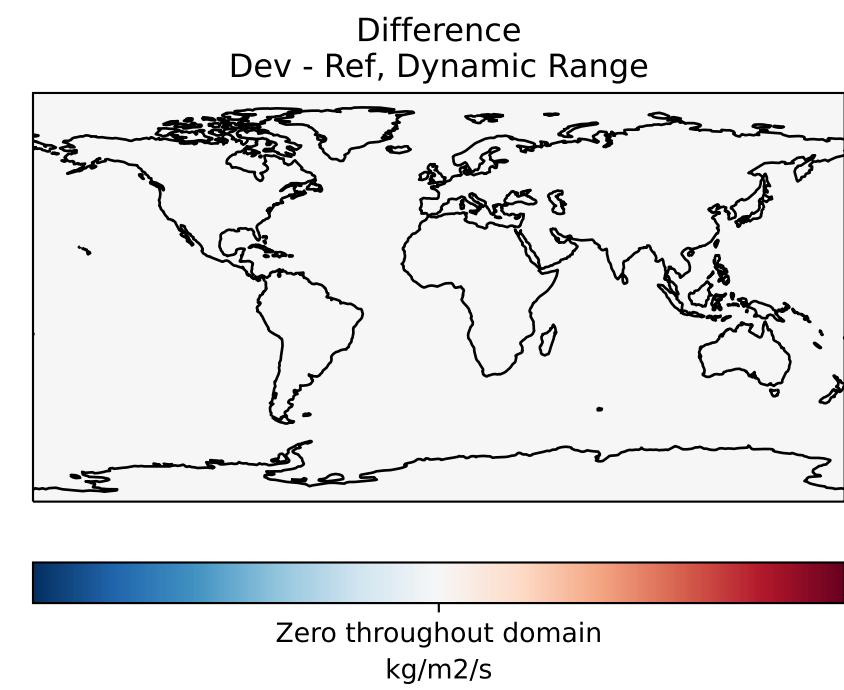
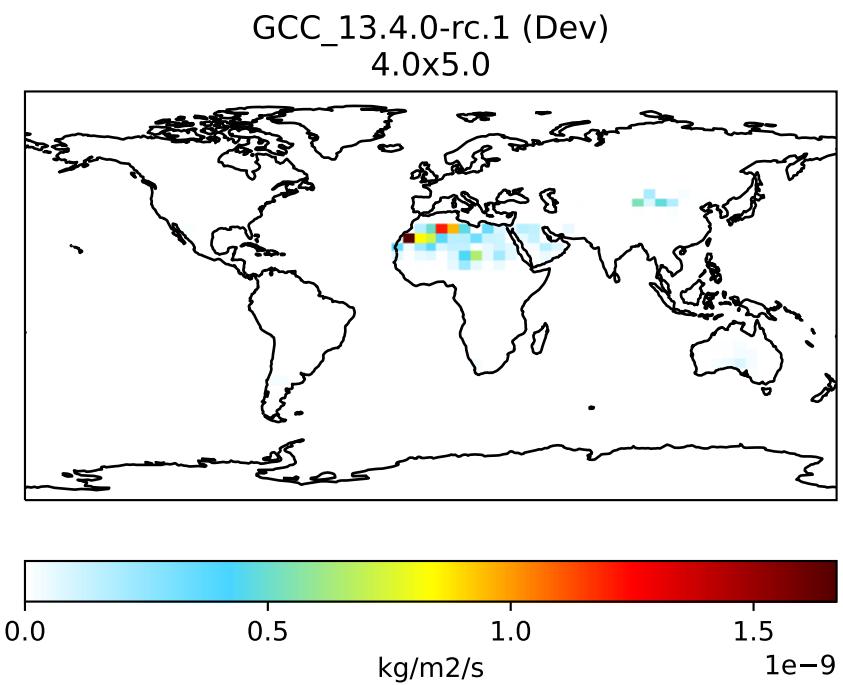
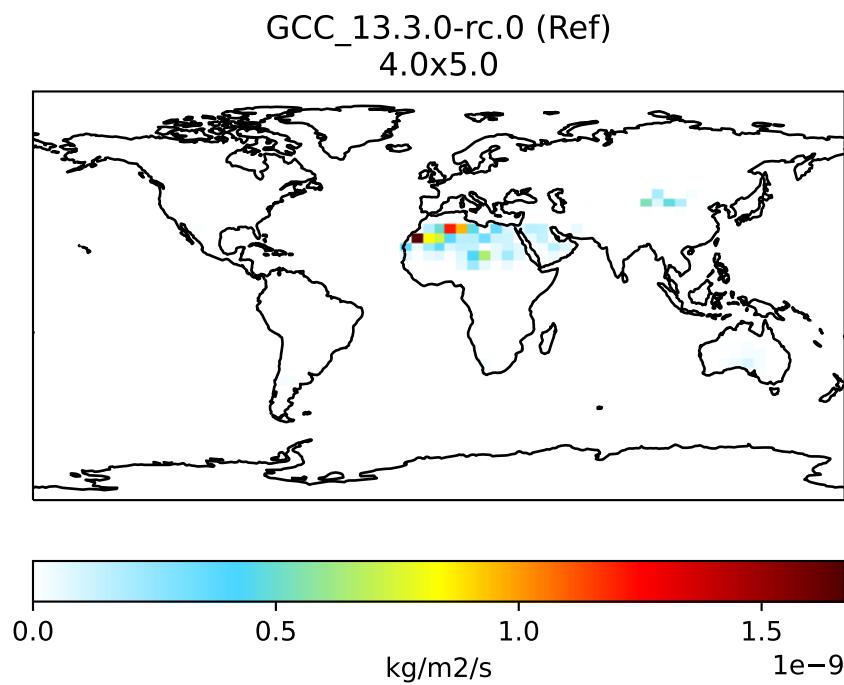
Ratio
Dev/Ref, Dynamic Range



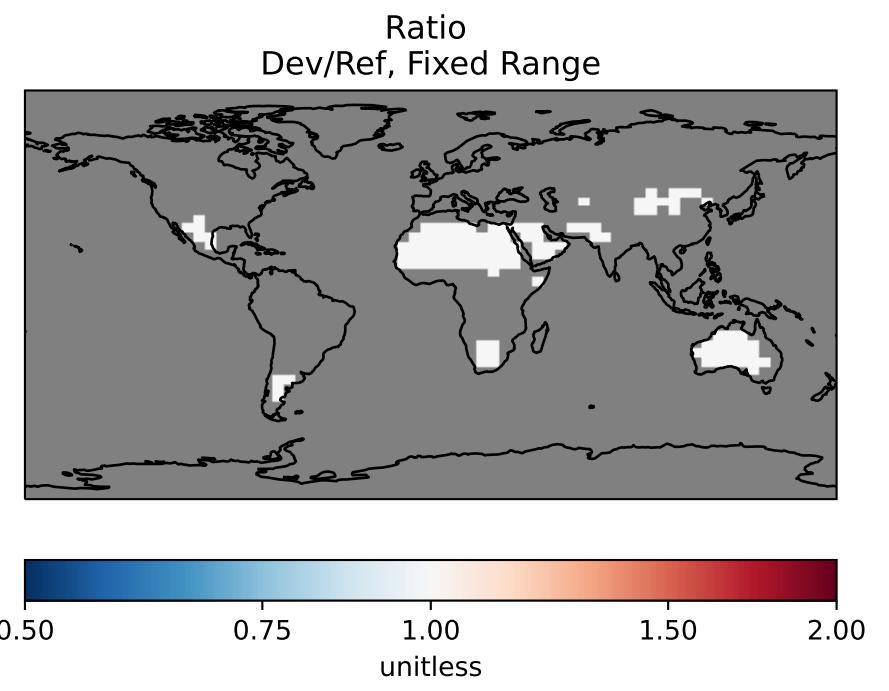
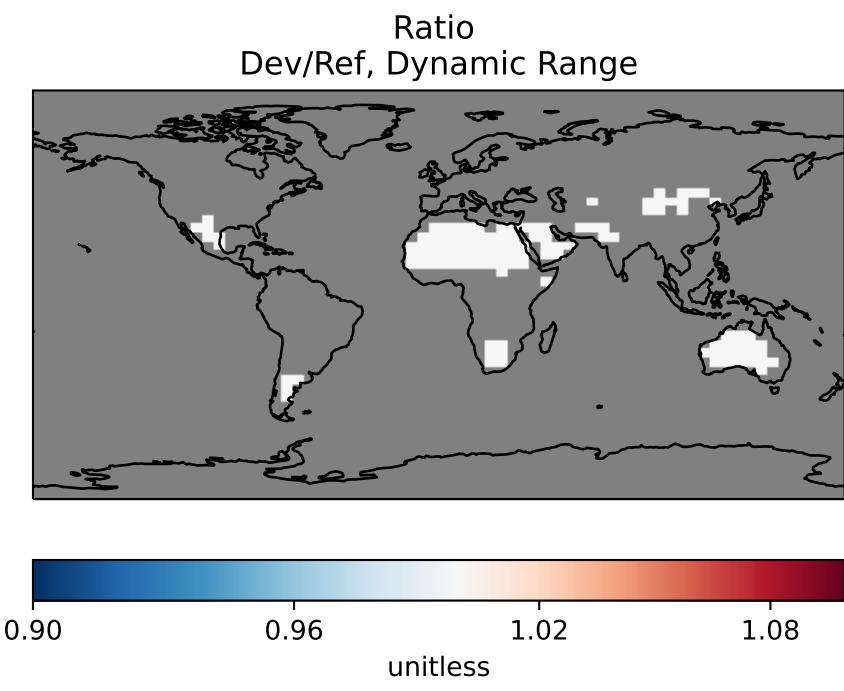
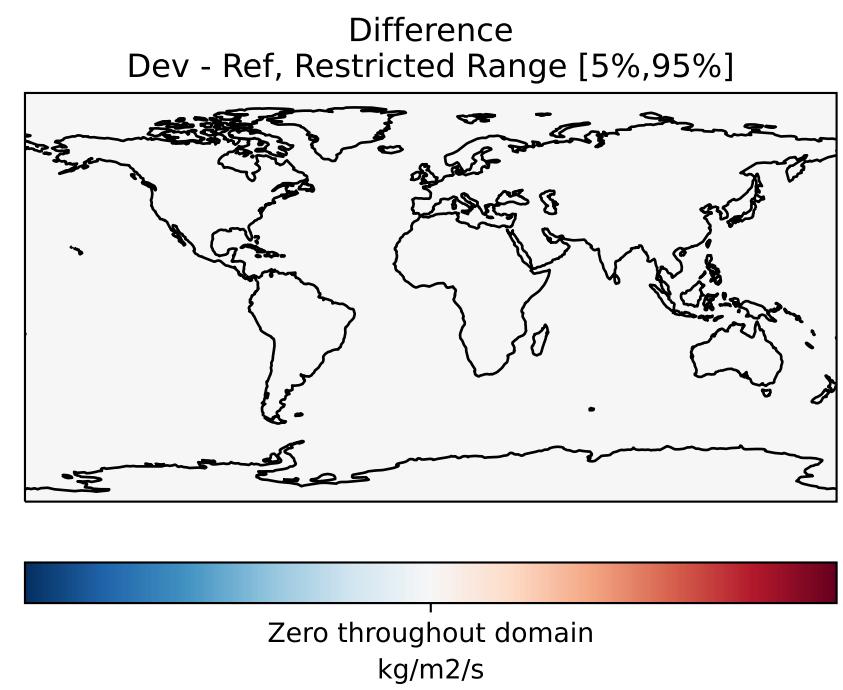
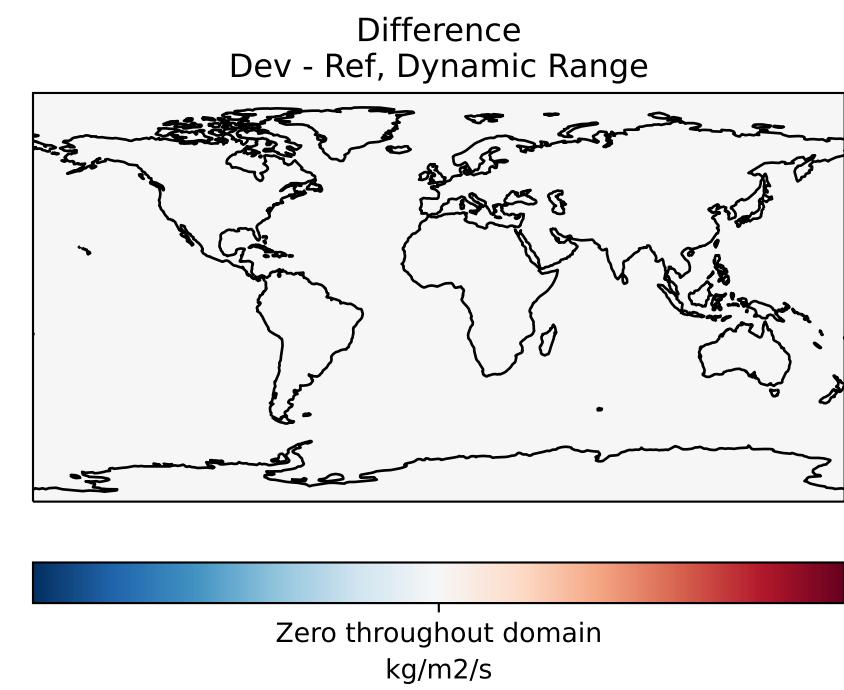
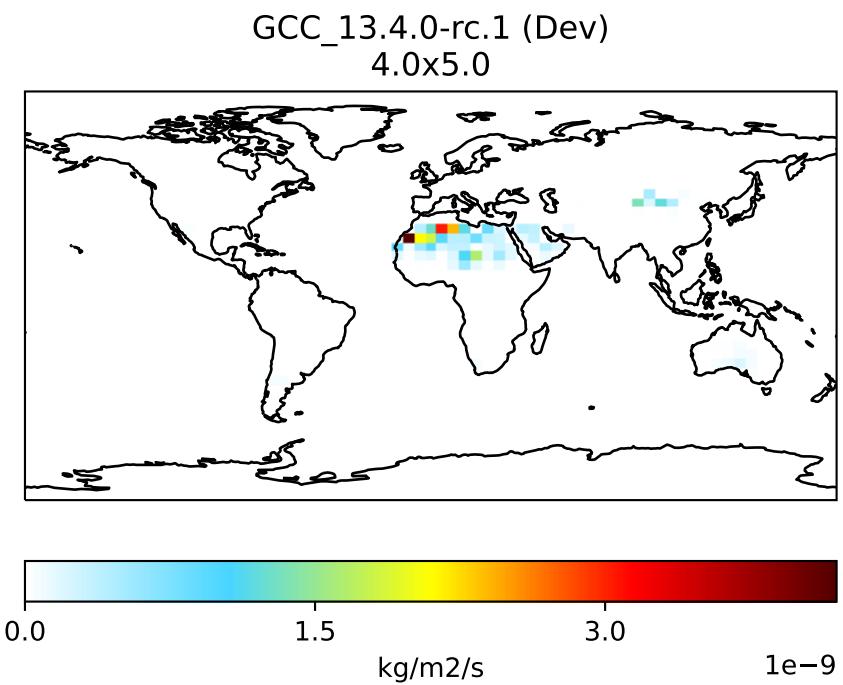
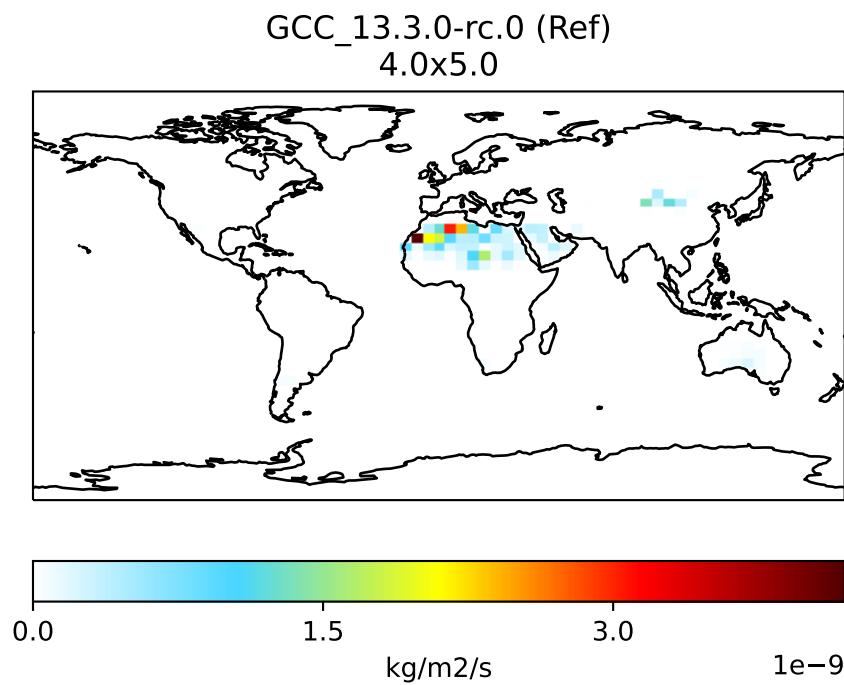
Ratio
Dev/Ref, Fixed Range



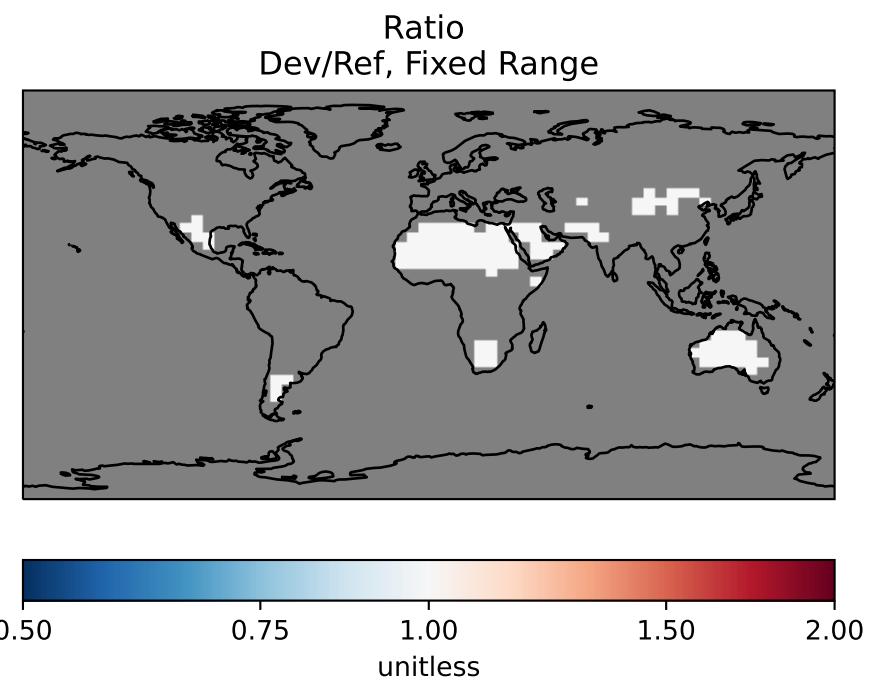
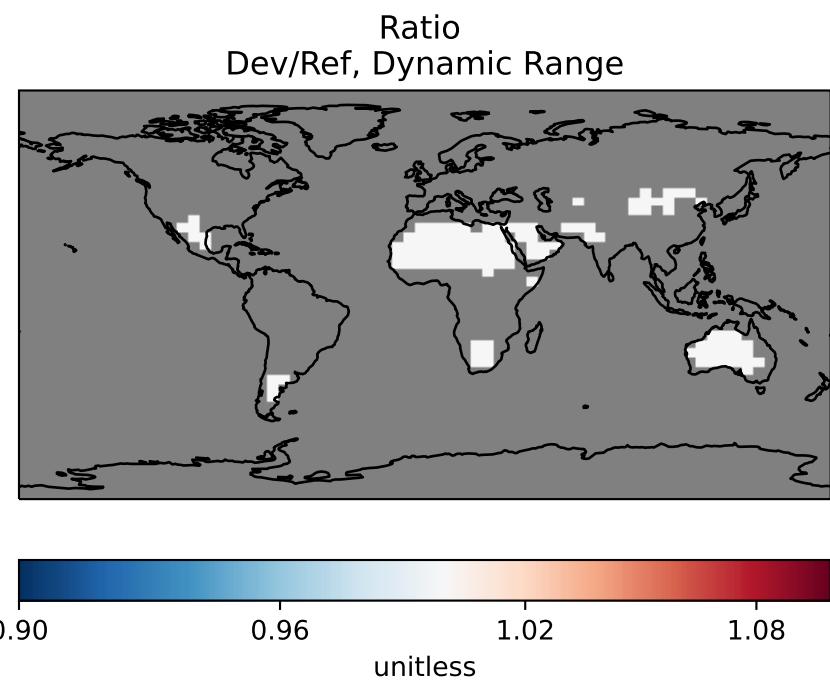
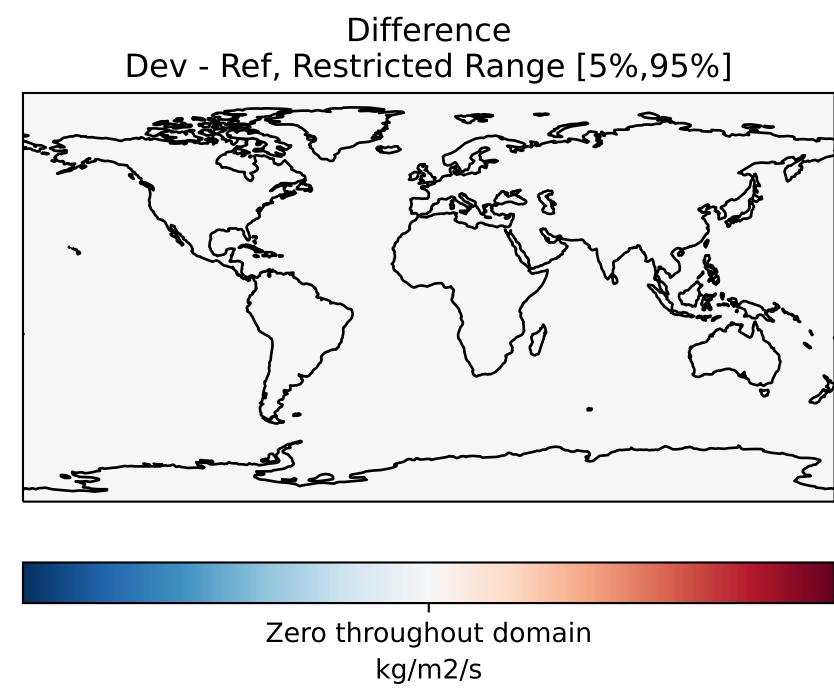
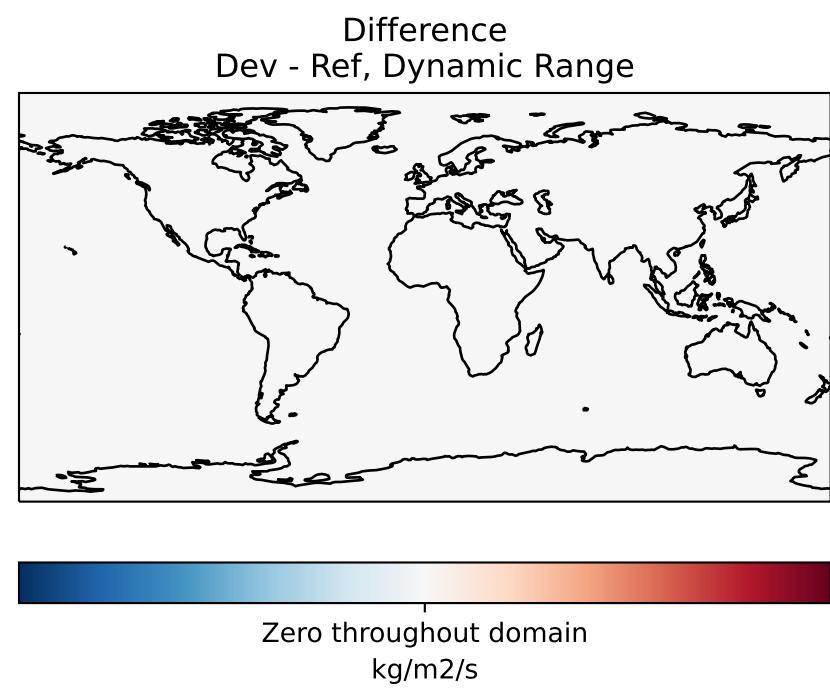
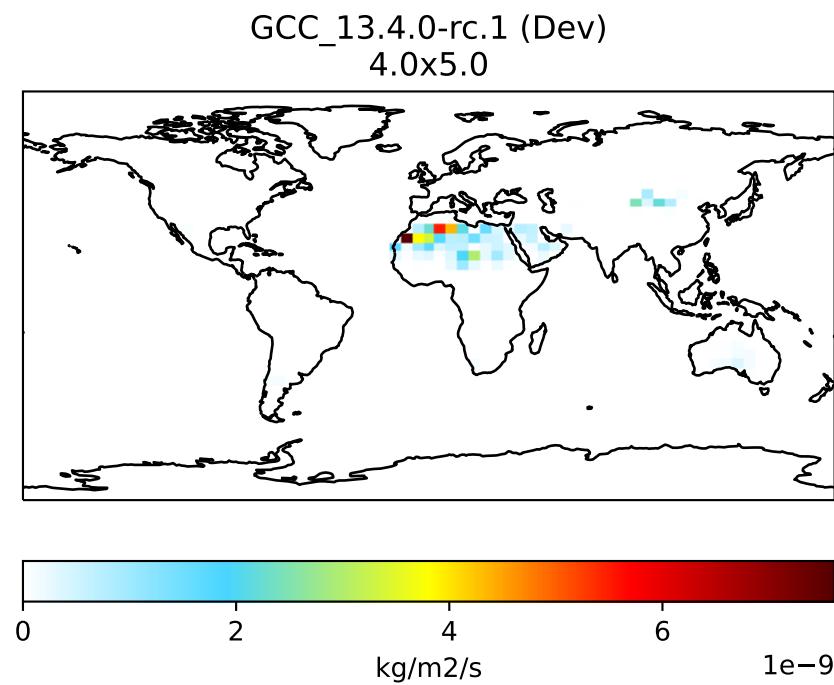
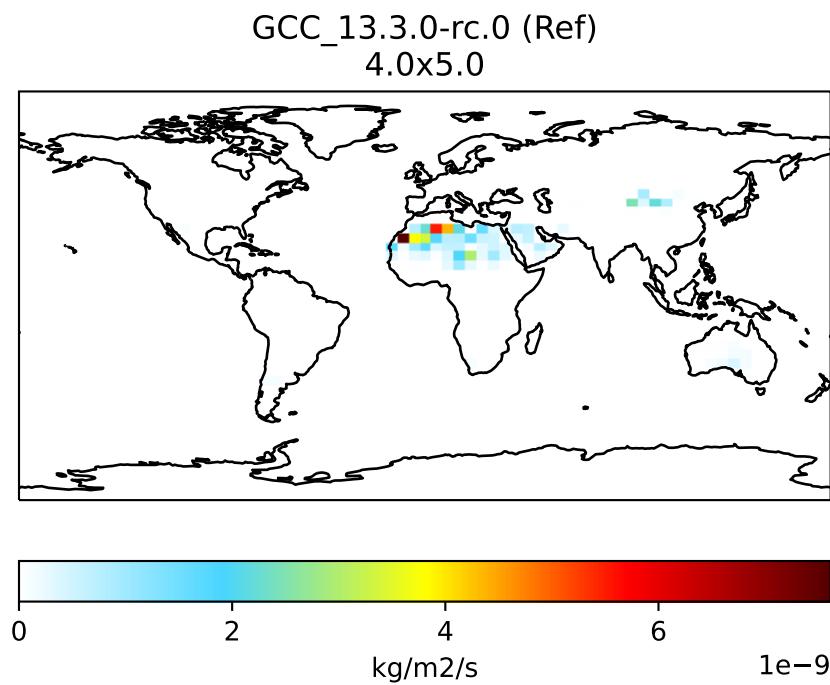
EmisDST1_Natural (Apr2019)



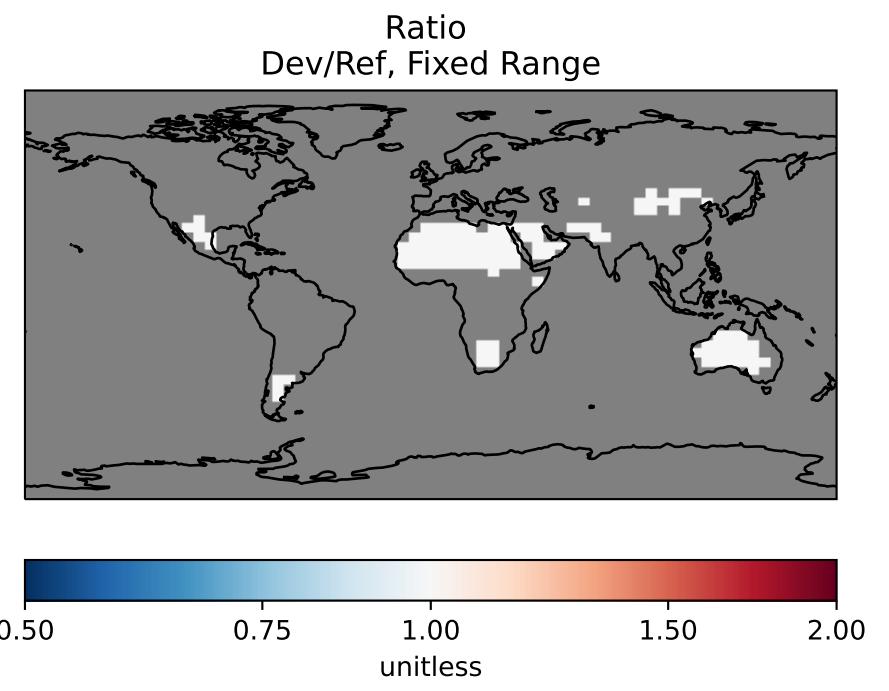
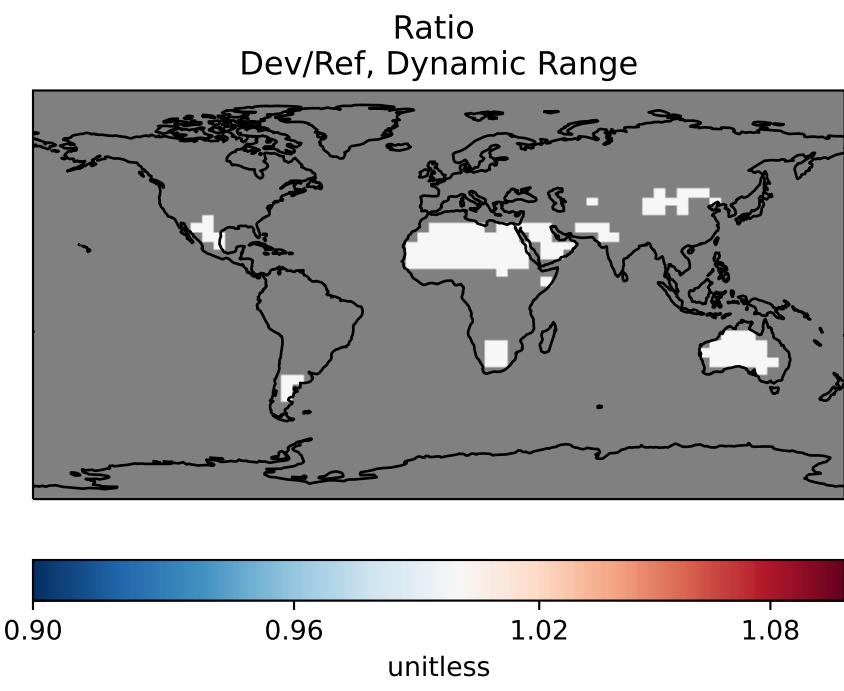
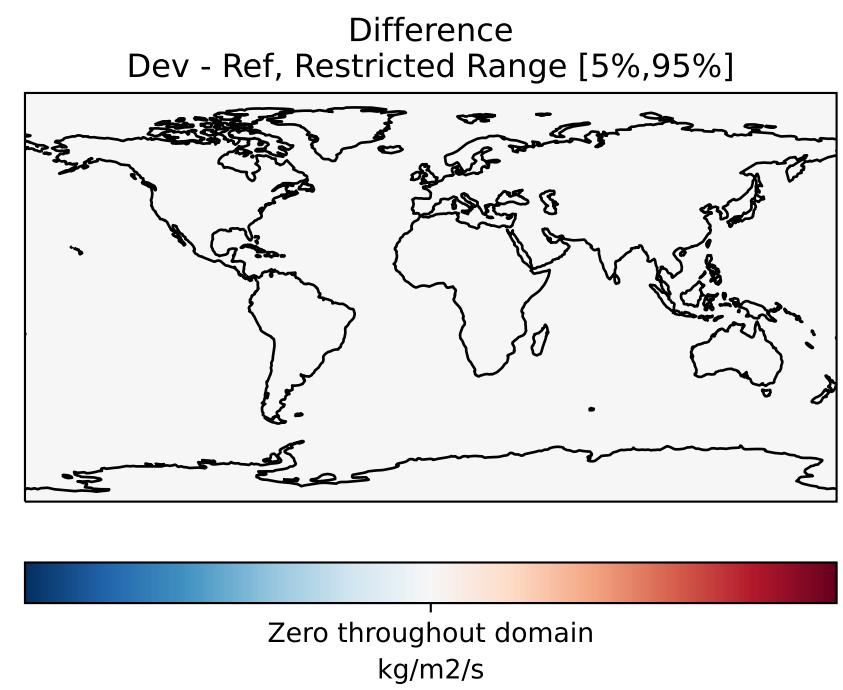
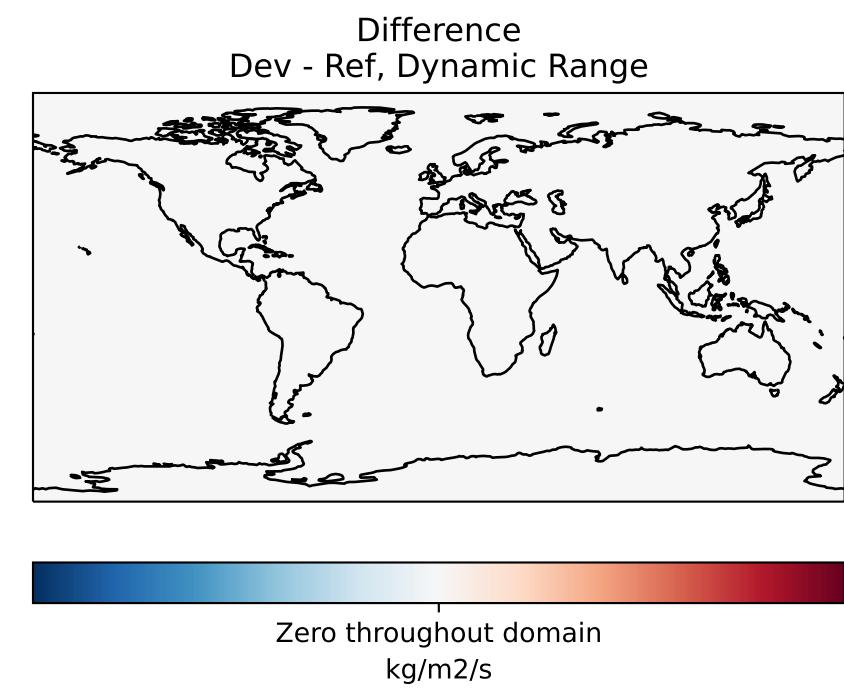
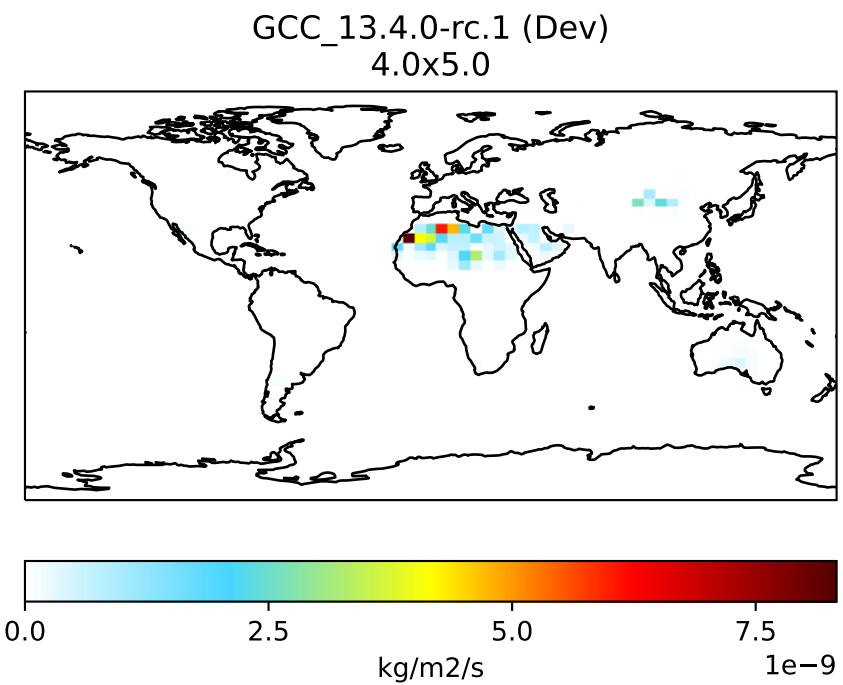
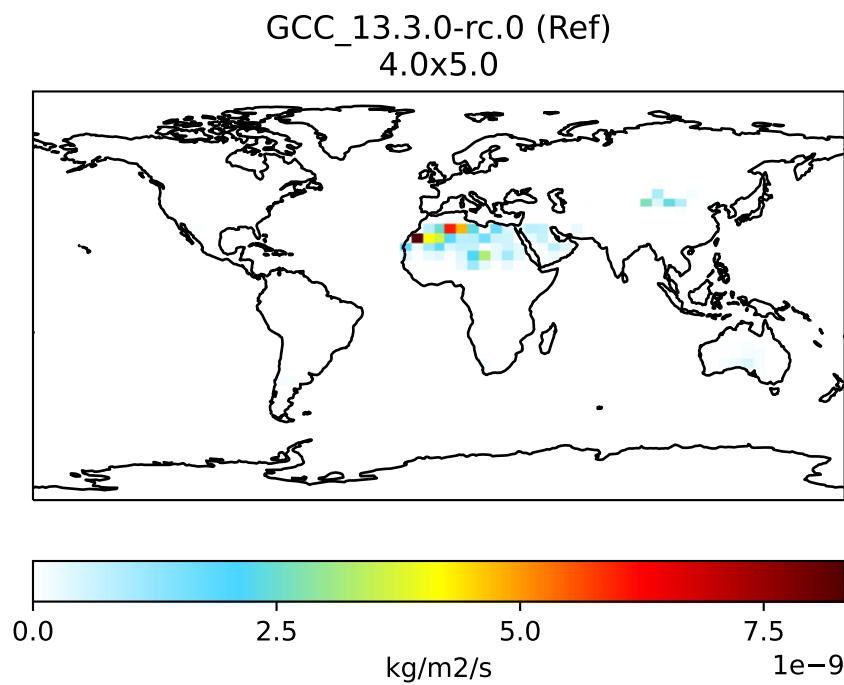
EmisDST2_Natural (Apr2019)



EmisDST3_Natural (Apr2019)

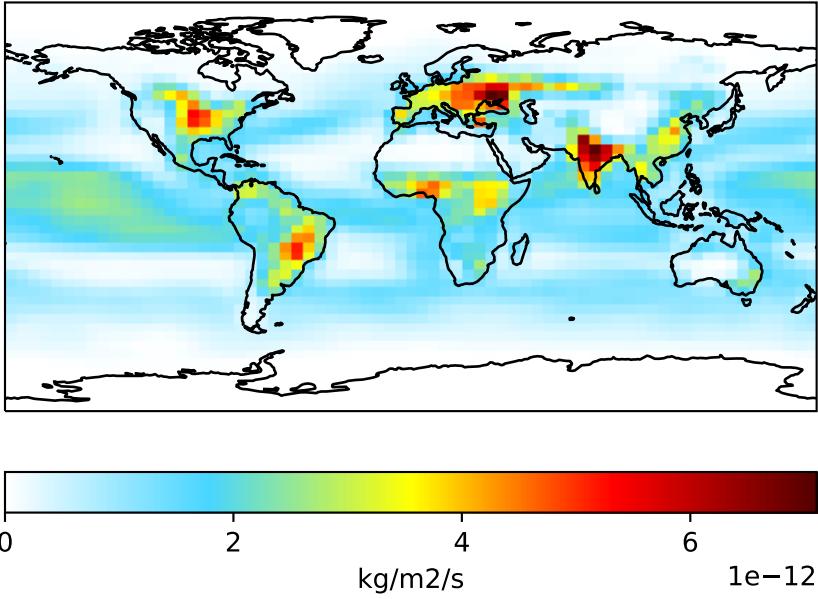


EmisDST4_Natural (Apr2019)

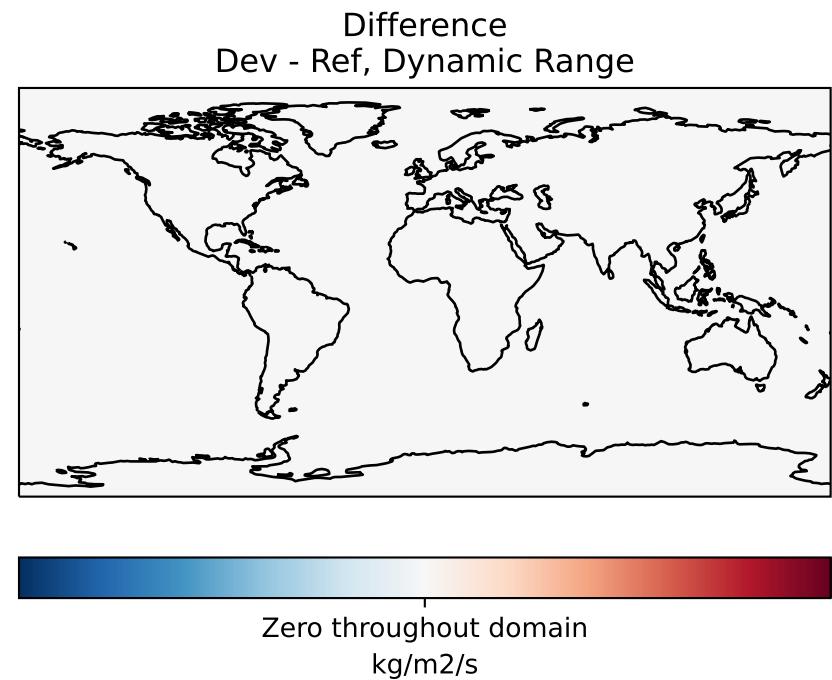
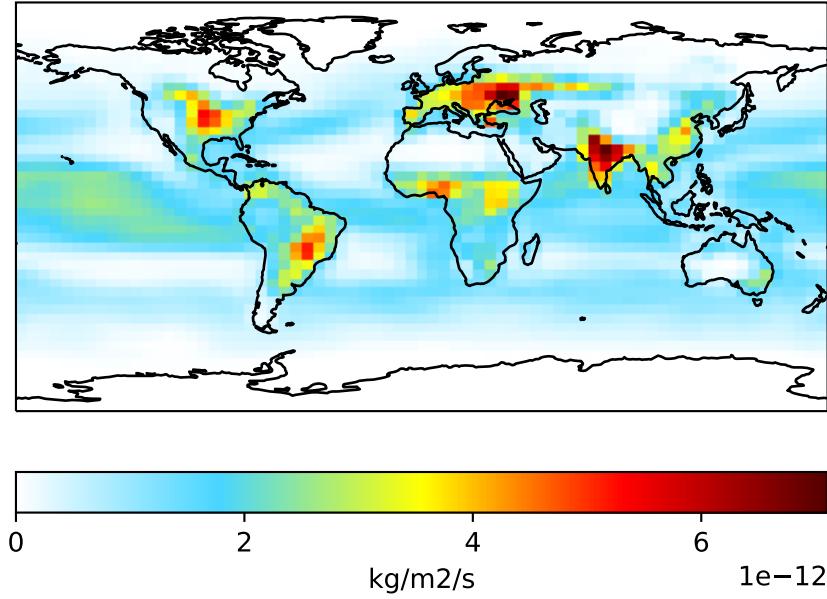


EmisNH3_Natural (Apr2019)

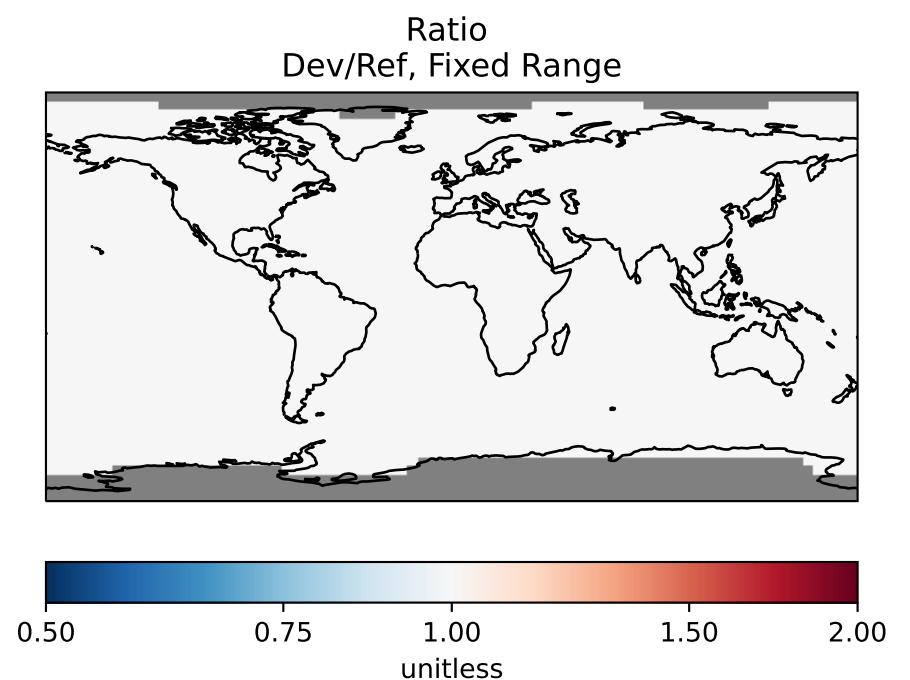
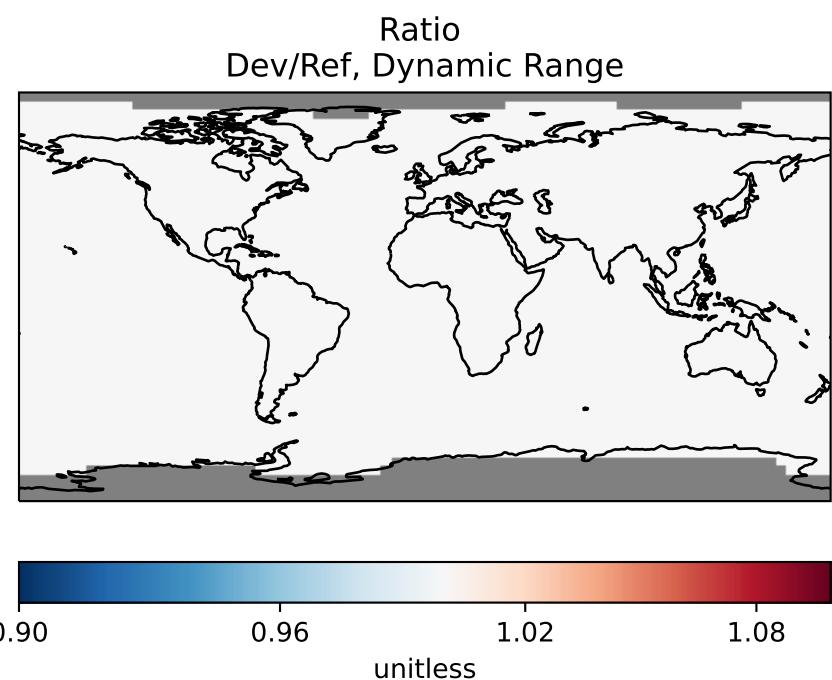
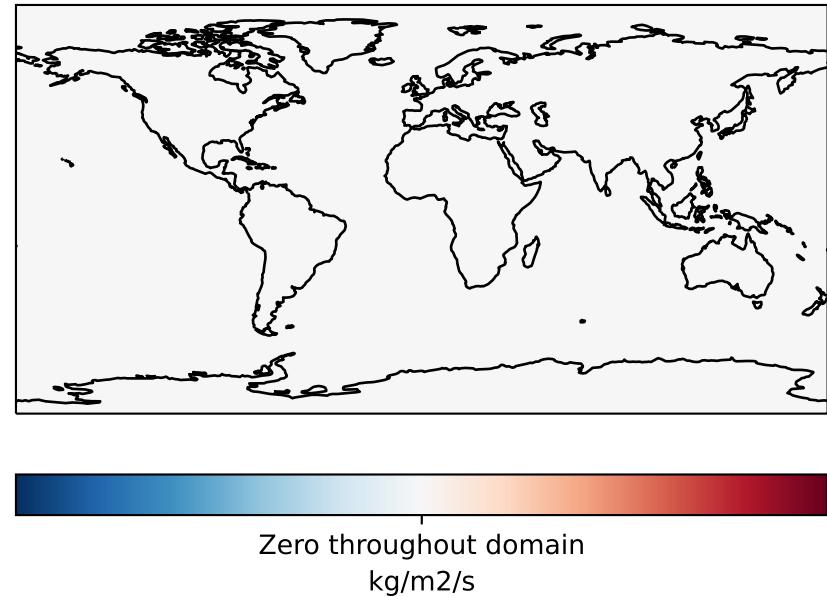
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



GCC_13.4.0-rc.1 (Dev)
4.0x5.0

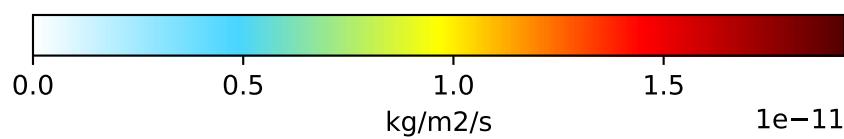
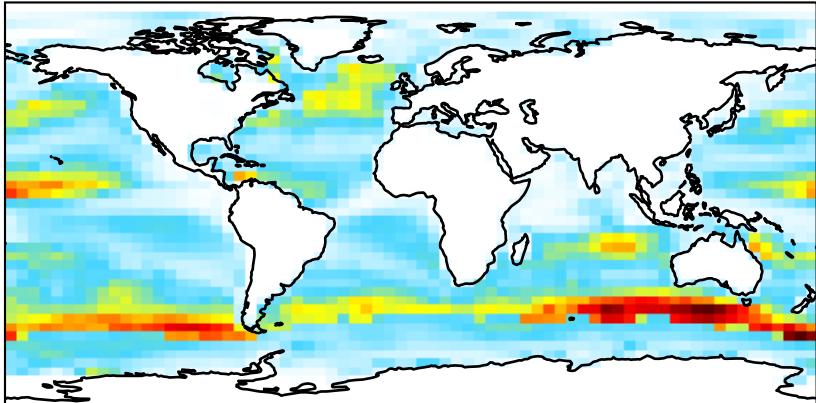


Difference
Dev - Ref, Restricted Range [5%,95%]

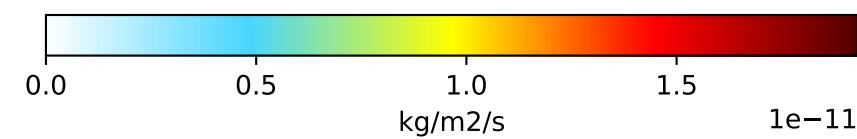
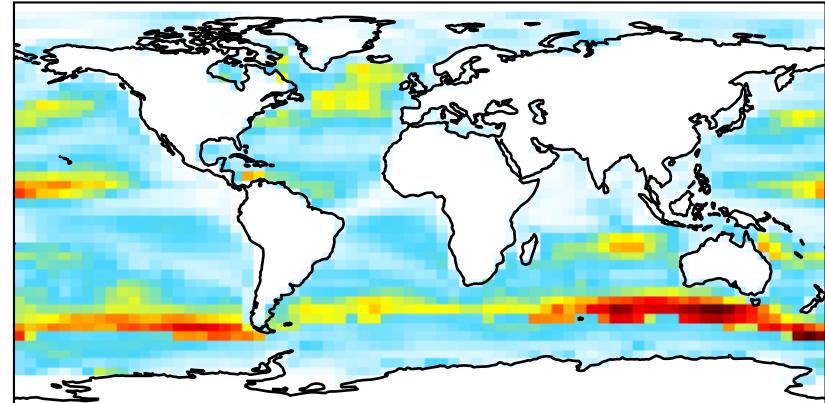


EmisSALA_Natural (Apr2019)

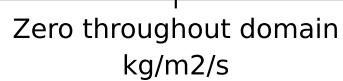
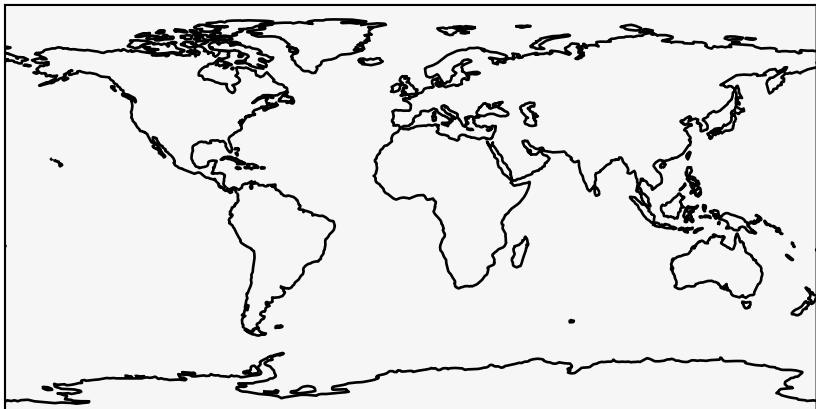
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



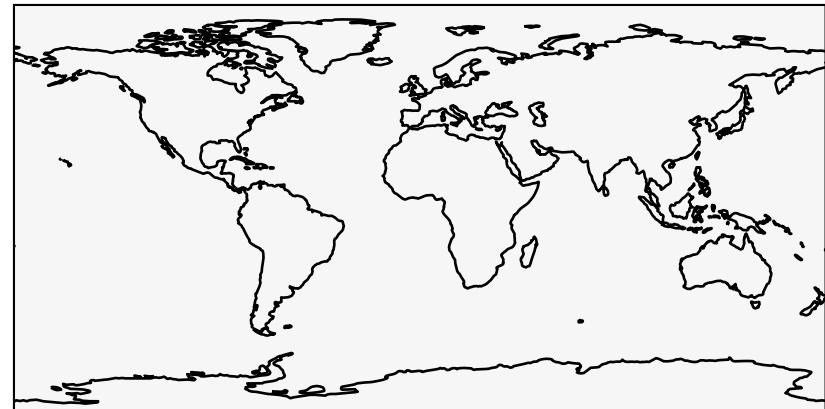
GCC_13.4.0-rc.1 (Dev)
4.0x5.0



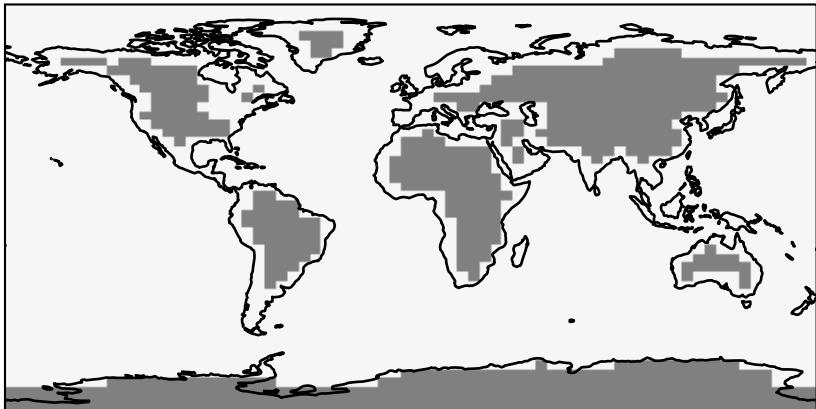
Difference
Dev - Ref, Dynamic Range



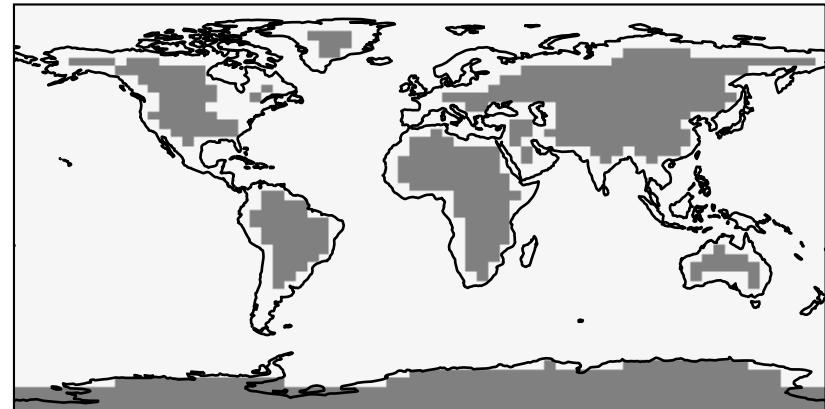
Difference
Dev - Ref, Restricted Range [5%, 95%]



Ratio
Dev/Ref, Dynamic Range

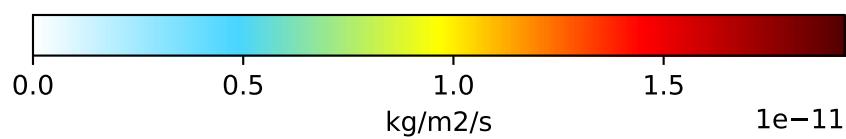
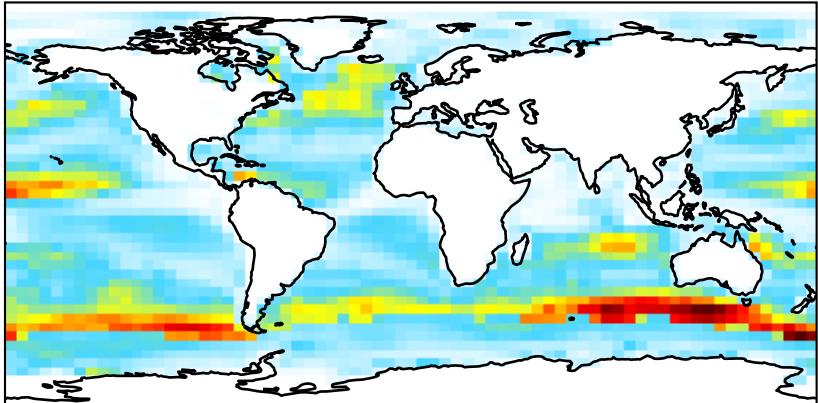


Ratio
Dev/Ref, Fixed Range

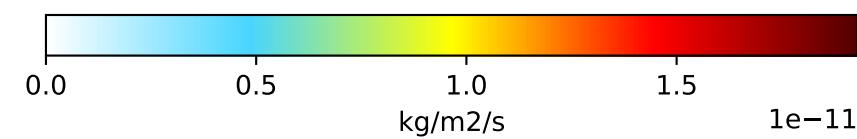
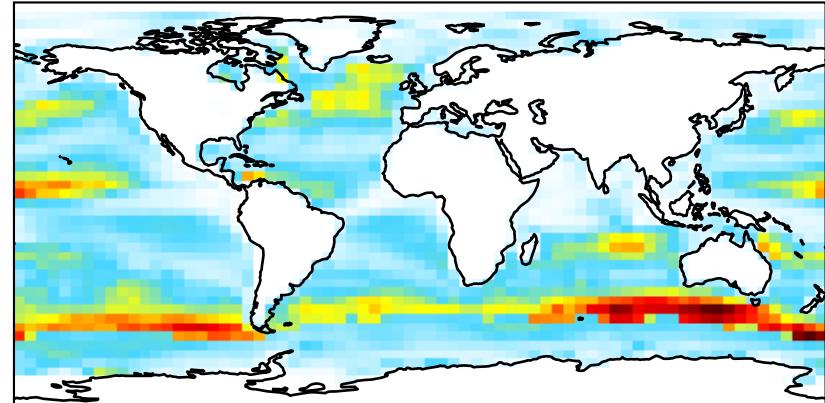


EmisSALAAL_Natural (Apr2019)

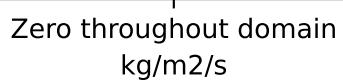
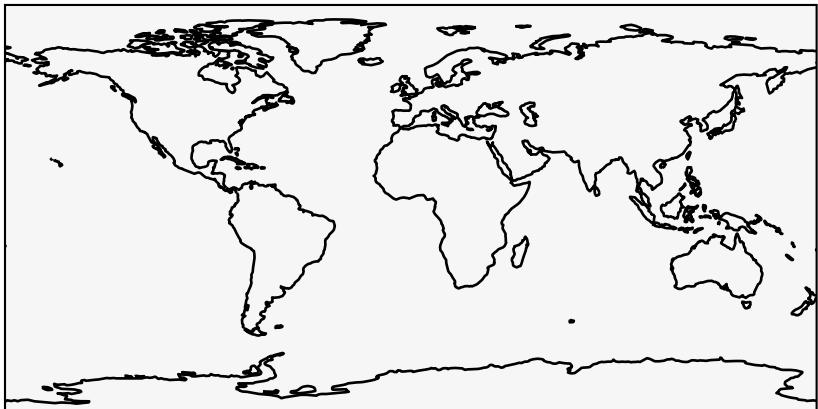
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



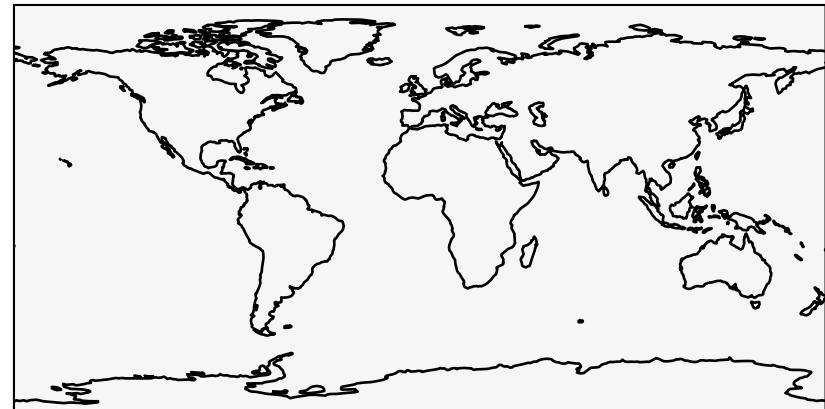
GCC_13.4.0-rc.1 (Dev)
4.0x5.0



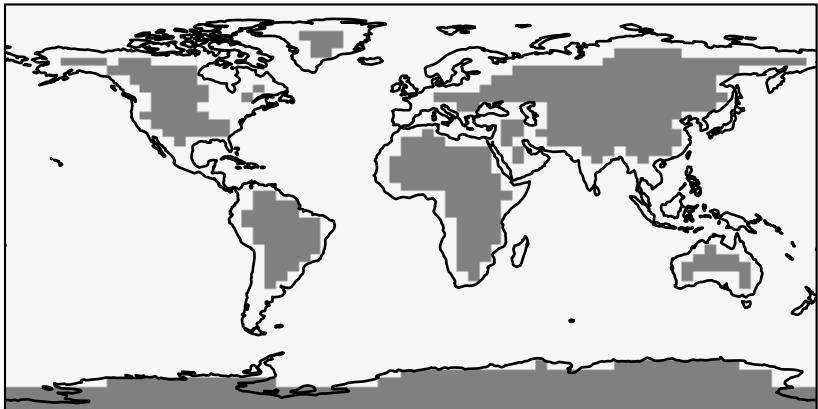
Difference
Dev - Ref, Dynamic Range



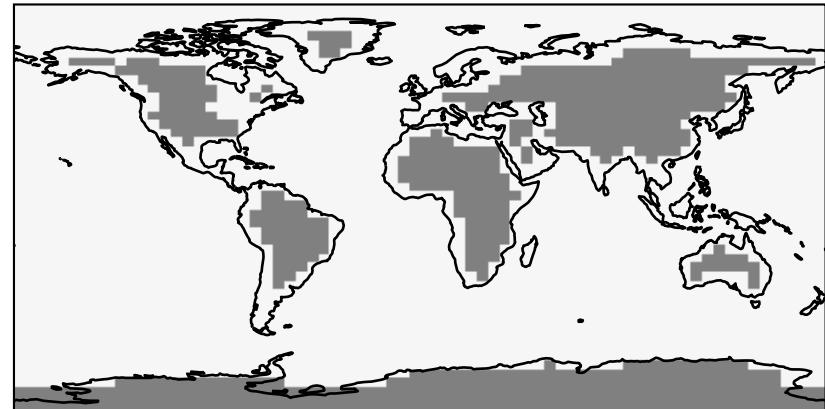
Difference
Dev - Ref, Restricted Range [5%, 95%]



Ratio
Dev/Ref, Dynamic Range

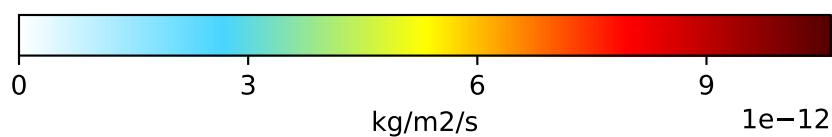
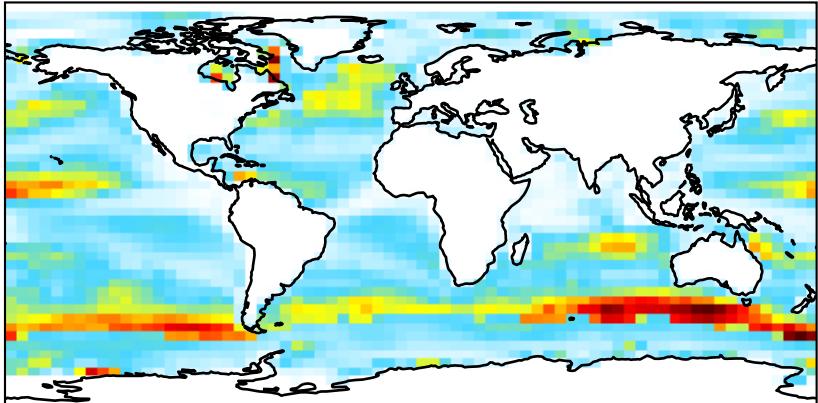


Ratio
Dev/Ref, Fixed Range

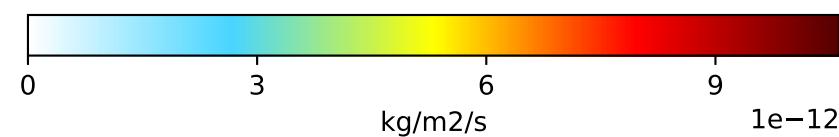
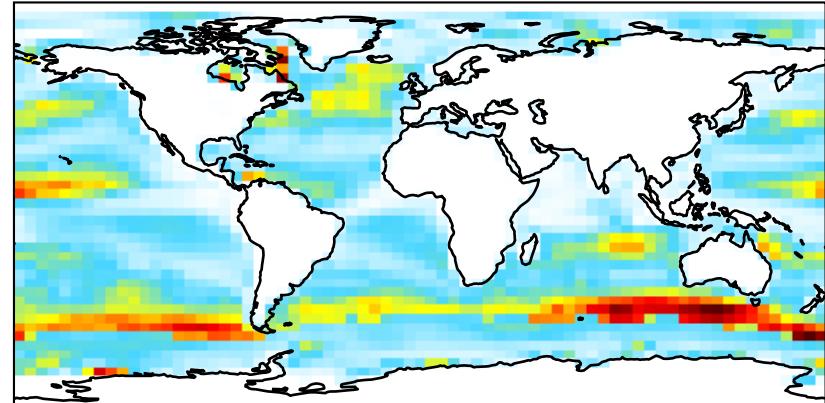


EmisSALACL_Natural (Apr2019)

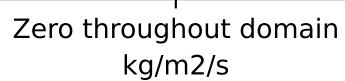
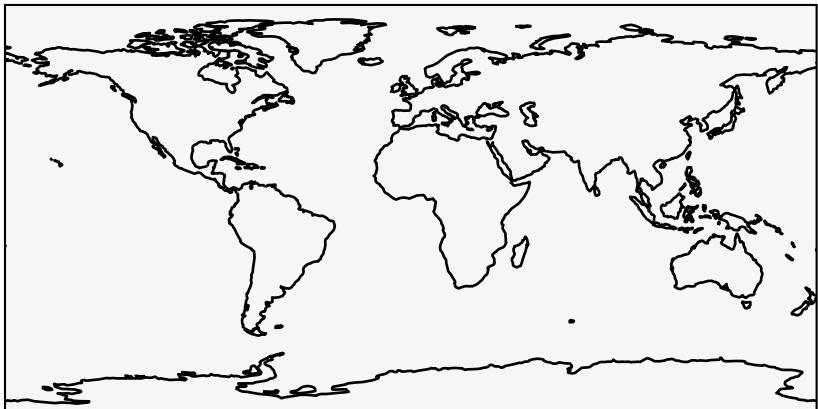
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



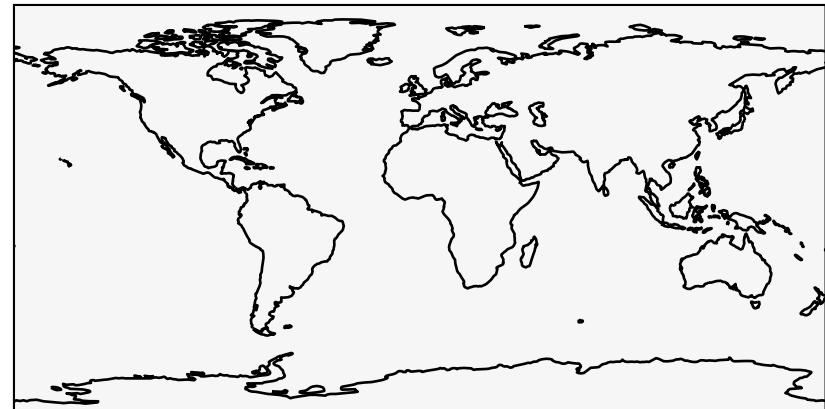
GCC_13.4.0-rc.1 (Dev)
4.0x5.0



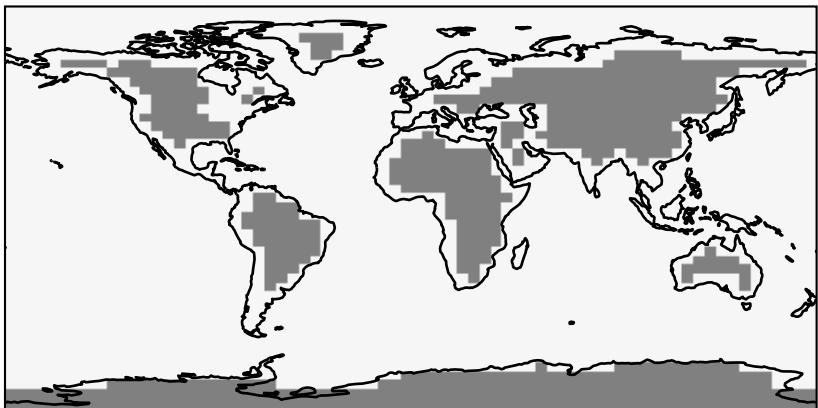
Difference
Dev - Ref, Dynamic Range



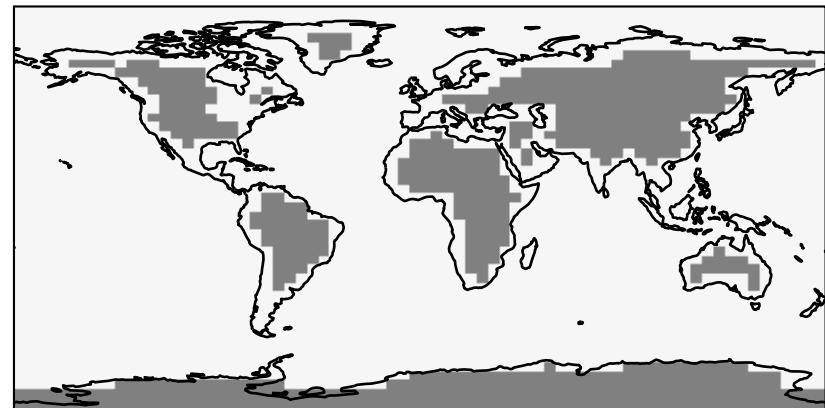
Difference
Dev - Ref, Restricted Range [5%, 95%]



Ratio
Dev/Ref, Dynamic Range

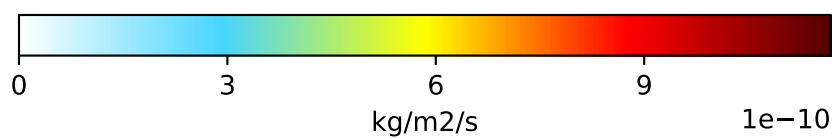
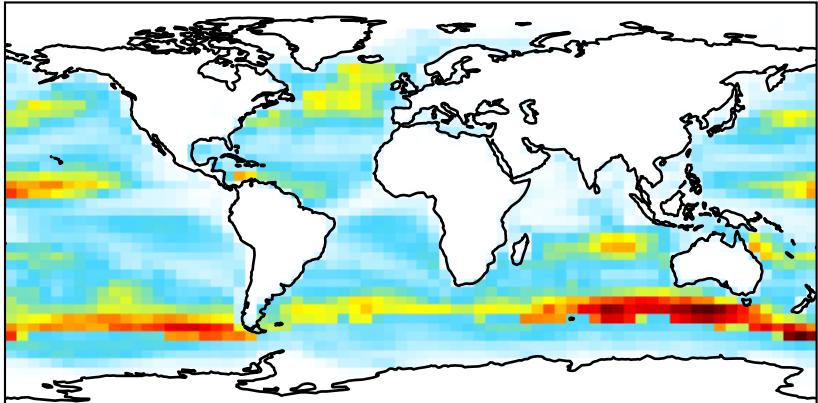


Ratio
Dev/Ref, Fixed Range

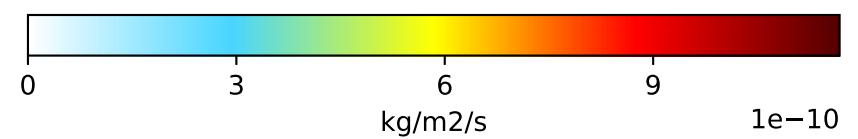
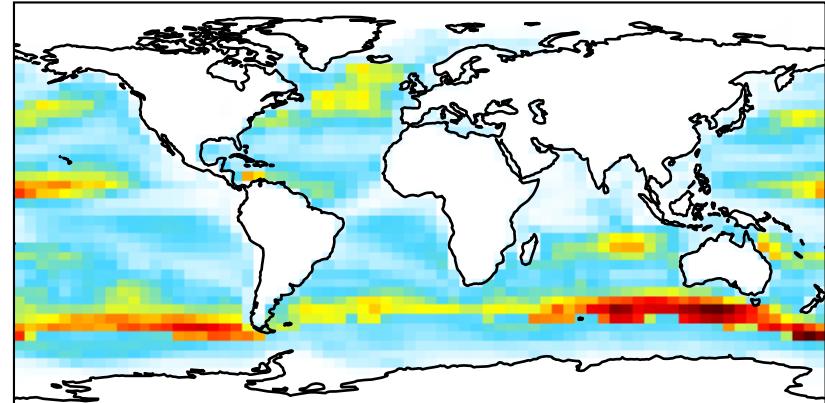


EmisSALC_Natural (Apr2019)

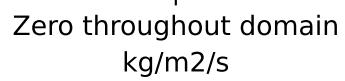
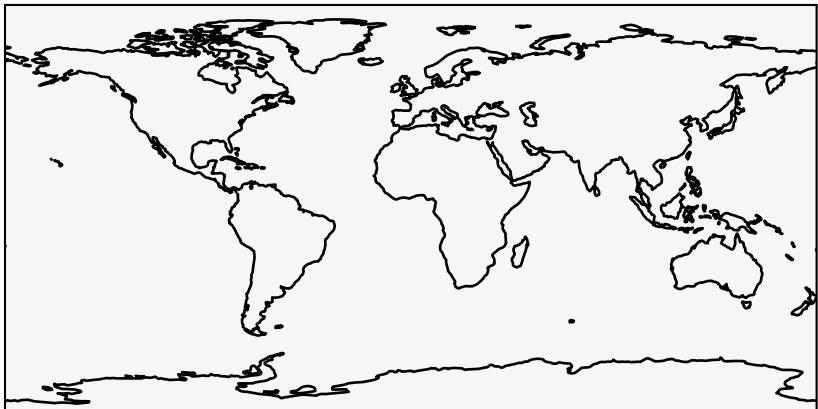
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



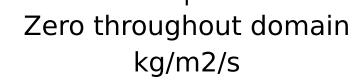
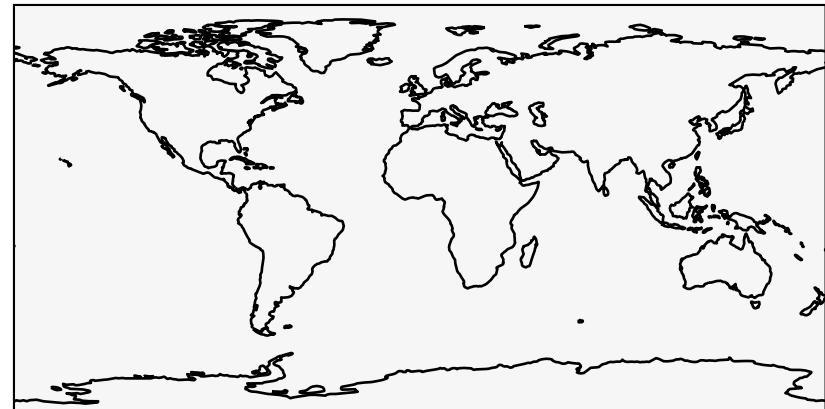
GCC_13.4.0-rc.1 (Dev)
4.0x5.0



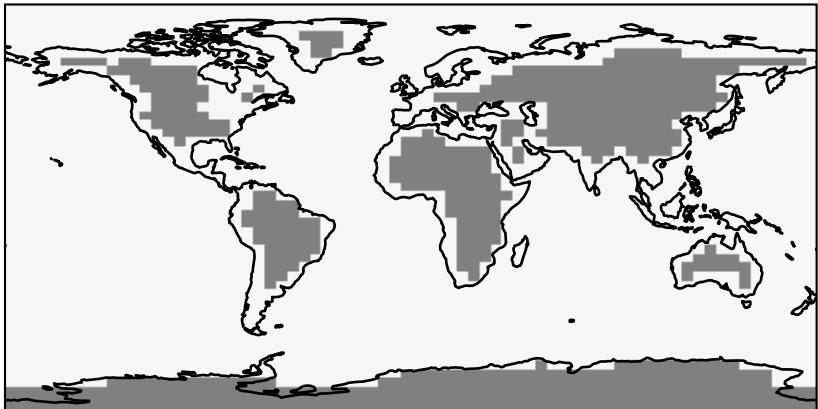
Difference
Dev - Ref, Dynamic Range



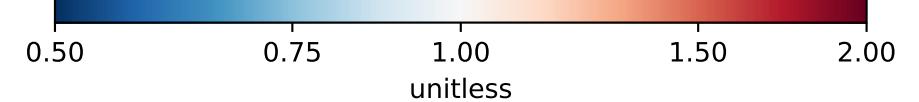
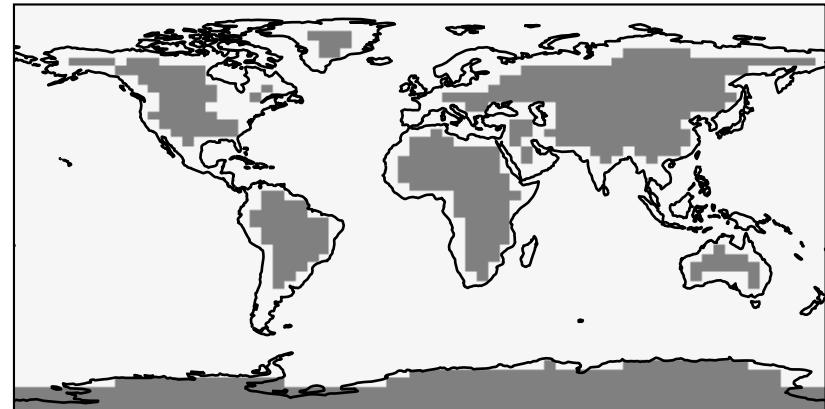
Difference
Dev - Ref, Restricted Range [5%, 95%]



Ratio
Dev/Ref, Dynamic Range

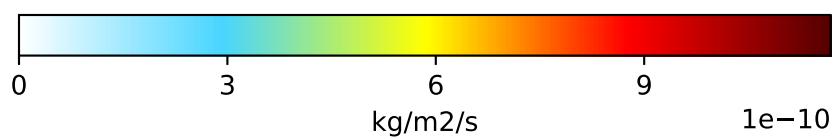
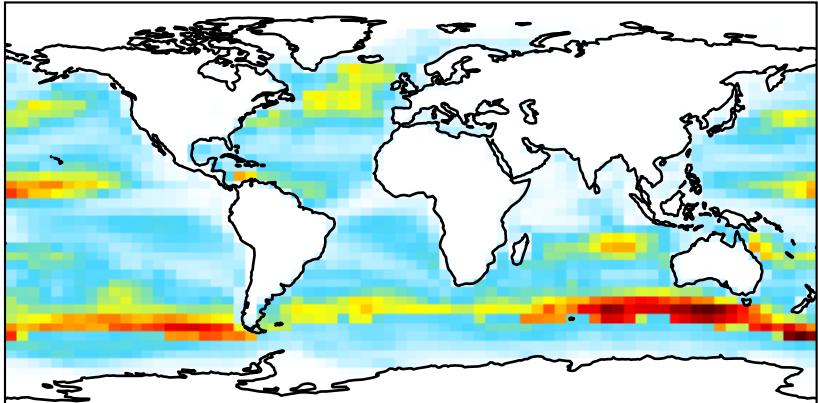


Ratio
Dev/Ref, Fixed Range

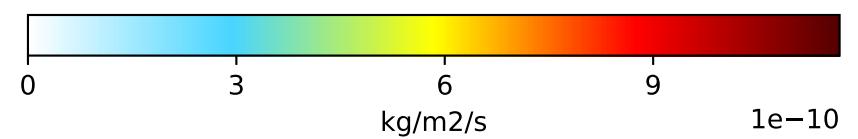
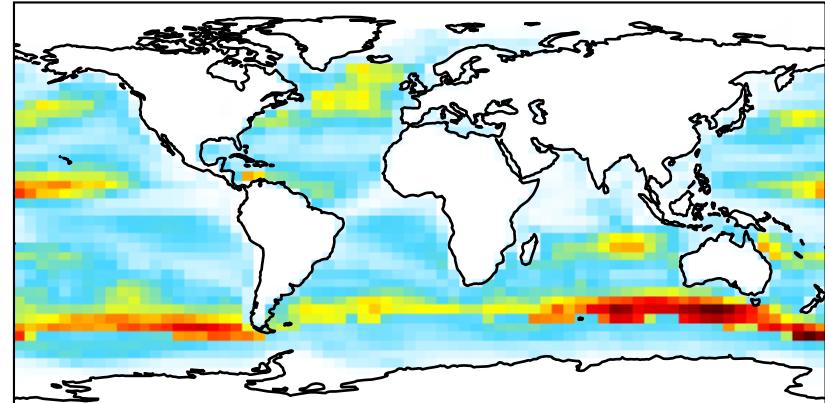


EmisSALCAL_Natural (Apr2019)

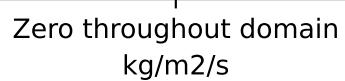
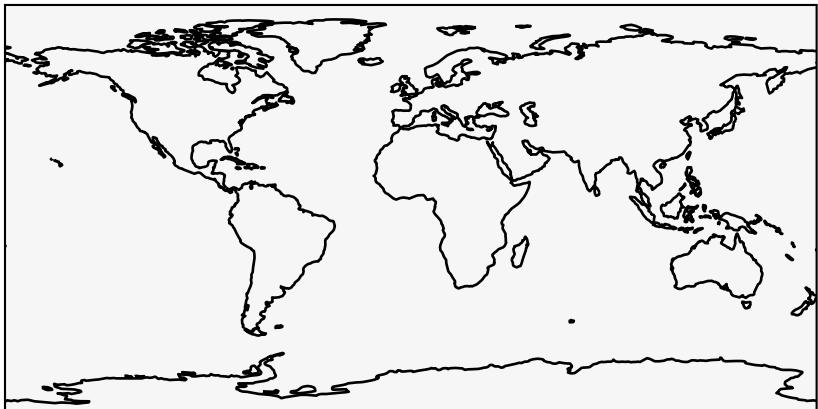
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



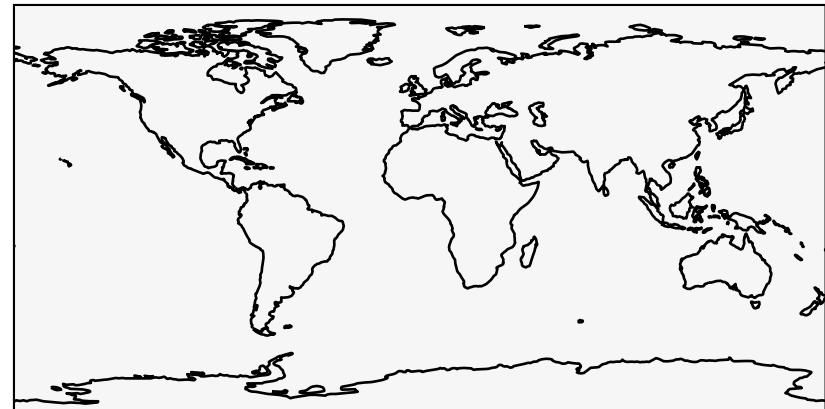
GCC_13.4.0-rc.1 (Dev)
4.0x5.0



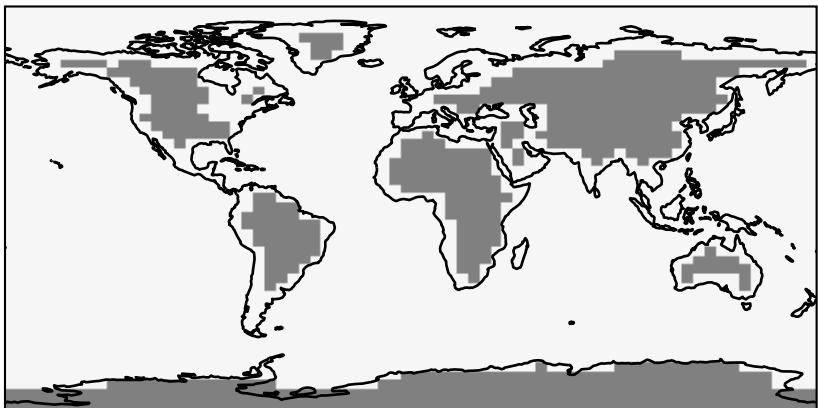
Difference
Dev - Ref, Dynamic Range



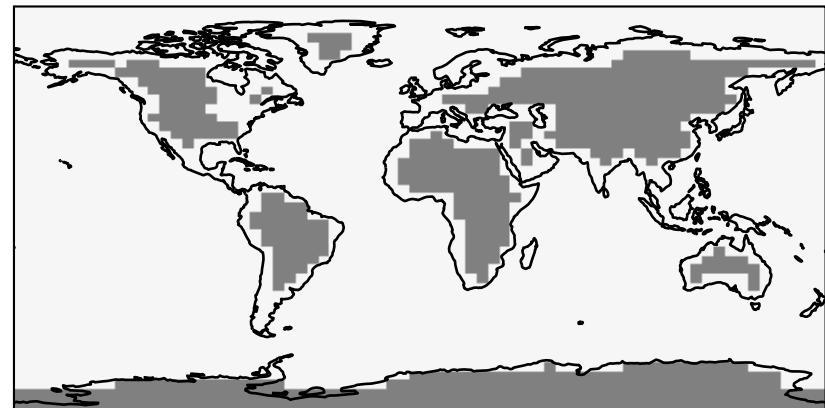
Difference
Dev - Ref, Restricted Range [5%, 95%]



Ratio
Dev/Ref, Dynamic Range

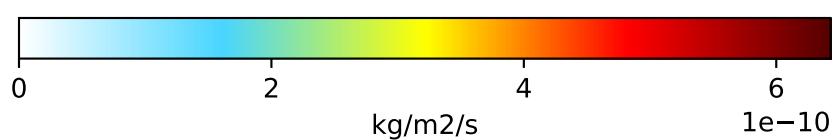
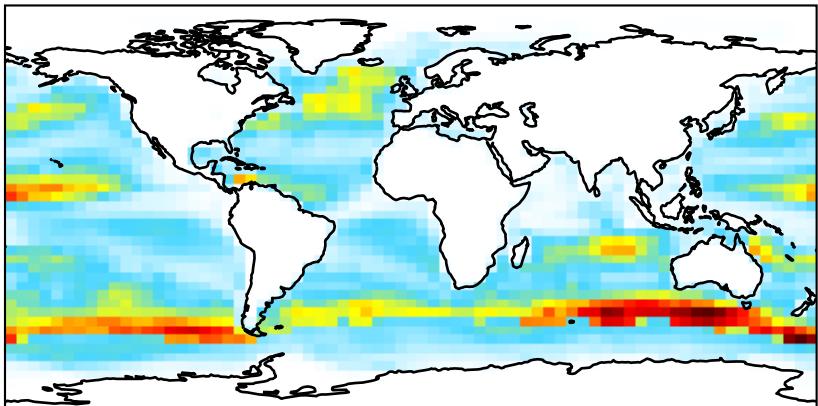


Ratio
Dev/Ref, Fixed Range

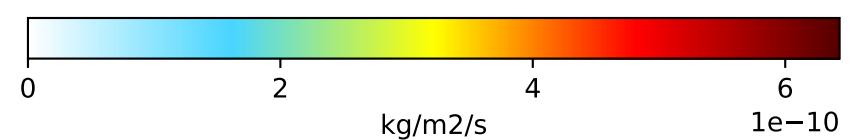
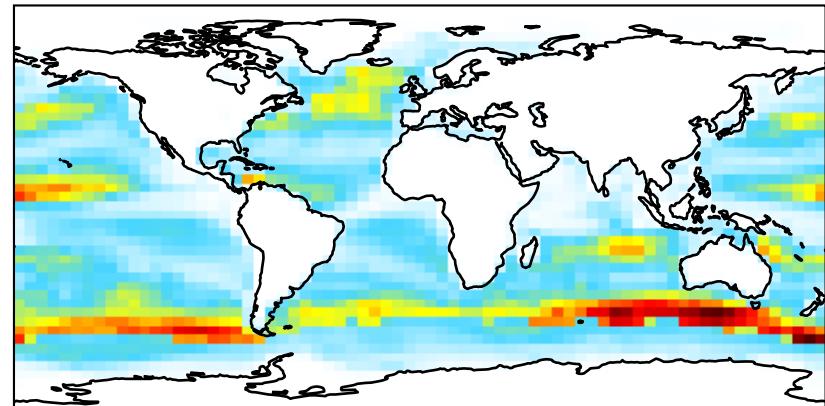


EmisSALCCL_Natural (Apr2019)

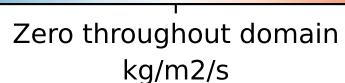
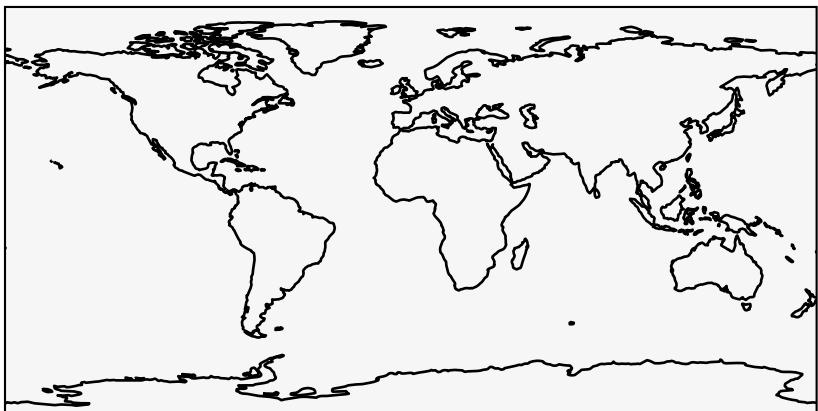
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



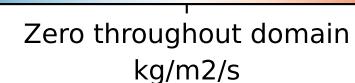
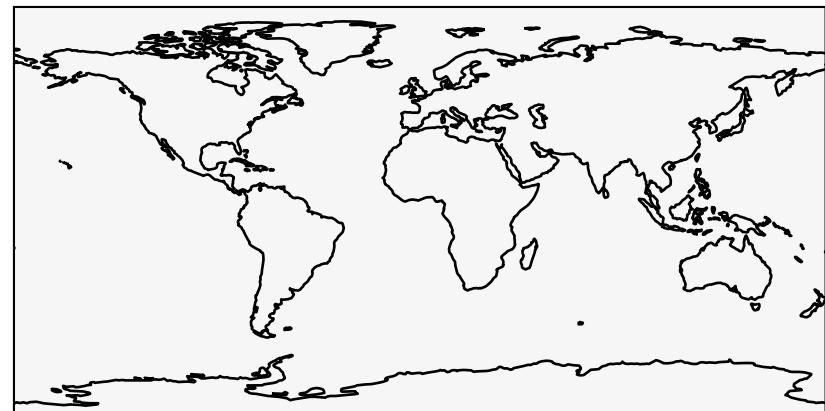
GCC_13.4.0-rc.1 (Dev)
4.0x5.0



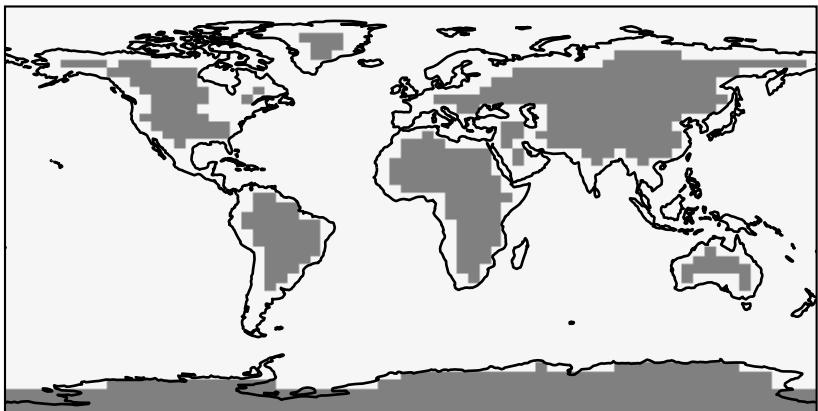
Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%, 95%]



Ratio
Dev/Ref, Dynamic Range



Ratio
Dev/Ref, Fixed Range

