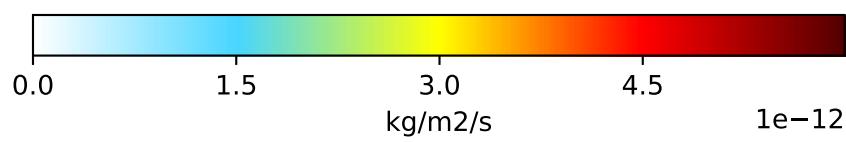
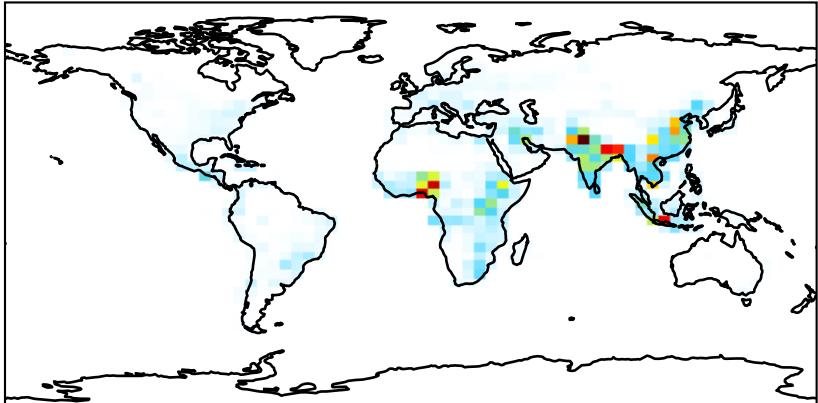
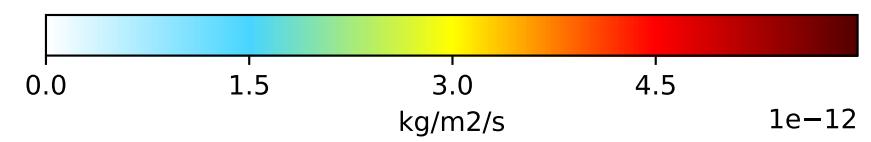
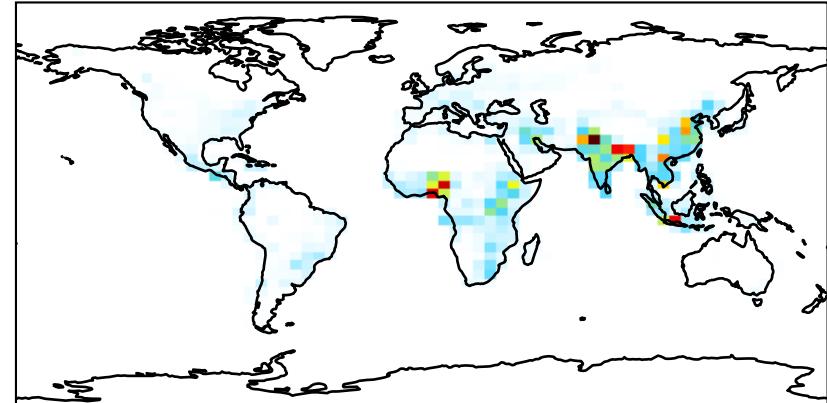


# EmisALD2\_Anthro (Jul2019)

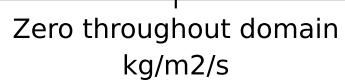
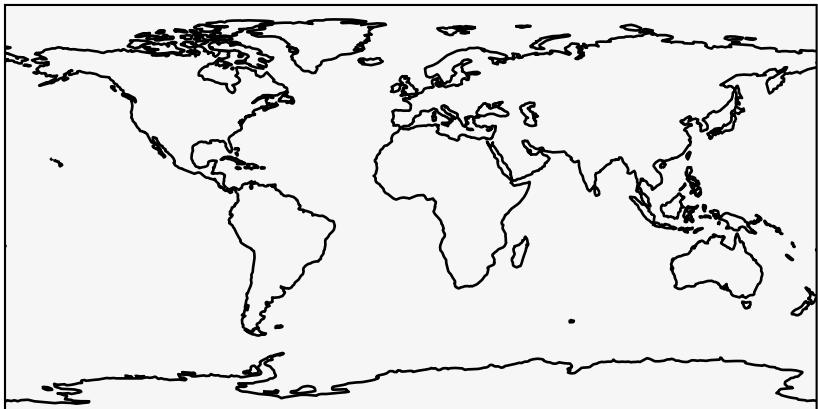
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



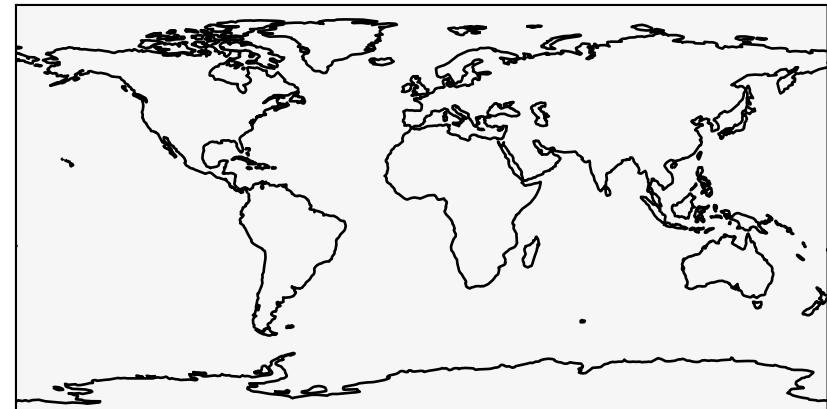
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



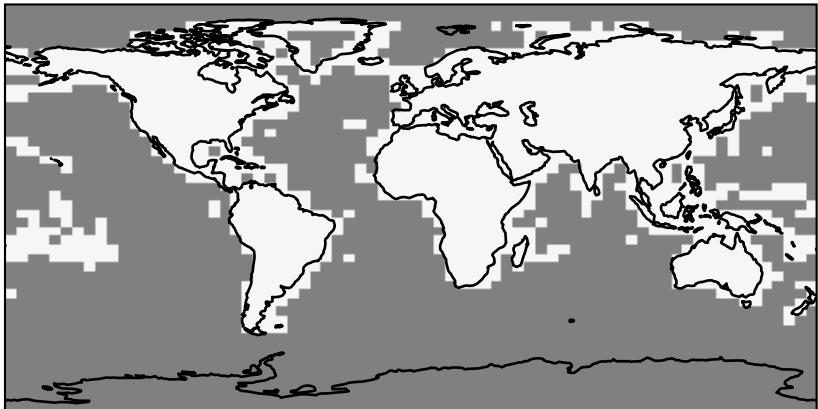
Difference  
Dev - Ref, Dynamic Range



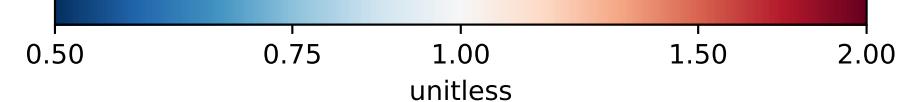
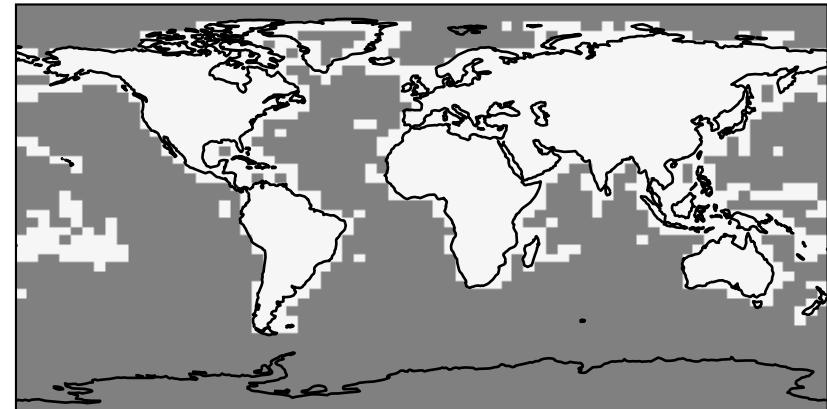
Difference  
Dev - Ref, Restricted Range [5%,95%]



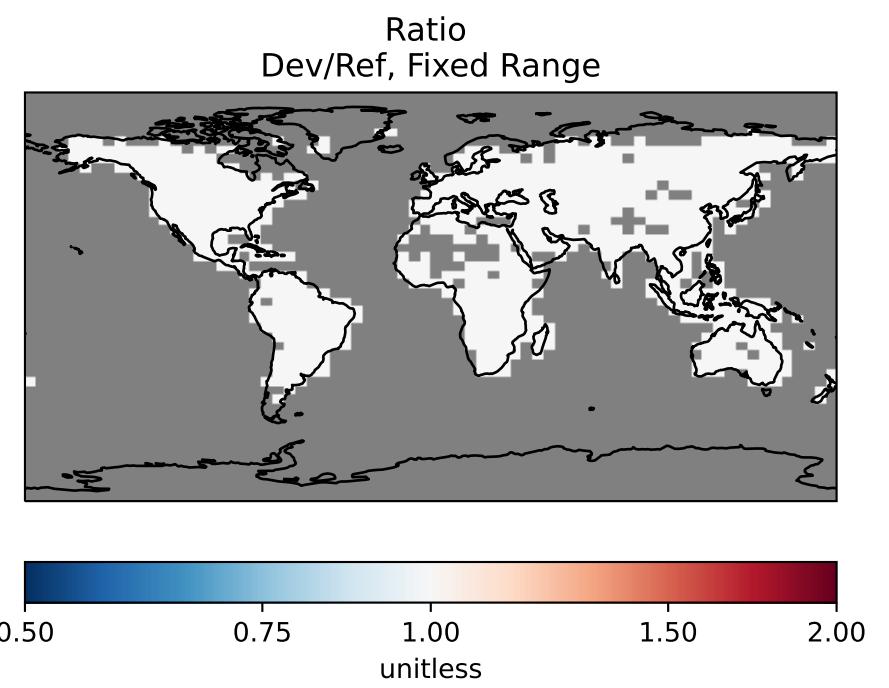
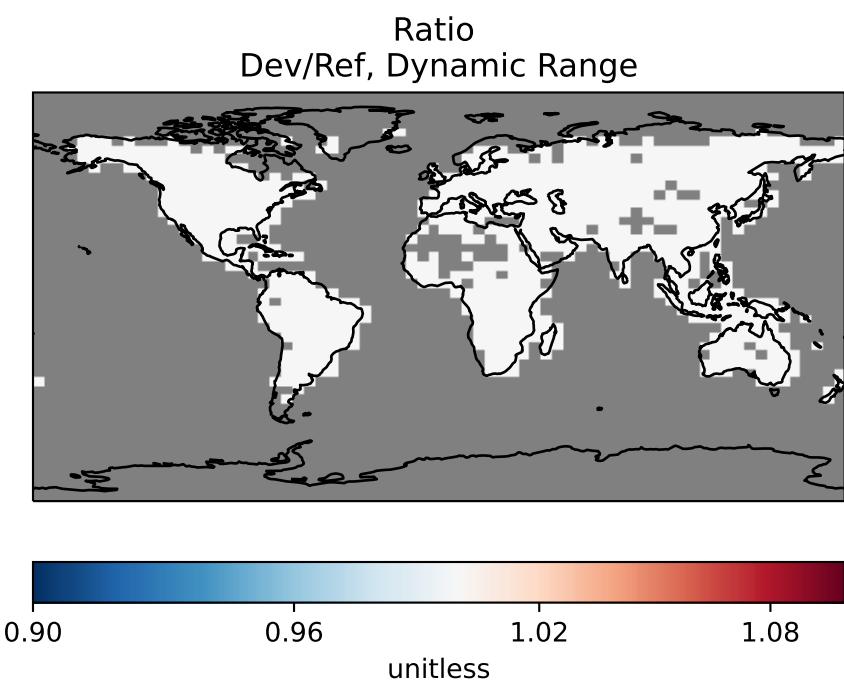
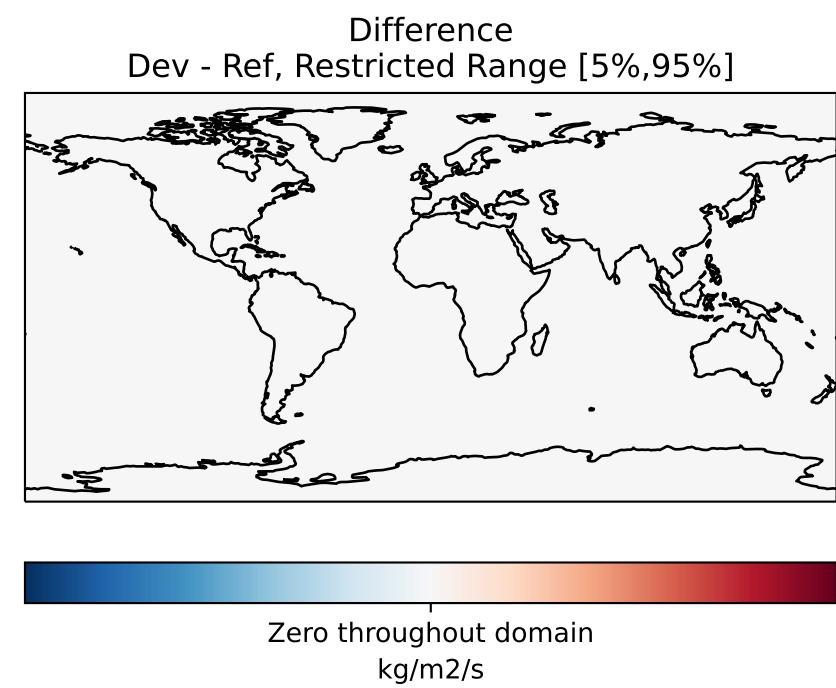
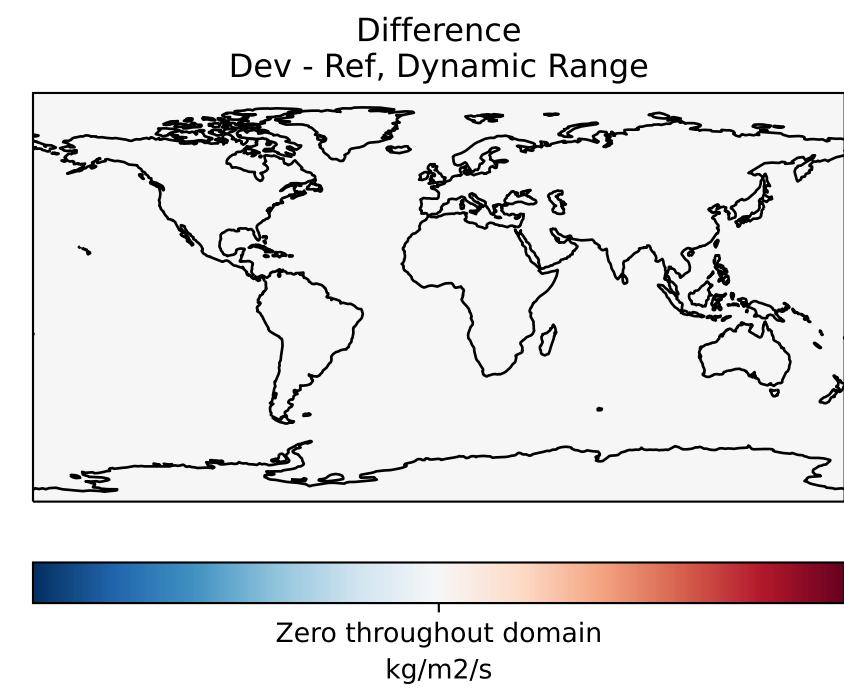
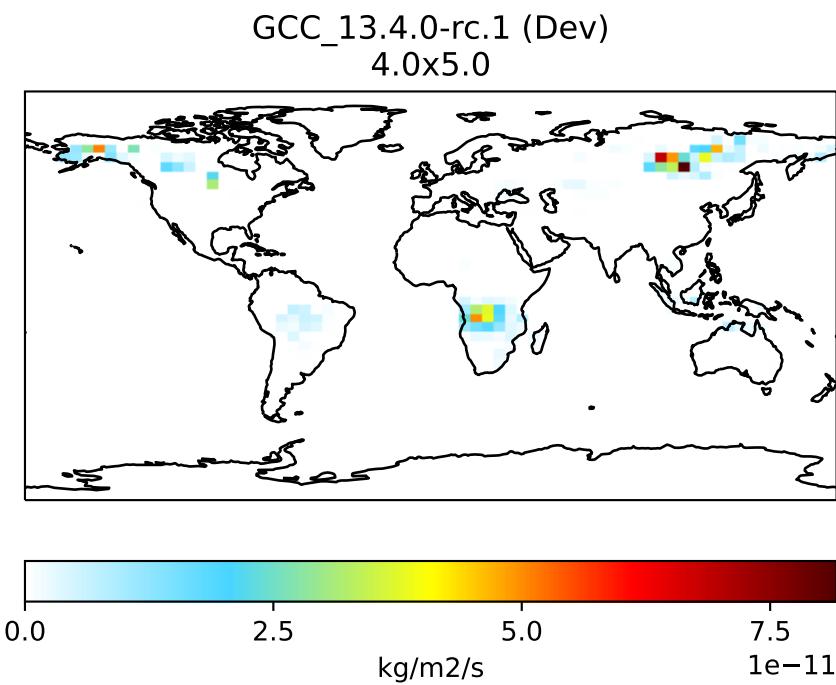
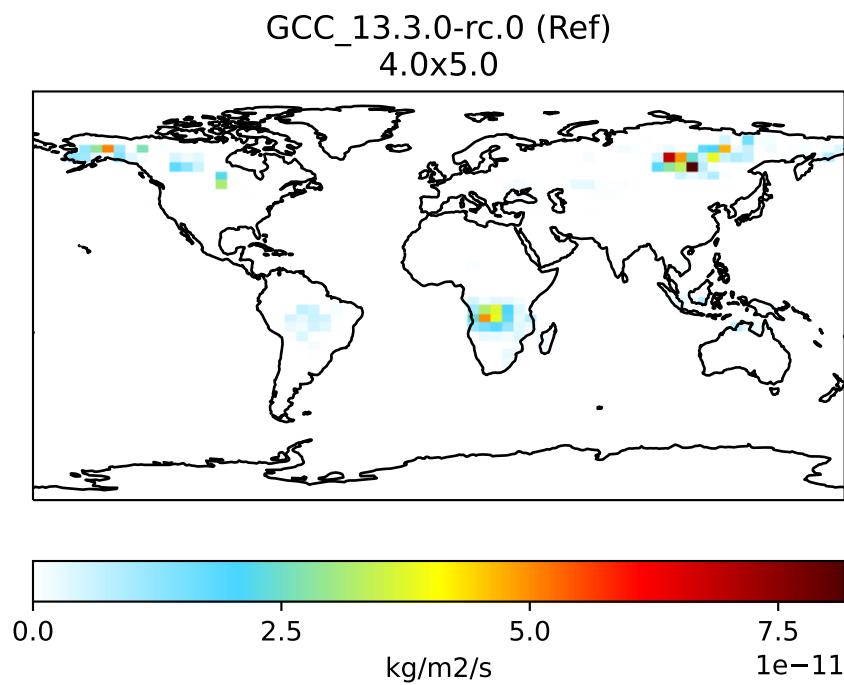
Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

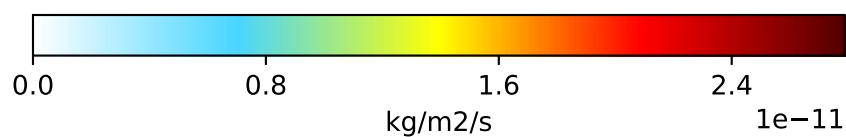
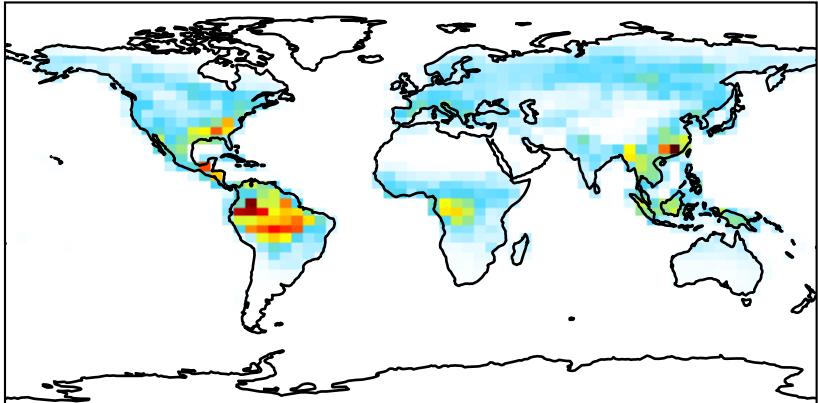


# EmisALD2\_BioBurn (Jul2019)

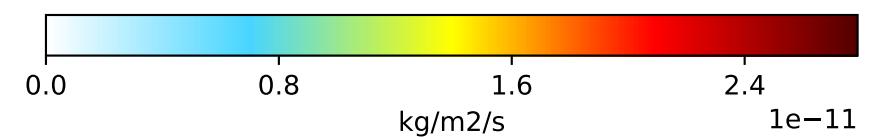
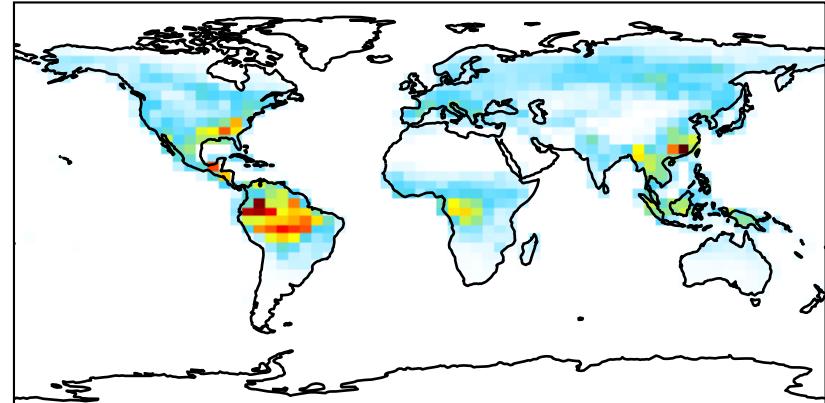


# EmisALD2\_Biogenic (Jul2019)

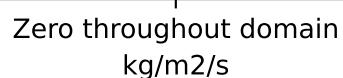
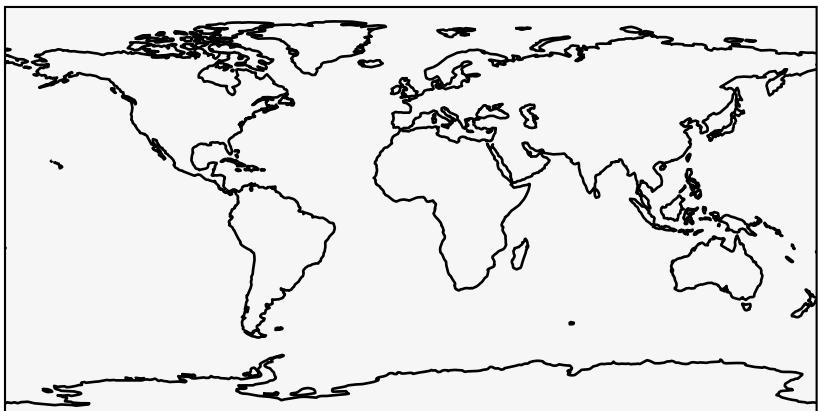
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



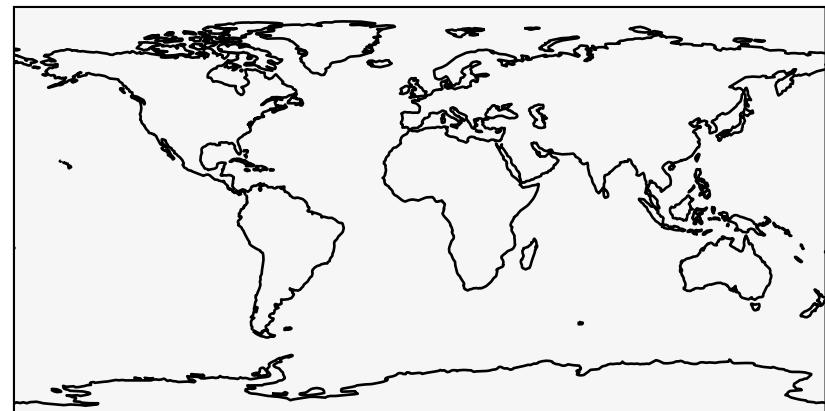
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



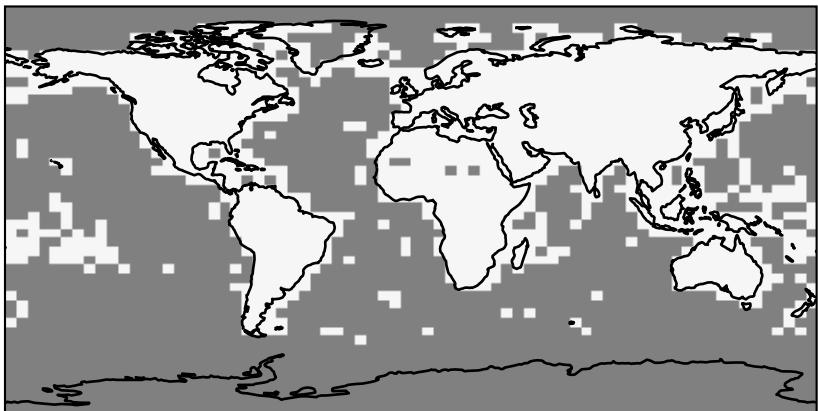
Difference  
Dev - Ref, Dynamic Range



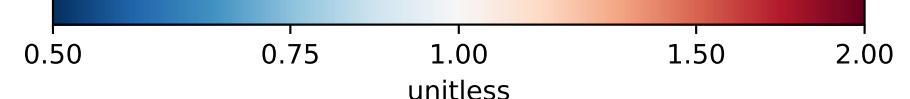
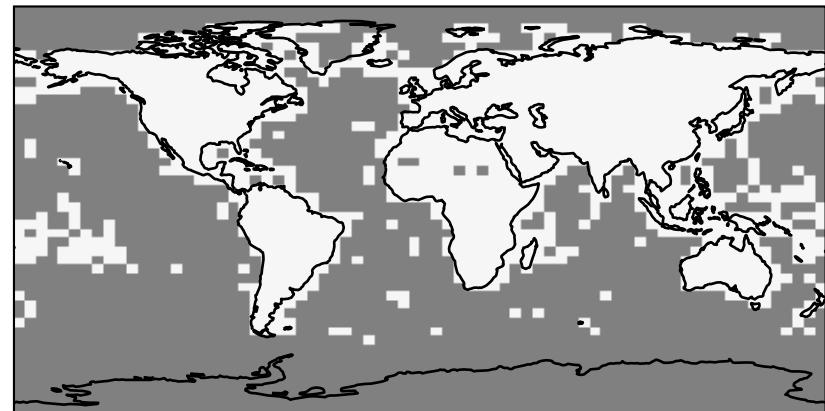
Difference  
Dev - Ref, Restricted Range [5%,95%]



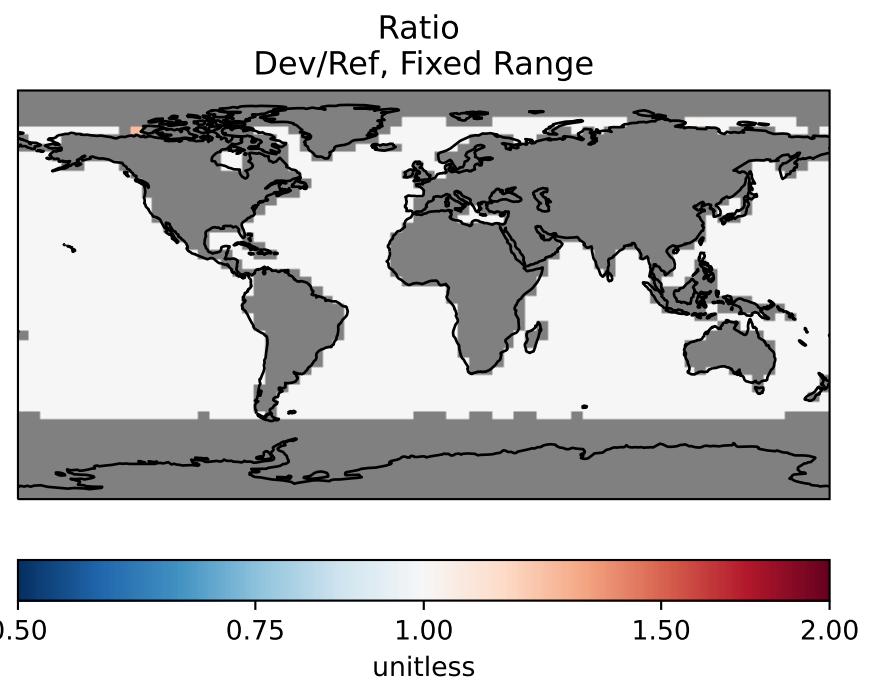
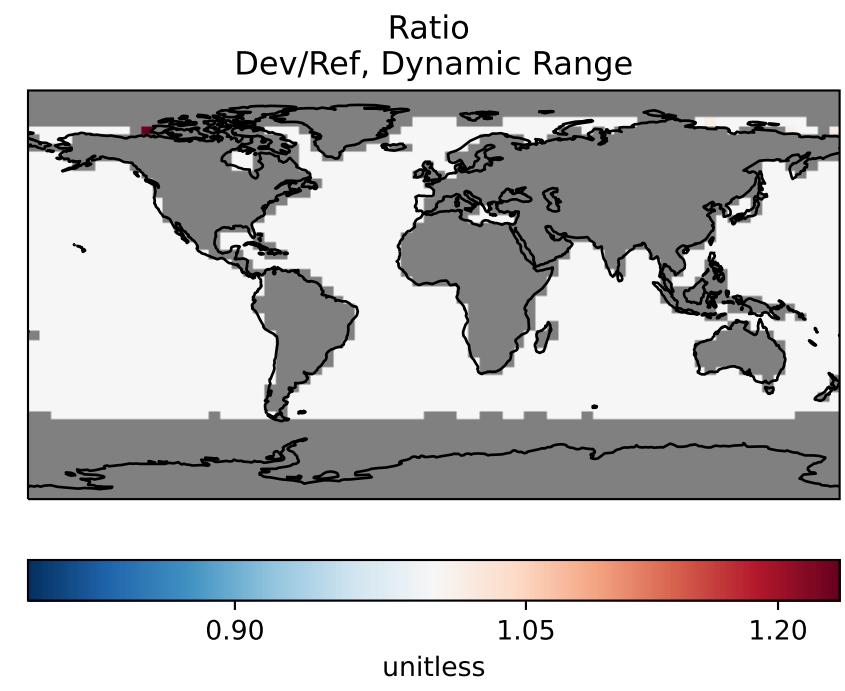
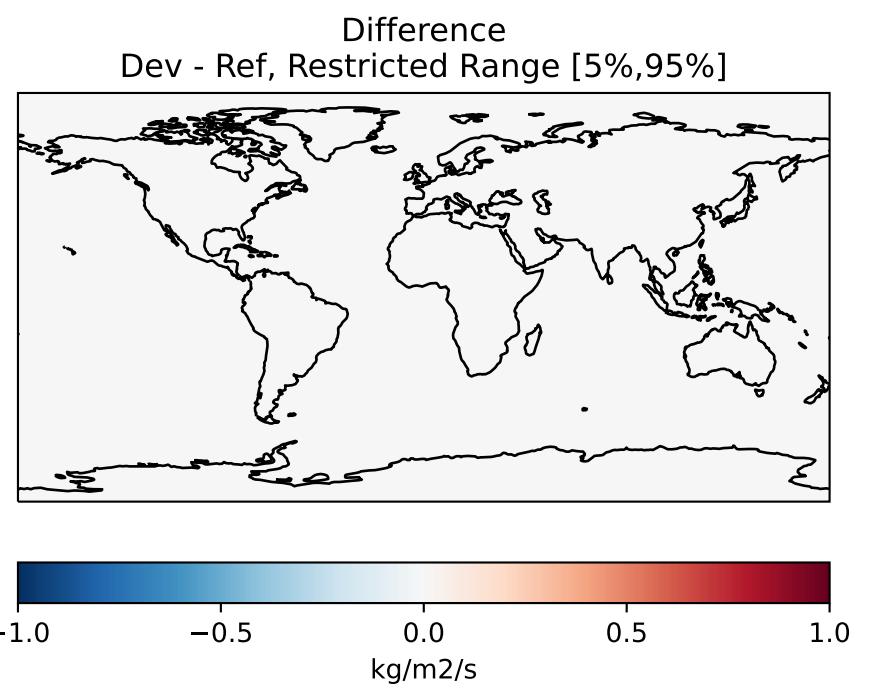
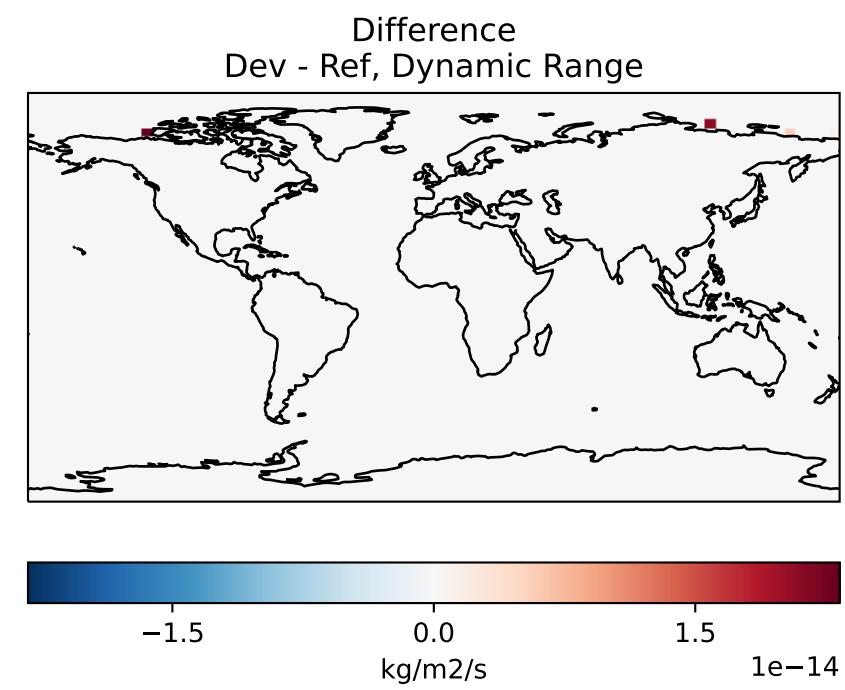
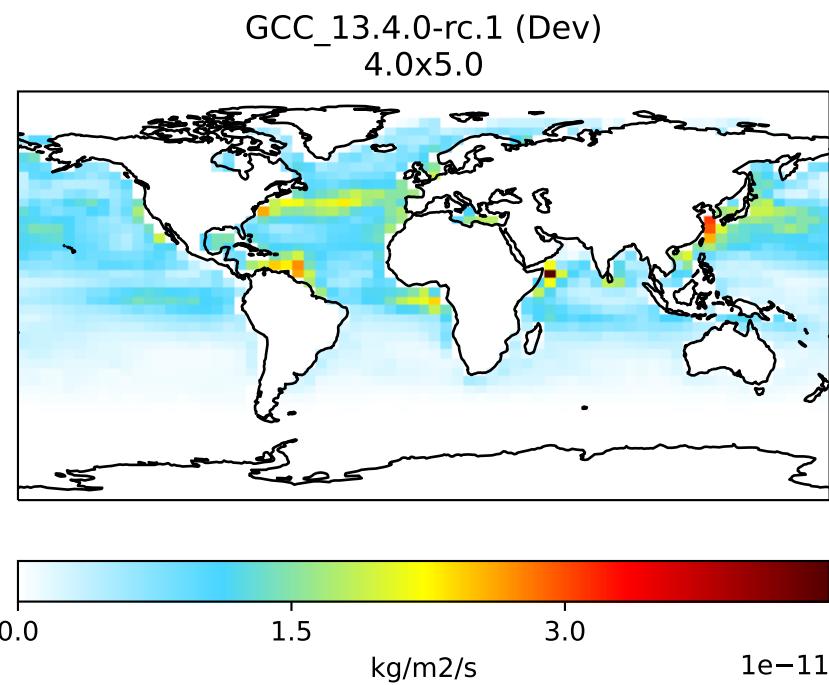
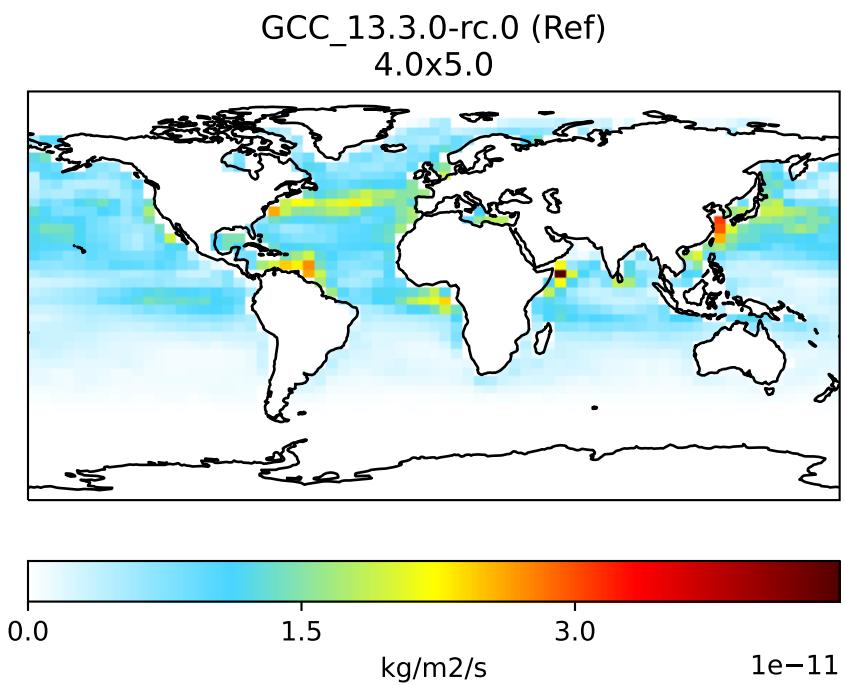
Ratio  
Dev/Ref, Dynamic Range



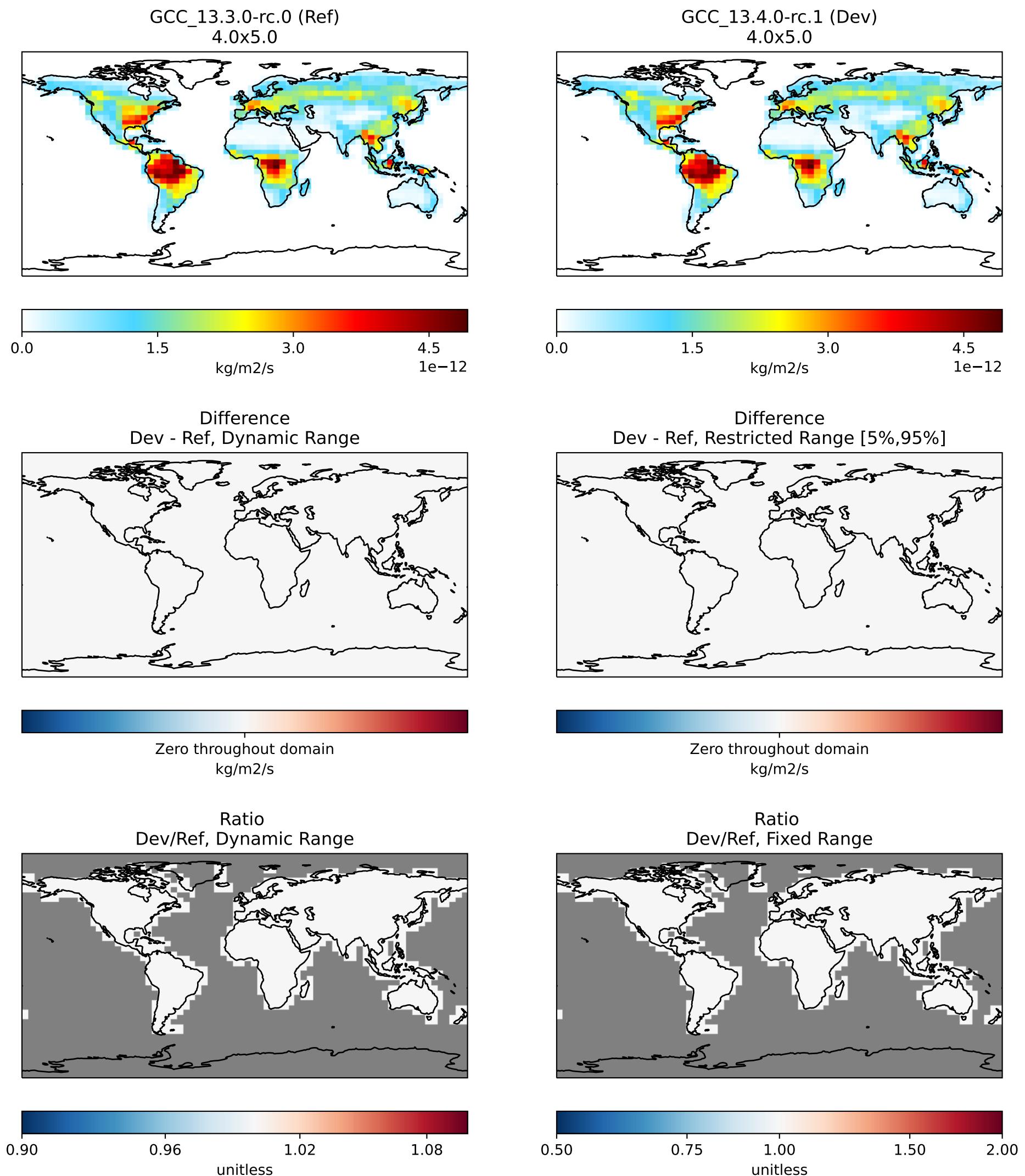
Ratio  
Dev/Ref, Fixed Range



# EmisALD2\_Ocean (Jul2019)

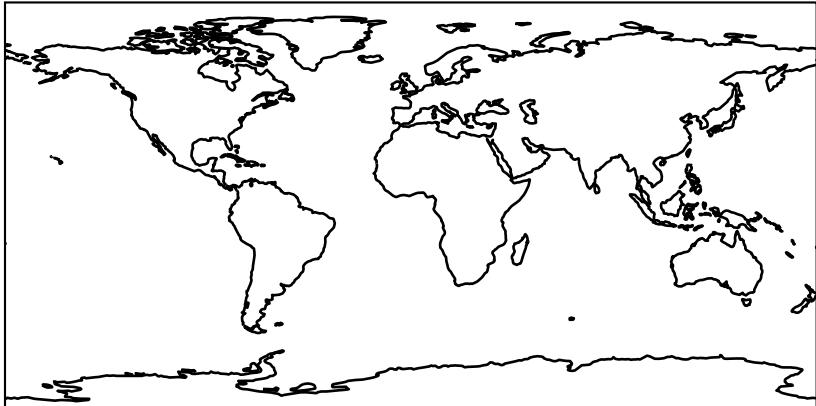


# EmisALD2\_PlantDecay (Jul2019)



# EmisALD2\_Ship (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



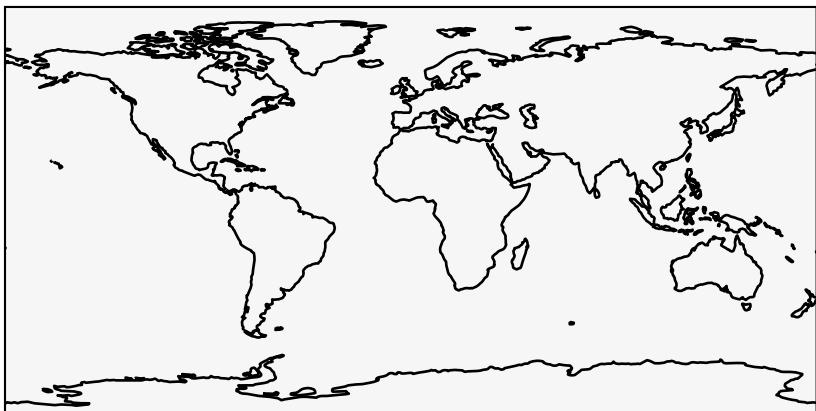
Zero throughout domain  
kg/m<sup>2</sup>/s

GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



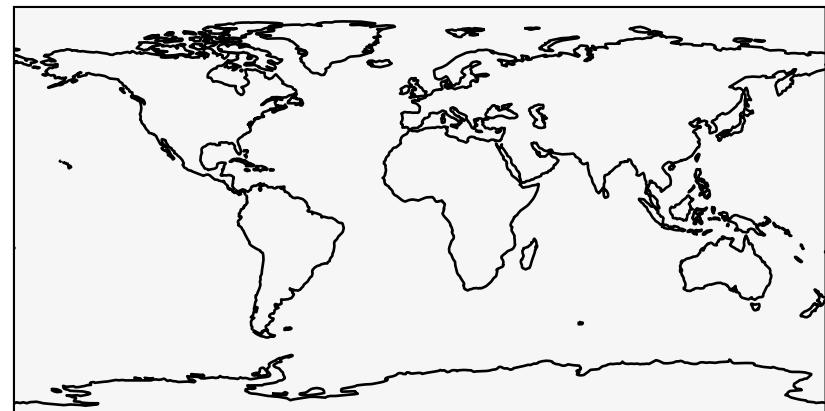
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Dynamic Range



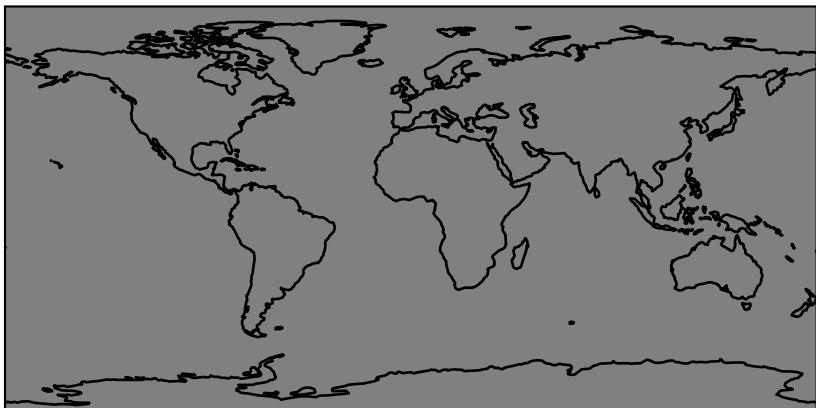
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



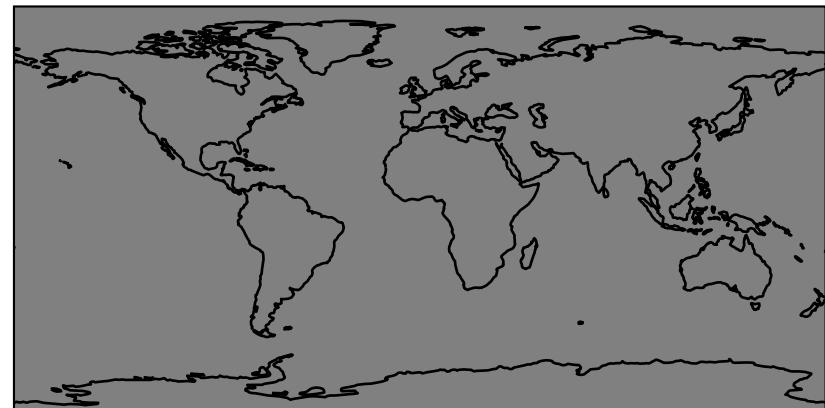
Zero throughout domain  
kg/m<sup>2</sup>/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

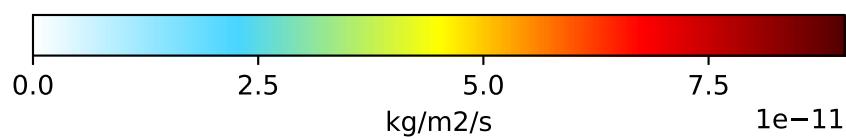
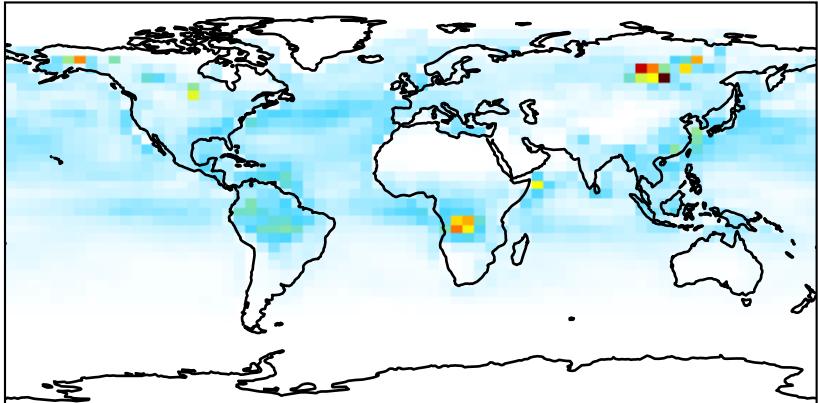
Ratio  
Dev/Ref, Fixed Range



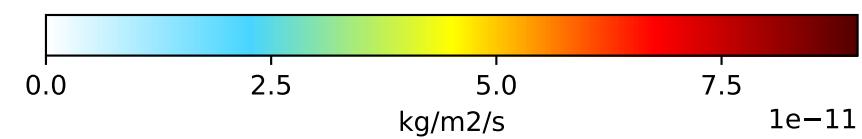
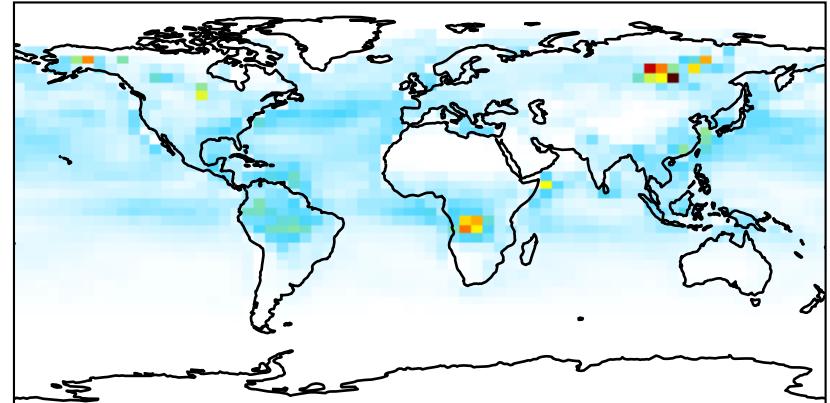
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unitless

# EmisALD2\_Total (Jul2019)

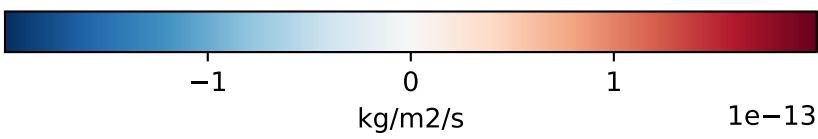
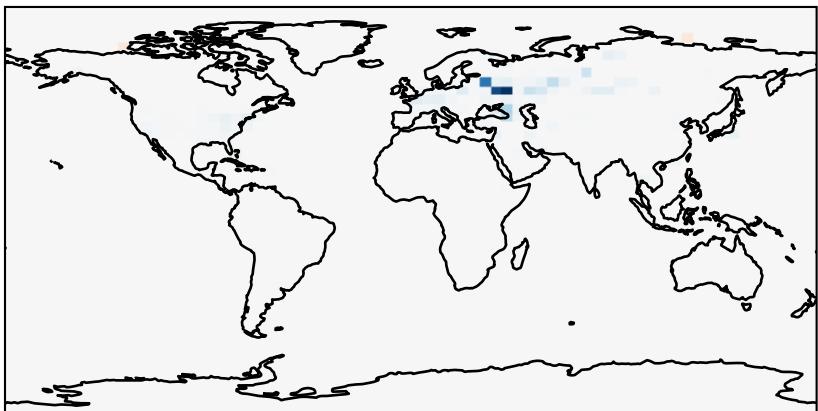
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



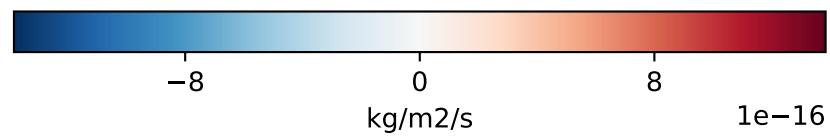
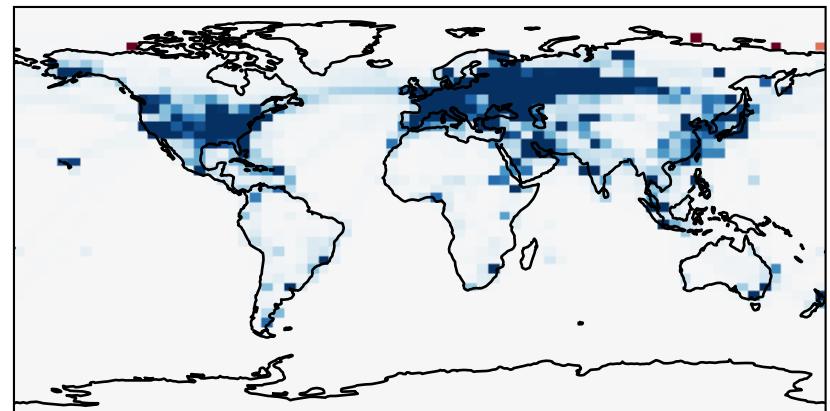
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



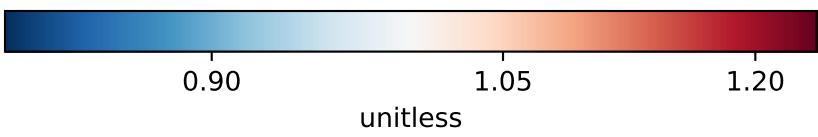
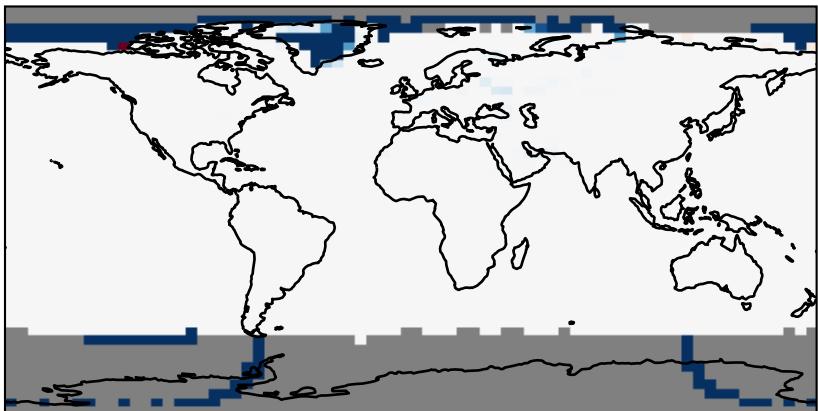
Difference  
Dev - Ref, Dynamic Range



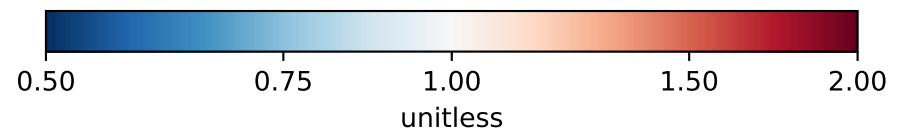
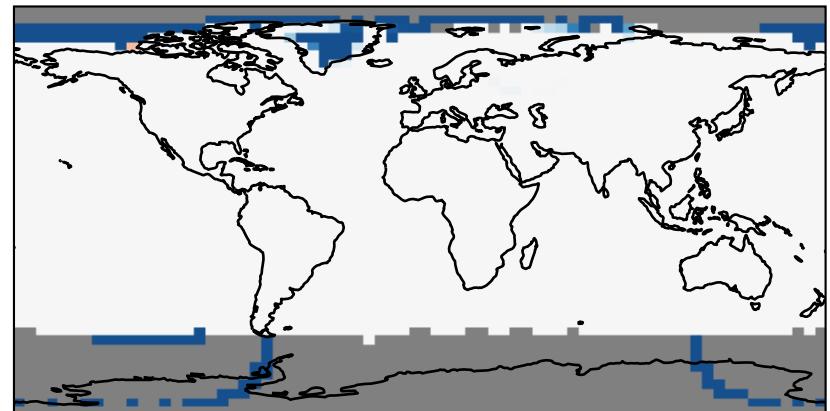
Difference  
Dev - Ref, Restricted Range [5%, 95%]



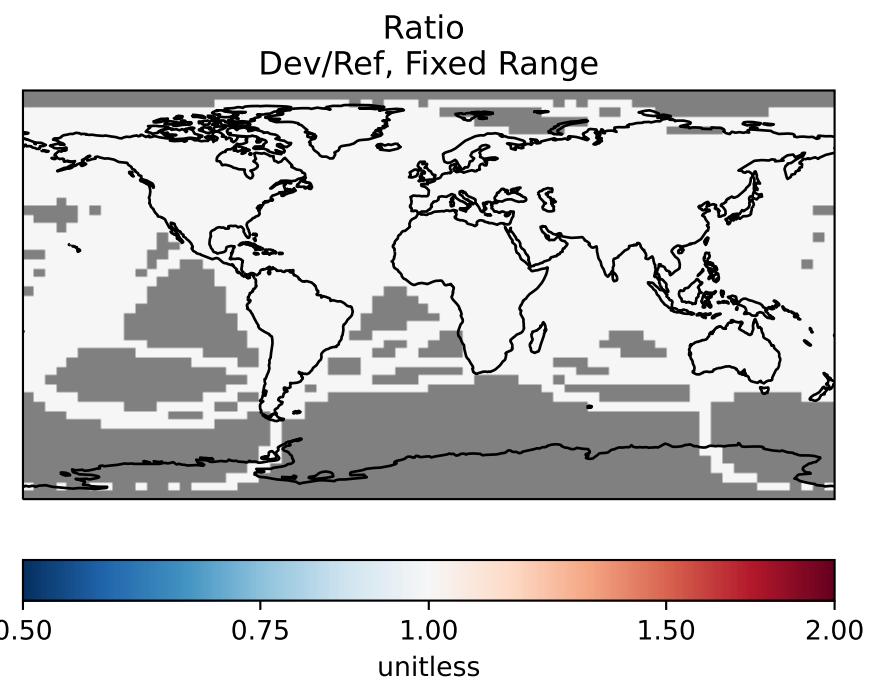
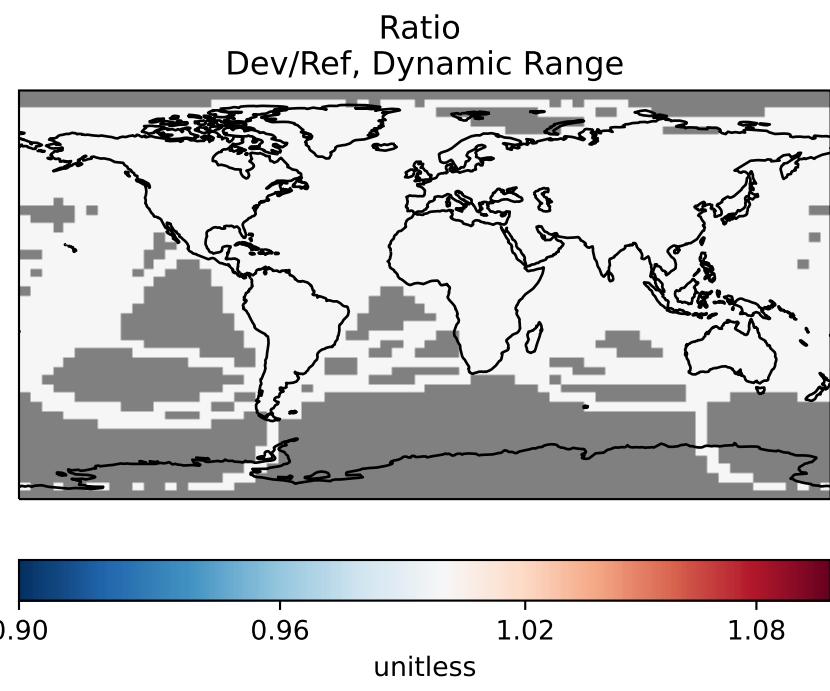
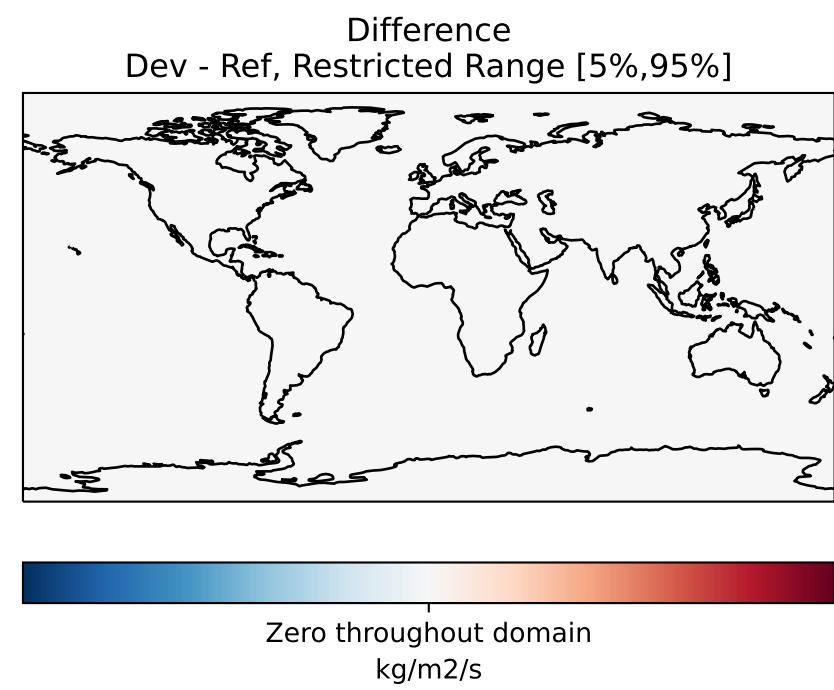
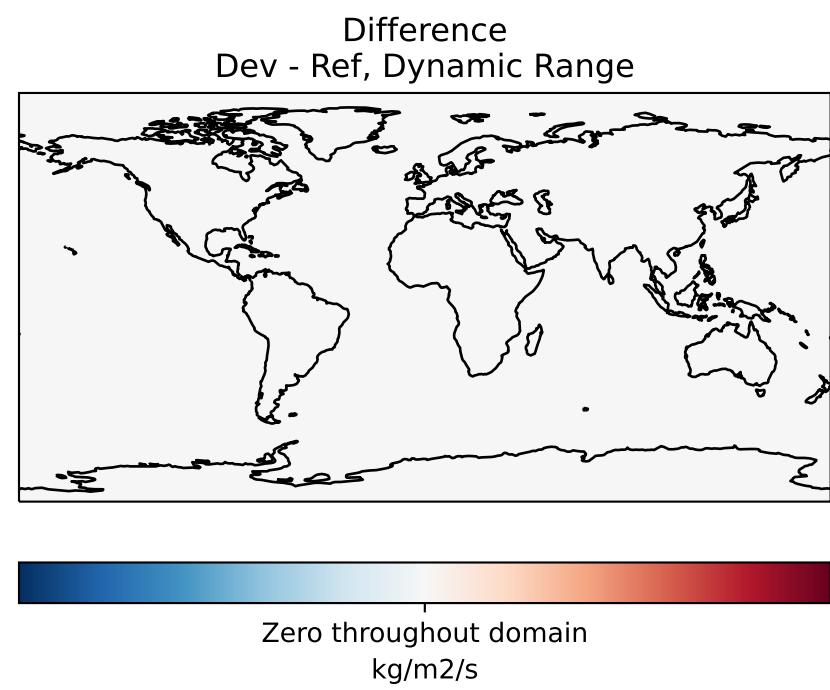
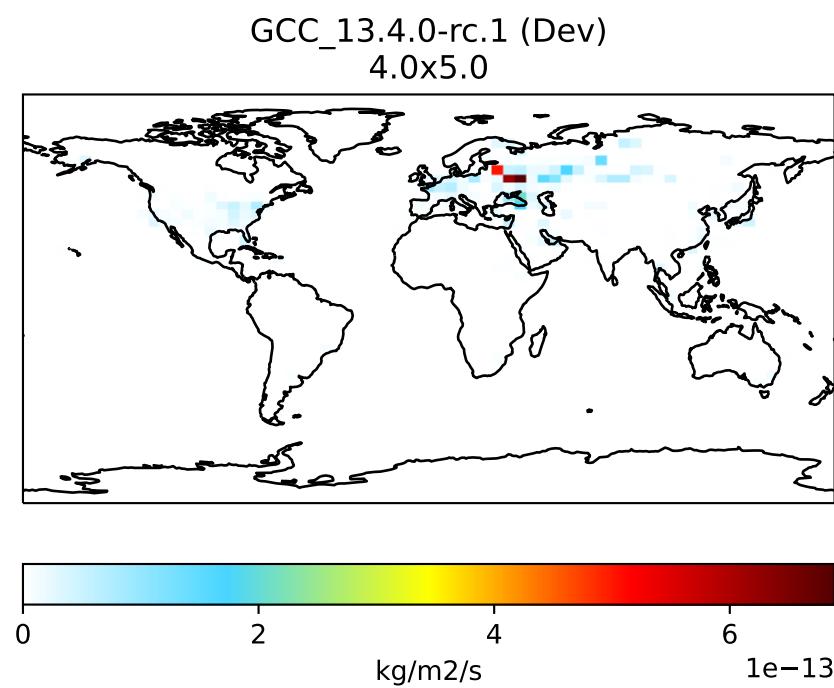
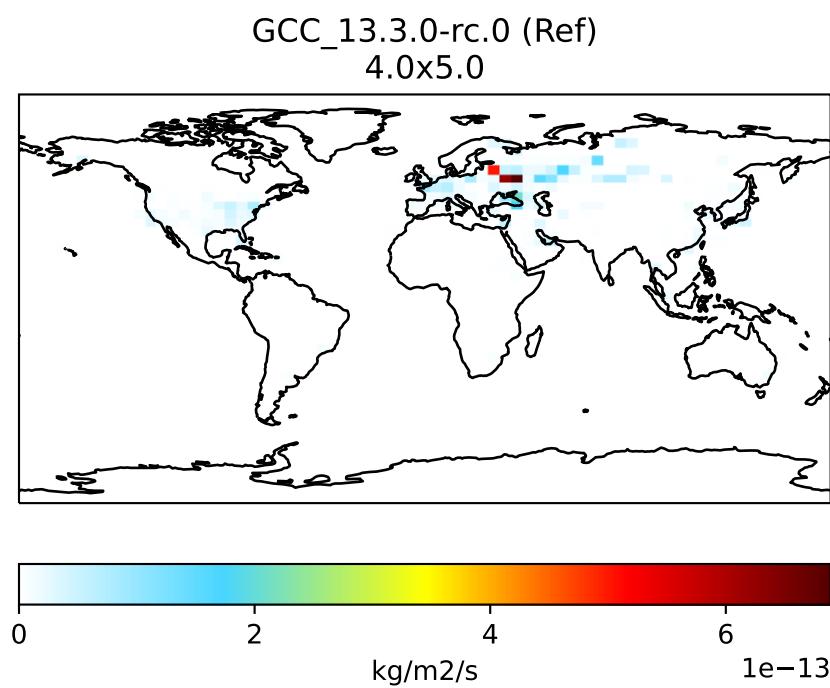
Ratio  
Dev/Ref, Dynamic Range



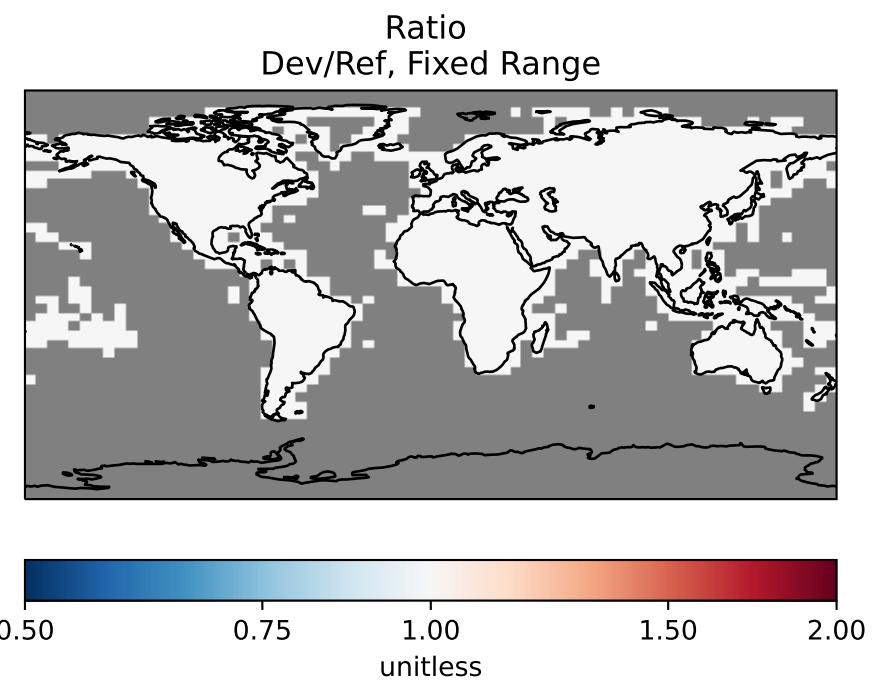
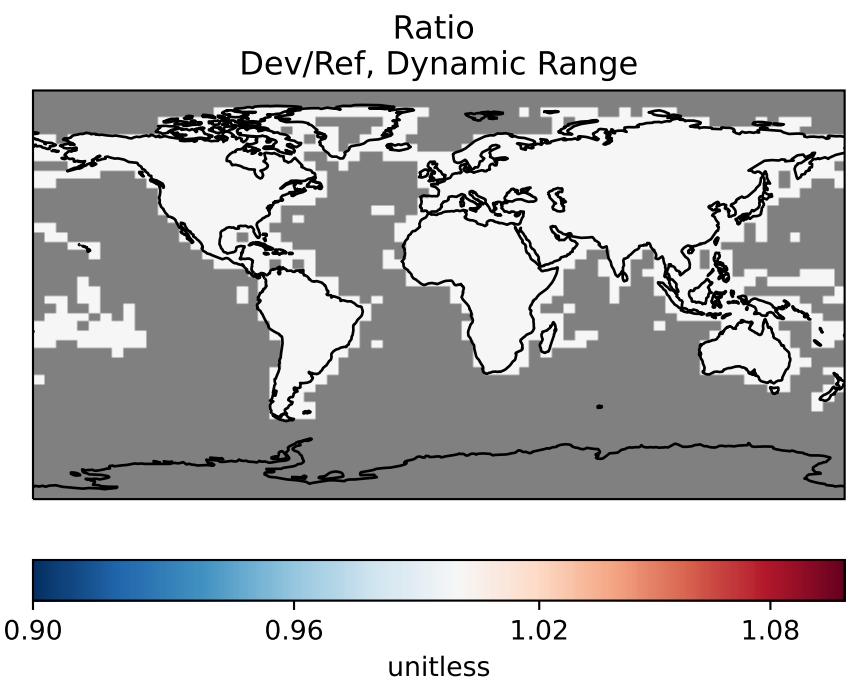
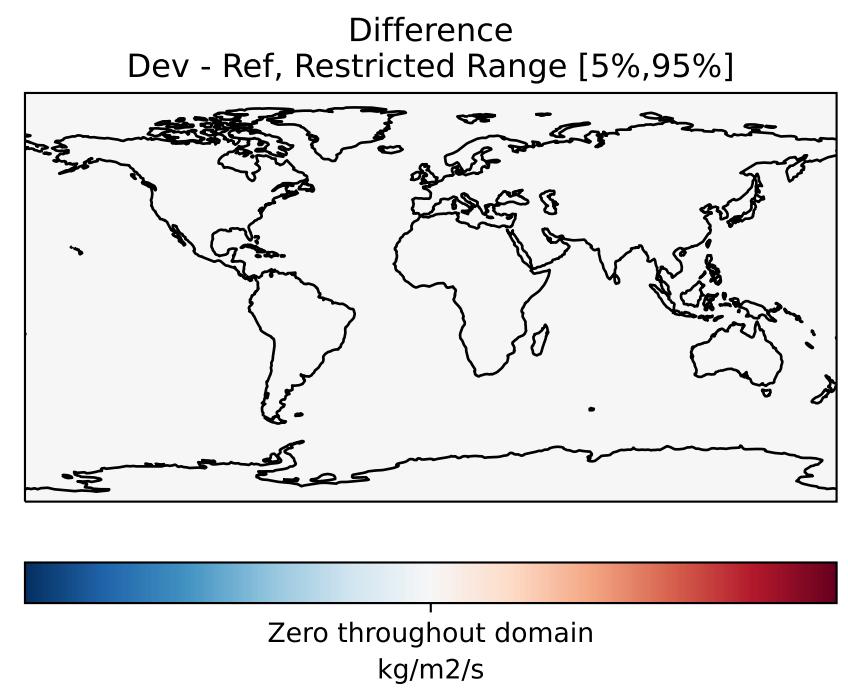
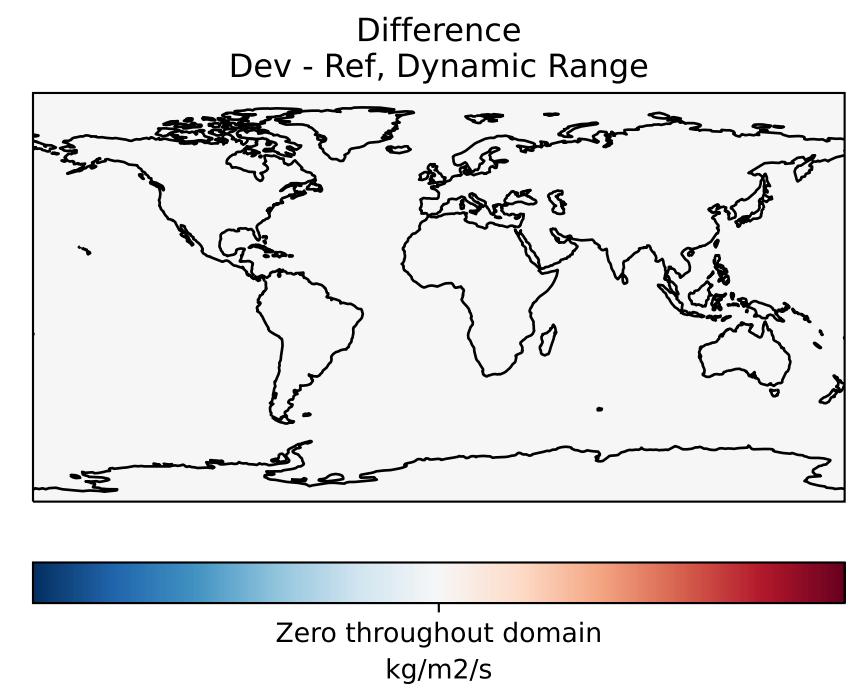
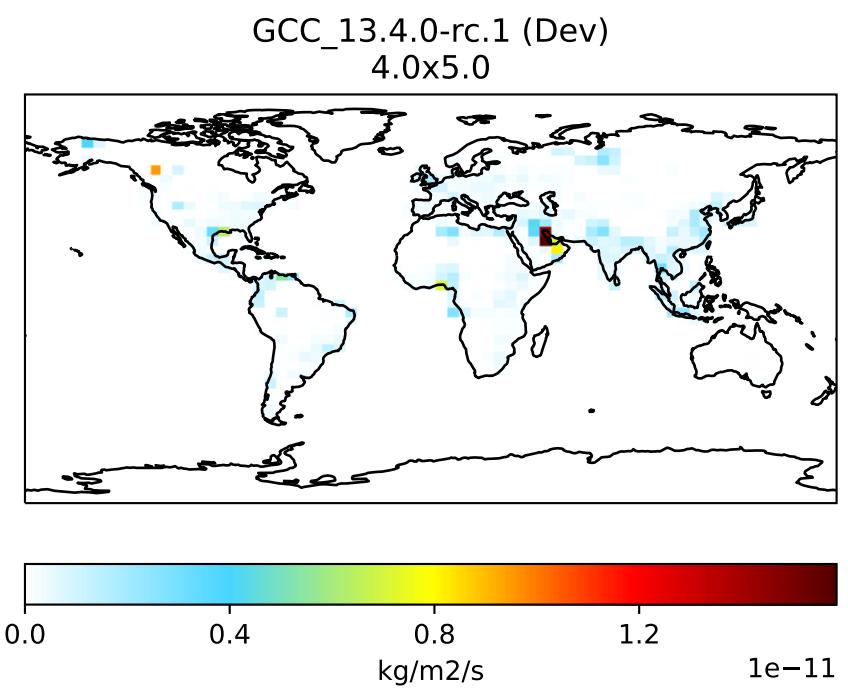
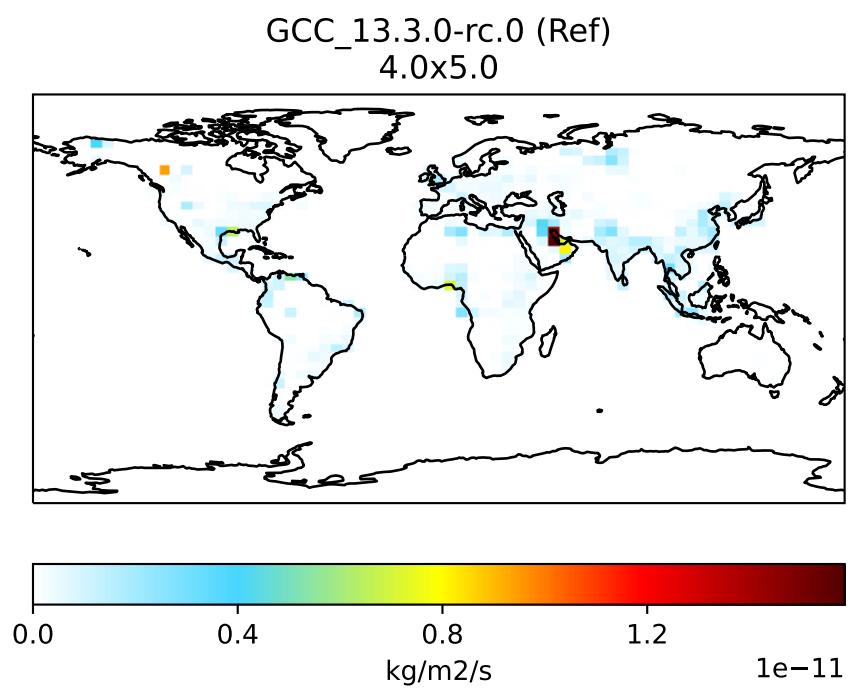
Ratio  
Dev/Ref, Fixed Range



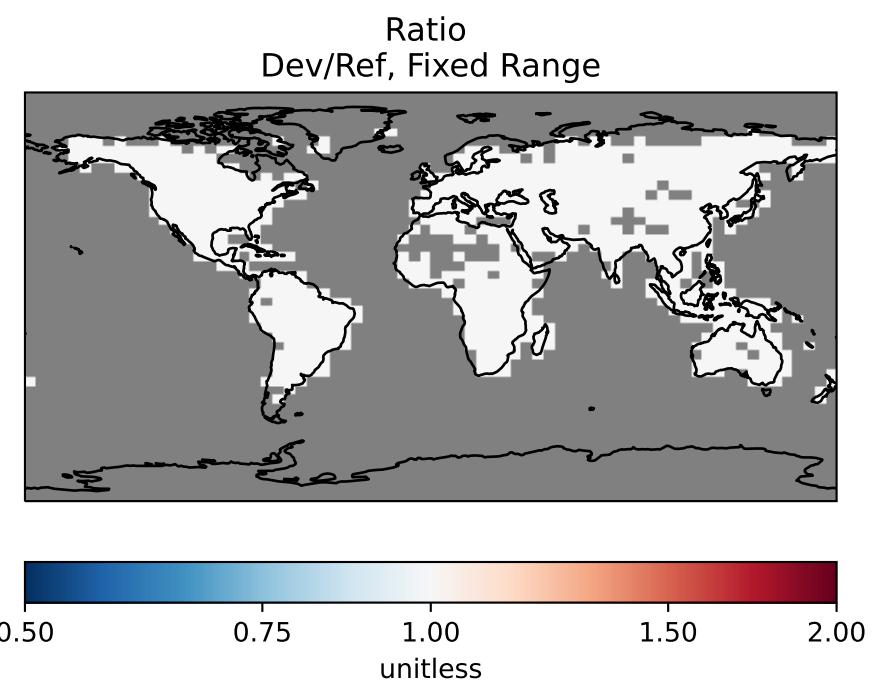
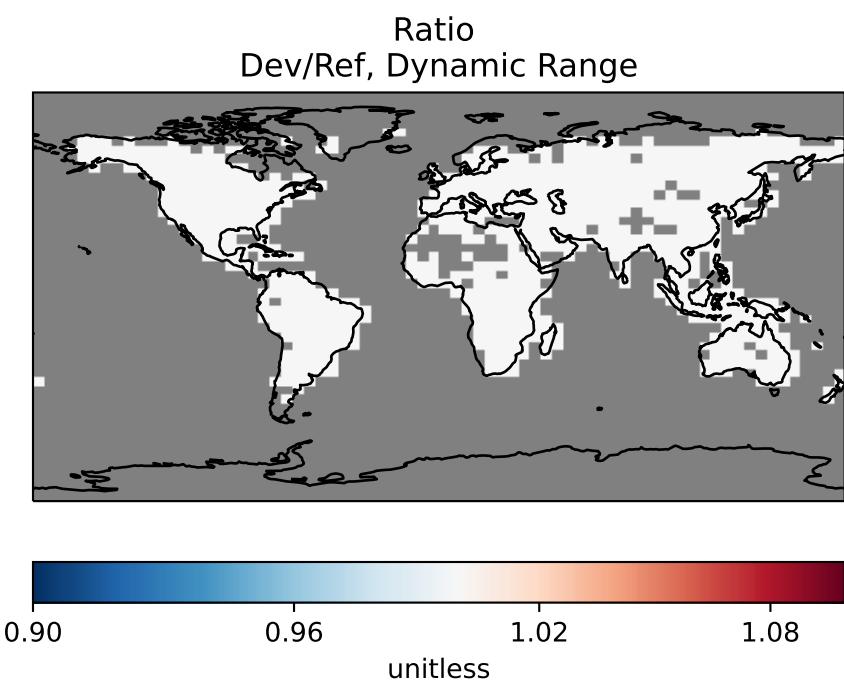
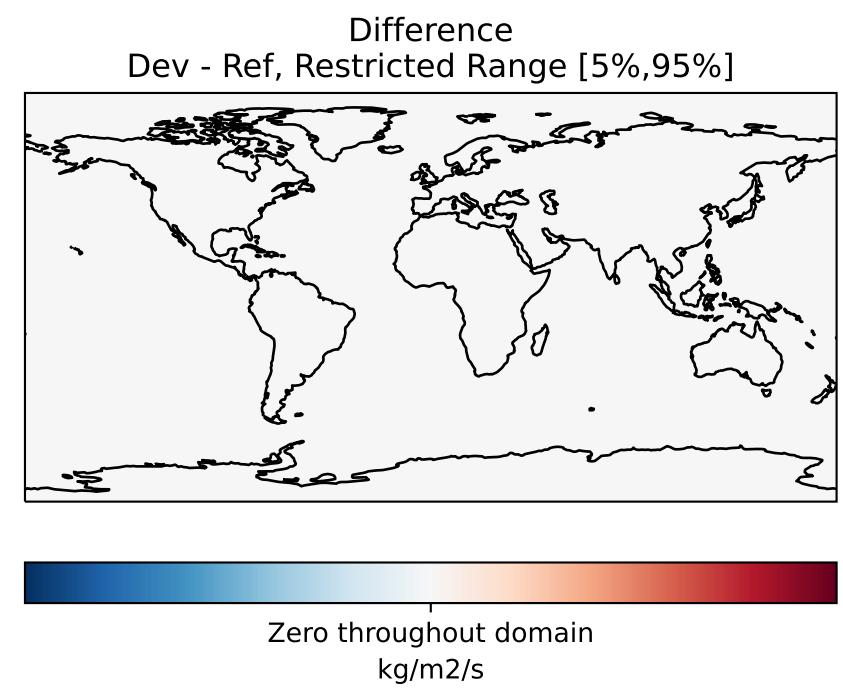
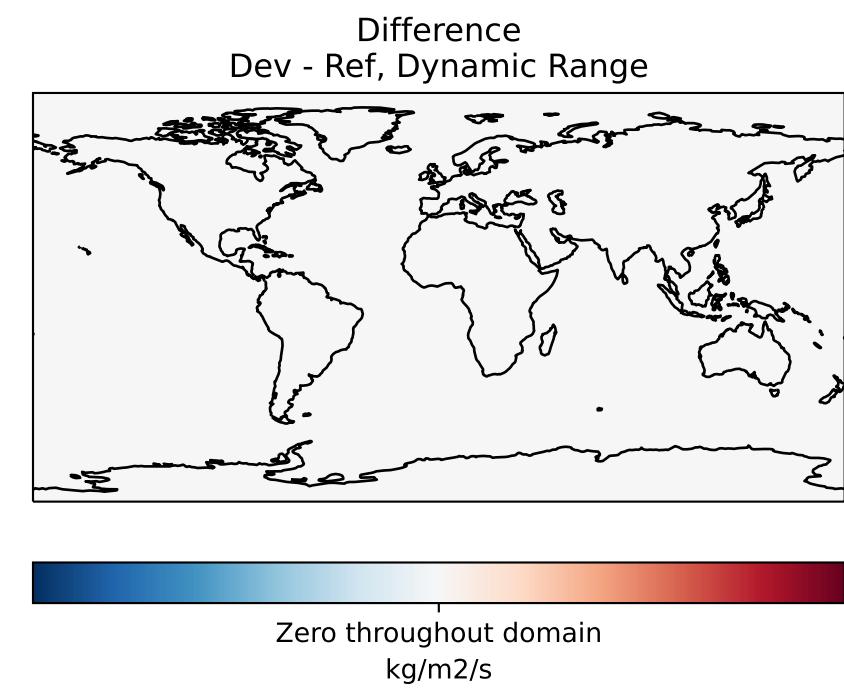
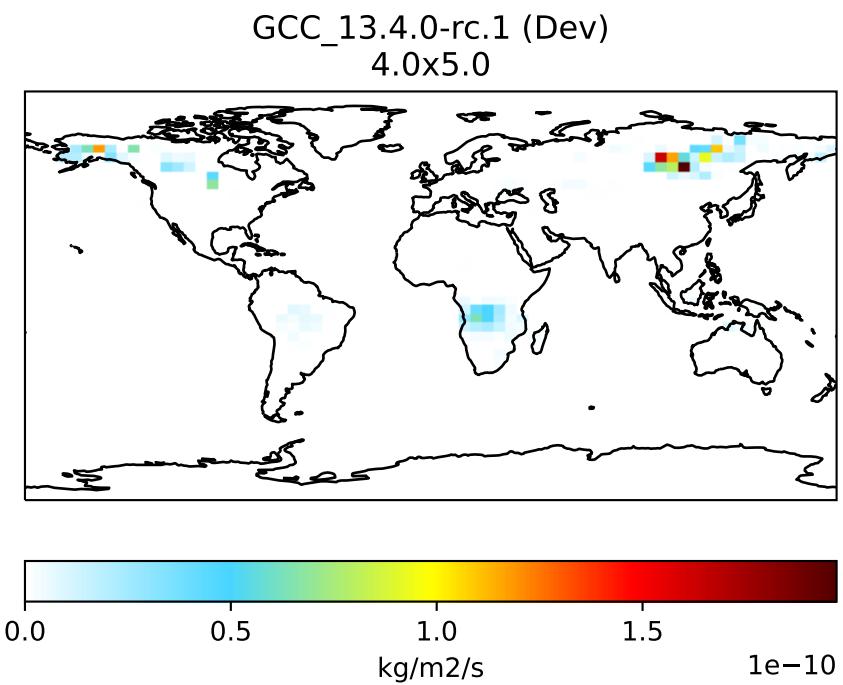
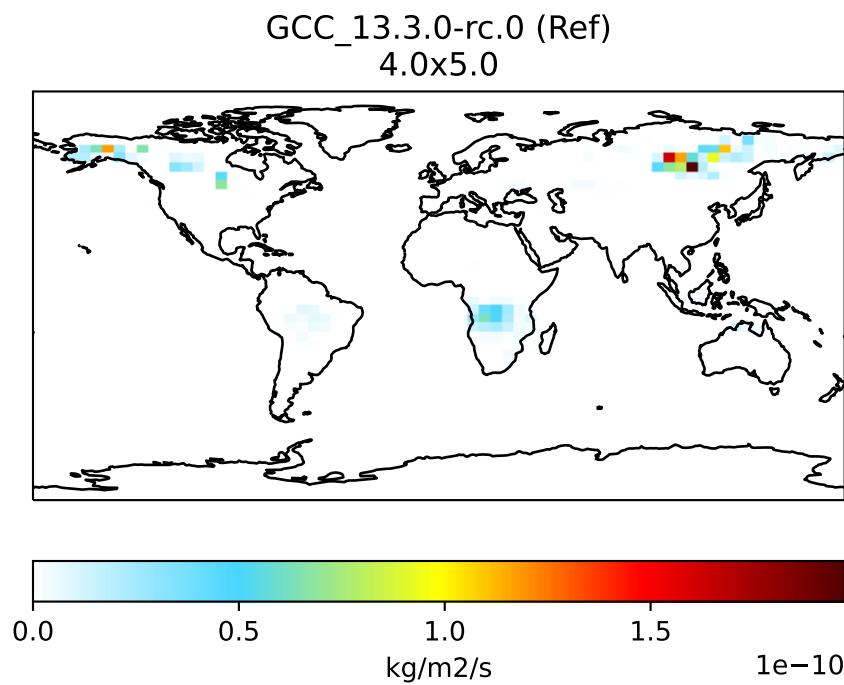
# EmisCH2O\_Aircraft (Jul2019)



# EmisCH2O\_Anthro (Jul2019)

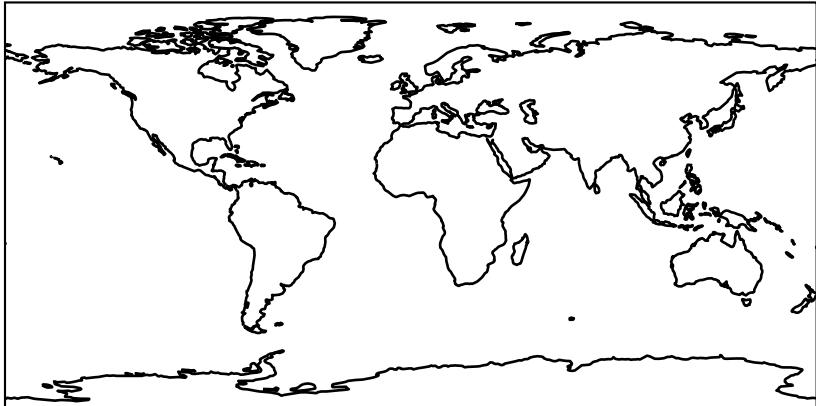


# EmisCH2O\_BioBurn (Jul2019)



# EmisCH2O\_Ship (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



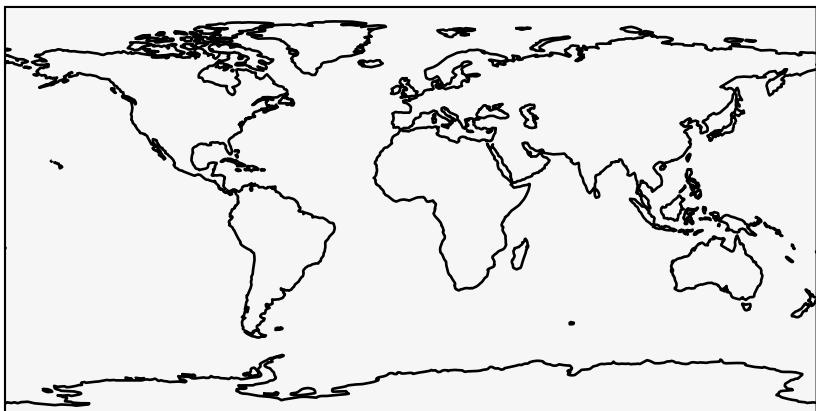
Zero throughout domain  
kg/m<sup>2</sup>/s

GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



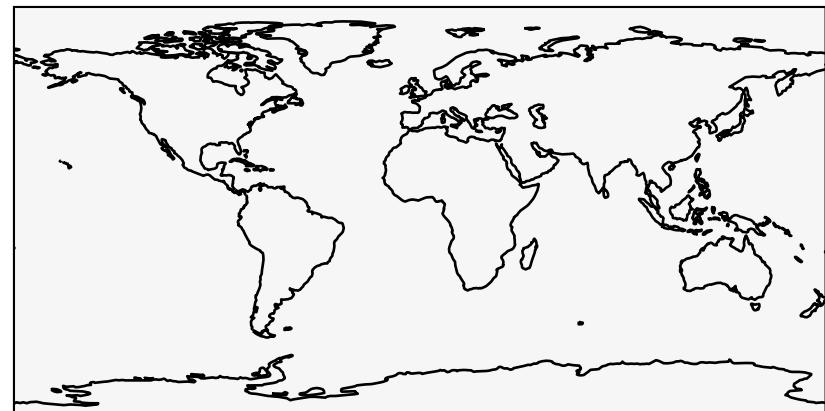
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Dynamic Range



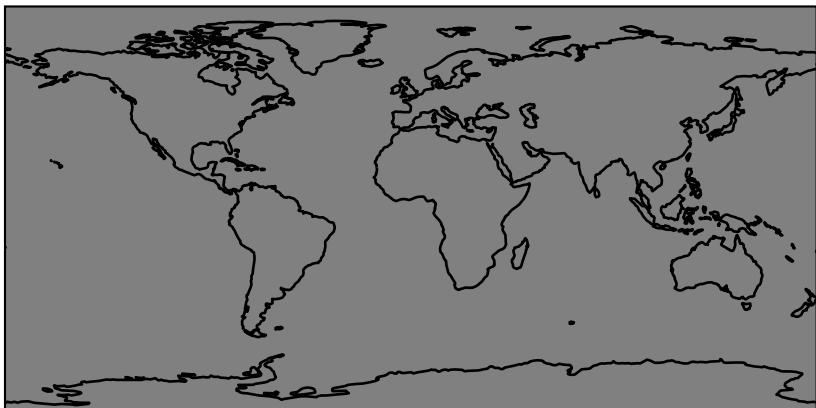
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



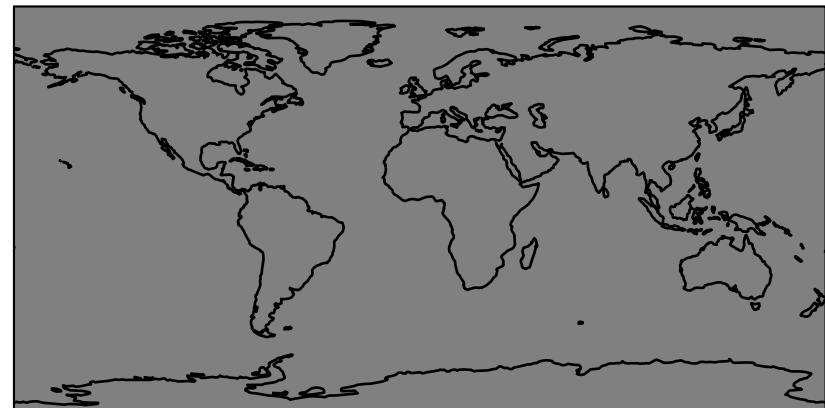
Zero throughout domain  
kg/m<sup>2</sup>/s

Ratio  
Dev/Ref, Dynamic Range



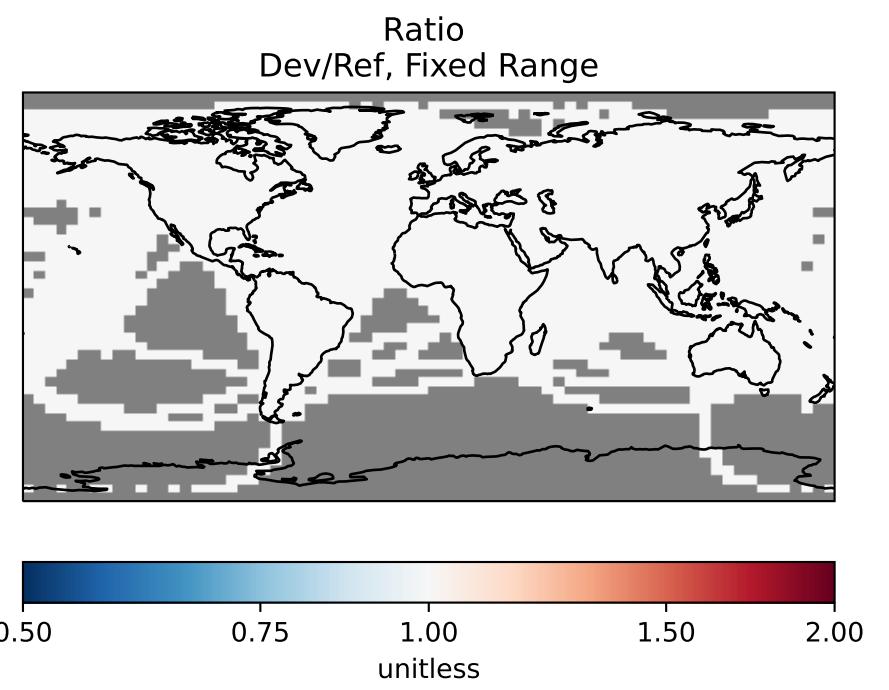
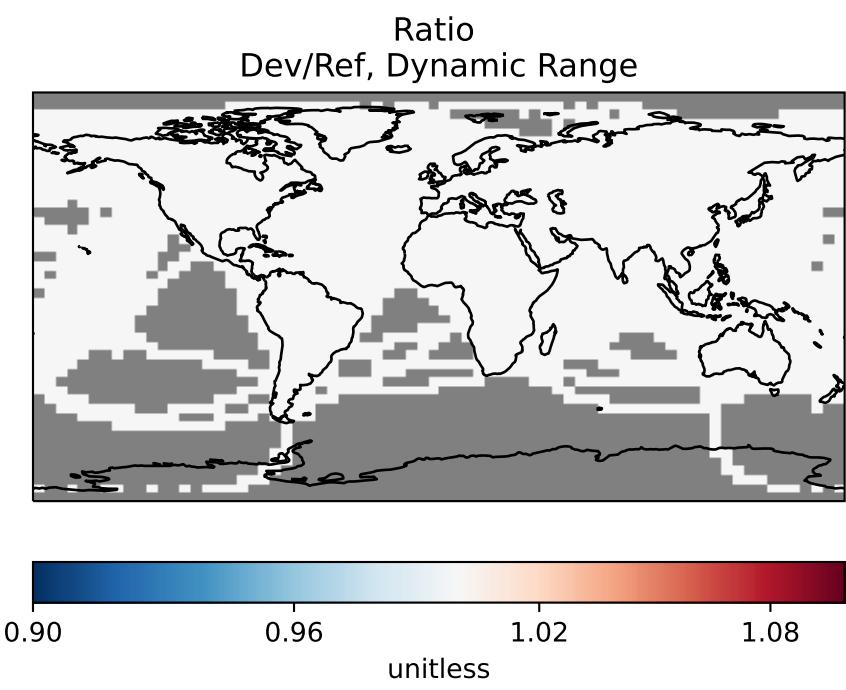
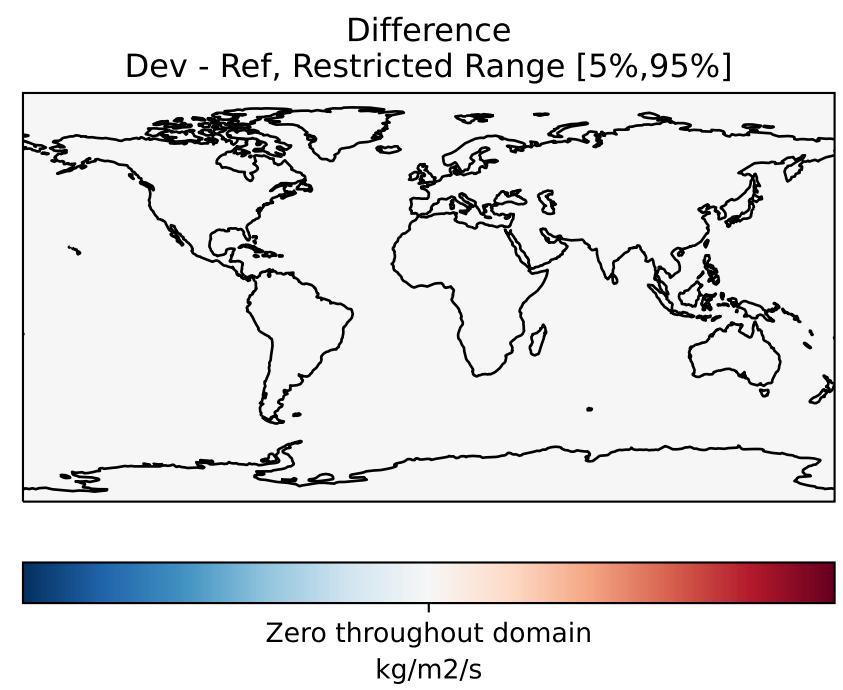
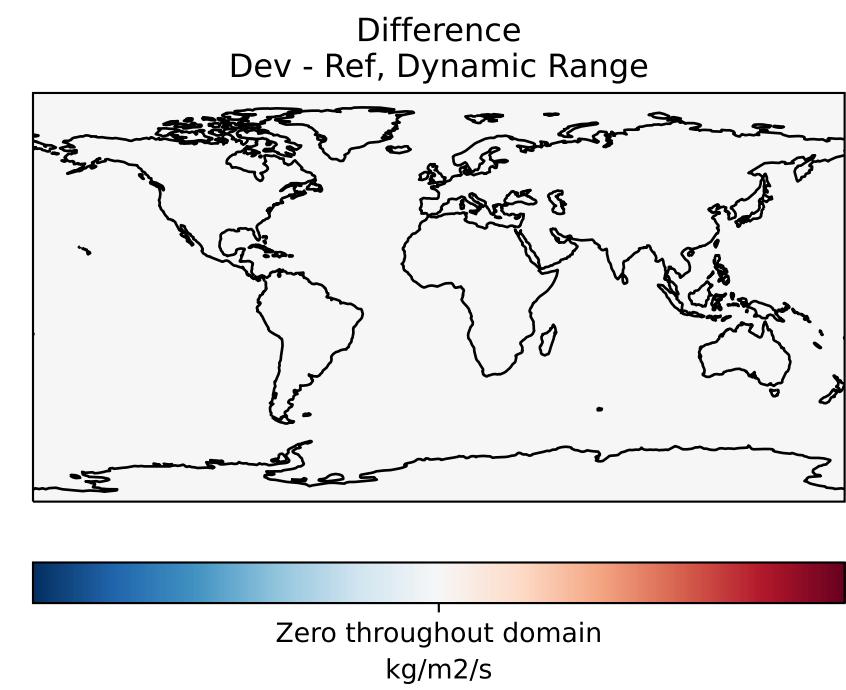
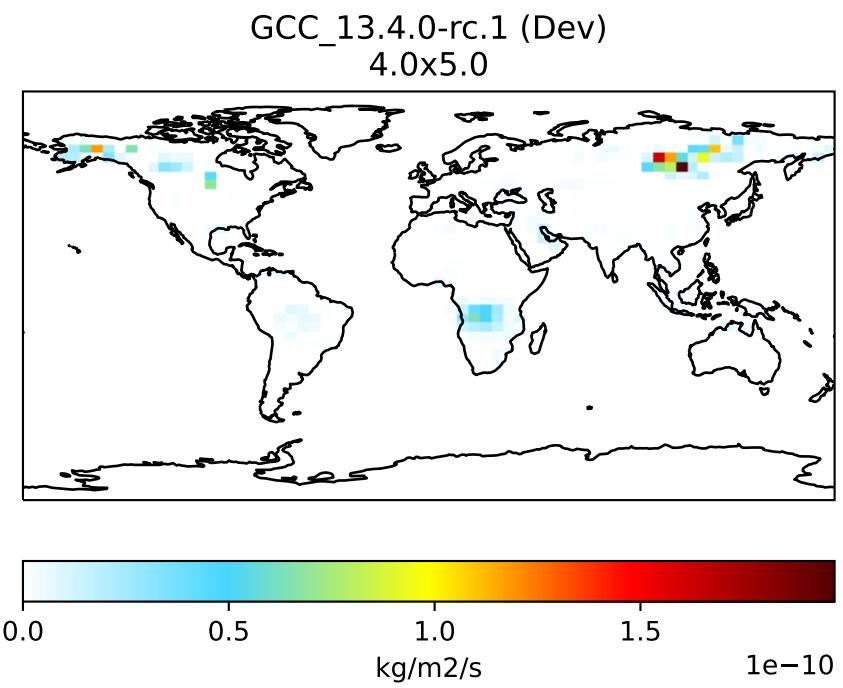
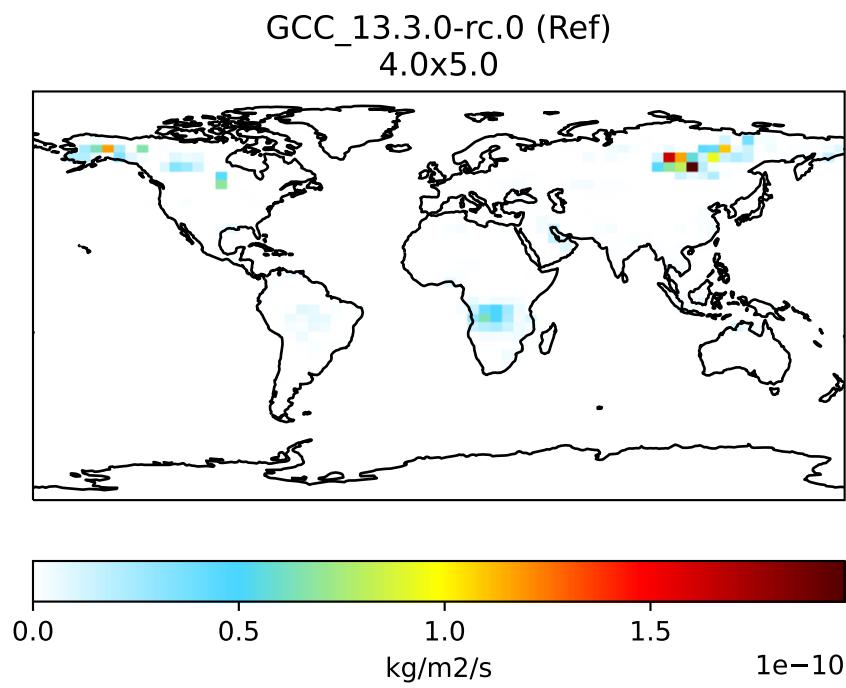
Undefined throughout domain  
unitless

Ratio  
Dev/Ref, Fixed Range

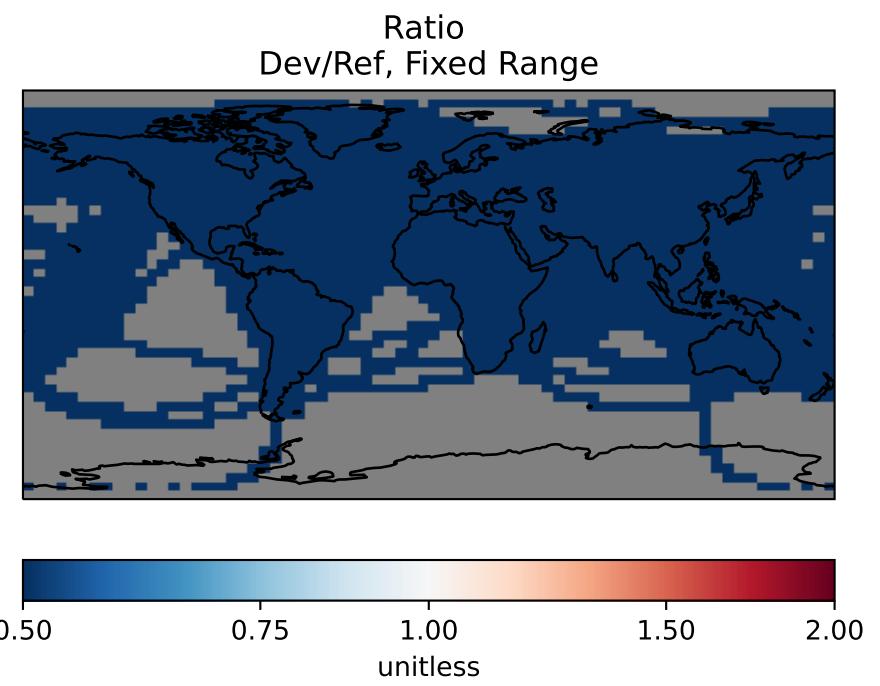
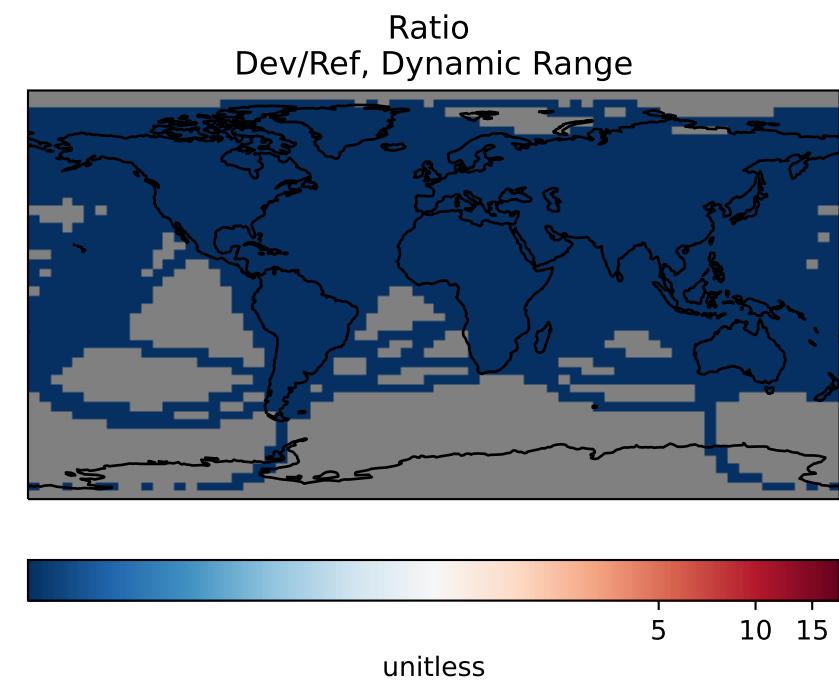
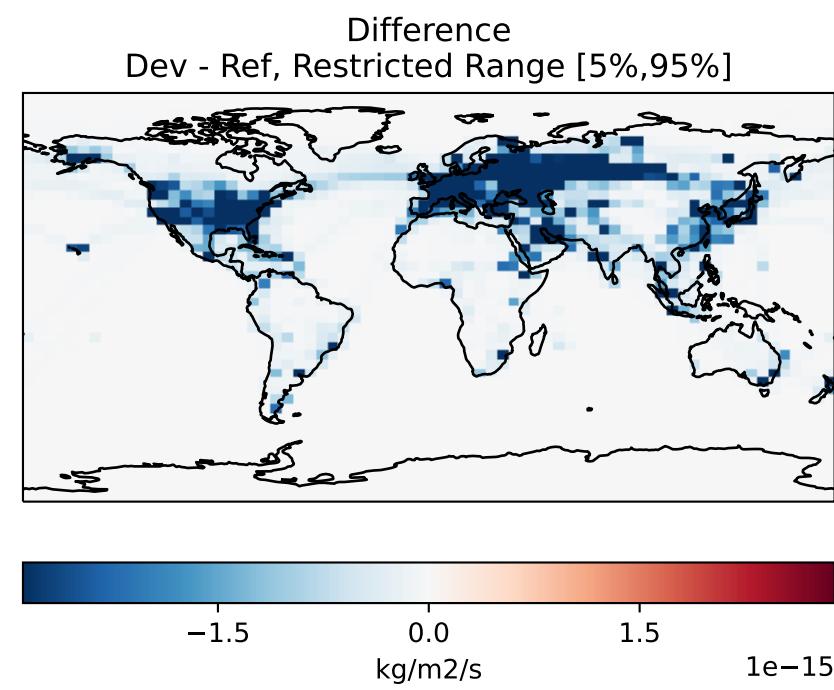
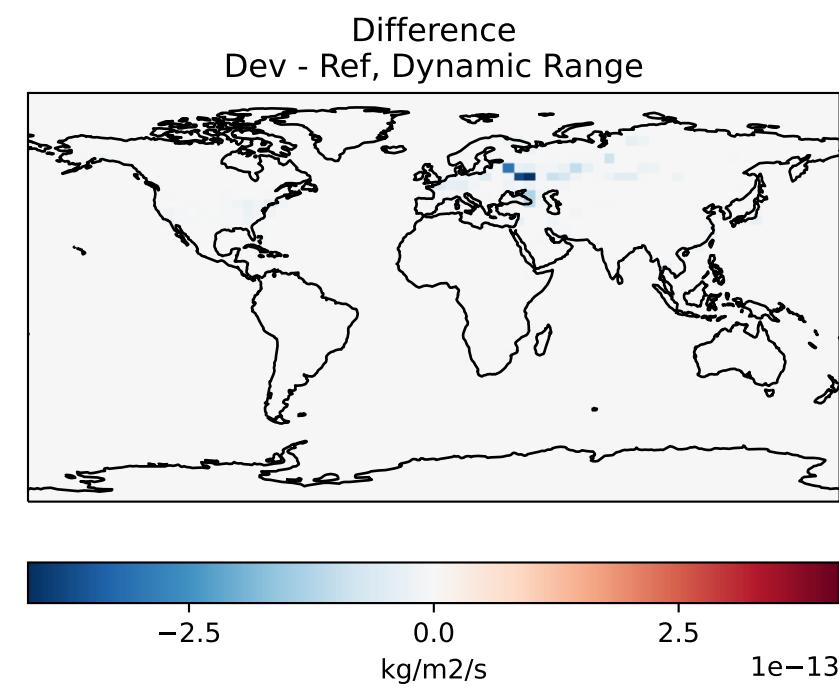
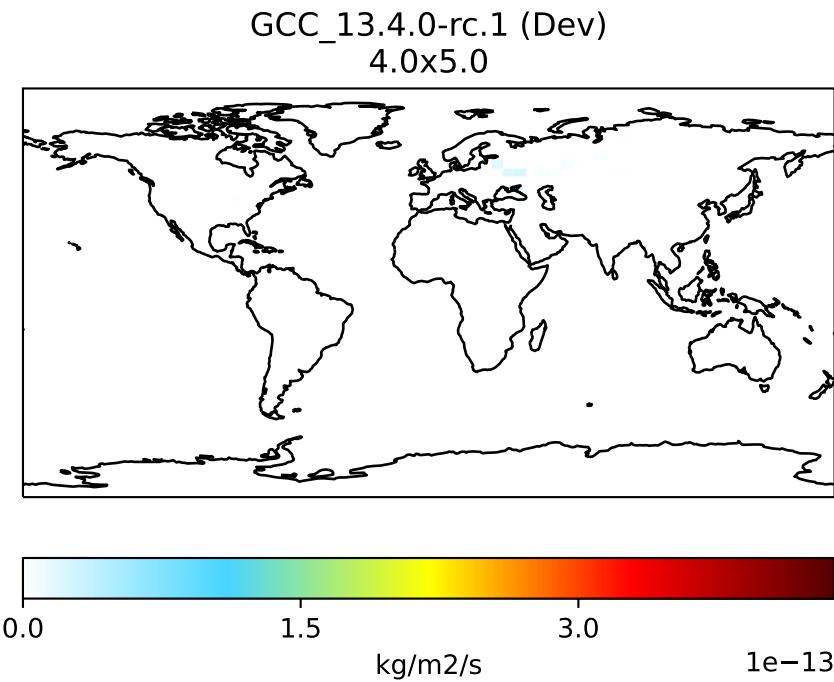
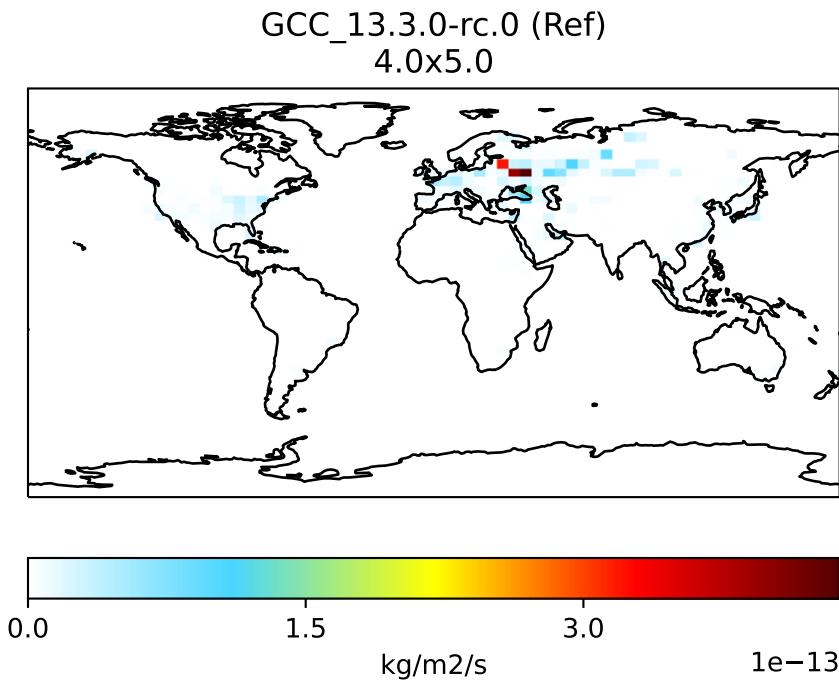


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unitless

# EmisCH2O\_Total (Jul2019)

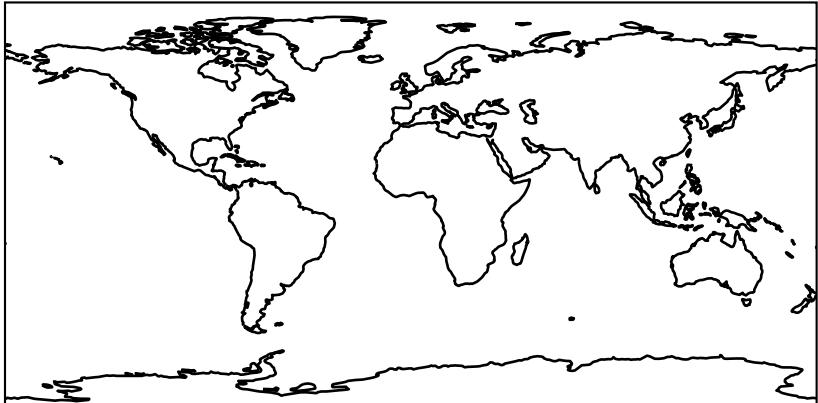


# EmisMACR\_Aircraft (Jul2019)



# EmisMACR\_Anthro (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



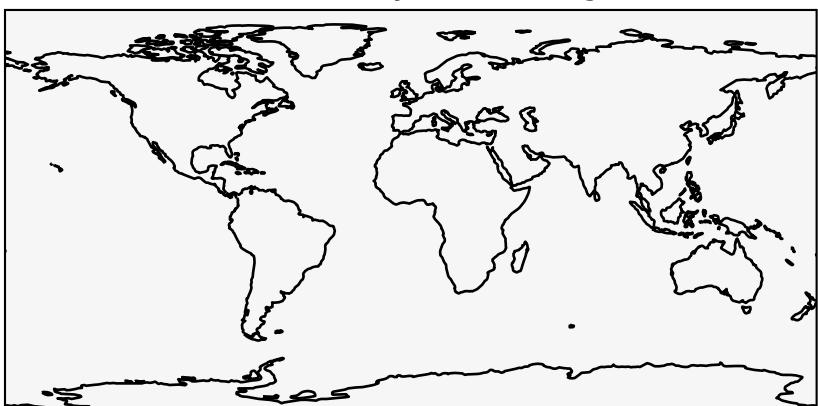
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kg/m<sup>2</sup>/s

GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



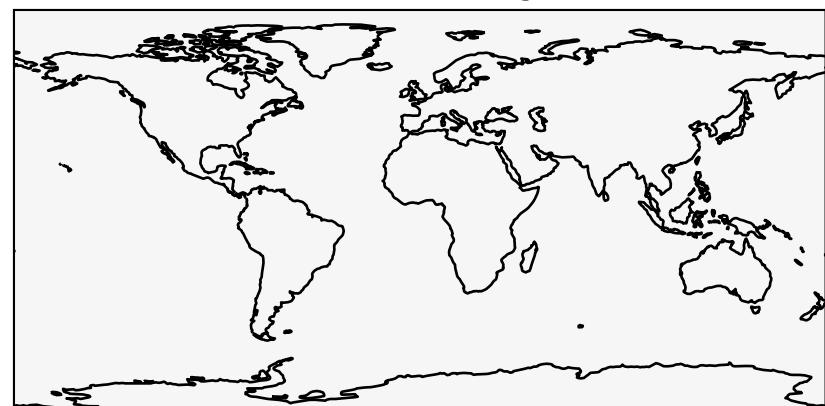
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kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Dynamic Range



Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



Zero throughout domain  
kg/m<sup>2</sup>/s

Ratio  
Dev/Ref, Dynamic Range



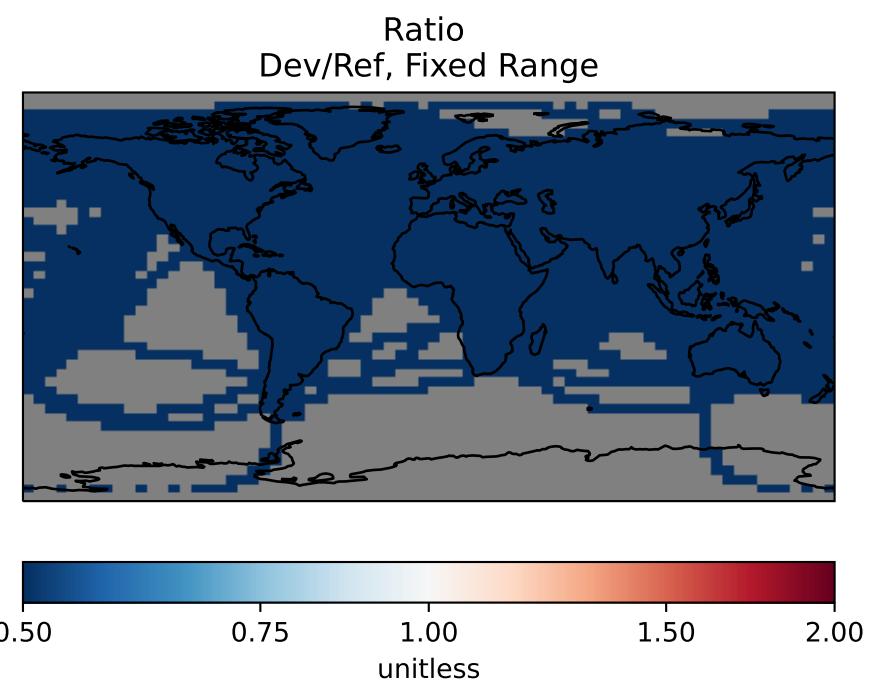
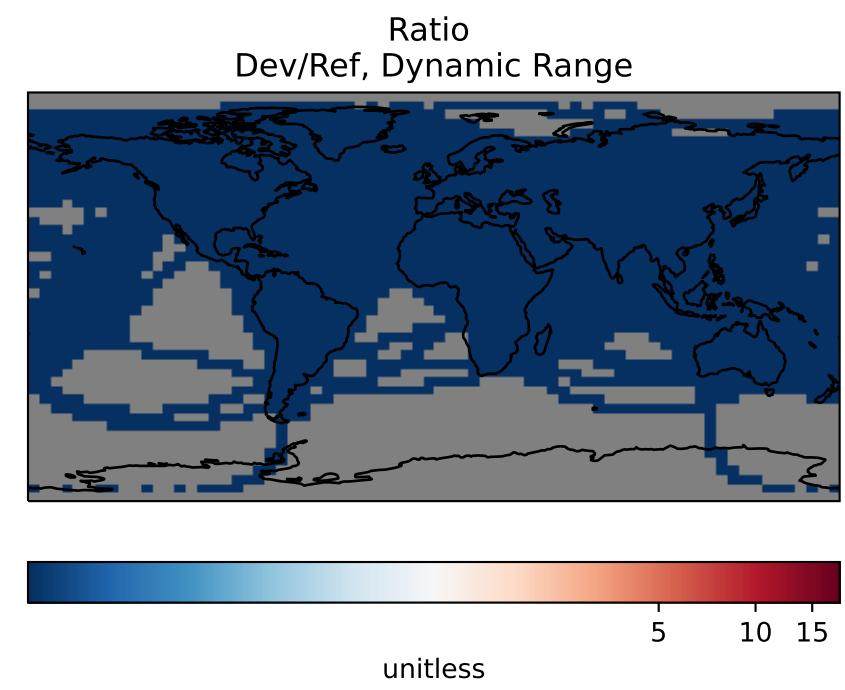
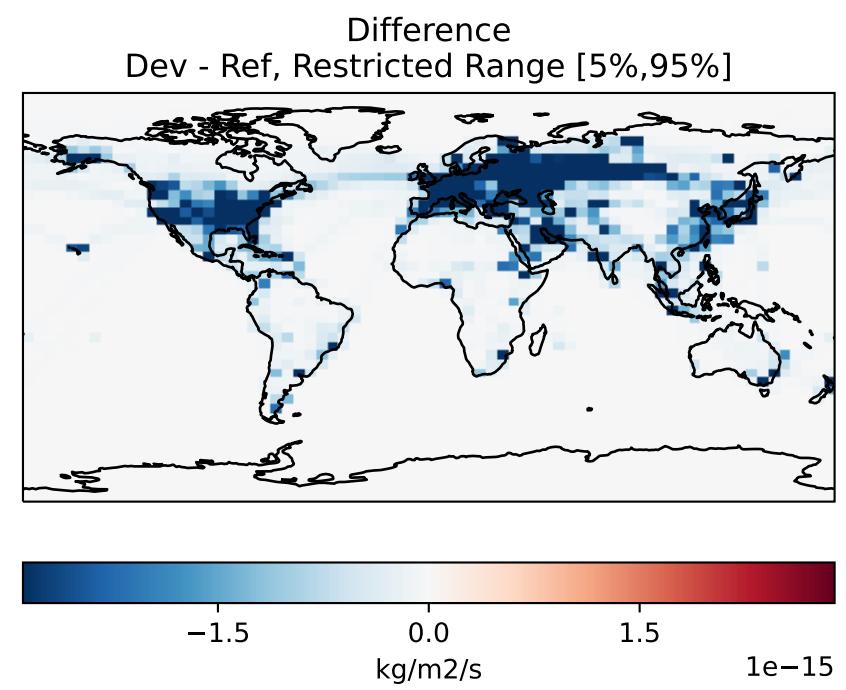
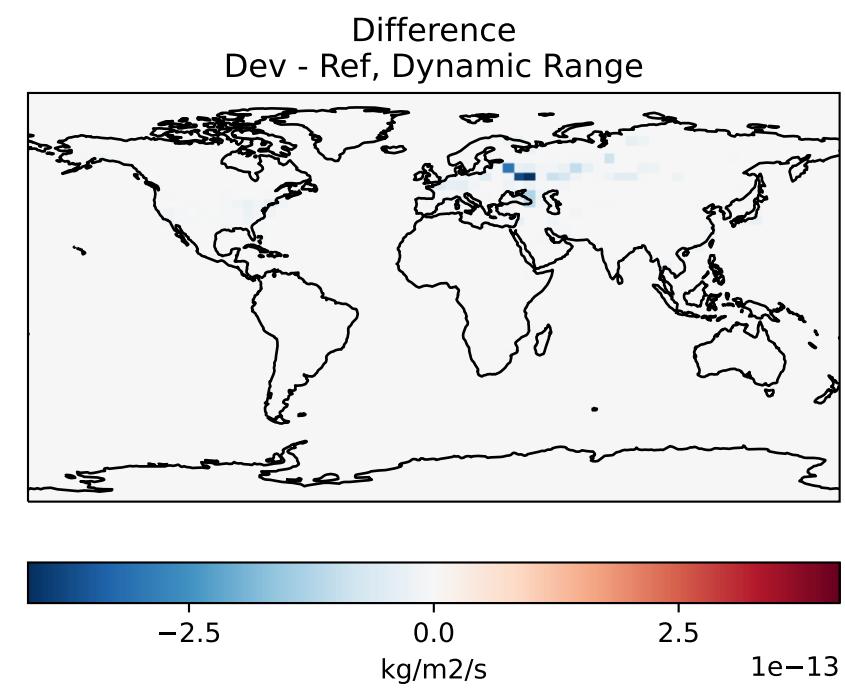
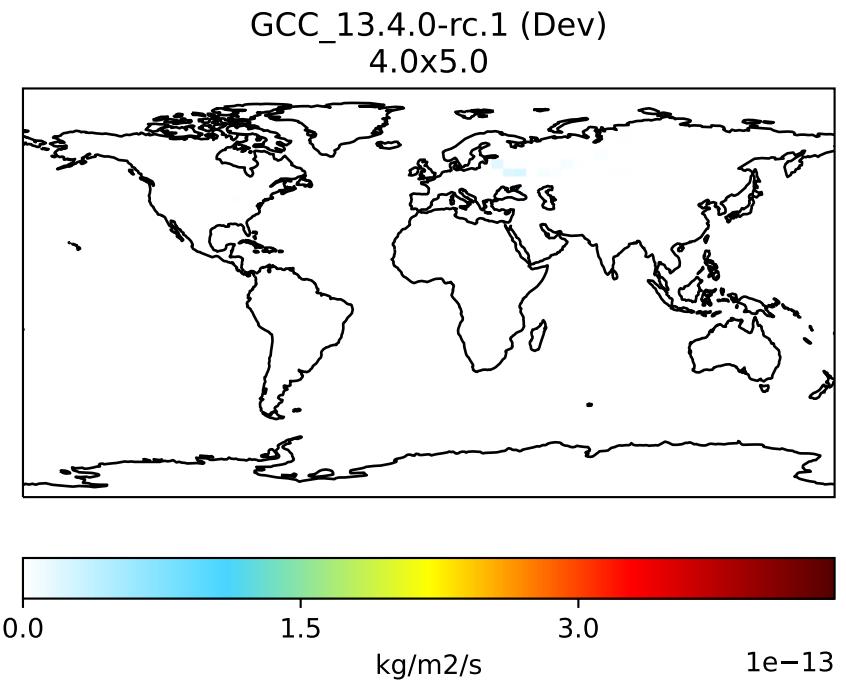
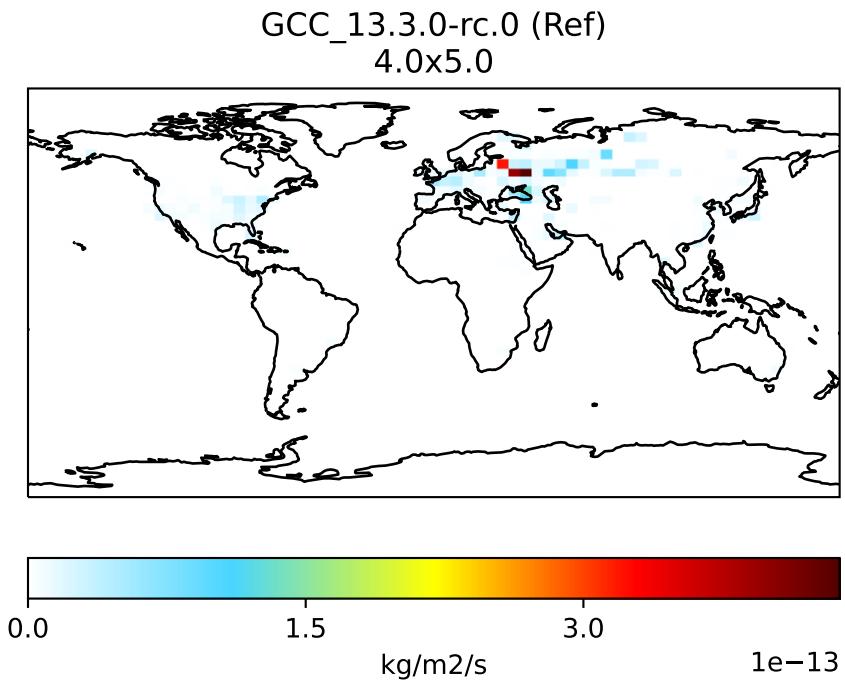
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unitless

Ratio  
Dev/Ref, Fixed Range



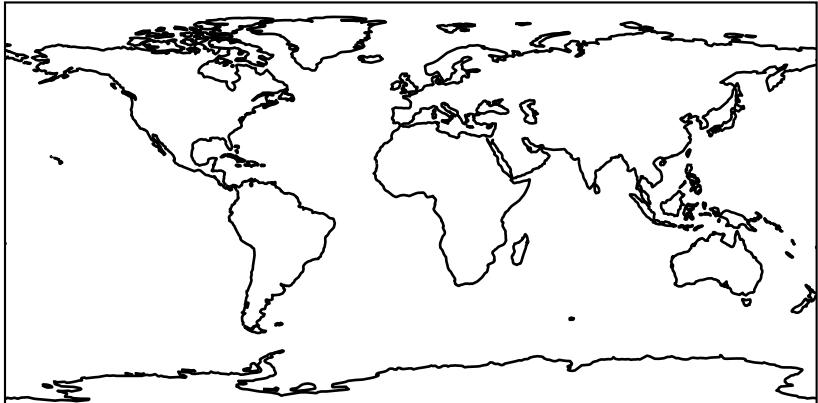
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unitless

# EmisMACR\_Total (Jul2019)



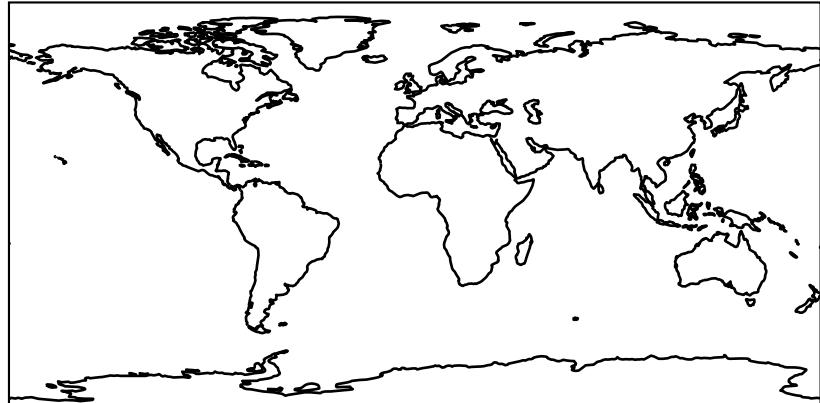
# EmisACET\_Anthro (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



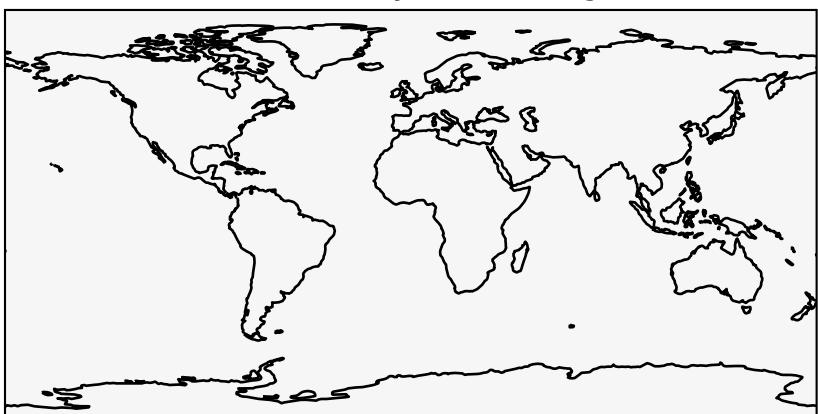
Zero throughout domain  
kg/m<sup>2</sup>/s

GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



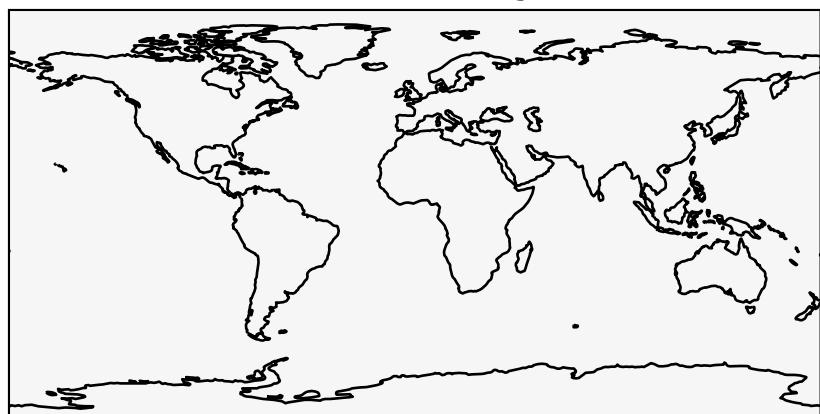
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Dynamic Range



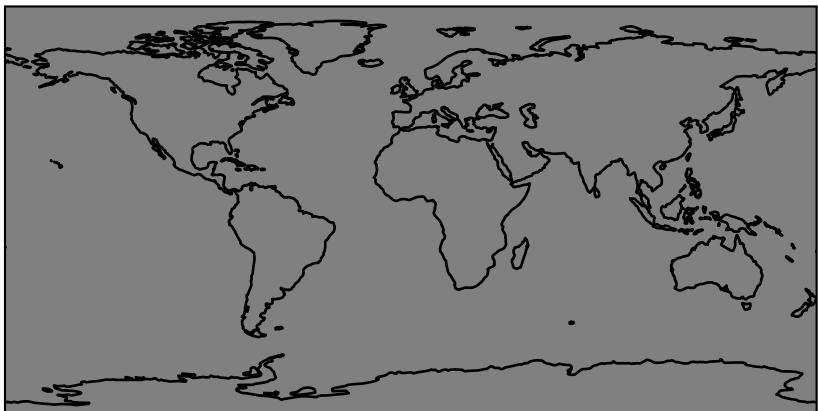
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



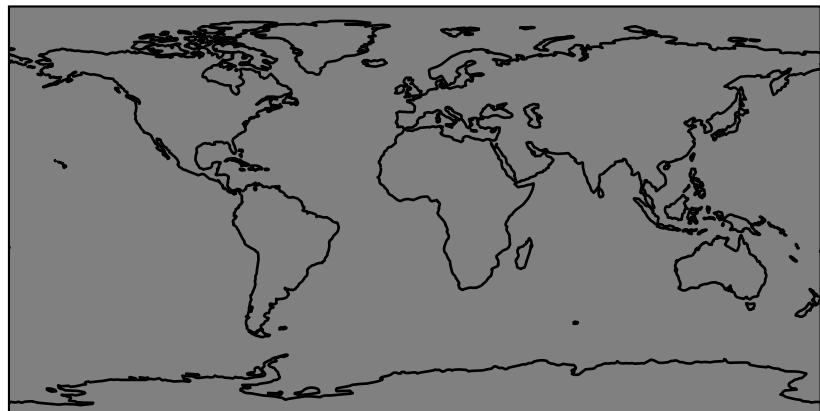
Zero throughout domain  
kg/m<sup>2</sup>/s

Ratio  
Dev/Ref, Dynamic Range



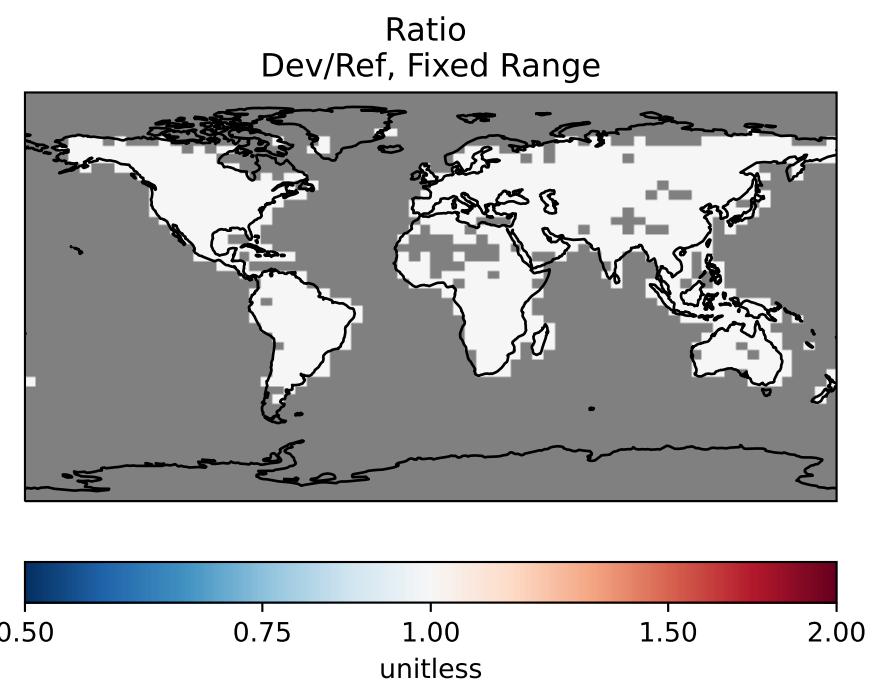
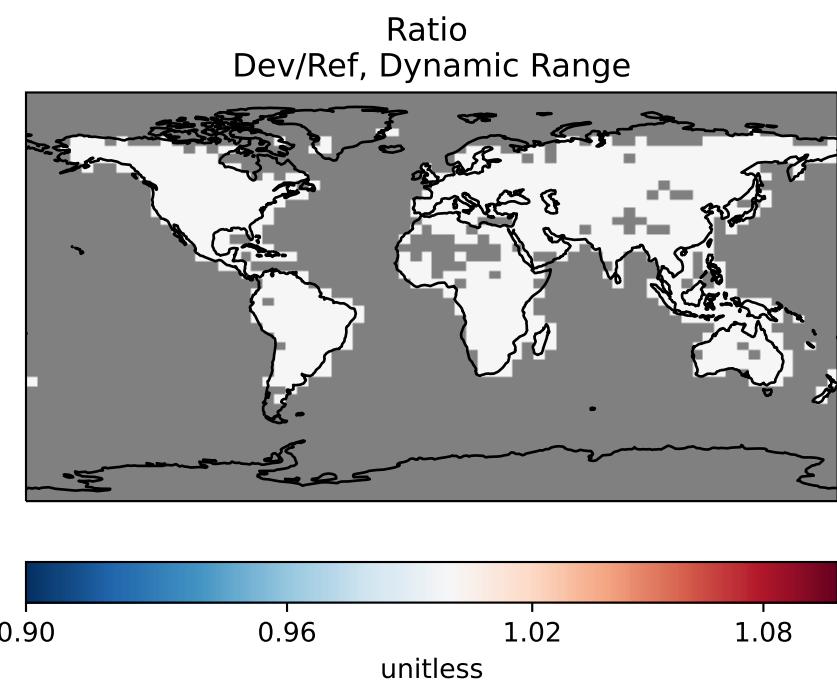
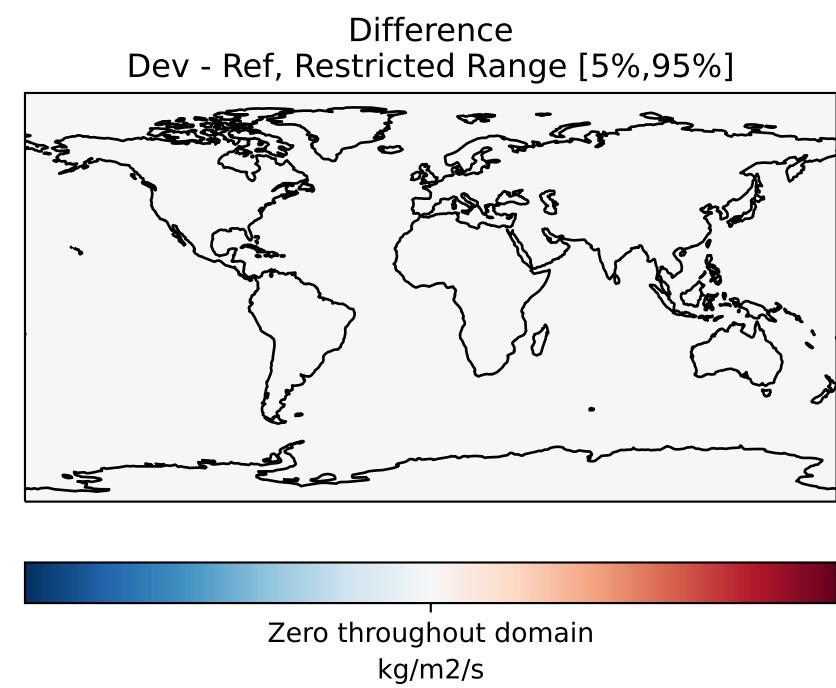
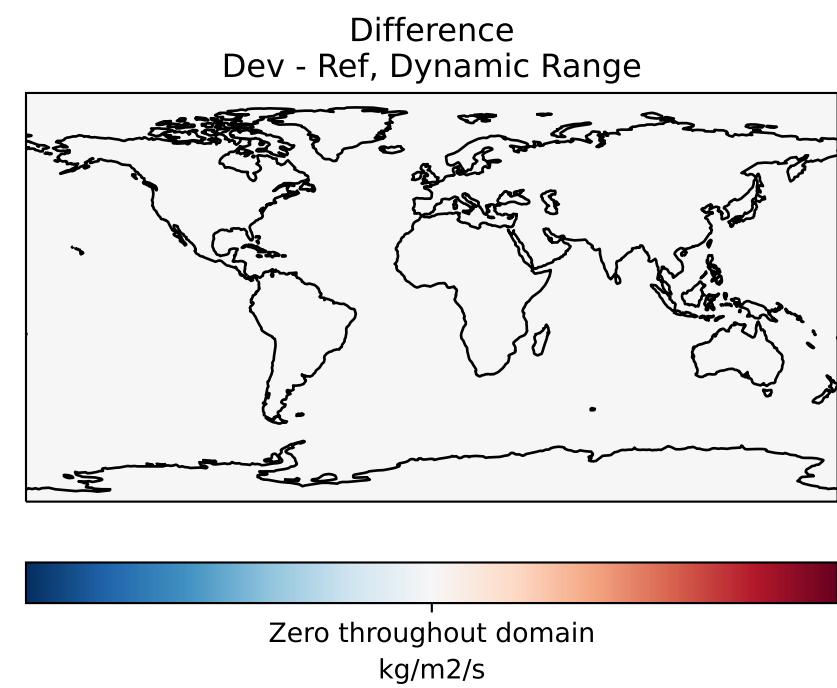
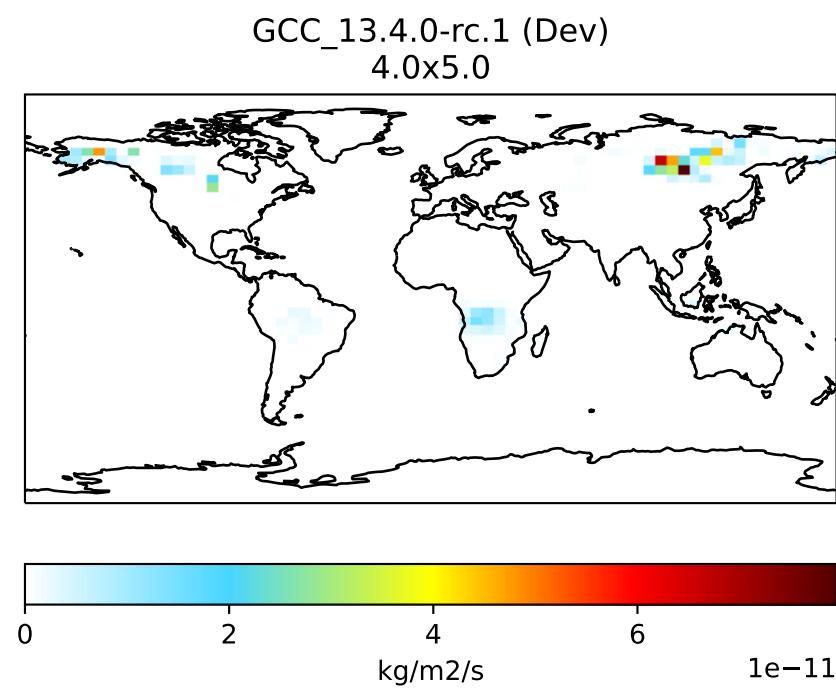
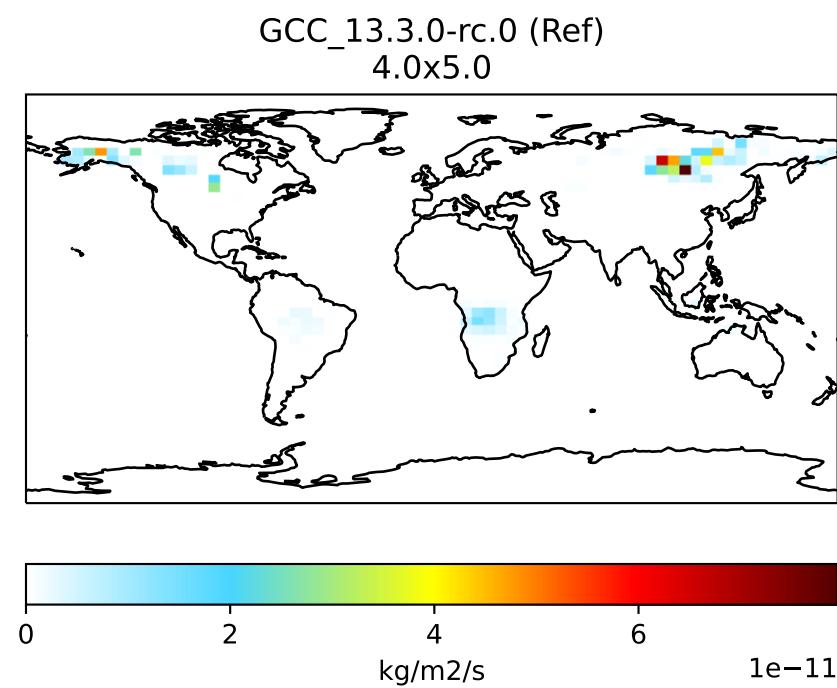
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unitless

Ratio  
Dev/Ref, Fixed Range



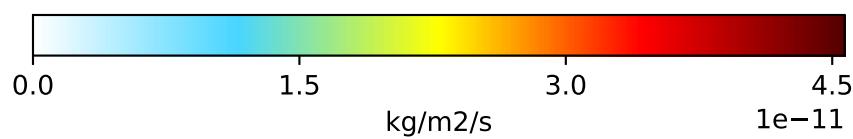
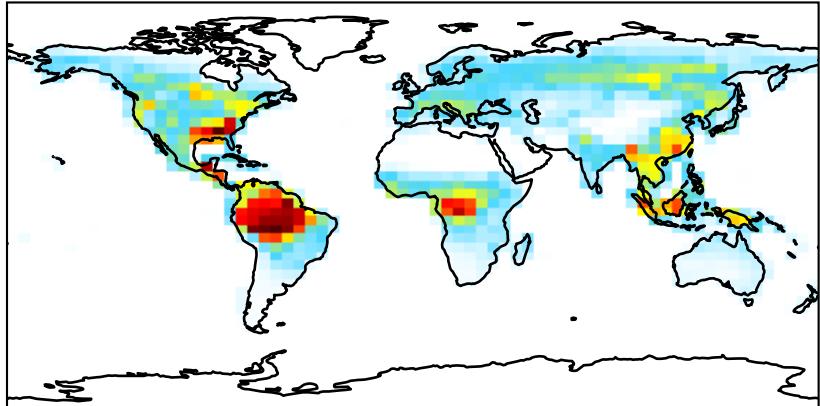
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# EmisACET\_BioBurn (Jul2019)

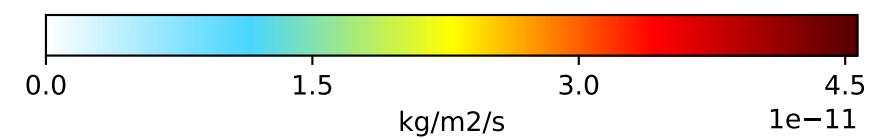
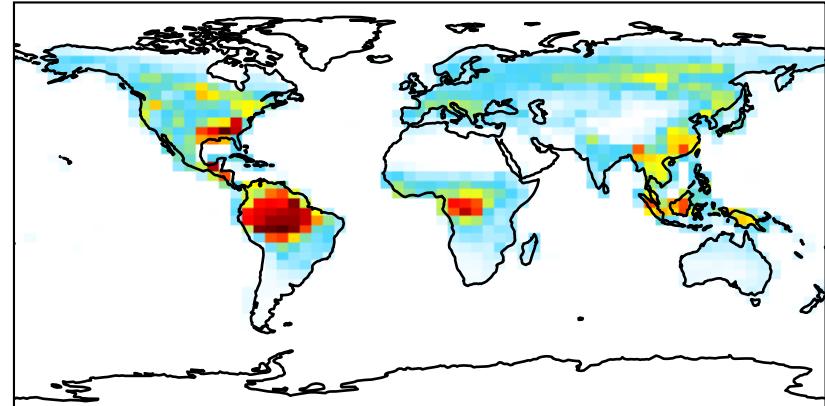


# EmisACET\_Biogenic (Jul2019)

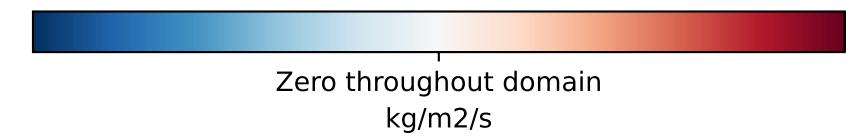
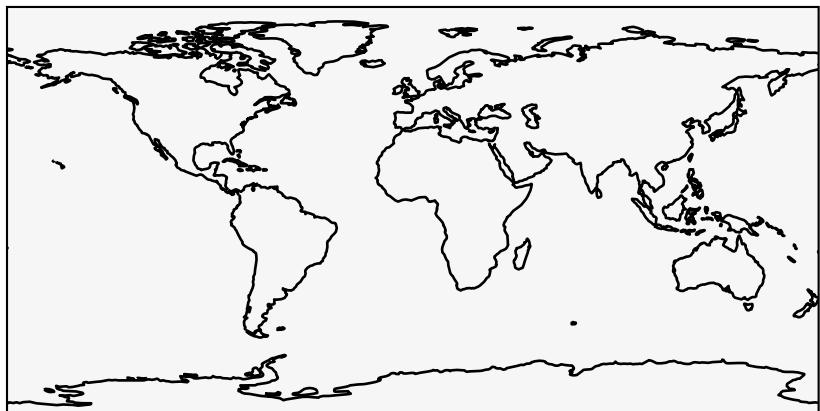
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



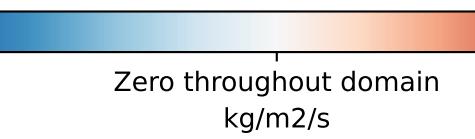
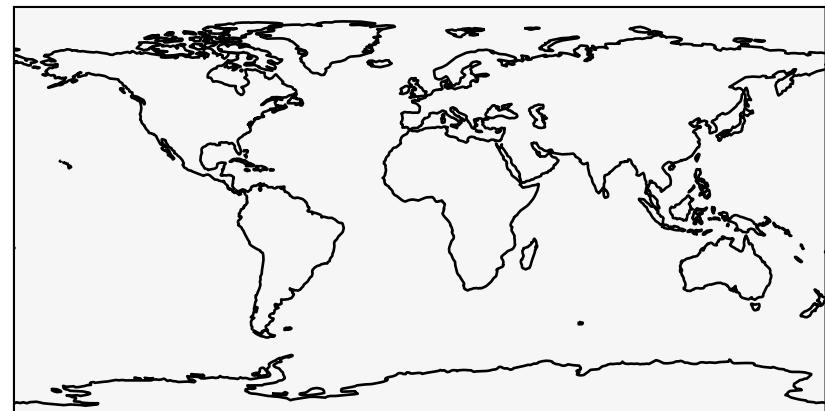
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



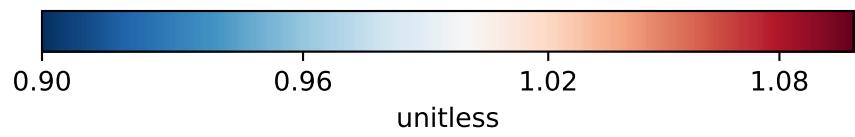
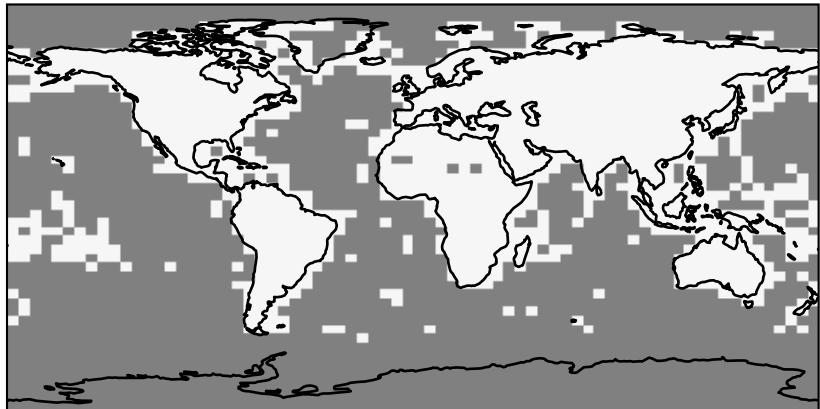
Difference  
Dev - Ref, Dynamic Range



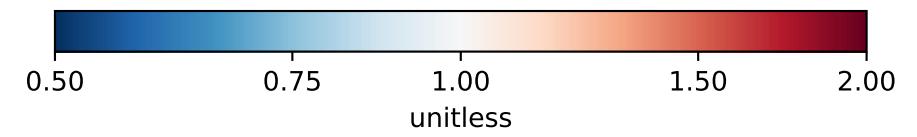
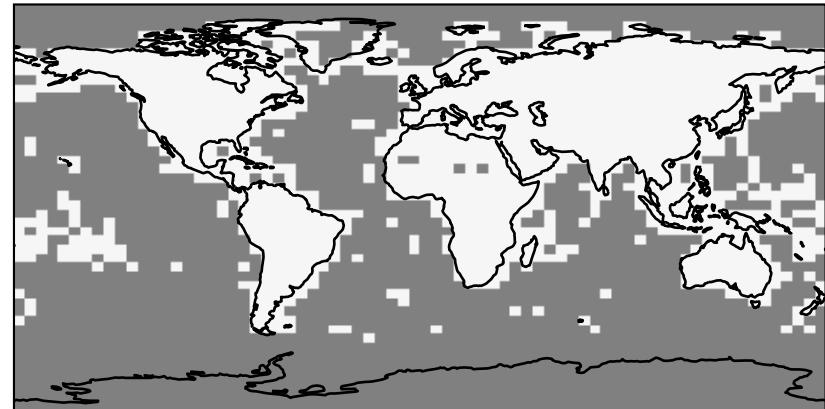
Difference  
Dev - Ref, Restricted Range [5%, 95%]



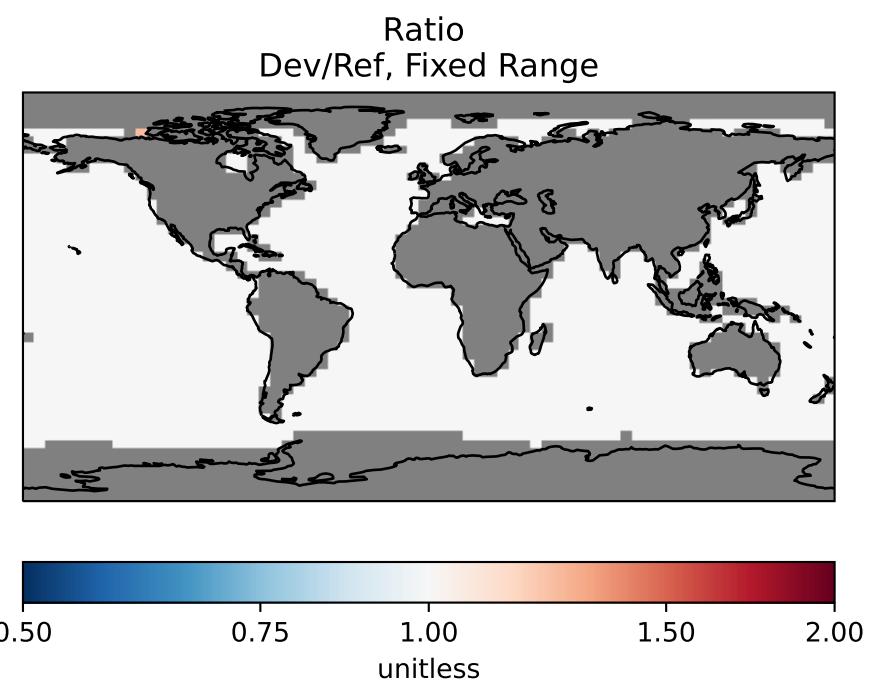
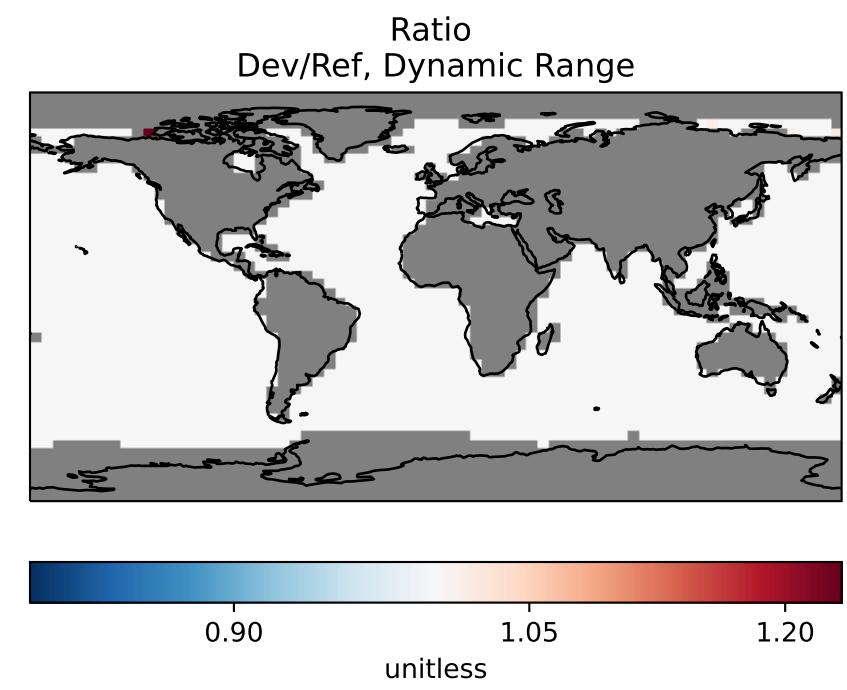
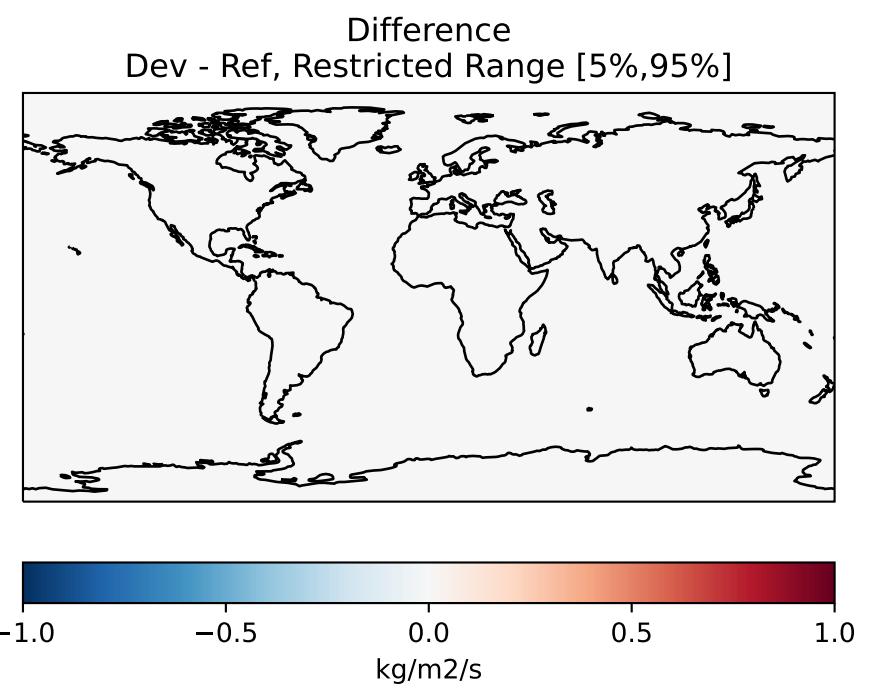
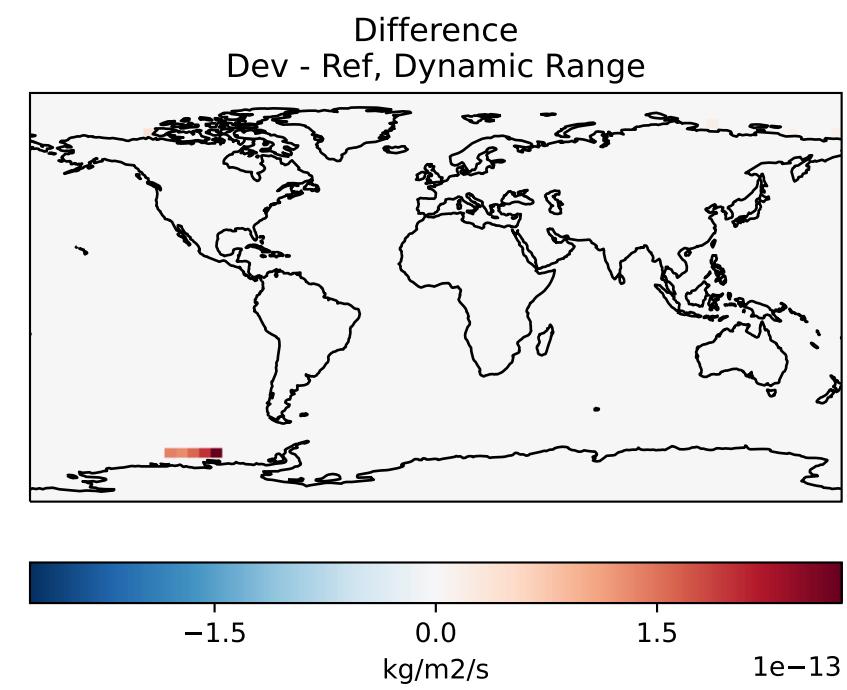
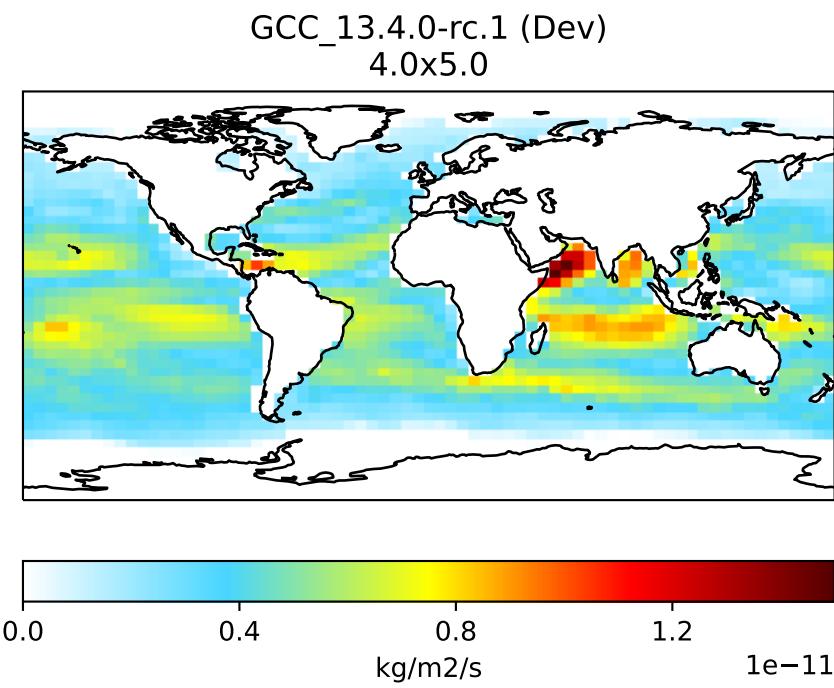
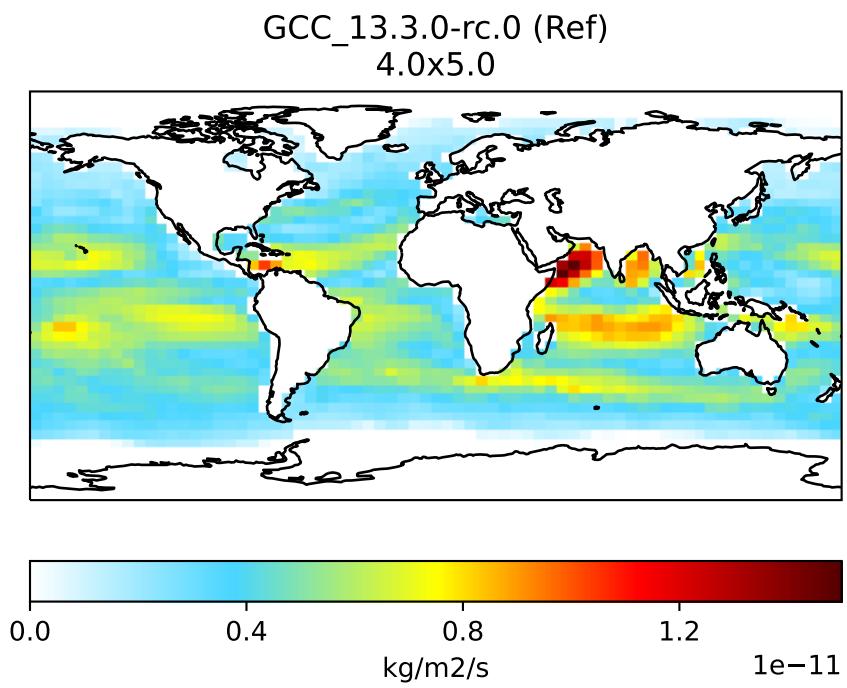
Ratio  
Dev/Ref, Dynamic Range



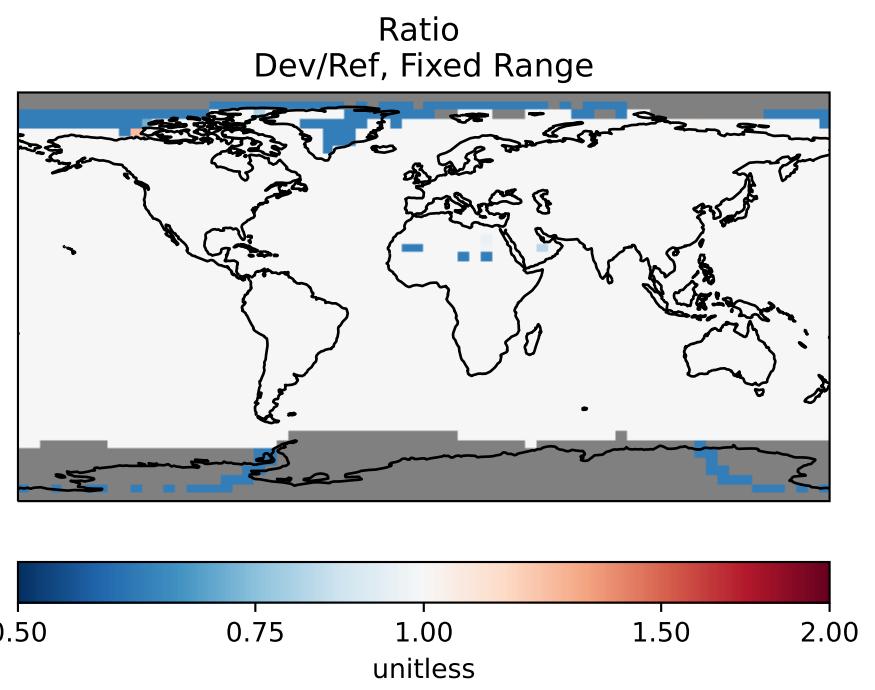
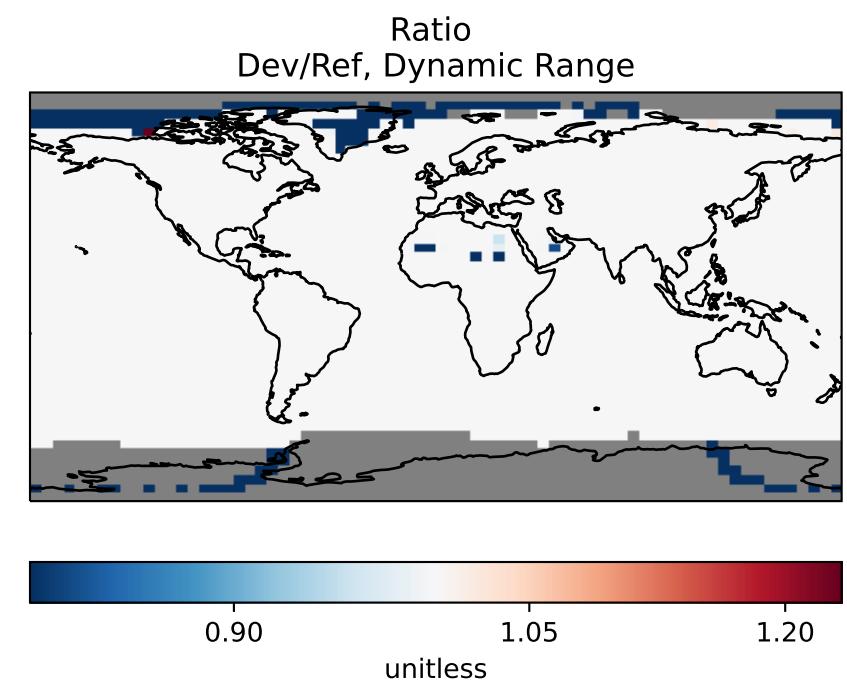
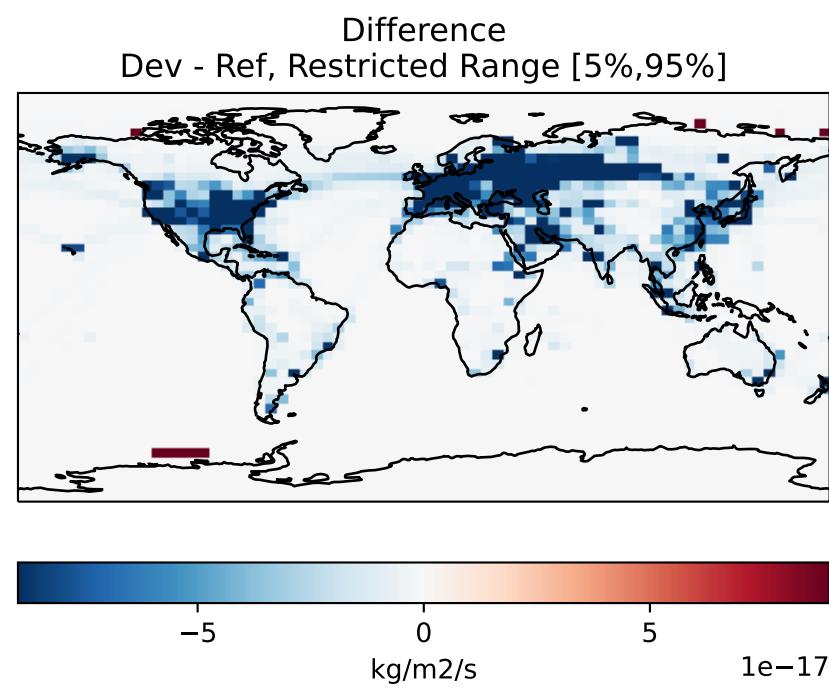
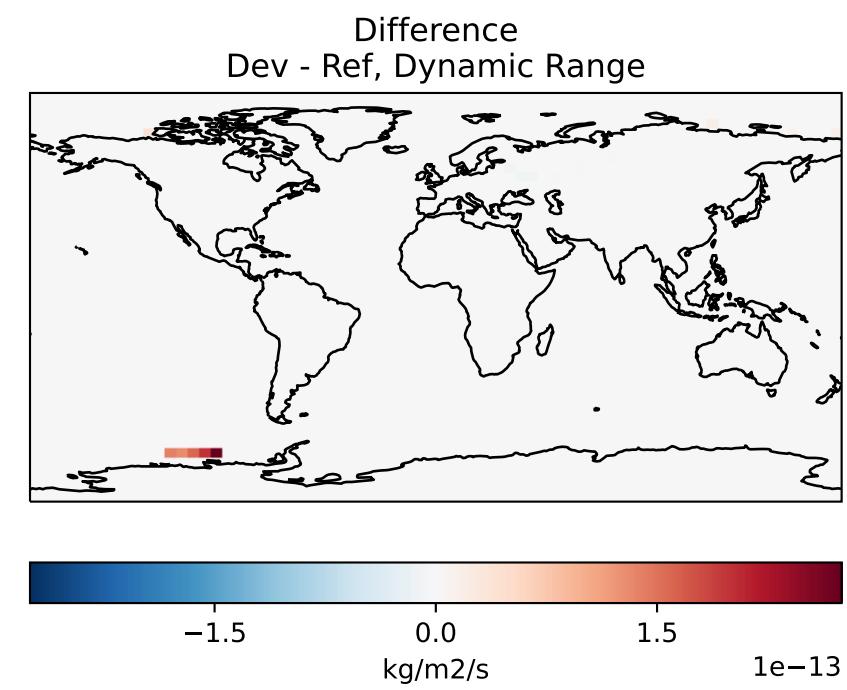
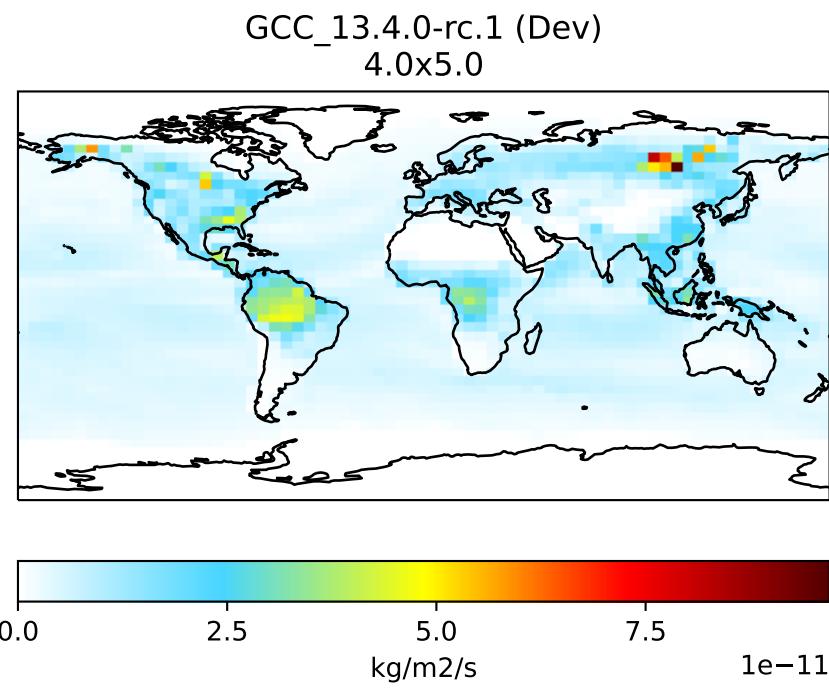
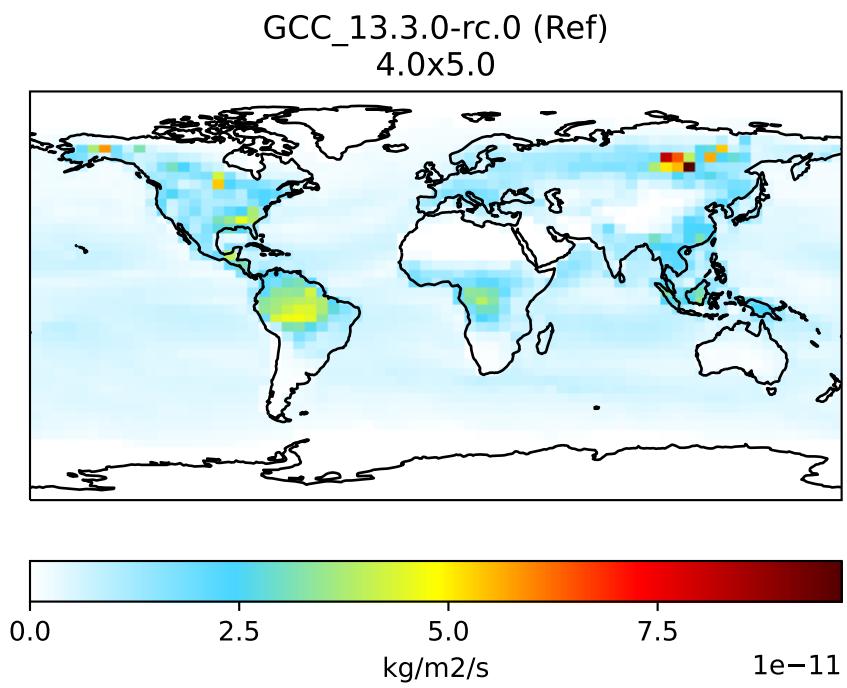
Ratio  
Dev/Ref, Fixed Range



# EmisACET\_Ocean (Jul2019)

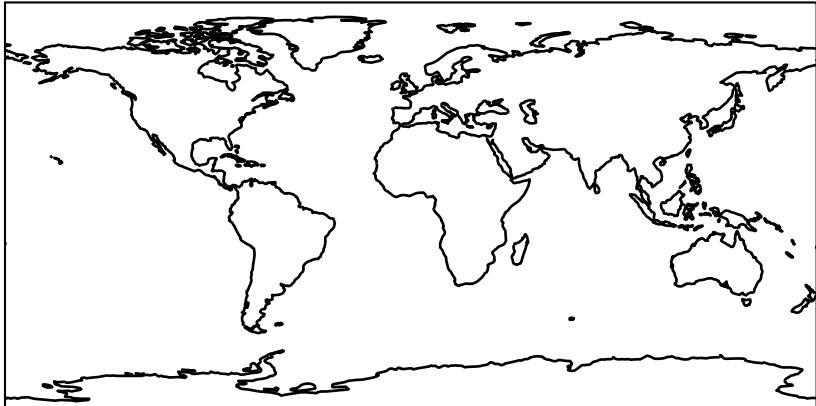


# EmisACET\_Total (Jul2019)



# EmisHAC\_Anthro (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



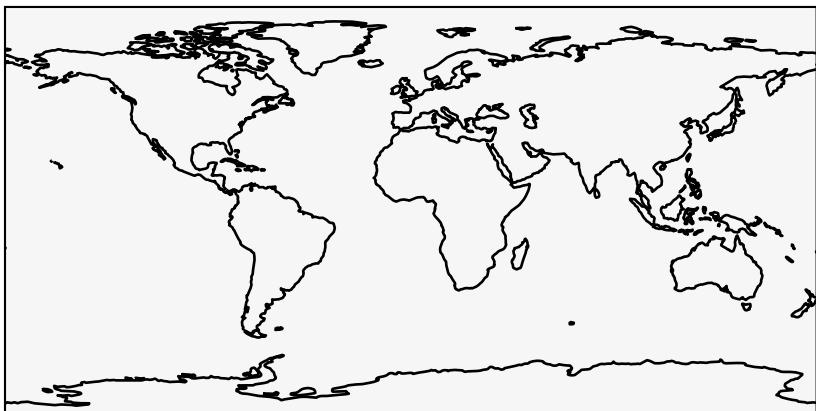
Zero throughout domain  
kg/m<sup>2</sup>/s

GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



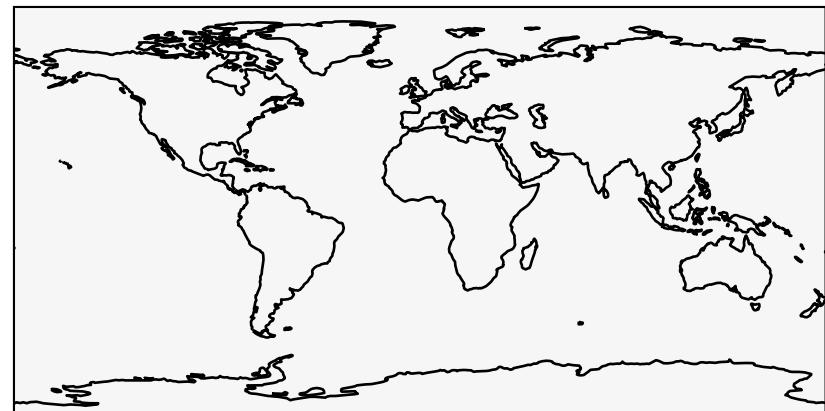
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Dynamic Range



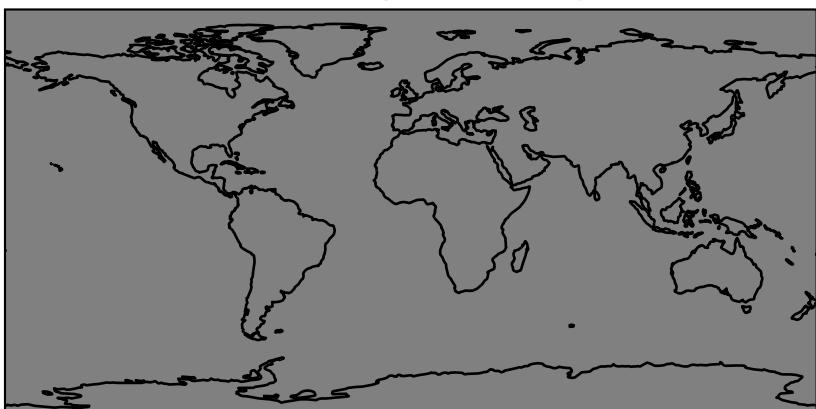
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



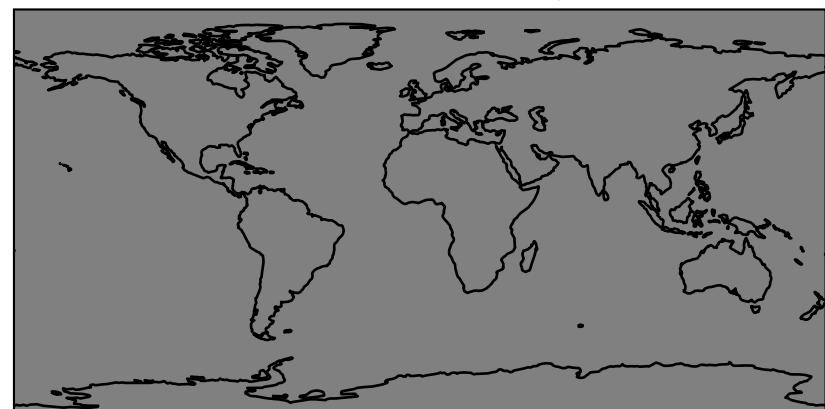
Zero throughout domain  
kg/m<sup>2</sup>/s

Ratio  
Dev/Ref, Dynamic Range



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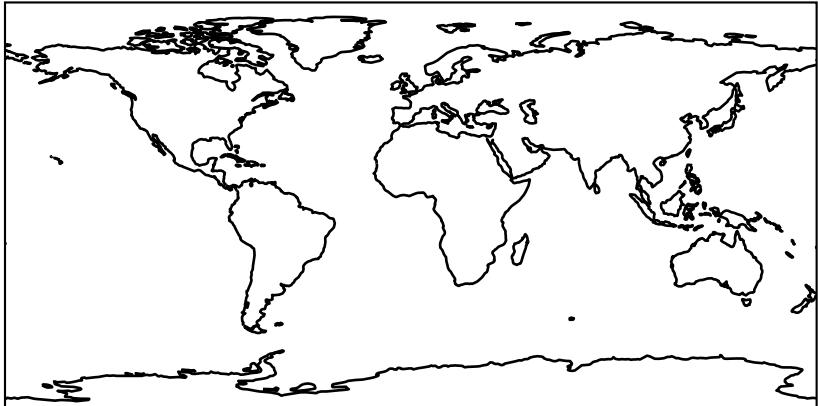
Ratio  
Dev/Ref, Fixed Range



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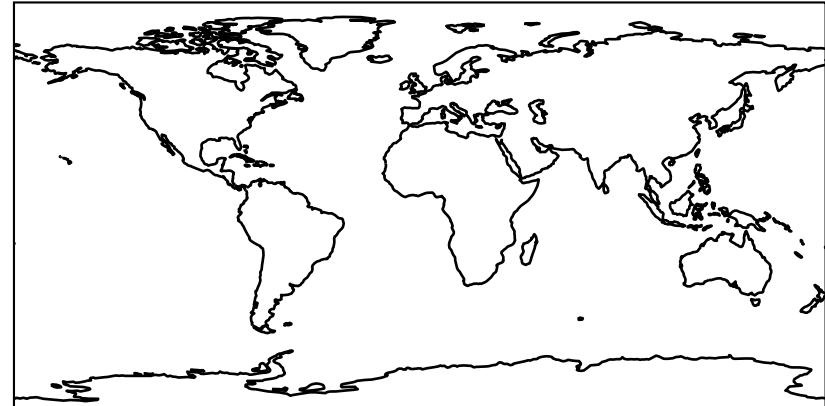
# EmisHAC\_Total (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



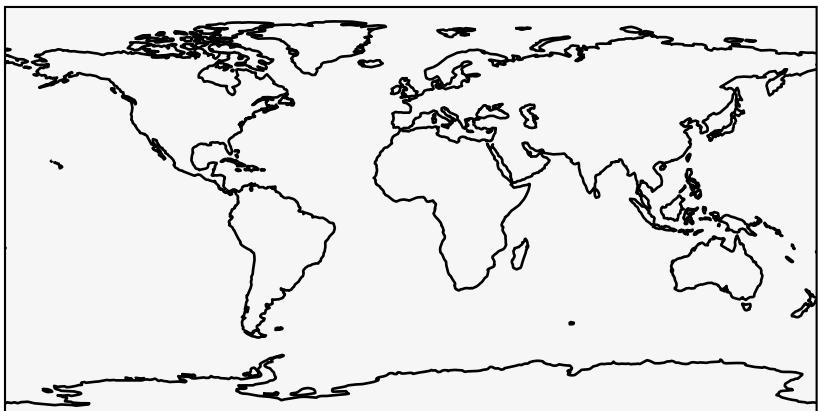
Zero throughout domain  
kg/m<sup>2</sup>/s

GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



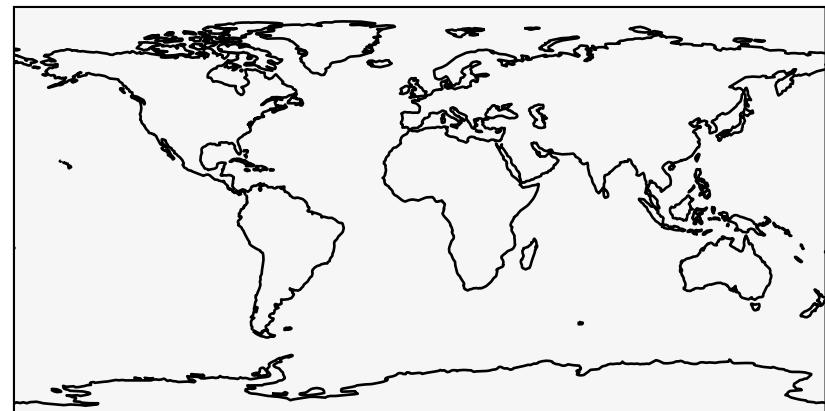
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Dynamic Range



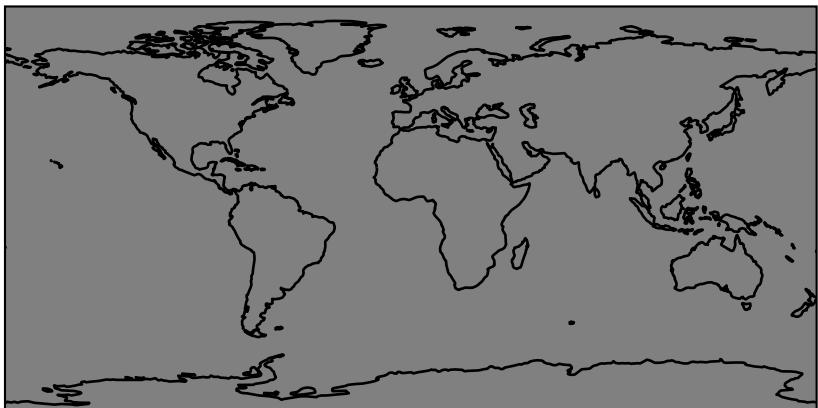
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



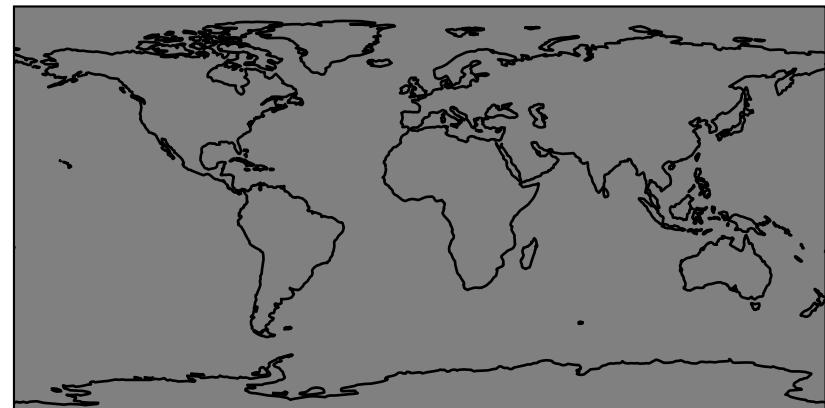
Zero throughout domain  
kg/m<sup>2</sup>/s

Ratio  
Dev/Ref, Dynamic Range



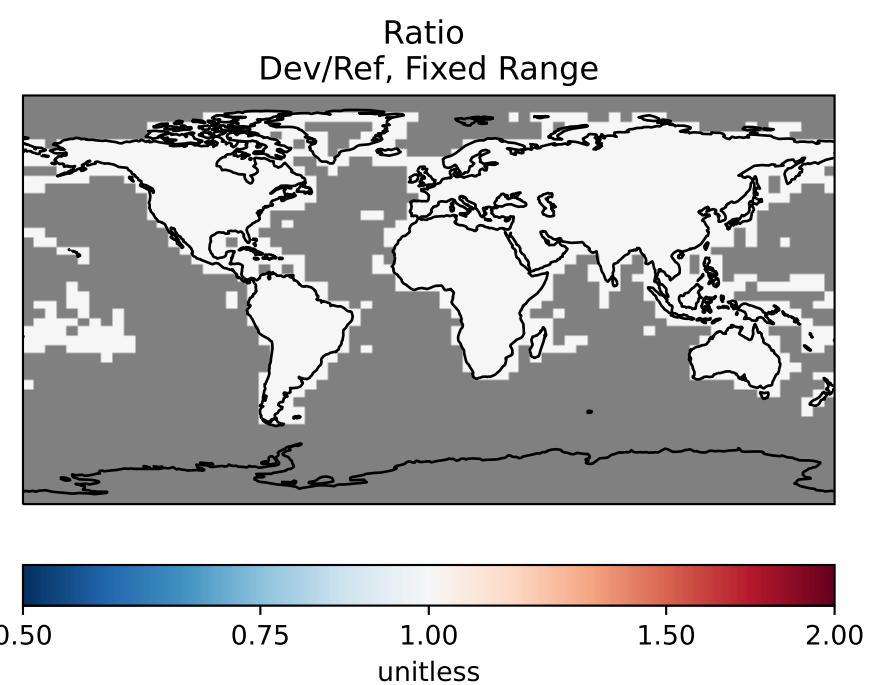
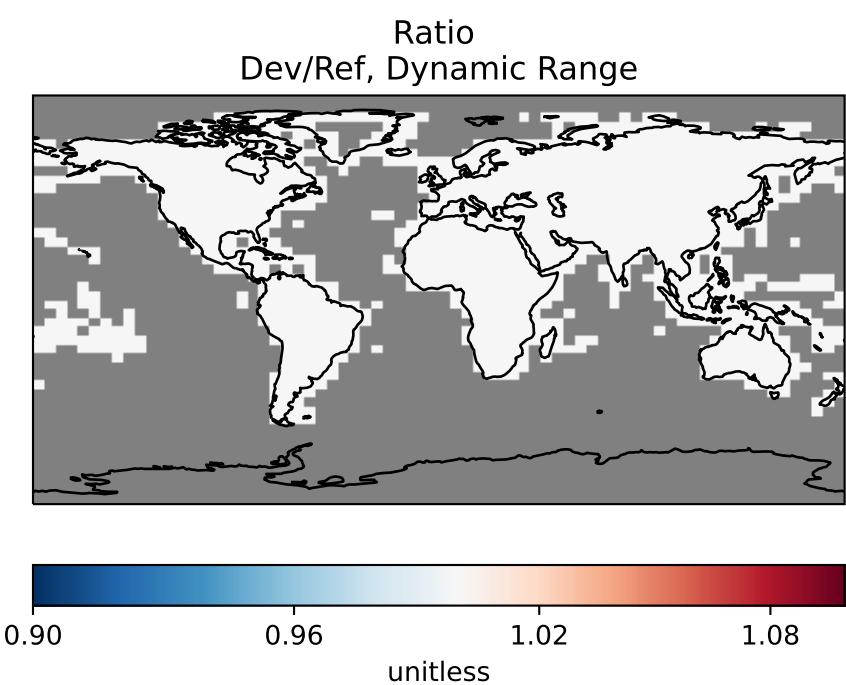
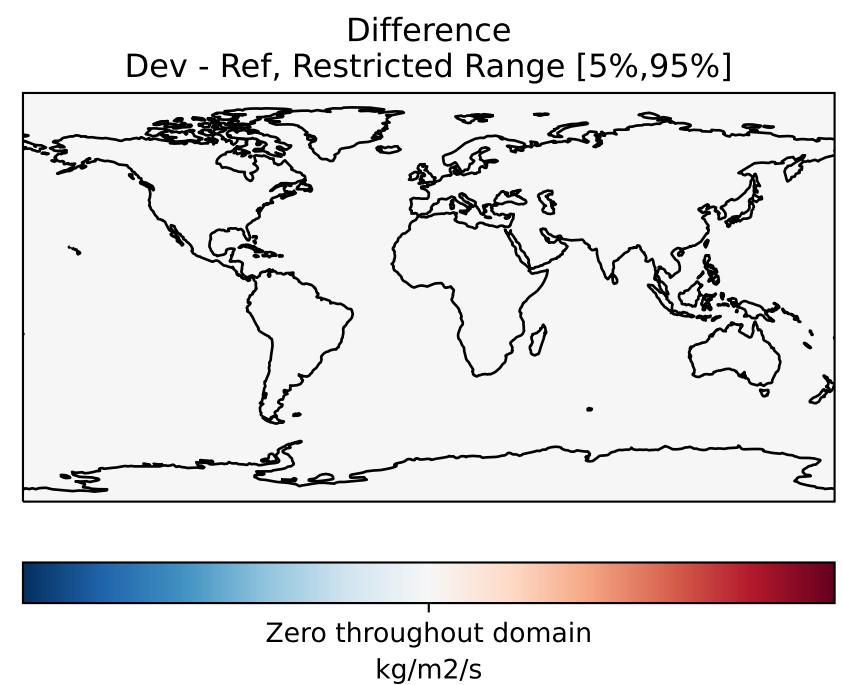
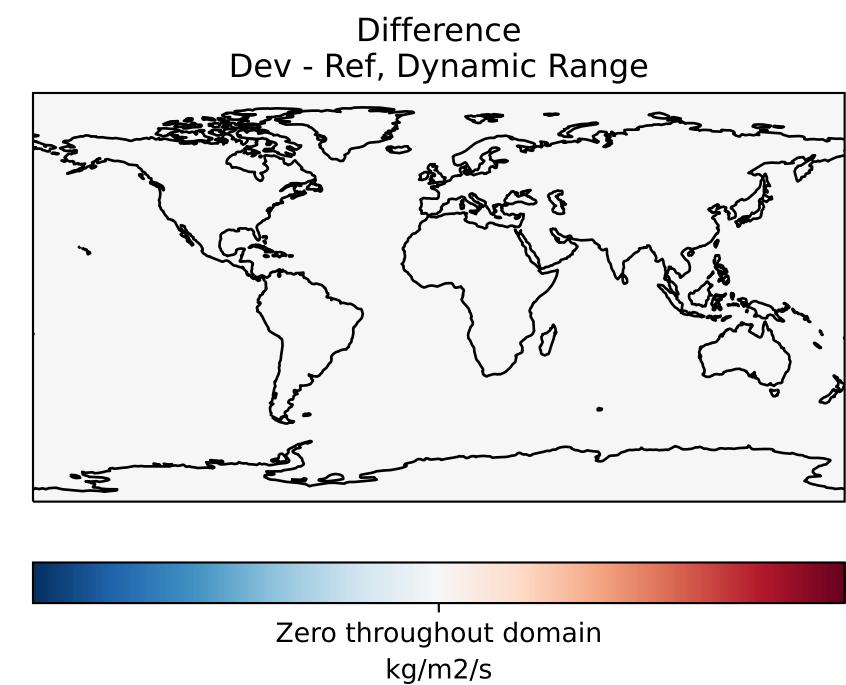
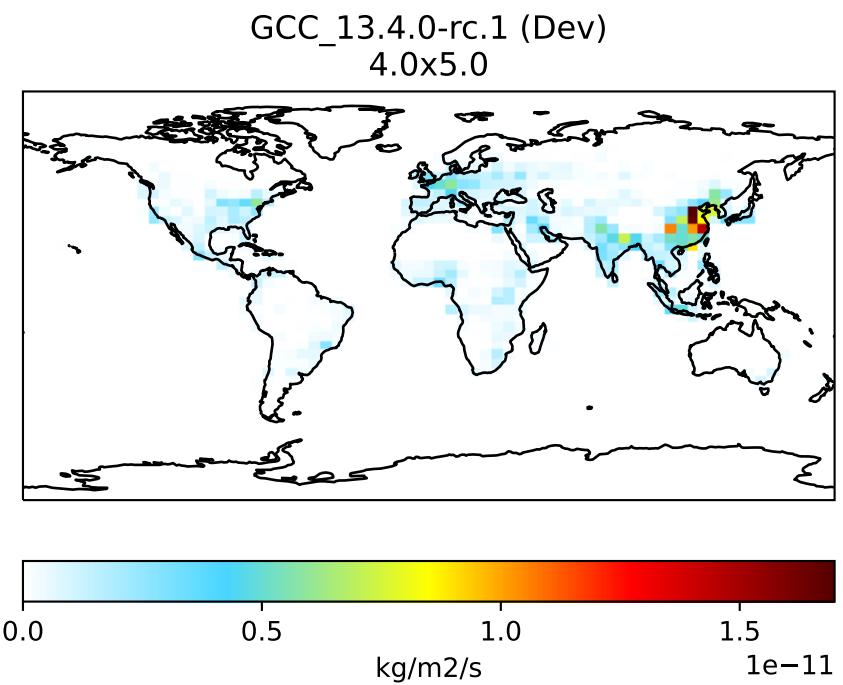
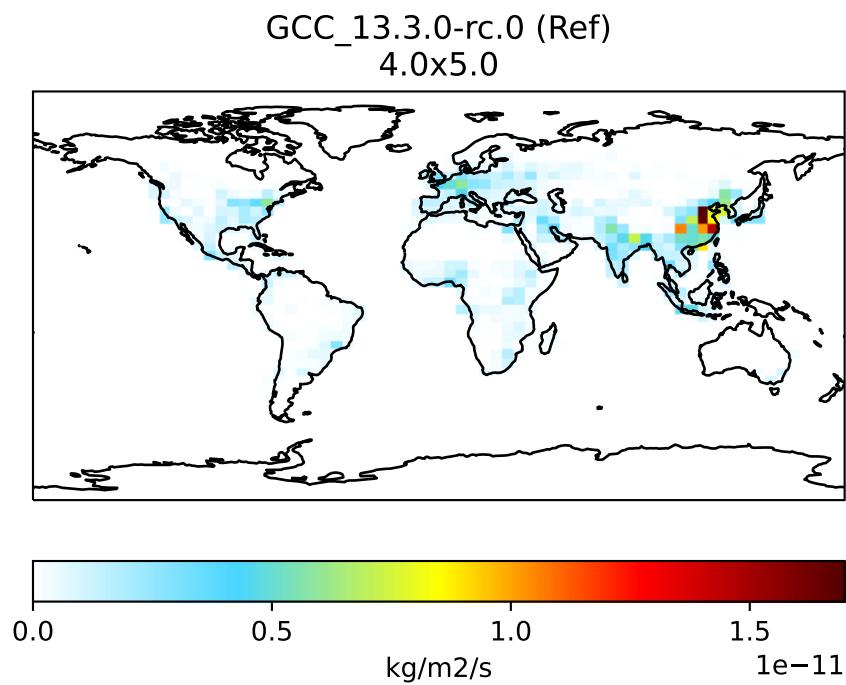
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Ratio  
Dev/Ref, Fixed Range



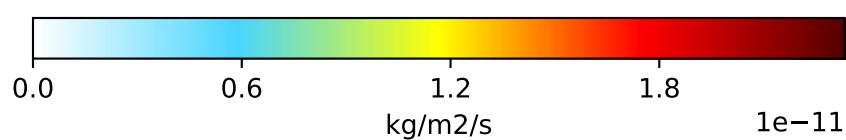
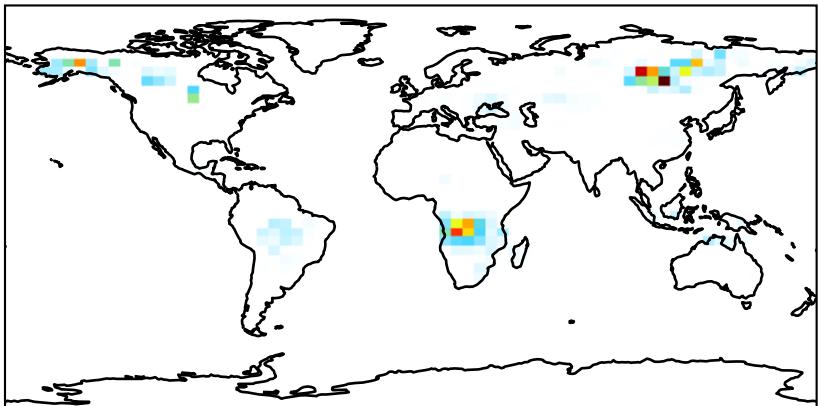
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# EmisMEK\_Anthro (Jul2019)

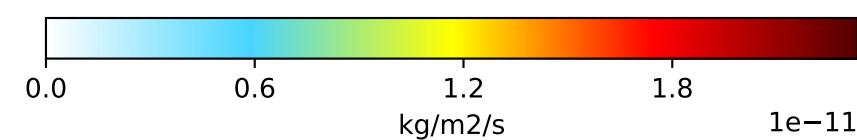
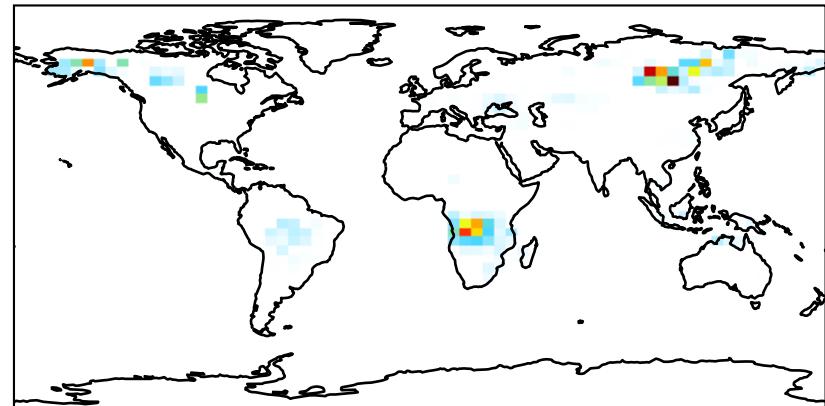


# EmisMEK\_BioBurn (Jul2019)

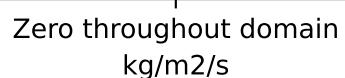
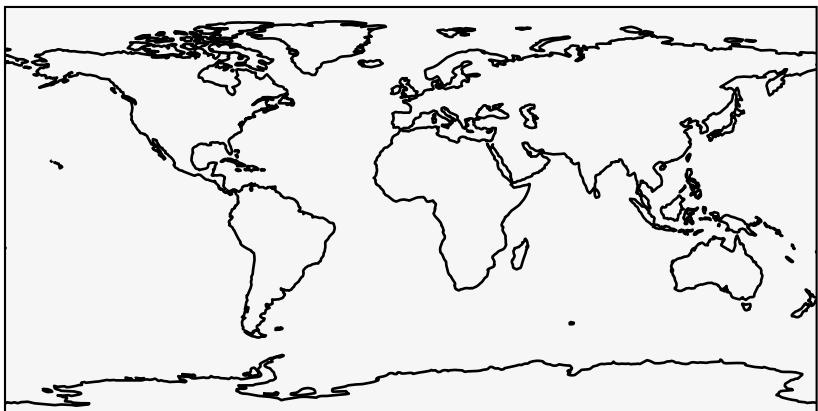
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



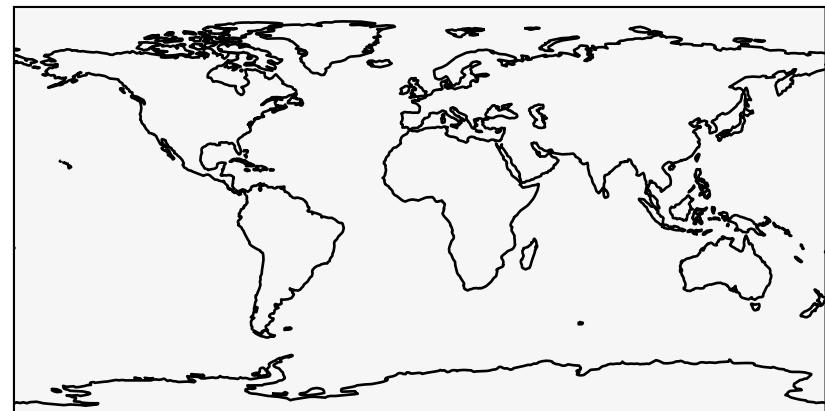
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



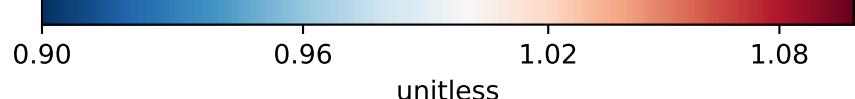
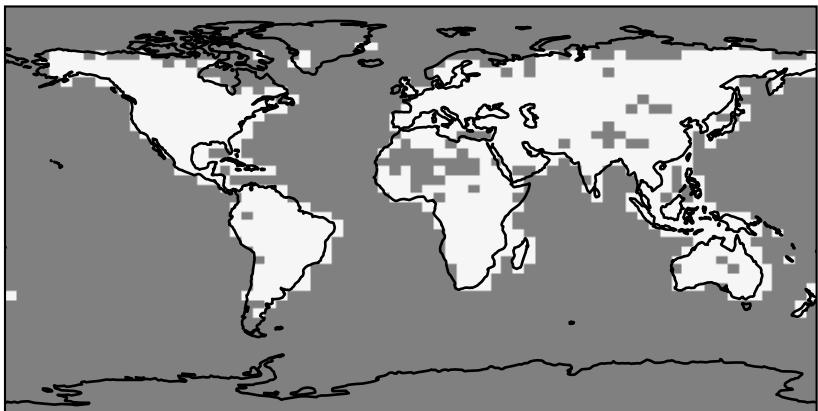
Difference  
Dev - Ref, Dynamic Range



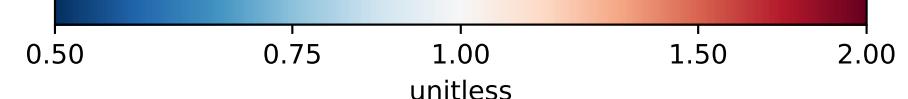
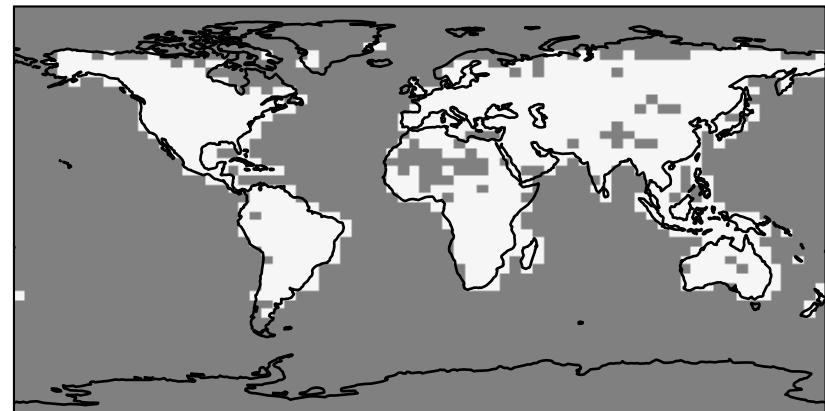
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range



# EmisMEK\_Ship (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



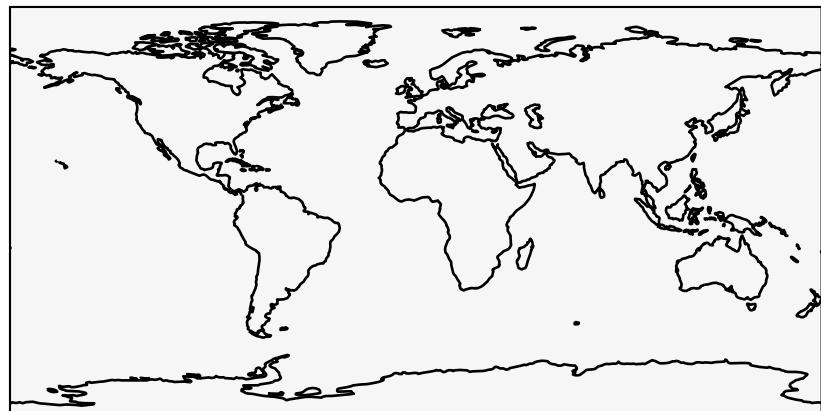
Zero throughout domain  
kg/m<sup>2</sup>/s

GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



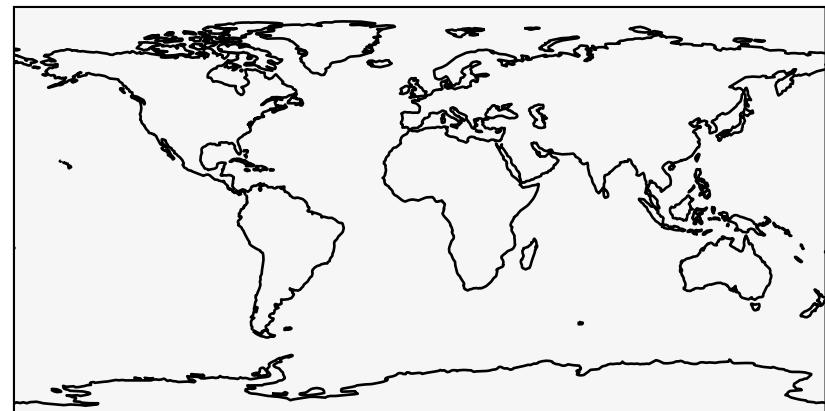
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Dynamic Range



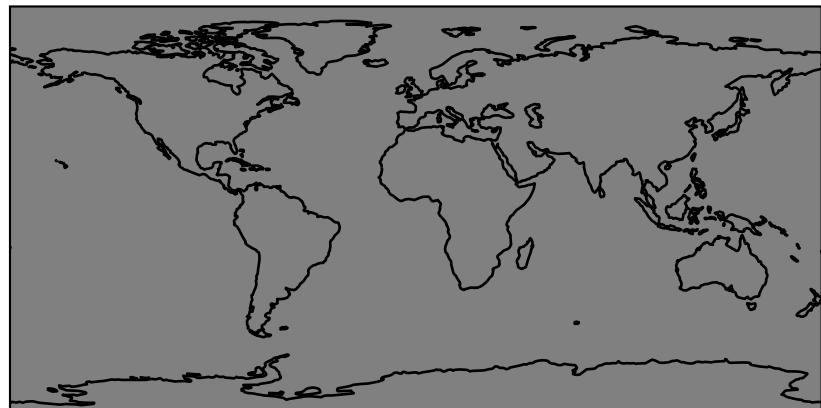
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



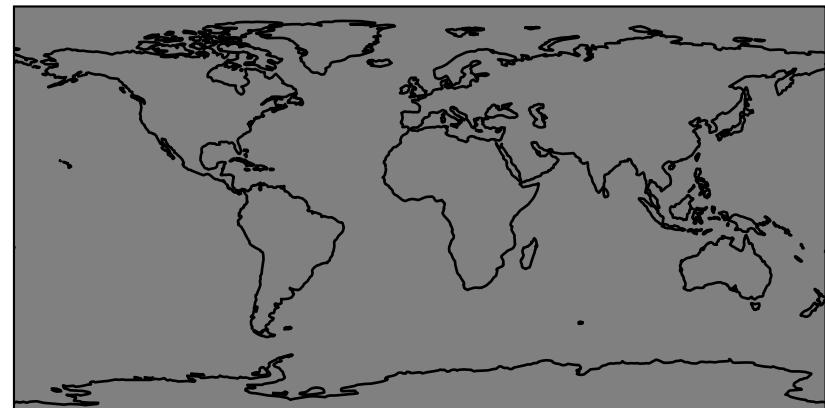
Zero throughout domain  
kg/m<sup>2</sup>/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

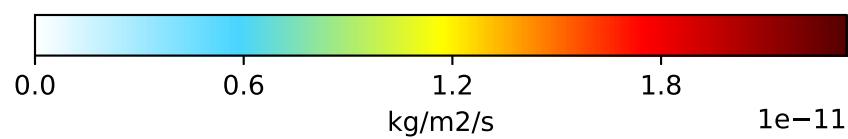
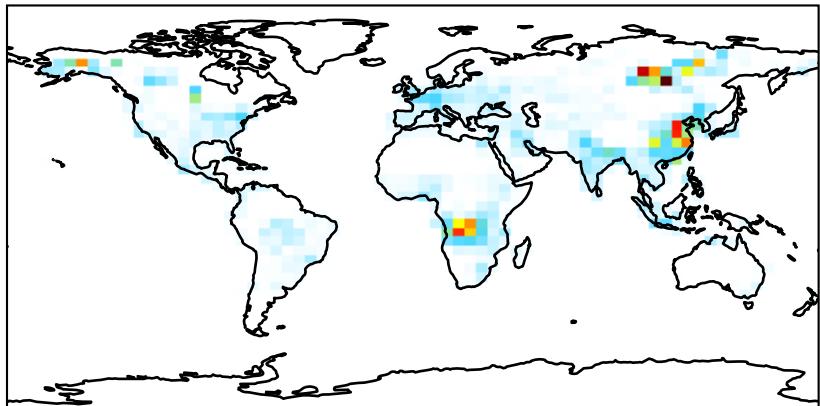
Ratio  
Dev/Ref, Fixed Range



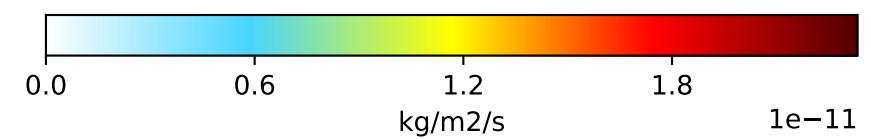
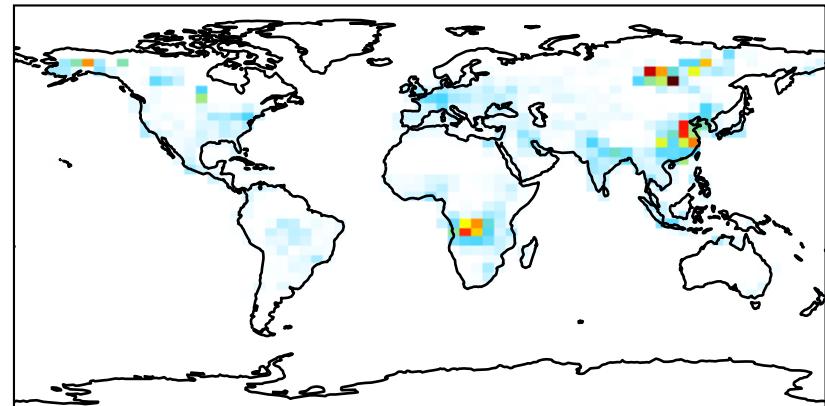
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unitless

# EmisMEK\_Total (Jul2019)

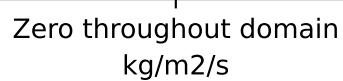
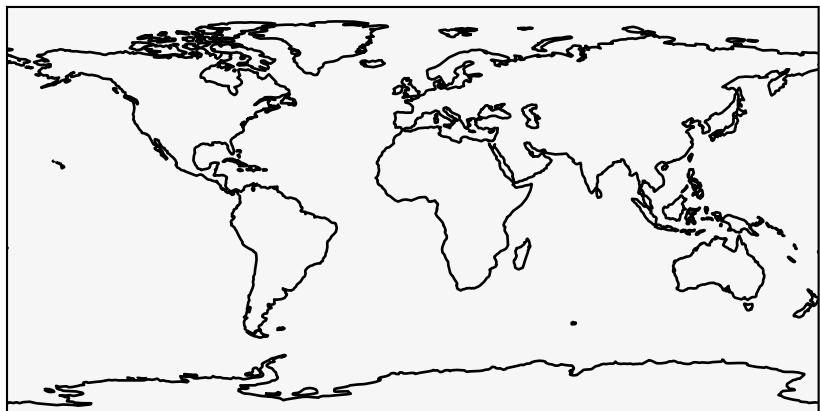
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



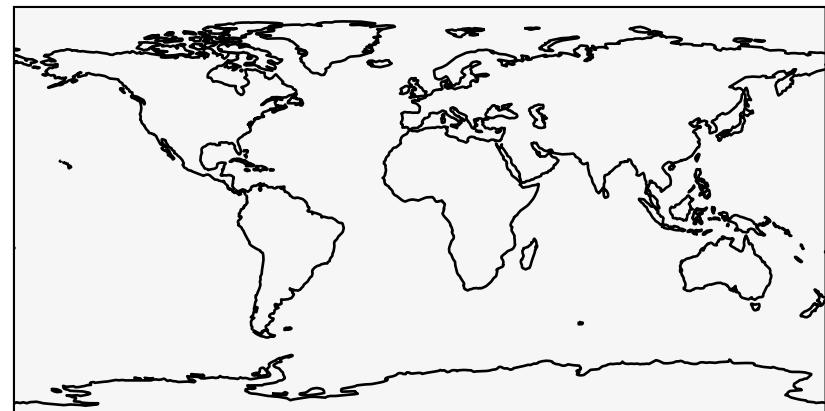
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



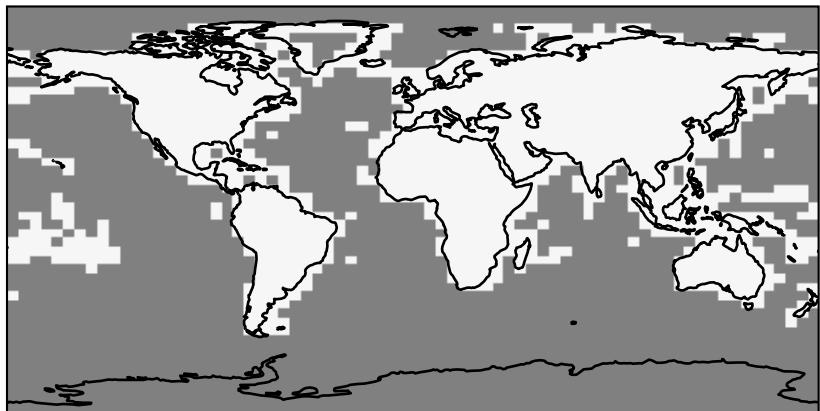
Difference  
Dev - Ref, Dynamic Range



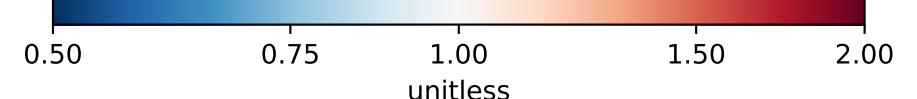
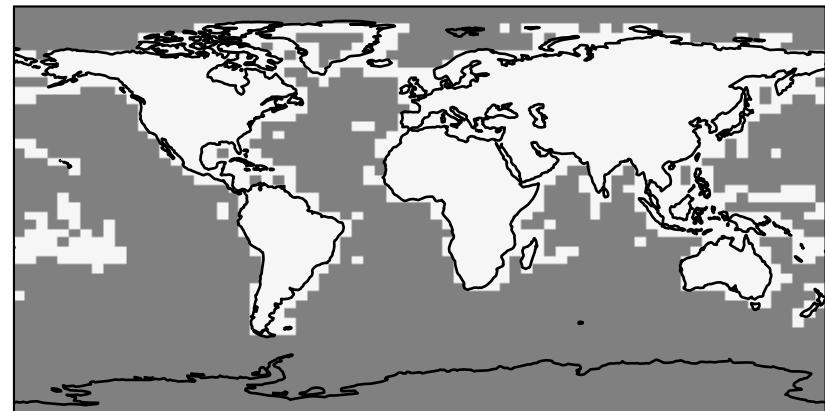
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

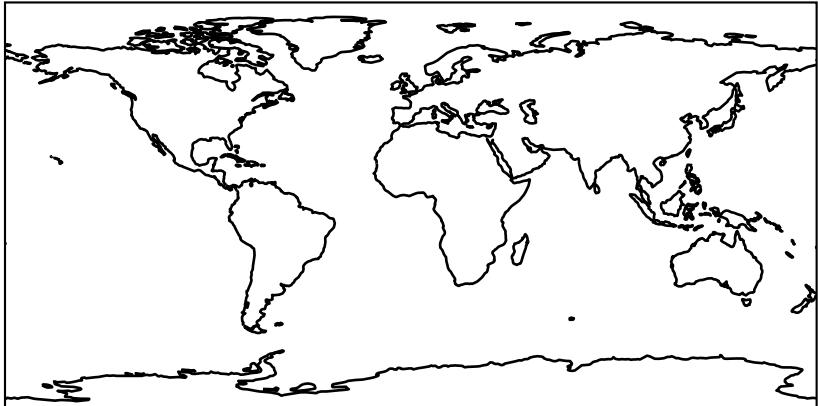


Ratio  
Dev/Ref, Fixed Range



# EmisMVK\_Anthro (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



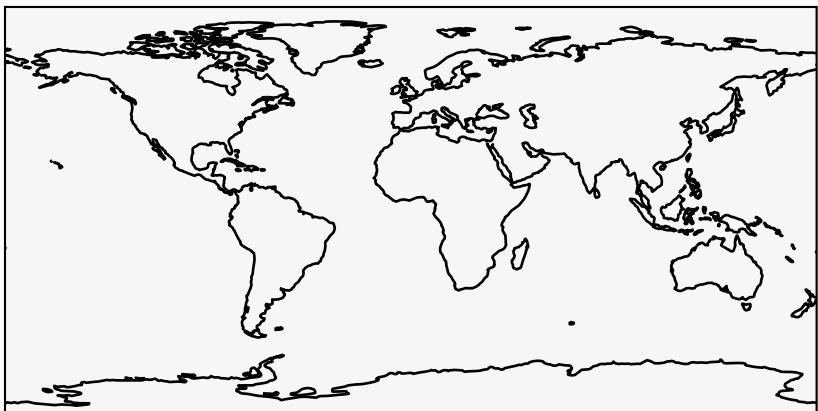
Zero throughout domain  
kg/m<sup>2</sup>/s

GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



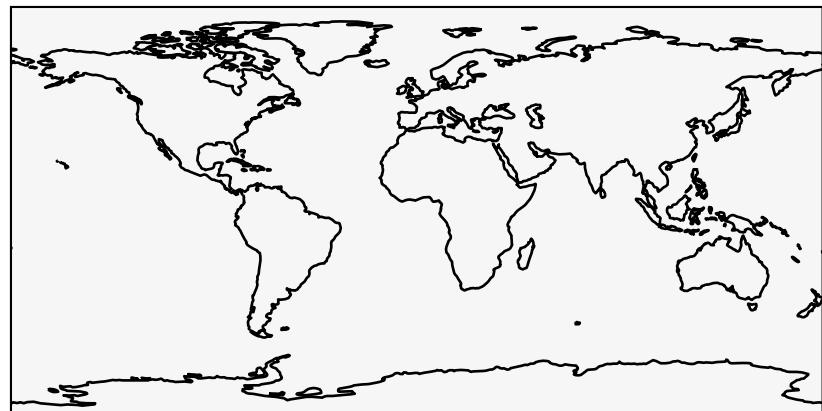
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Dynamic Range



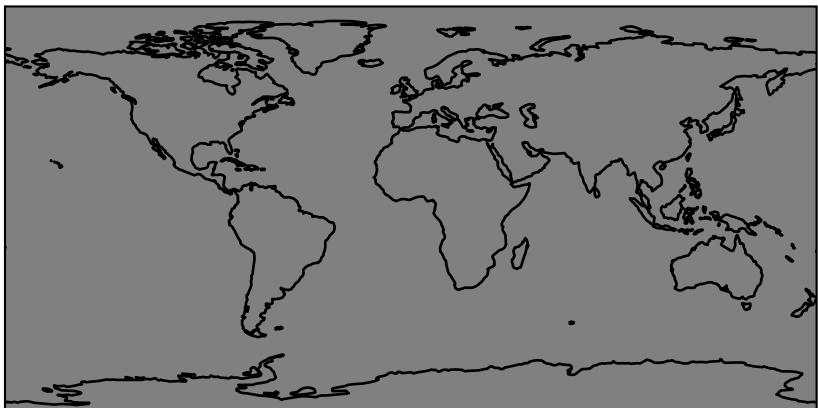
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



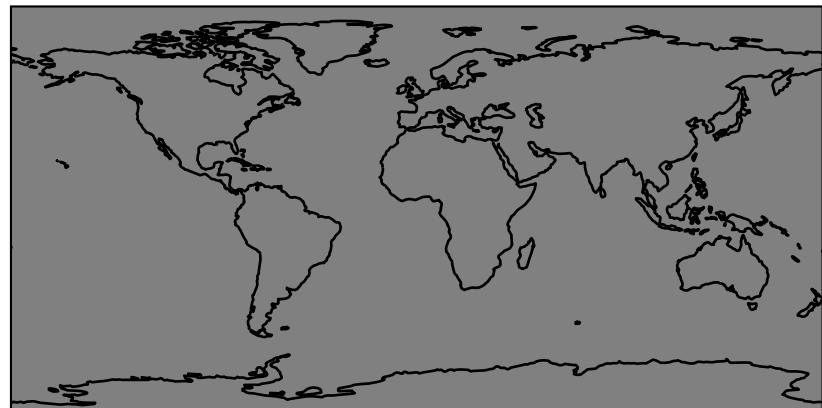
Zero throughout domain  
kg/m<sup>2</sup>/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

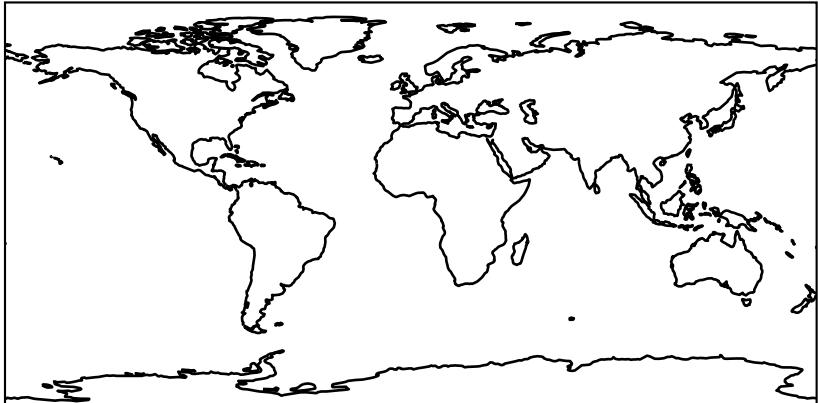
Ratio  
Dev/Ref, Fixed Range



Undefined throughout domain  
unitless

# EmisGLYC\_Anthro (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



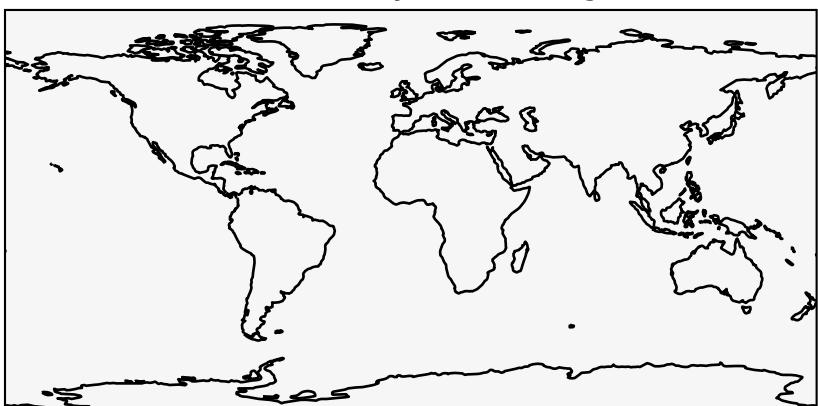
Zero throughout domain  
kg/m<sup>2</sup>/s

GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



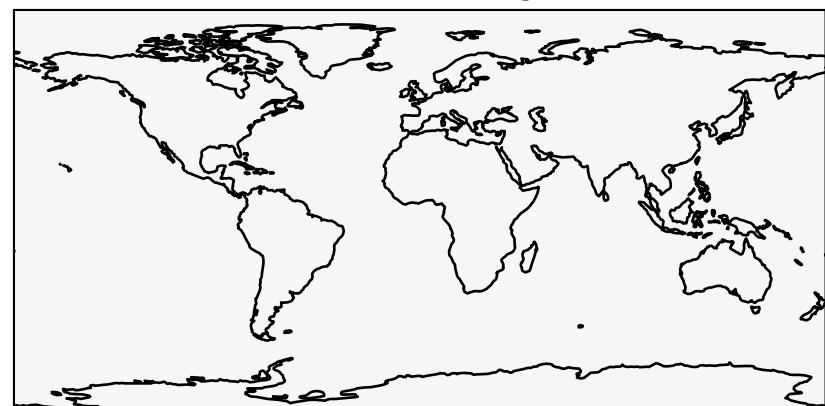
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Dynamic Range



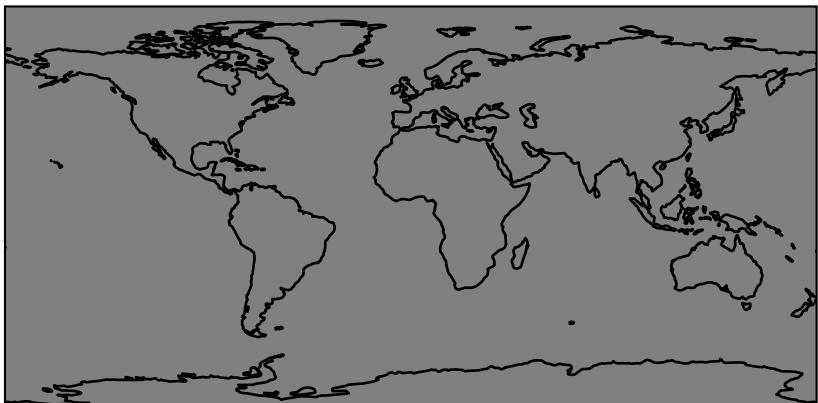
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



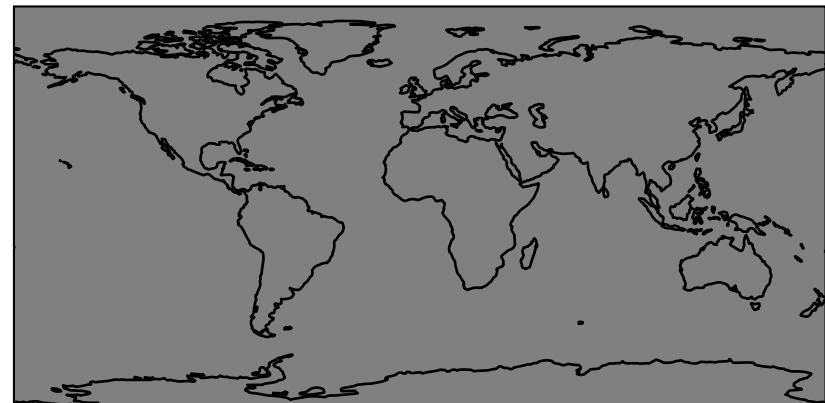
Zero throughout domain  
kg/m<sup>2</sup>/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

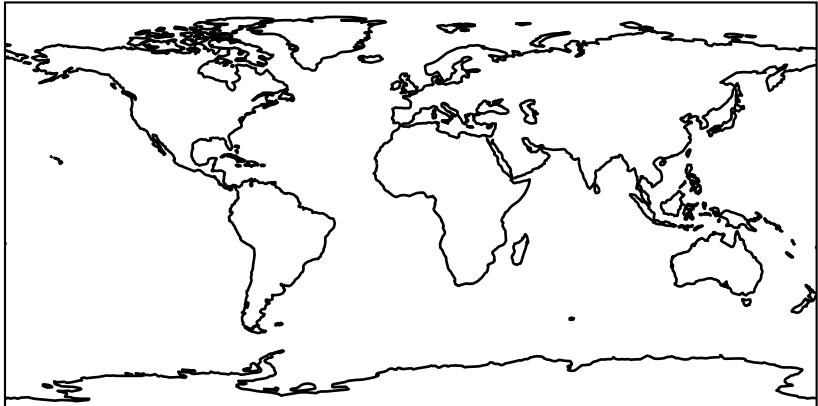
Ratio  
Dev/Ref, Fixed Range



Undefined throughout domain  
unitless

# EmisGLYC\_Total (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



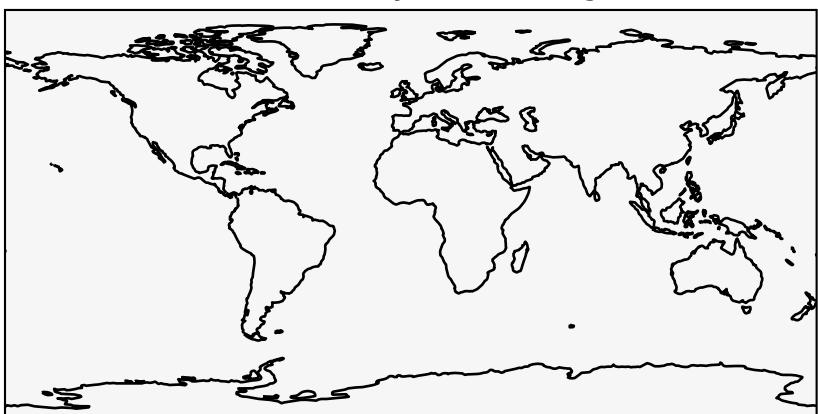
Zero throughout domain  
kg/m<sup>2</sup>/s

GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



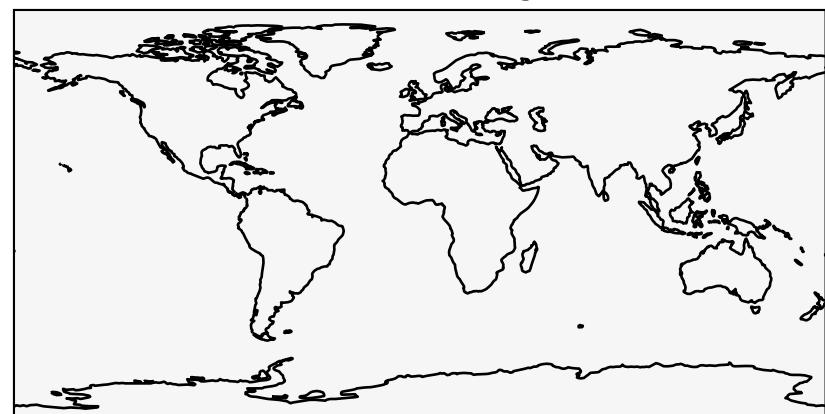
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Dynamic Range



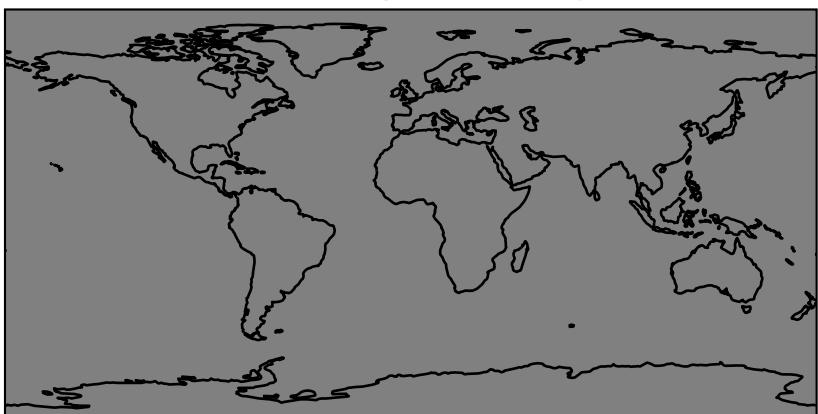
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%, 95%]



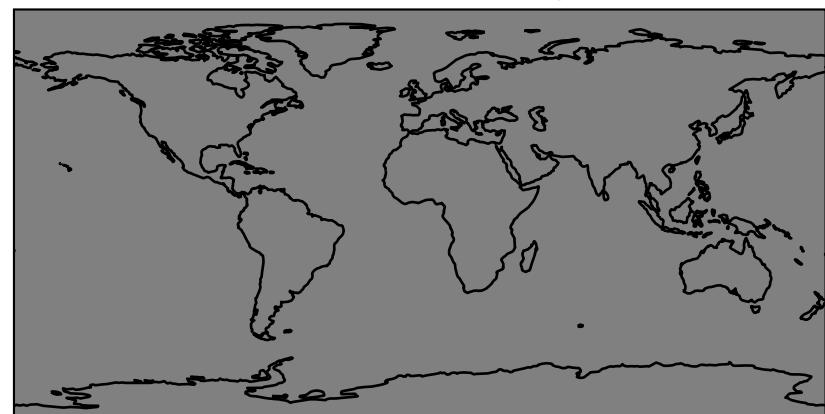
Zero throughout domain  
kg/m<sup>2</sup>/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

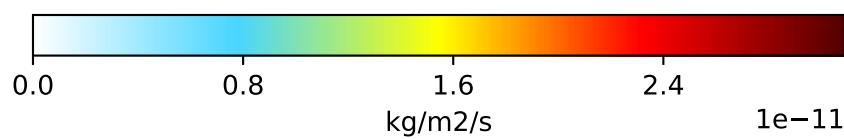
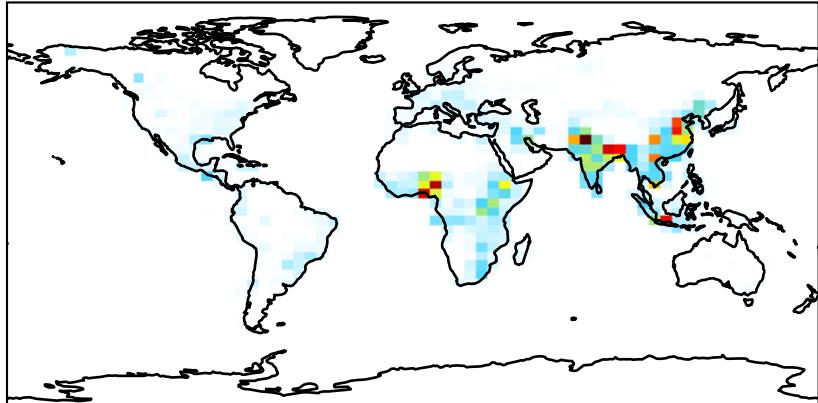
Ratio  
Dev/Ref, Fixed Range



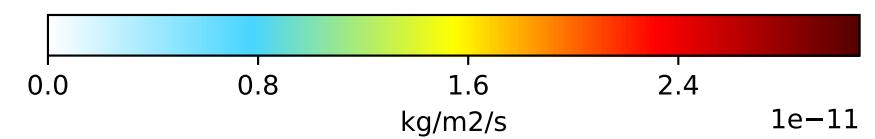
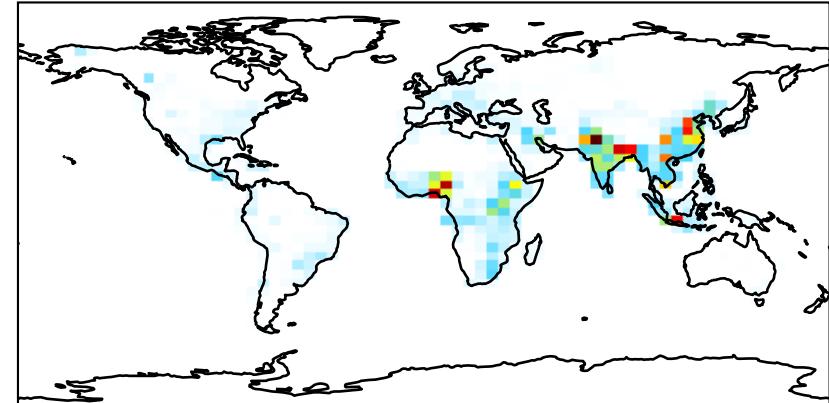
Undefined throughout domain  
unitless

# EmisHCOOH\_Anthro (Jul2019)

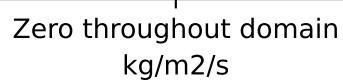
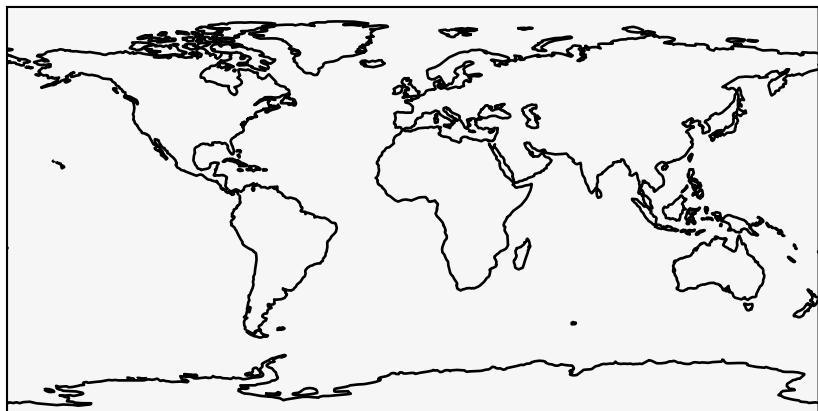
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



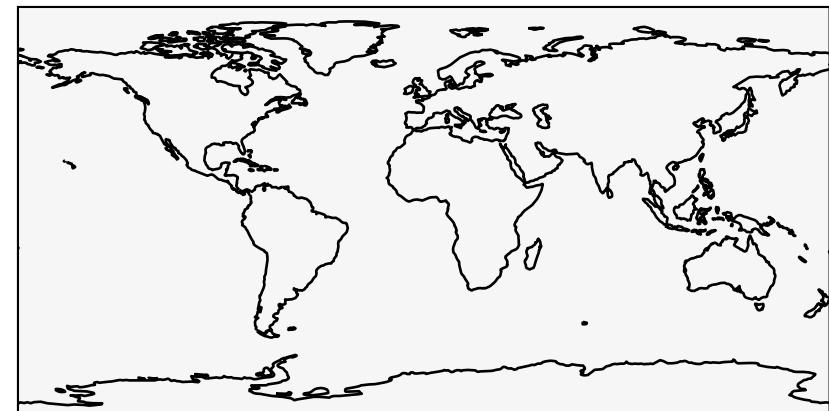
GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



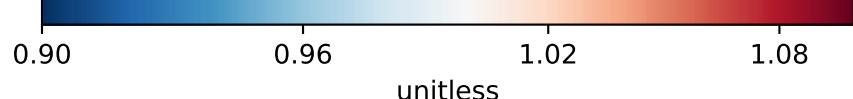
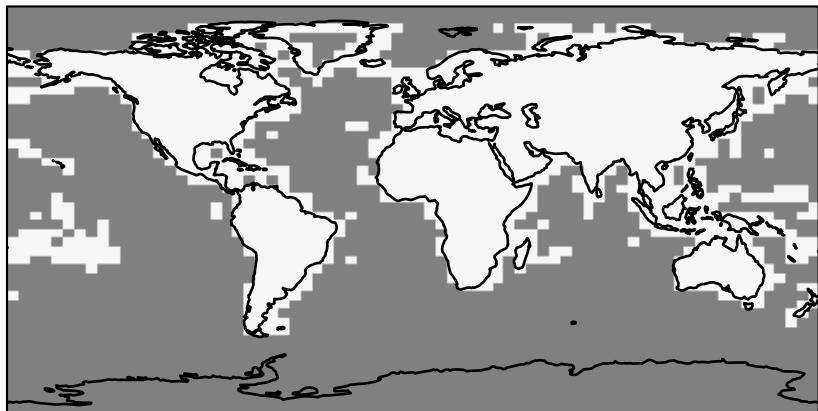
Difference  
Dev - Ref, Dynamic Range



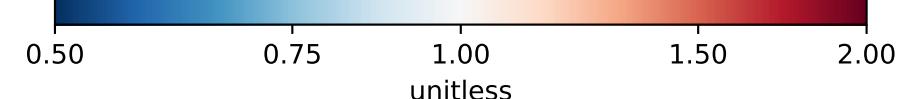
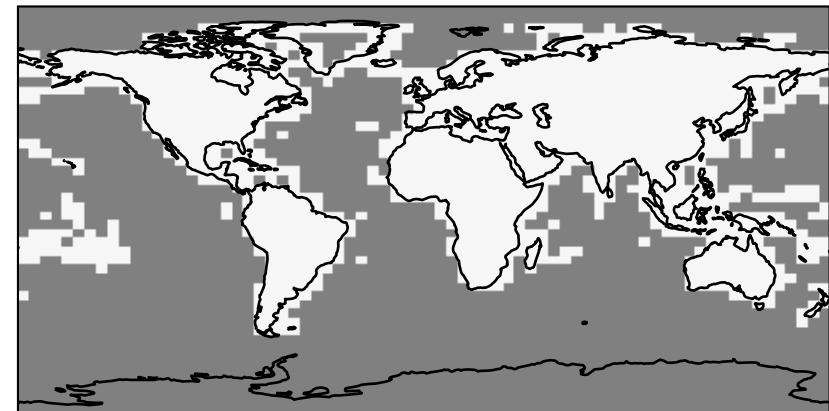
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

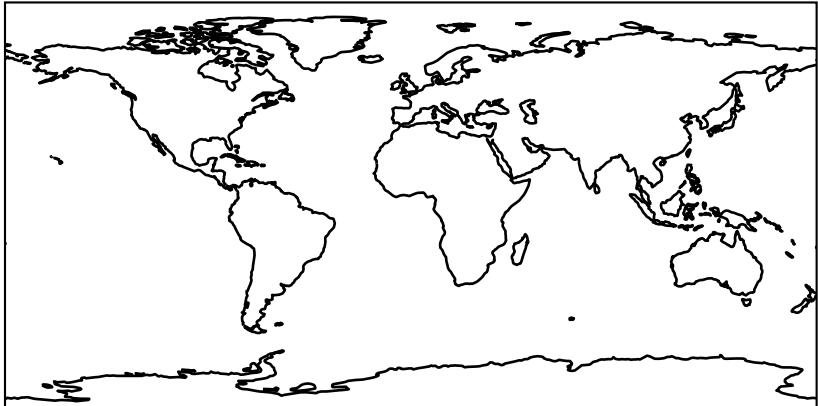


Ratio  
Dev/Ref, Fixed Range



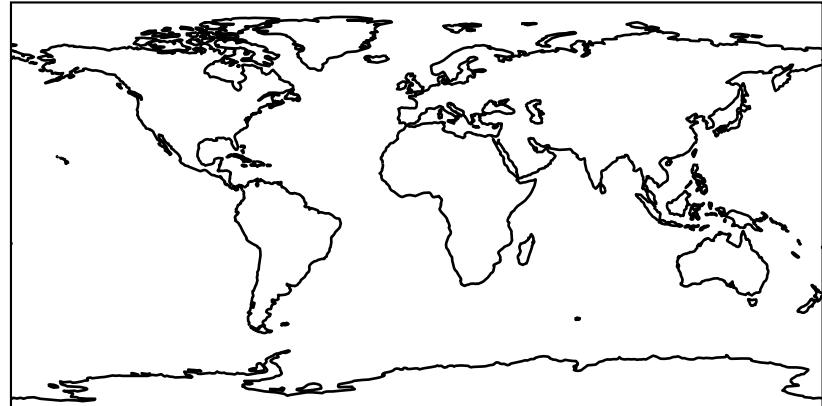
# EmisHCOOH\_Ship (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



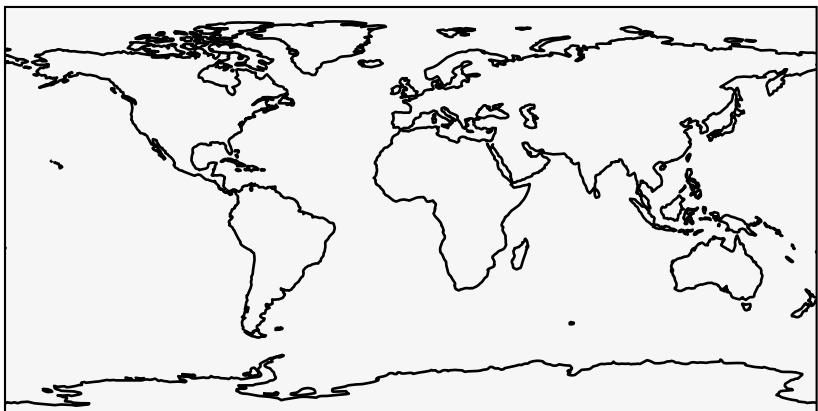
Zero throughout domain  
kg/m<sup>2</sup>/s

GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



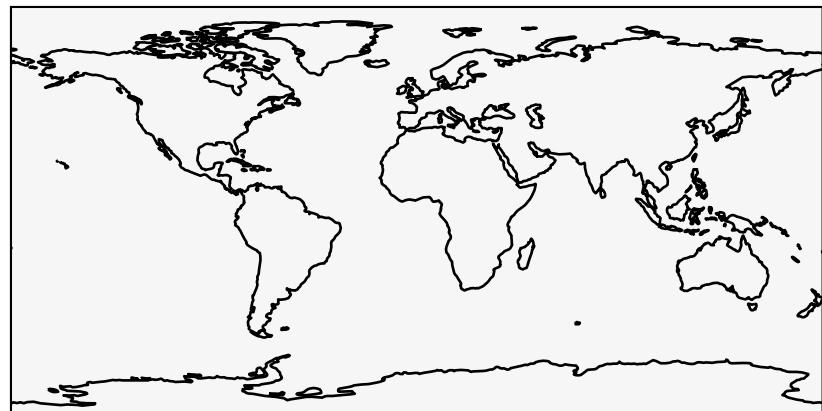
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Dynamic Range



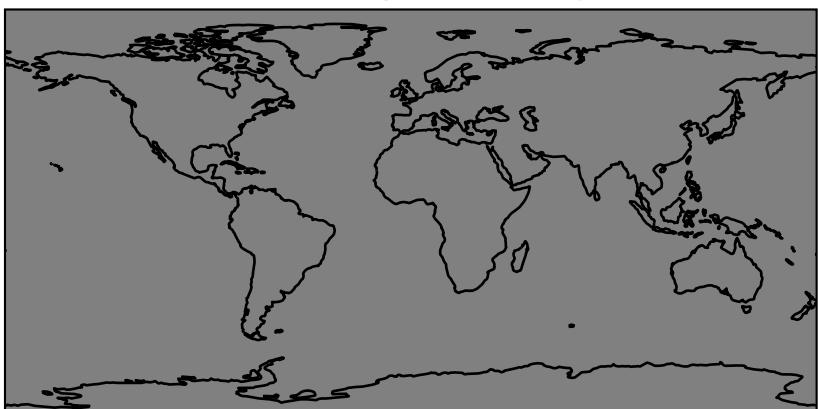
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



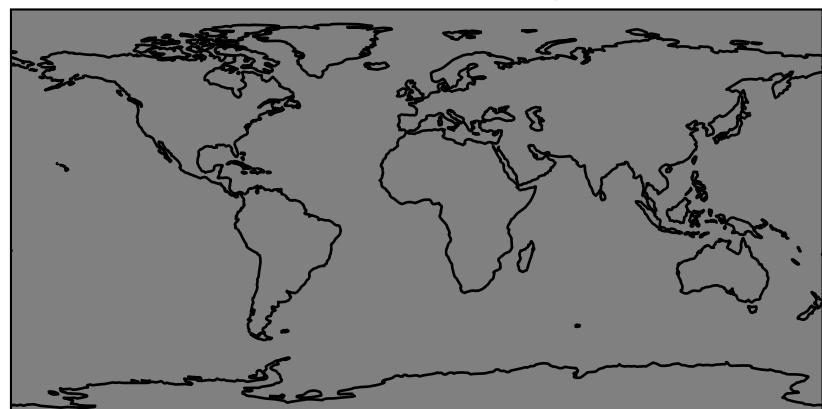
Zero throughout domain  
kg/m<sup>2</sup>/s

Ratio  
Dev/Ref, Dynamic Range



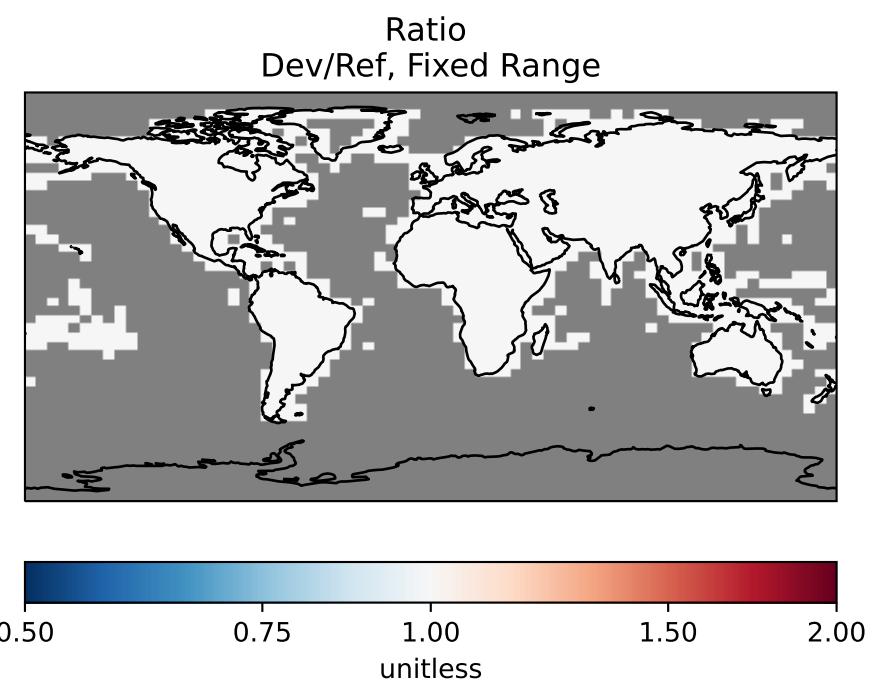
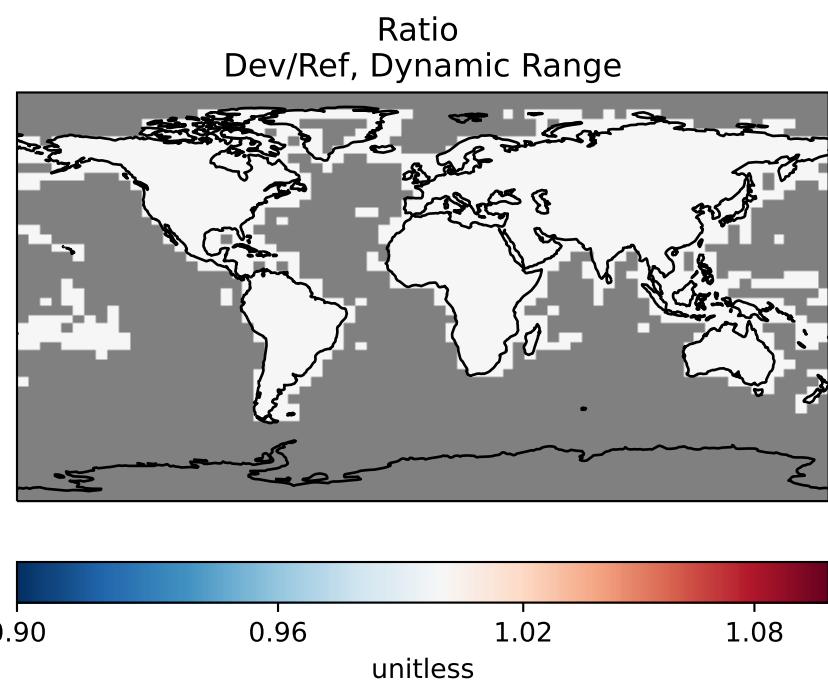
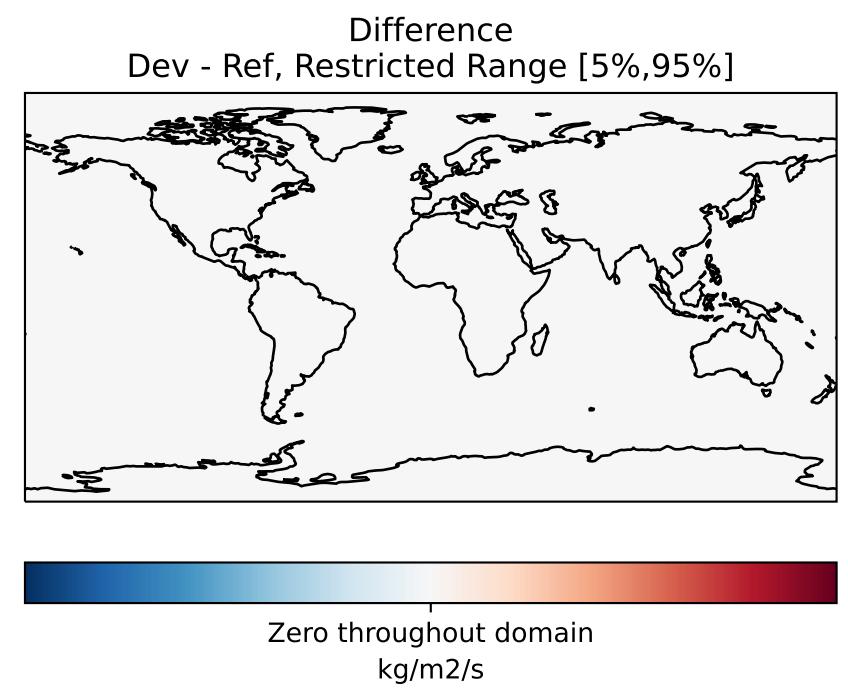
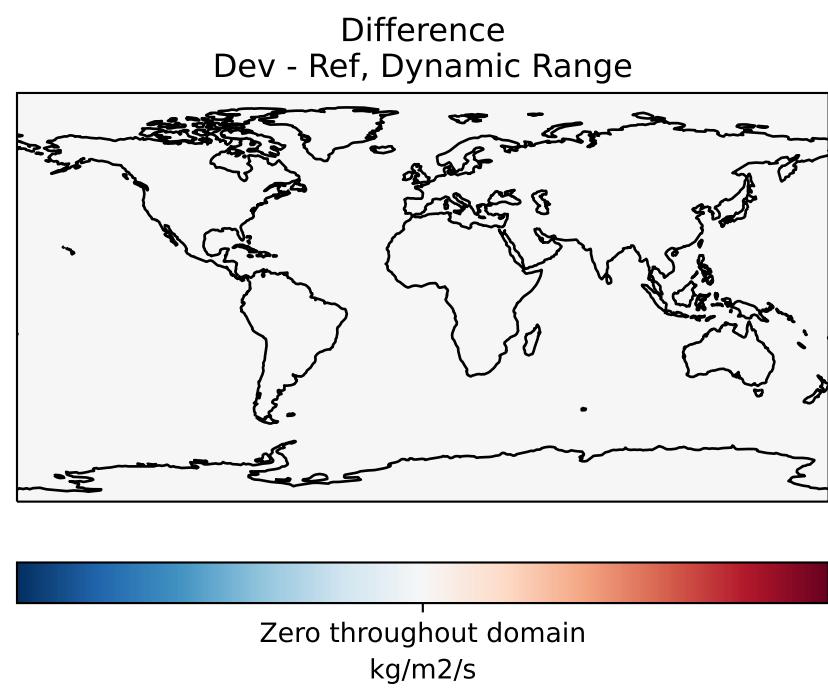
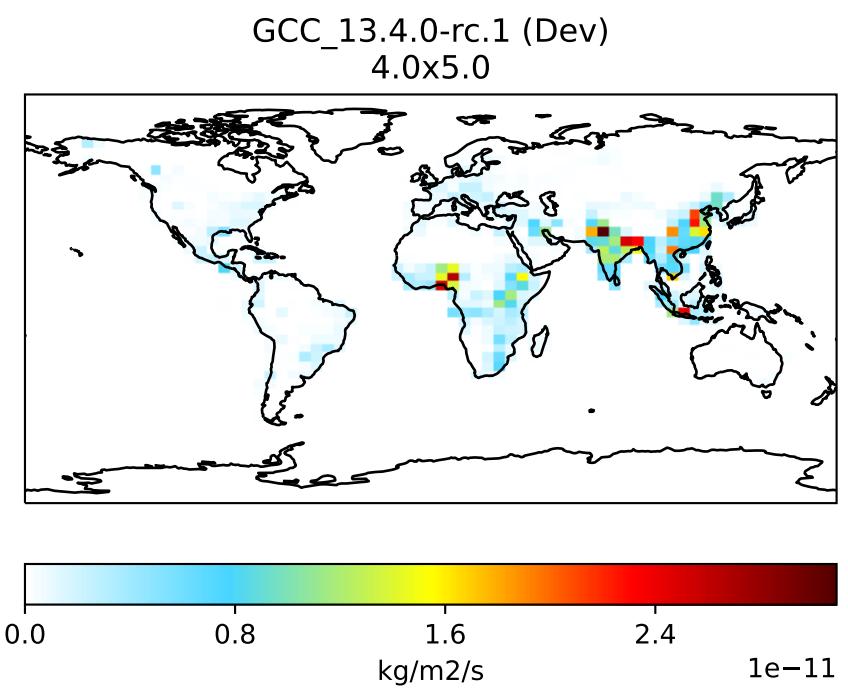
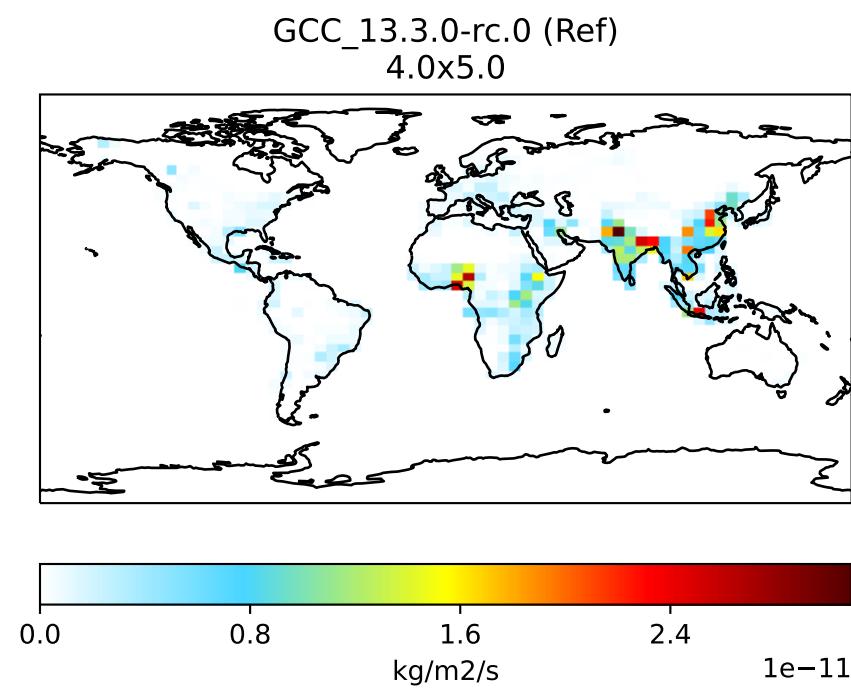
Undefined throughout domain  
unitless

Ratio  
Dev/Ref, Fixed Range

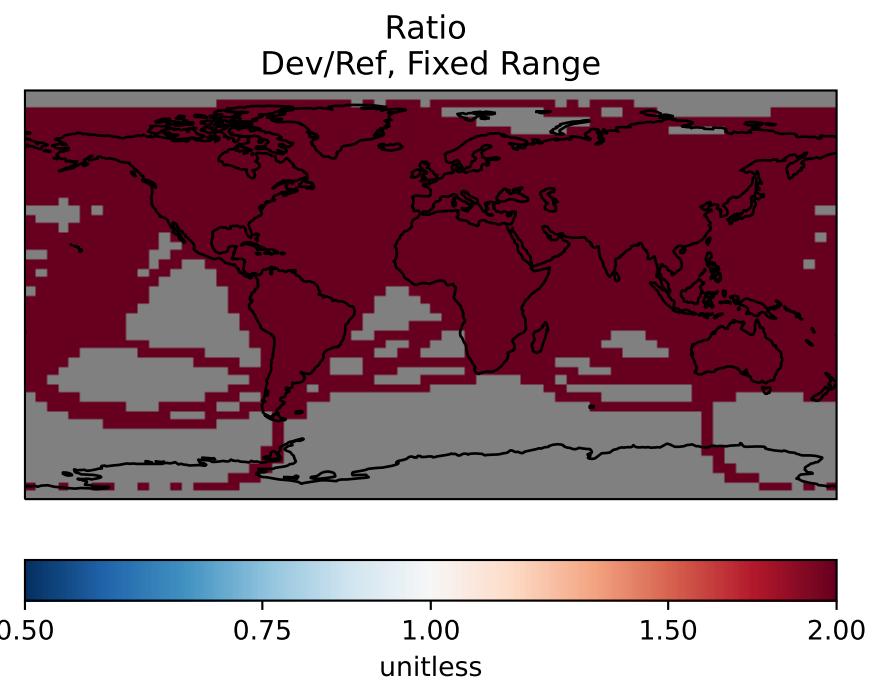
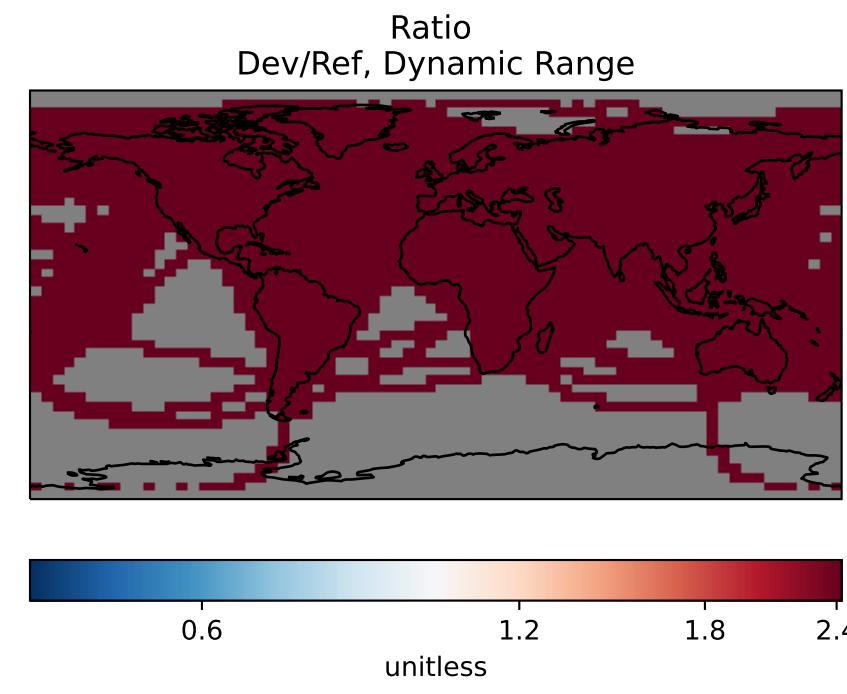
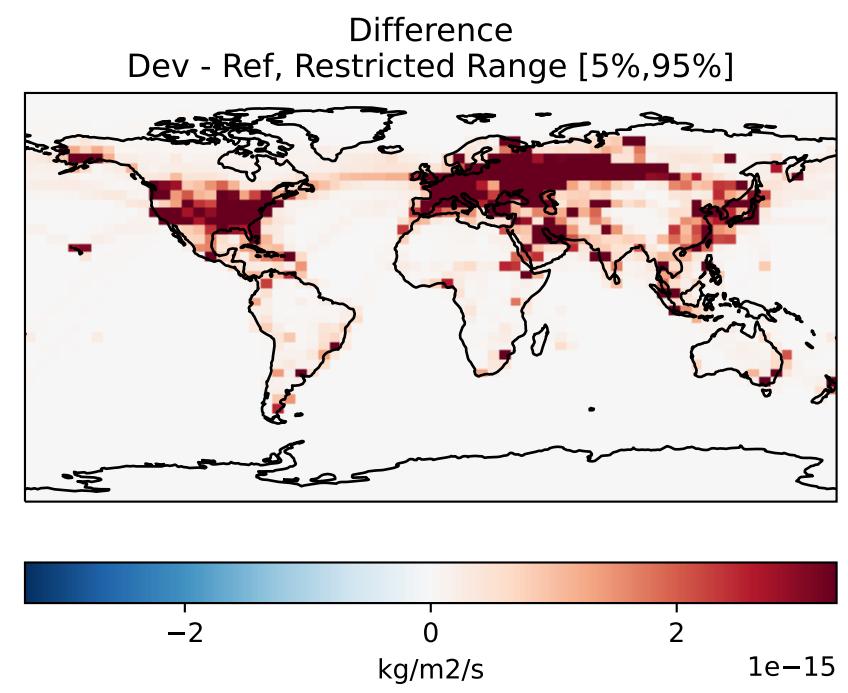
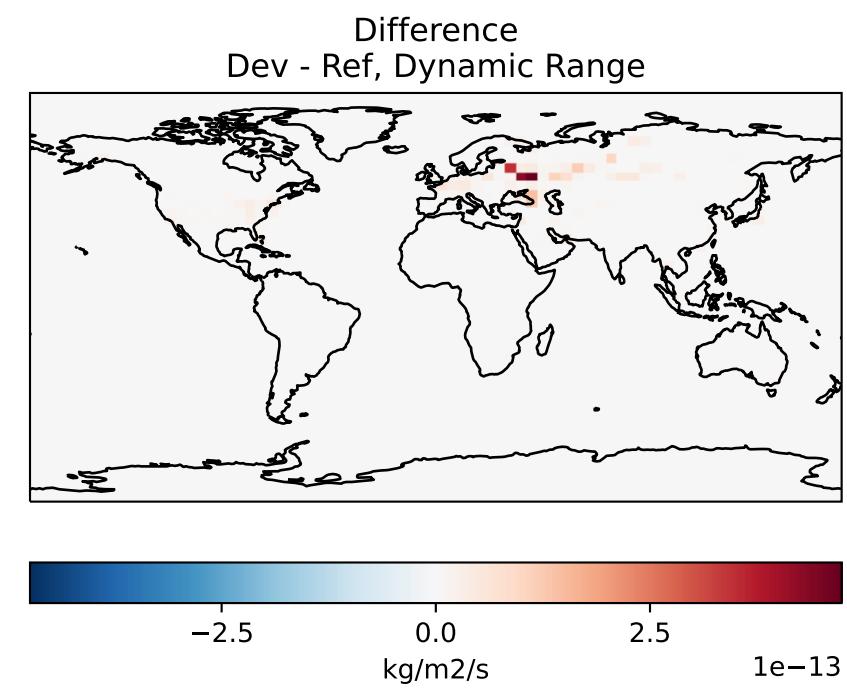
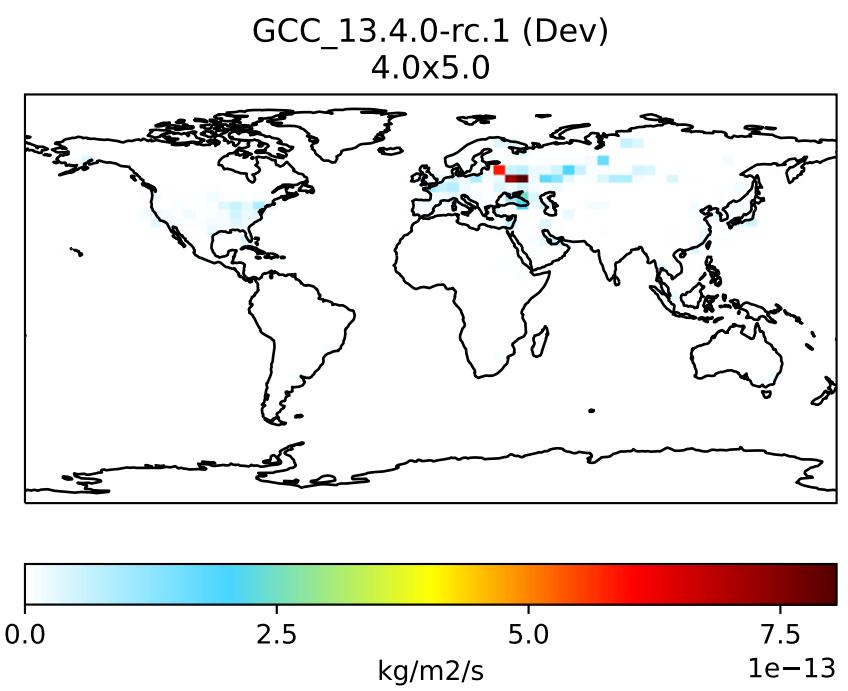
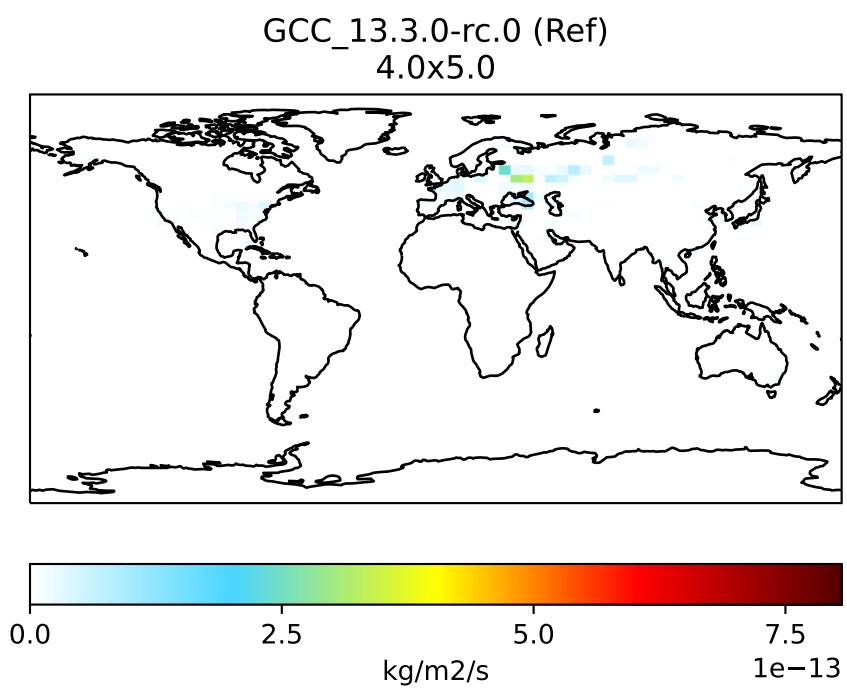


Undefined throughout domain  
unitless

# EmisHCOOH\_Total (Jul2019)

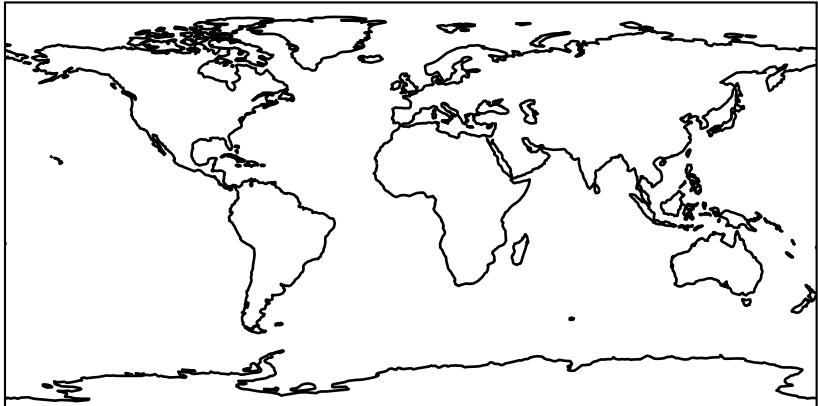


# EmisRCHO\_Aircraft (Jul2019)



# EmisRCHO\_Anthro (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



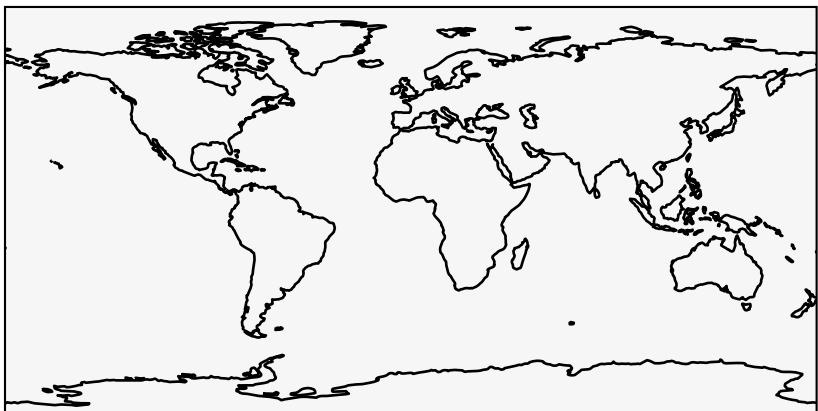
Zero throughout domain  
kg/m<sup>2</sup>/s

GCC\_13.4.0-rc.1 (Dev)  
4.0x5.0



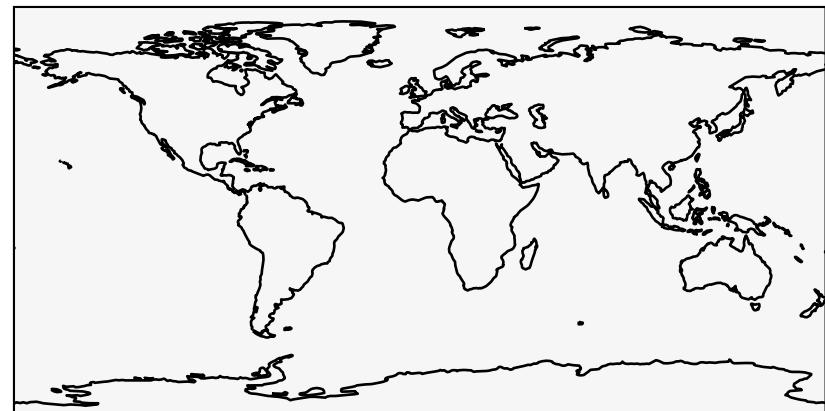
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Dynamic Range



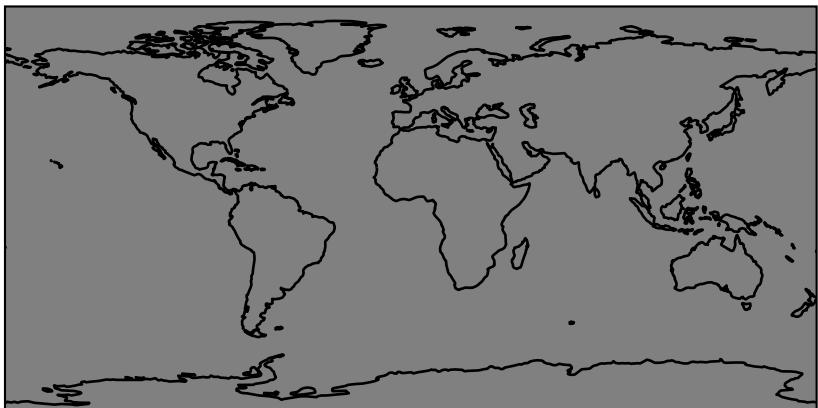
Zero throughout domain  
kg/m<sup>2</sup>/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



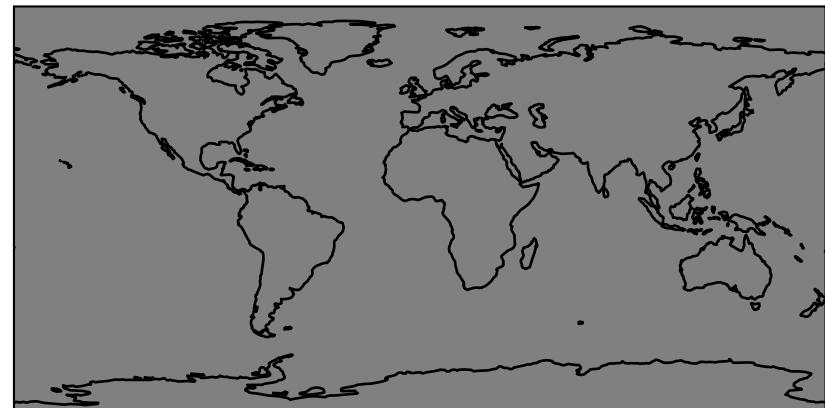
Zero throughout domain  
kg/m<sup>2</sup>/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

Ratio  
Dev/Ref, Fixed Range



Undefined throughout domain  
unitless

# EmisRCHO\_Total (Jul2019)

