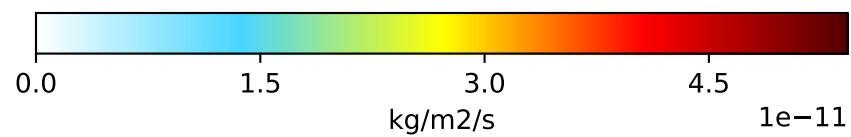
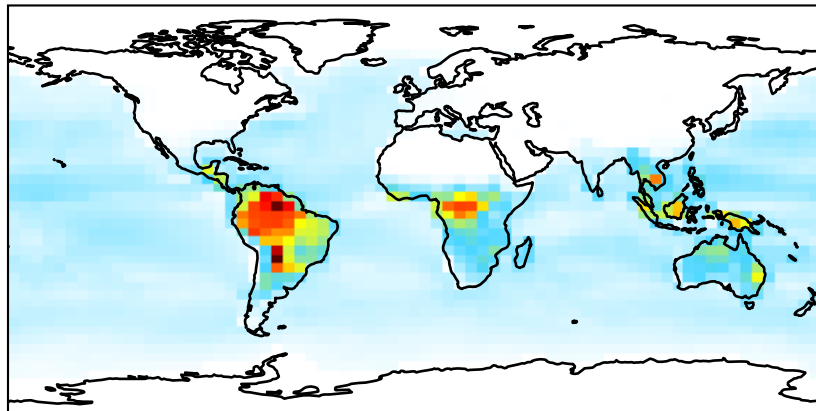
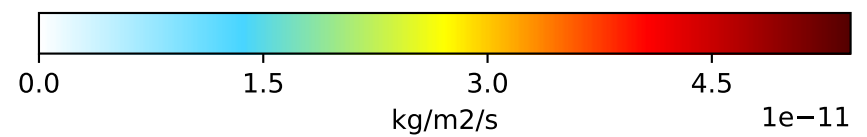
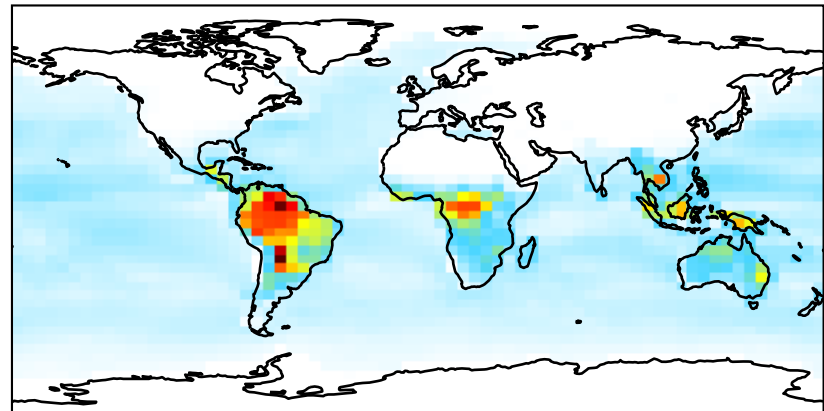


EmisACET_Total (Jan2019)

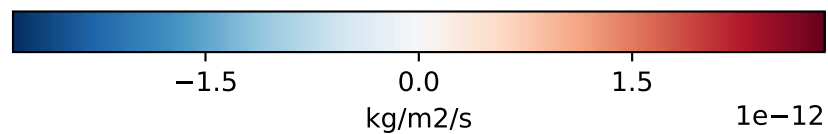
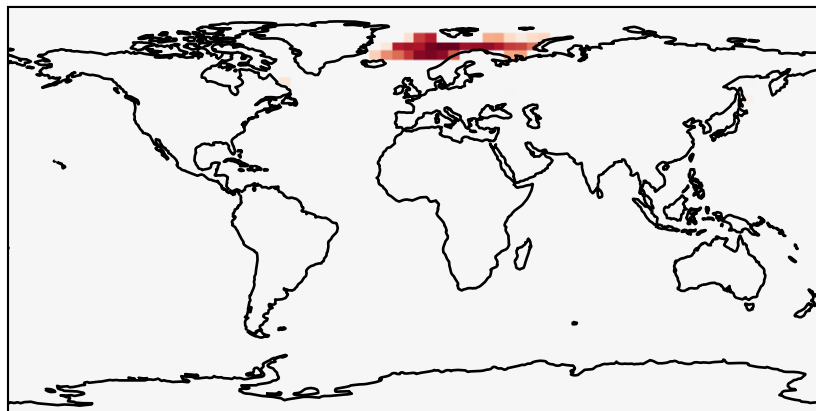
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



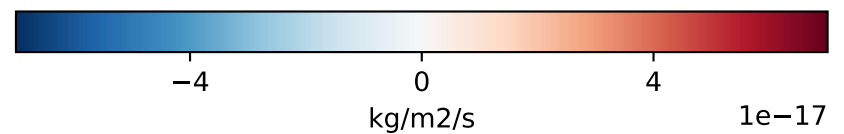
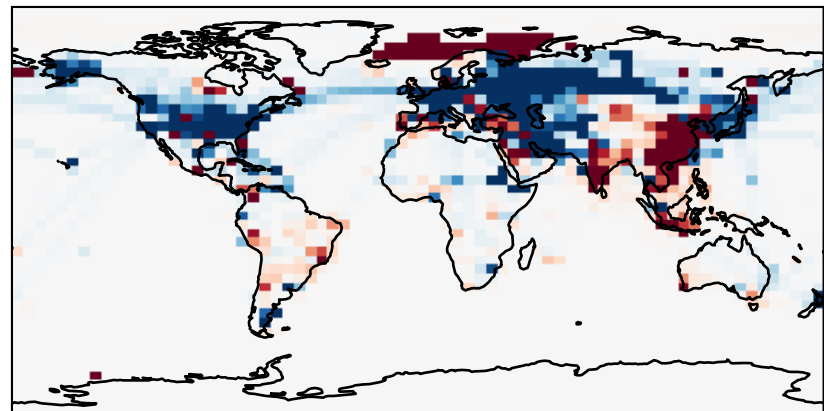
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



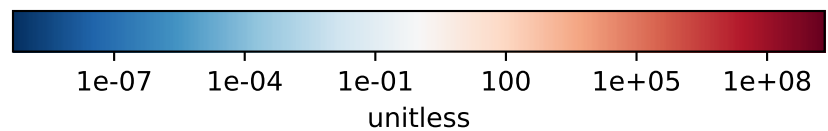
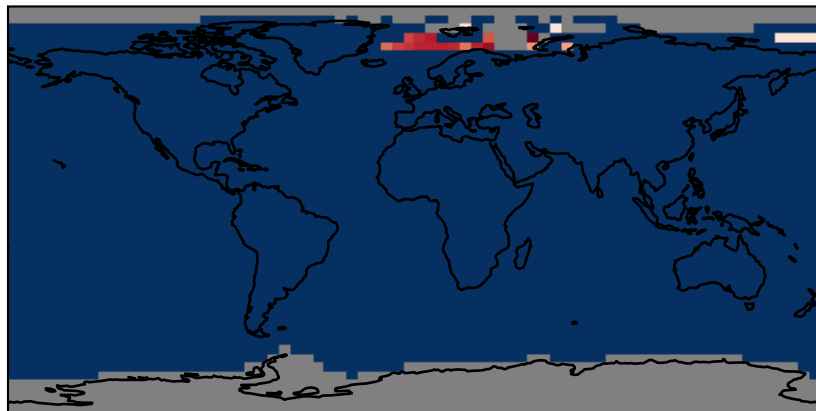
Difference
Dev - Ref, Dynamic Range



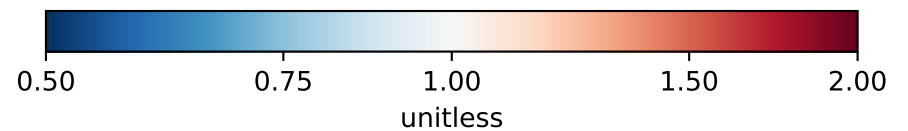
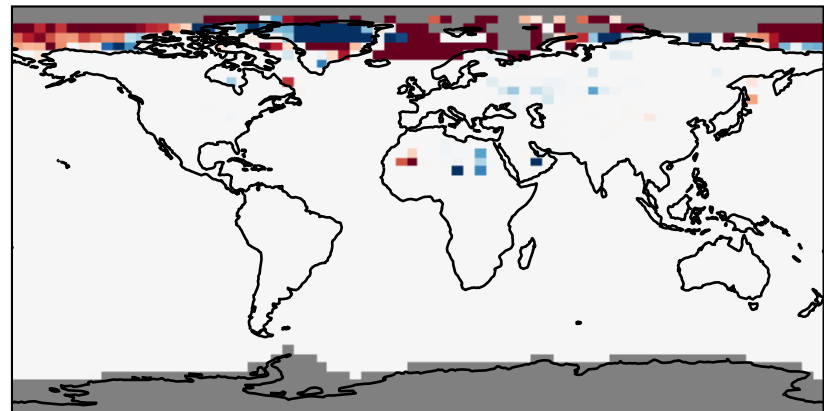
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

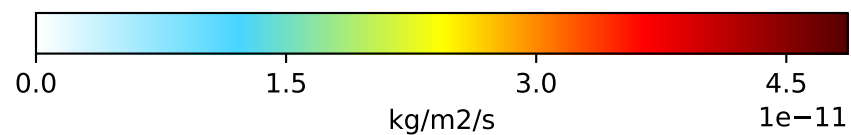
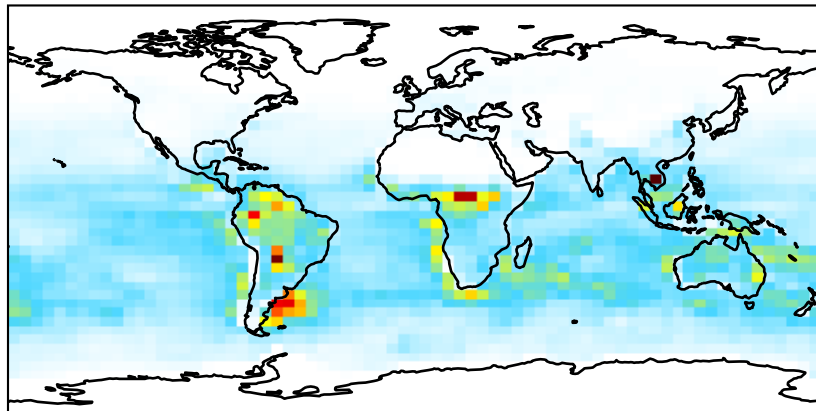


Ratio
Dev/Ref, Fixed Range

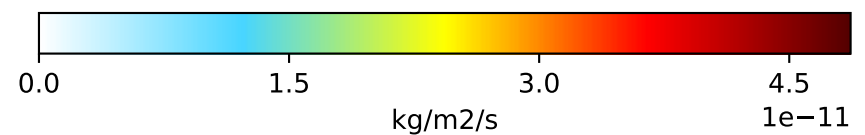
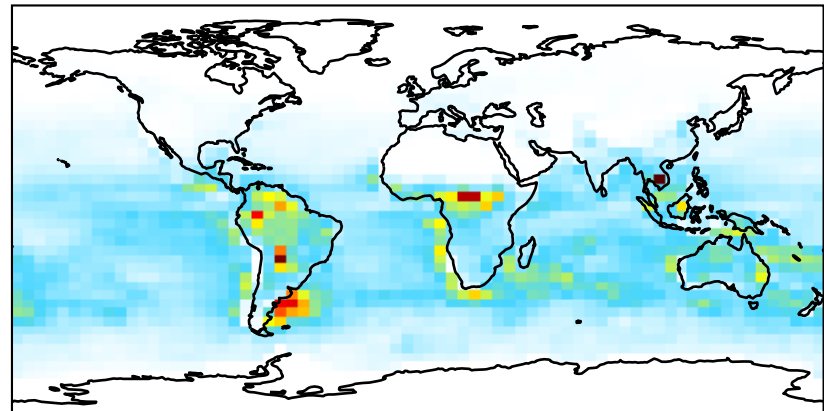


EmisALD2_Total (Jan2019)

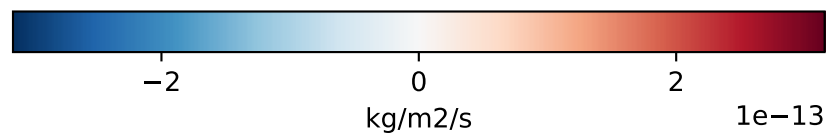
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



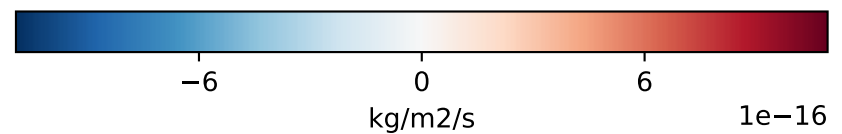
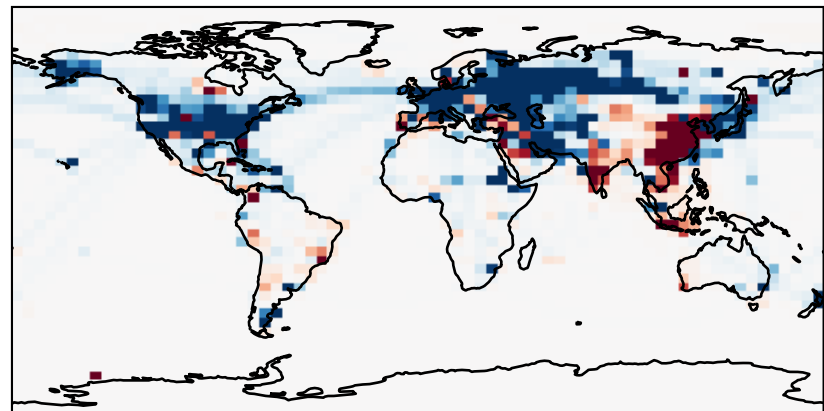
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



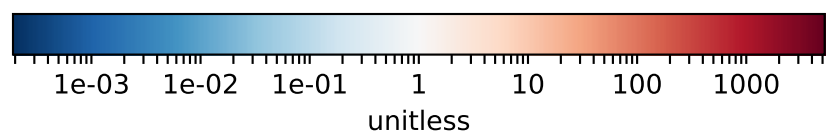
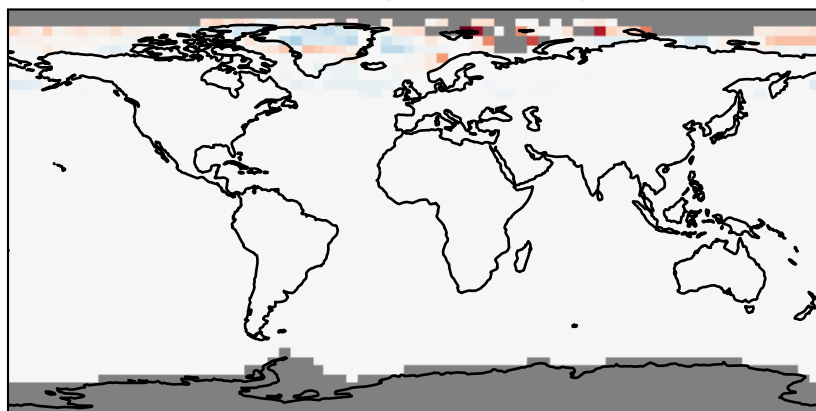
Difference
Dev - Ref, Dynamic Range



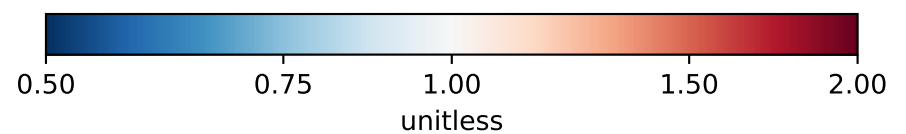
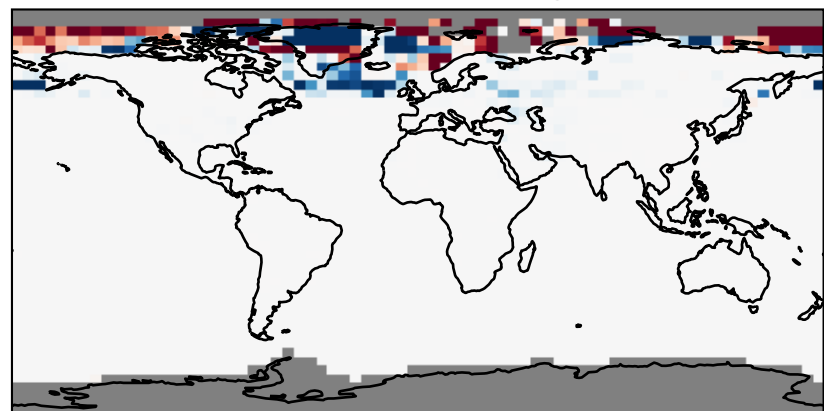
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

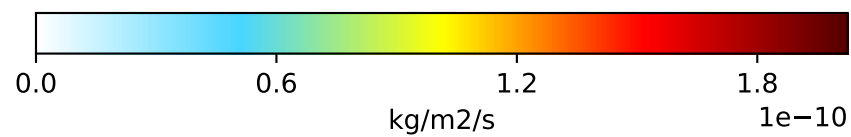
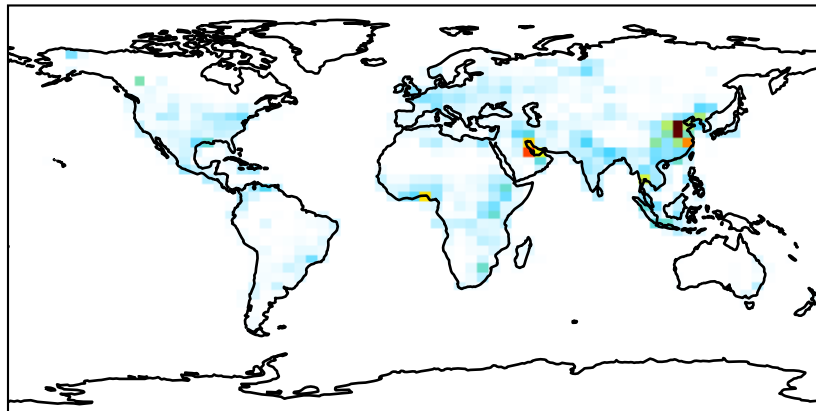


Ratio
Dev/Ref, Fixed Range

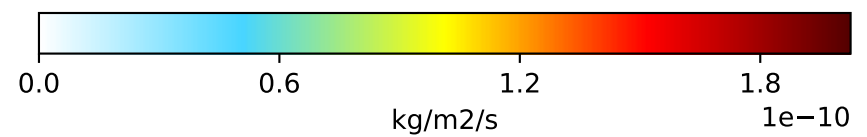
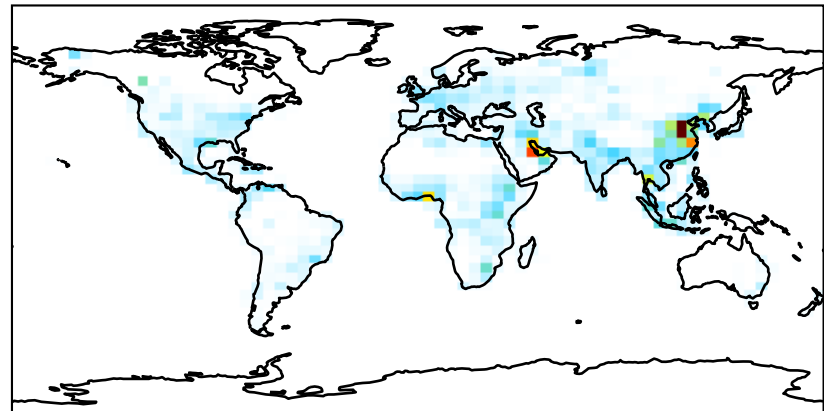


EmisALK4_Total (Jan2019)

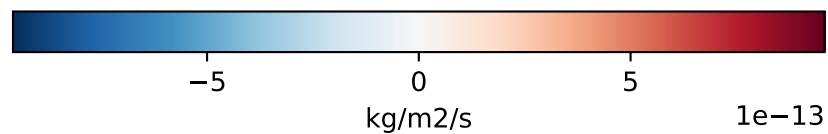
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



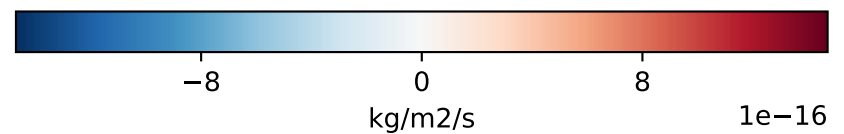
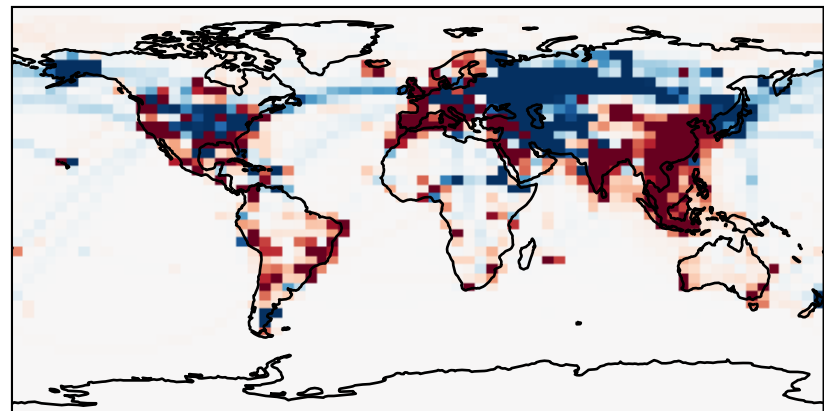
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



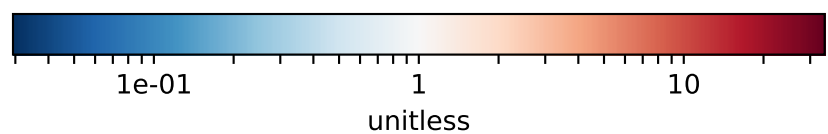
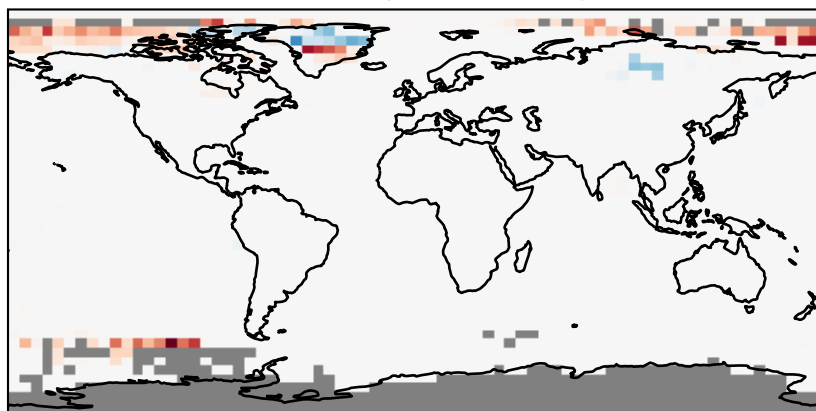
Difference
Dev - Ref, Dynamic Range



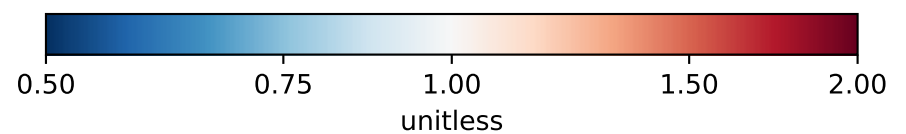
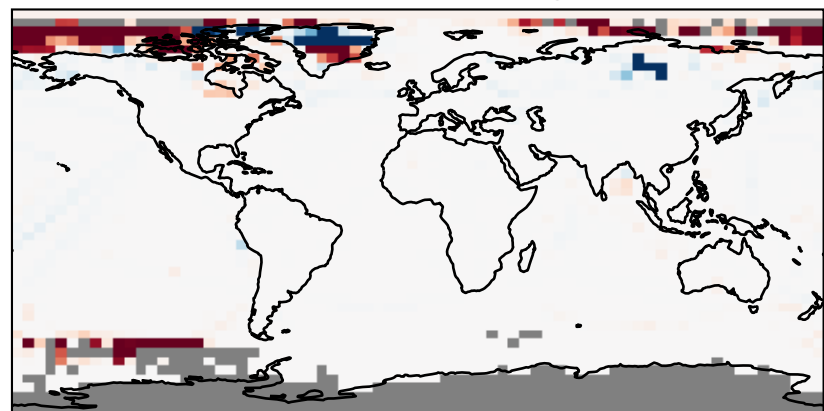
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

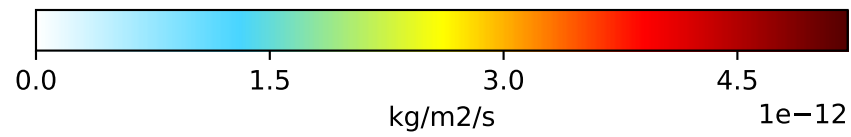
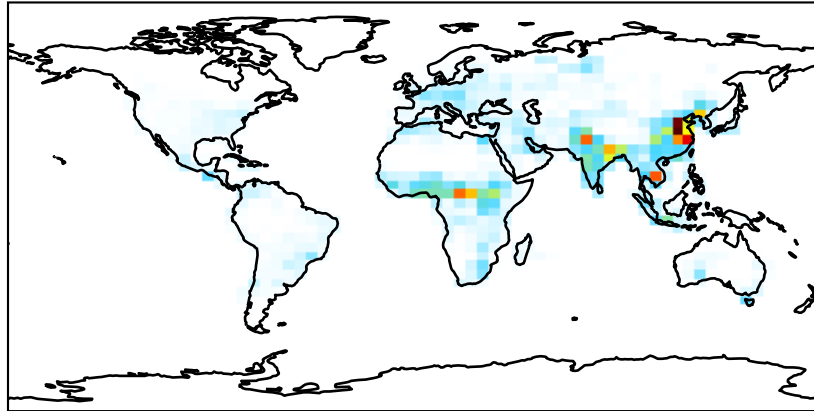


Ratio
Dev/Ref, Fixed Range

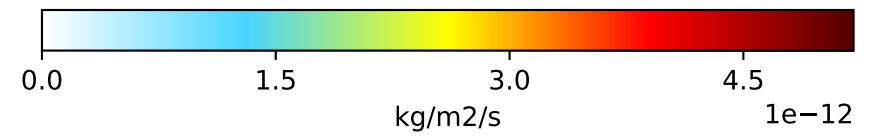
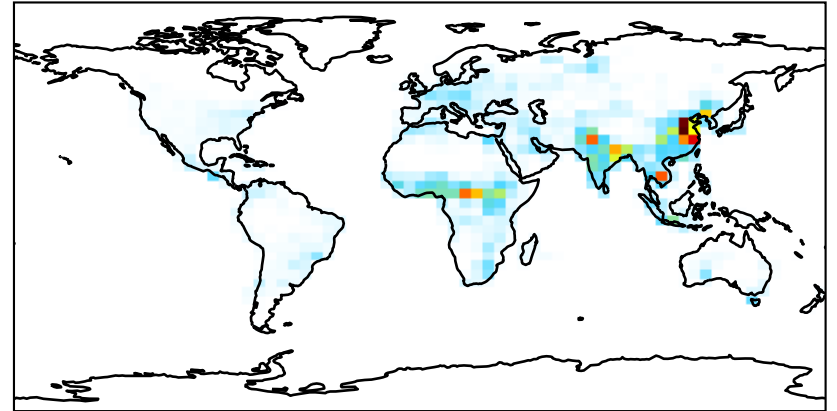


EmisBCPI_Total (Jan2019)

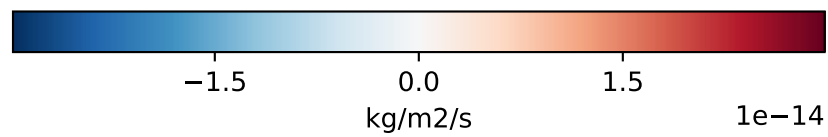
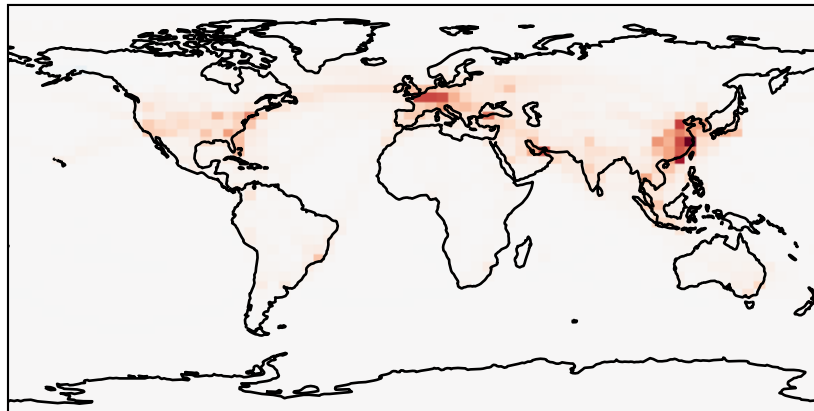
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



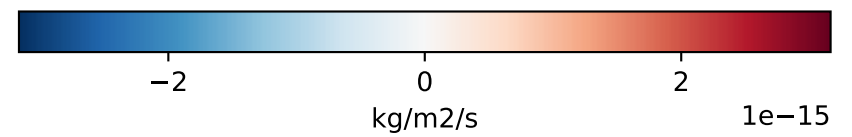
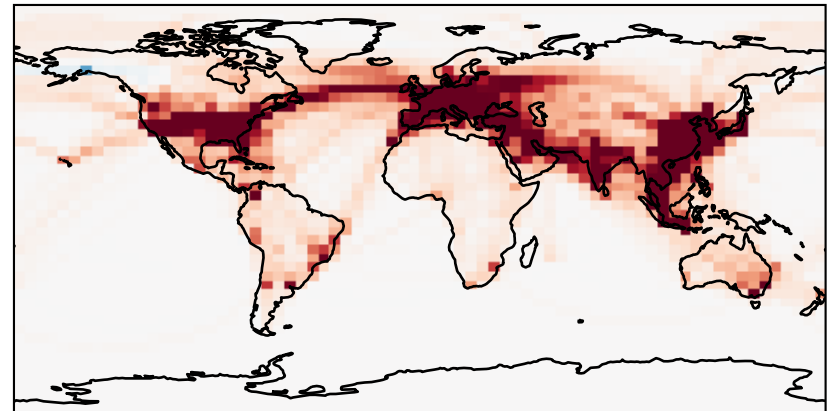
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



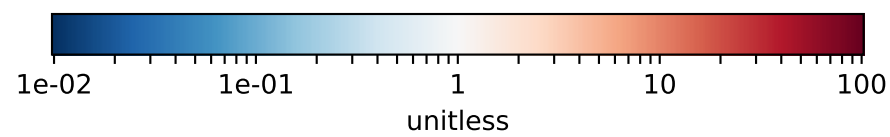
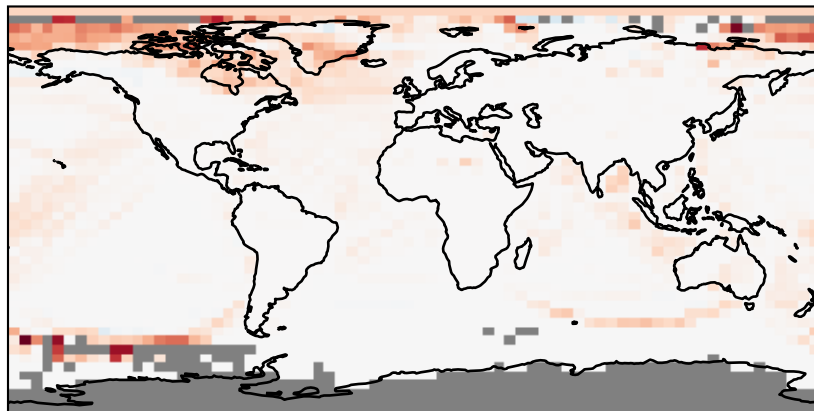
Difference
Dev - Ref, Dynamic Range



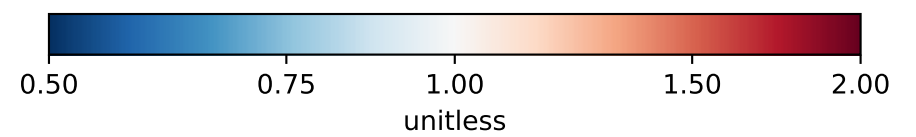
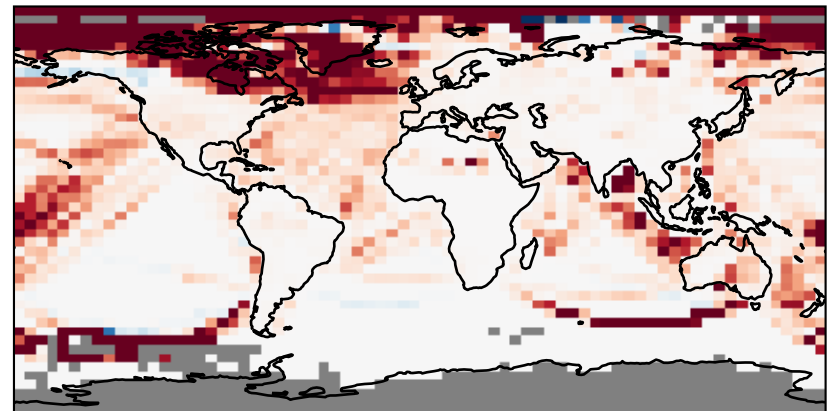
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

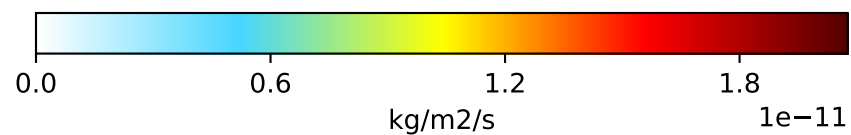
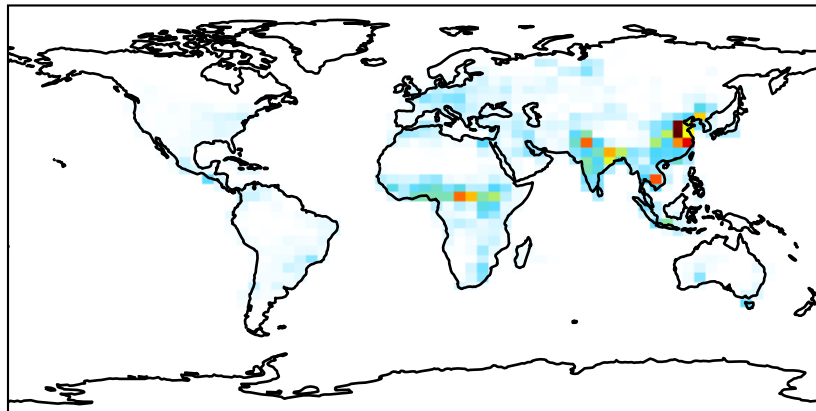


Ratio
Dev/Ref, Fixed Range

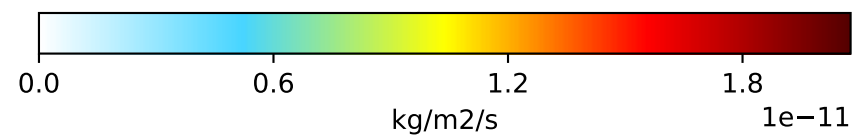
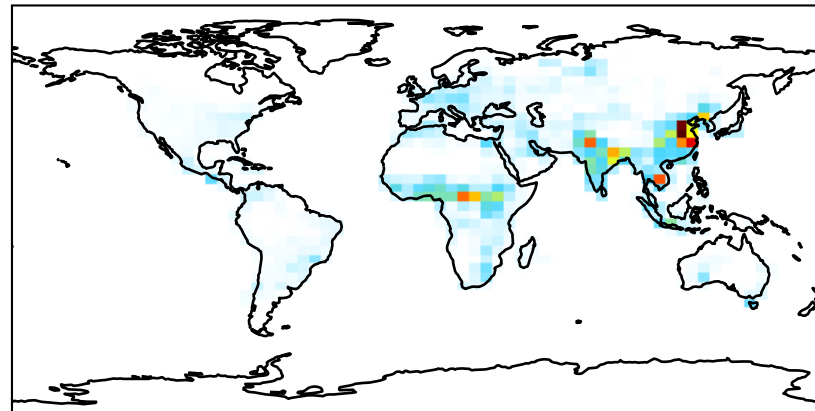


EmisBCPO_Total (Jan2019)

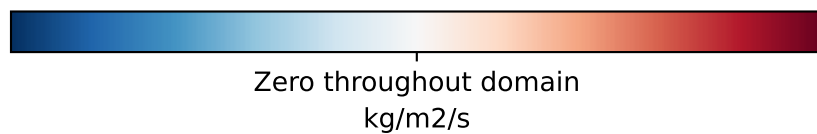
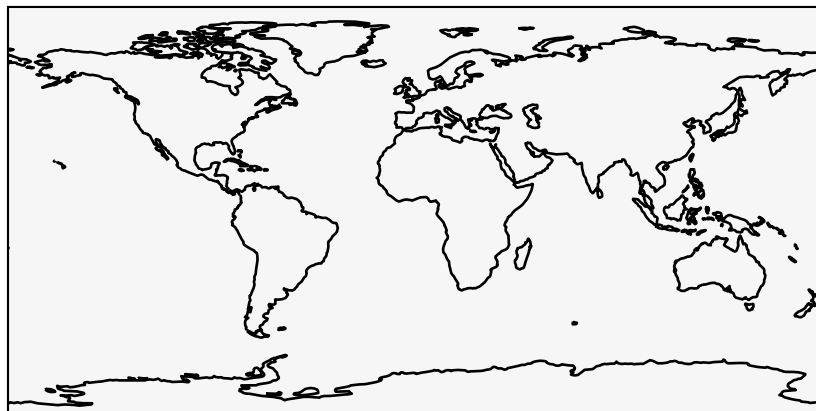
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



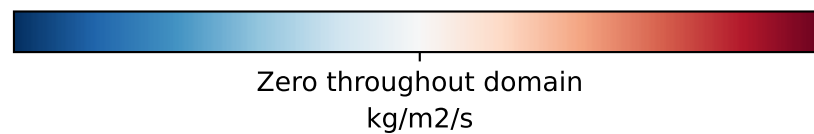
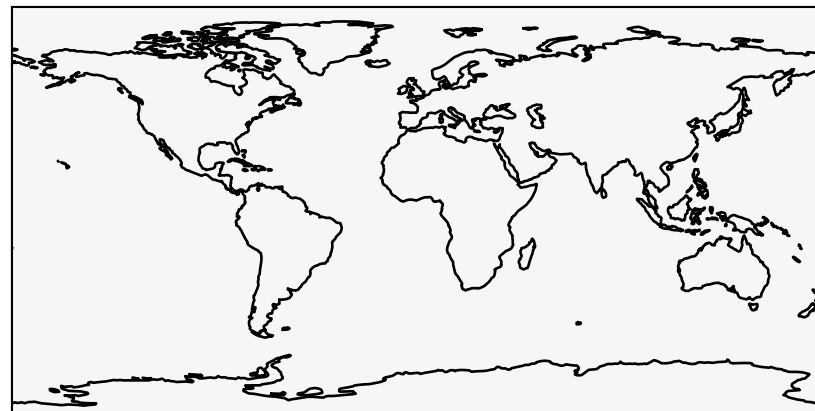
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



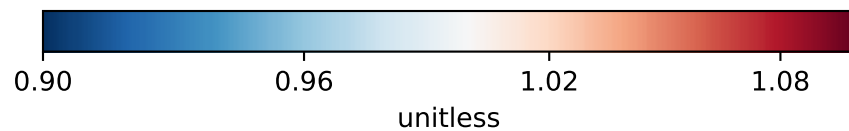
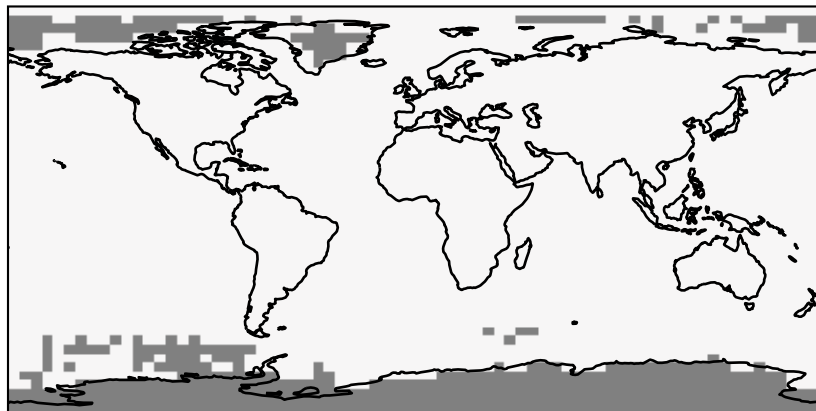
Difference
Dev - Ref, Dynamic Range



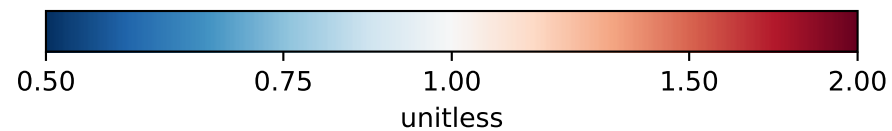
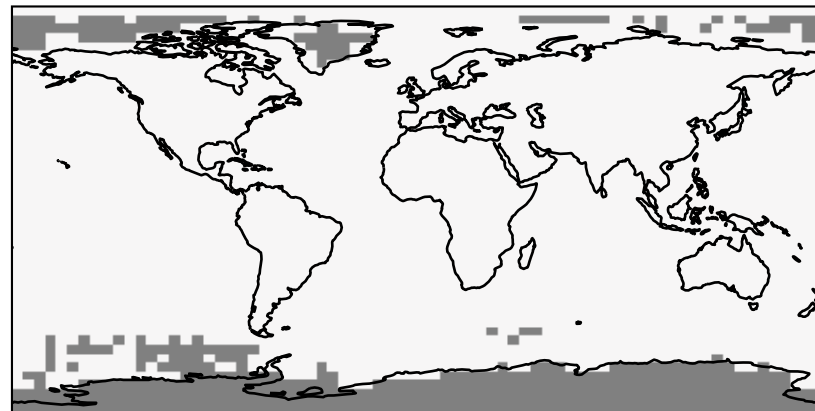
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

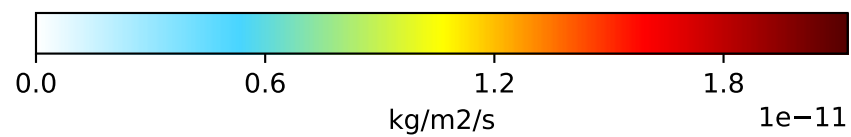
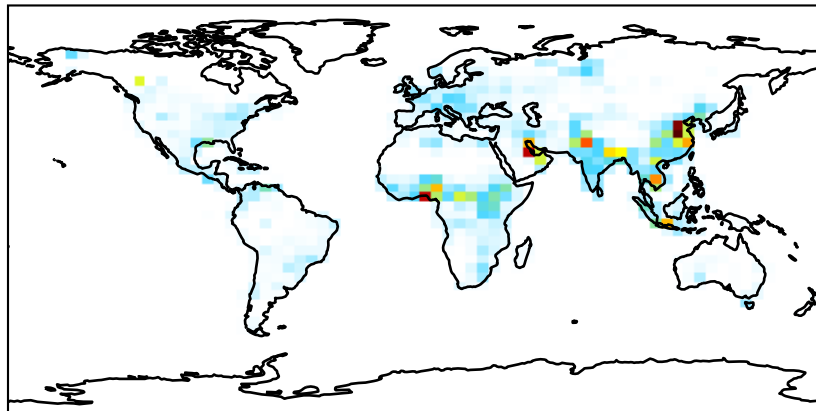


Ratio
Dev/Ref, Fixed Range

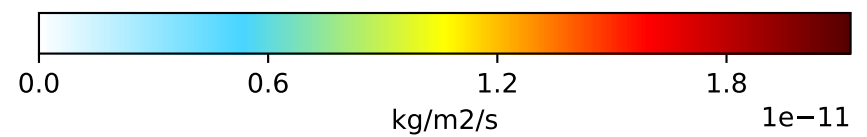
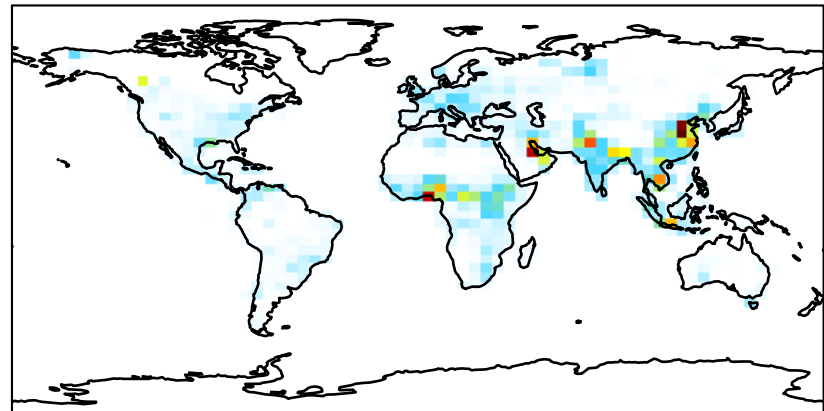


EmisBENZ_Total (Jan2019)

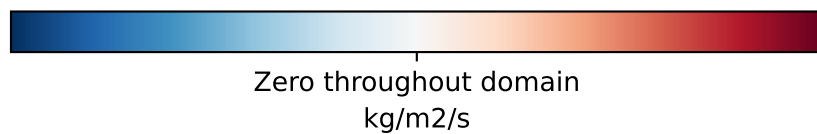
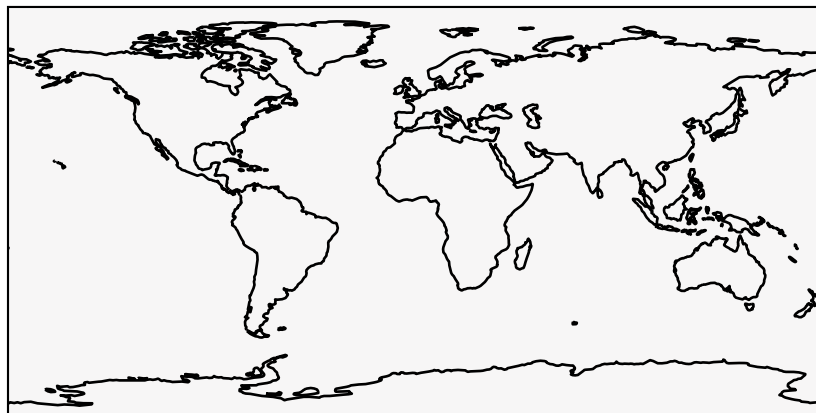
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



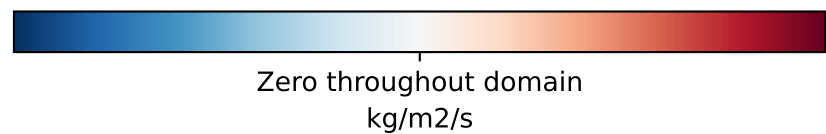
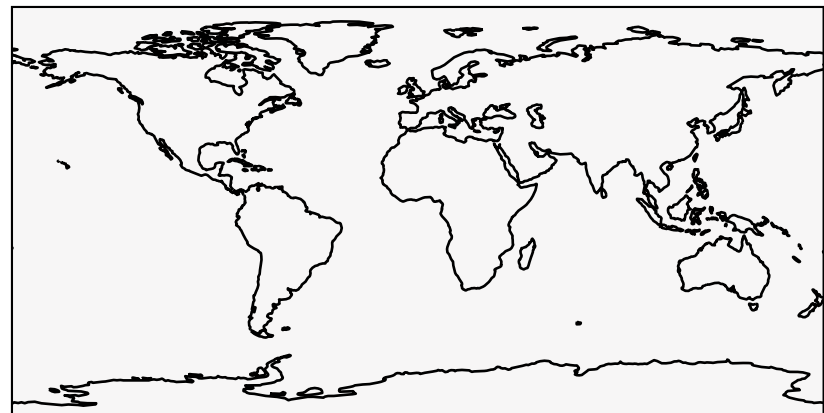
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



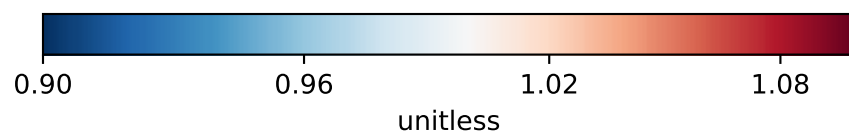
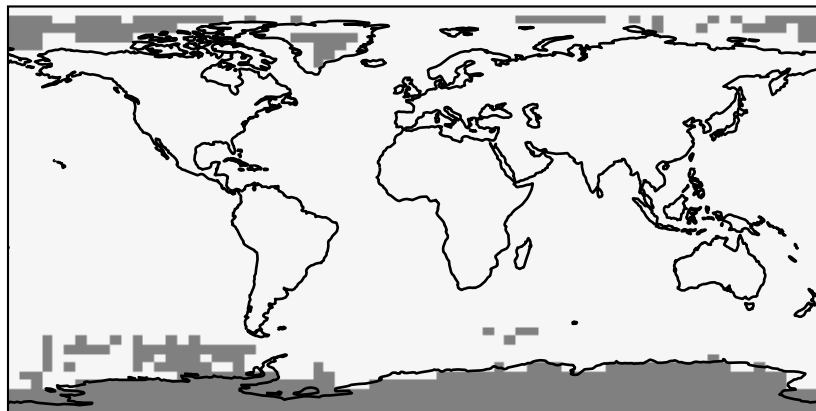
Difference
Dev - Ref, Dynamic Range



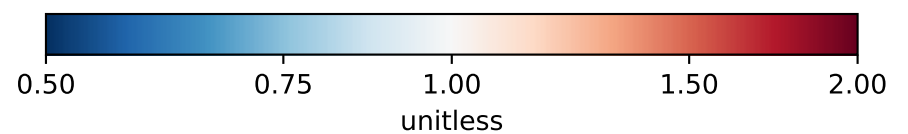
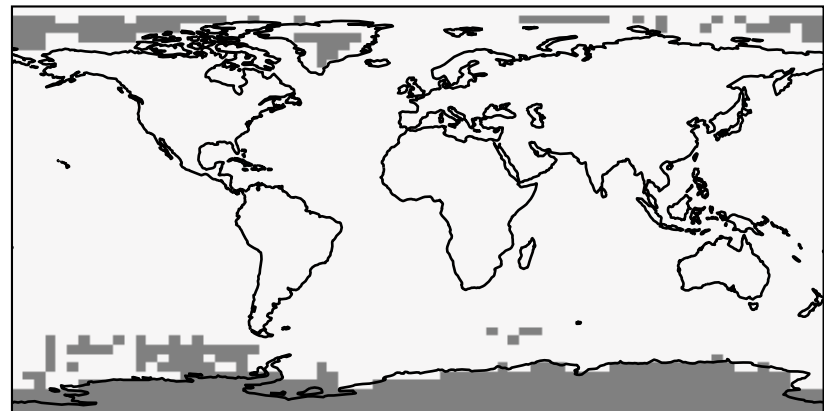
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

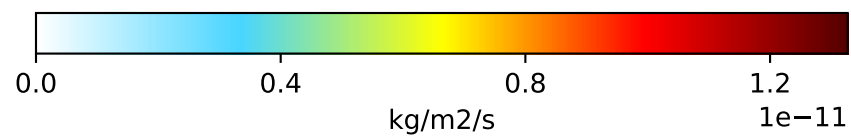
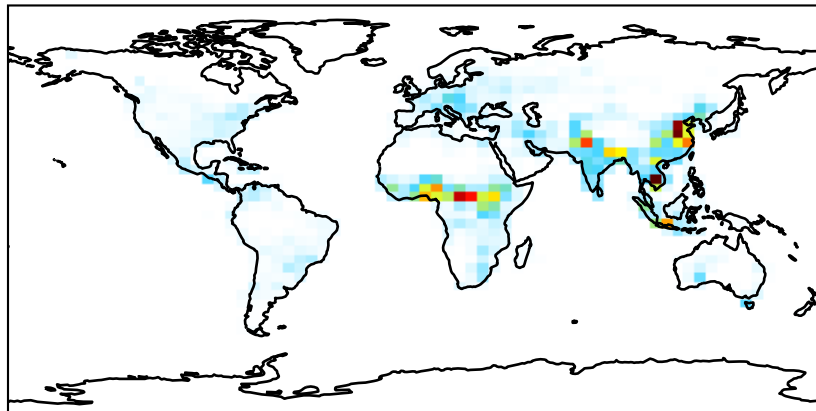


Ratio
Dev/Ref, Fixed Range

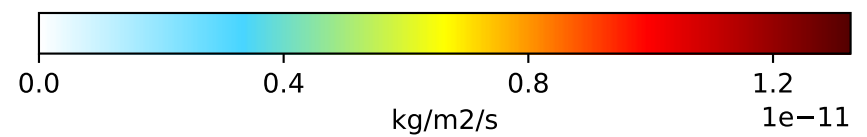
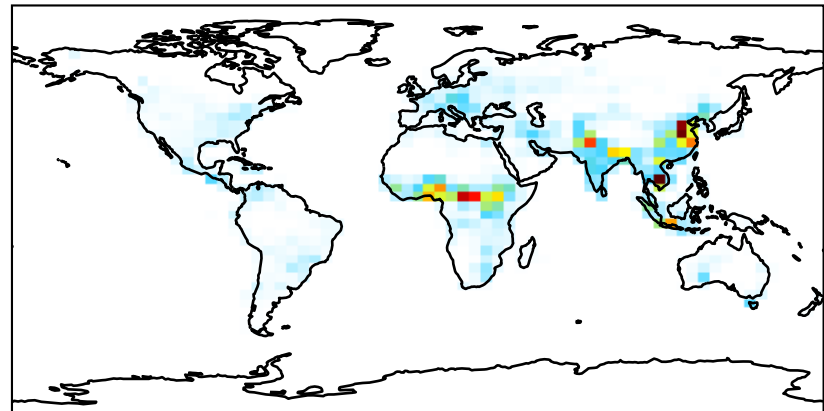


EmisC2H2_Total (Jan2019)

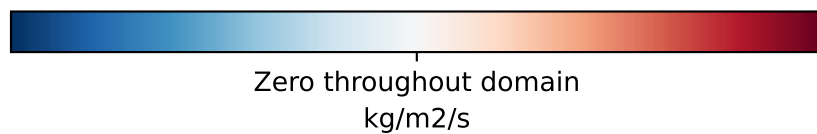
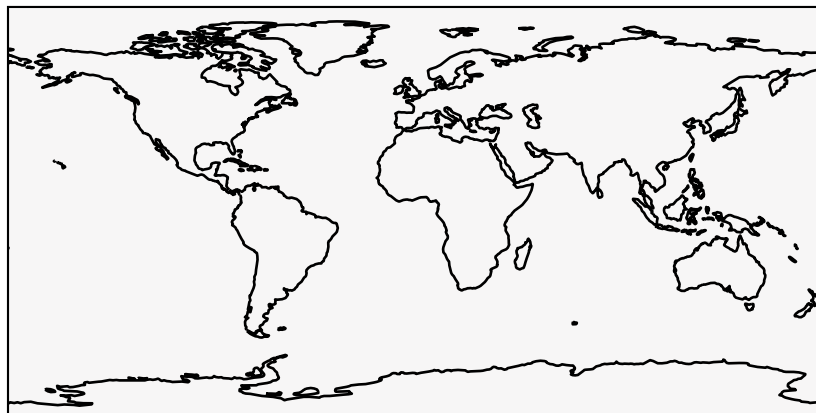
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



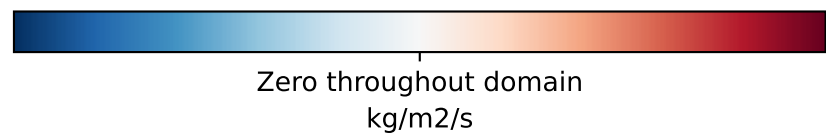
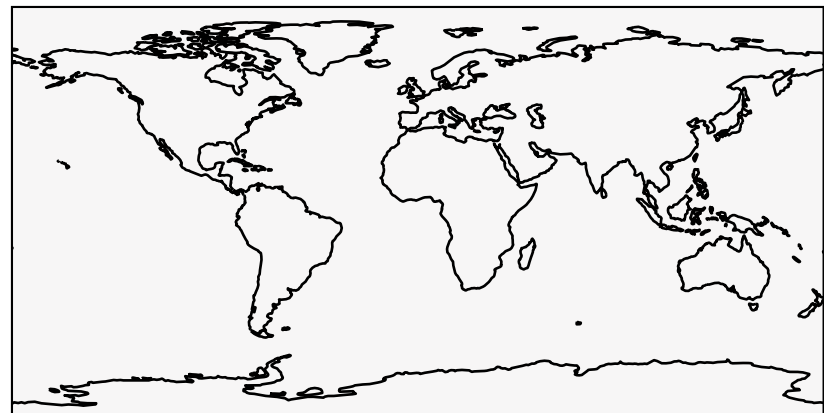
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



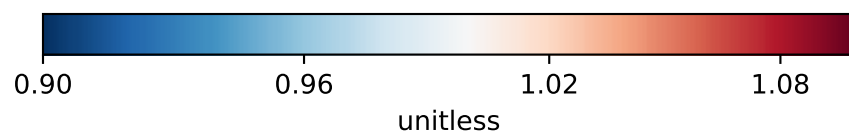
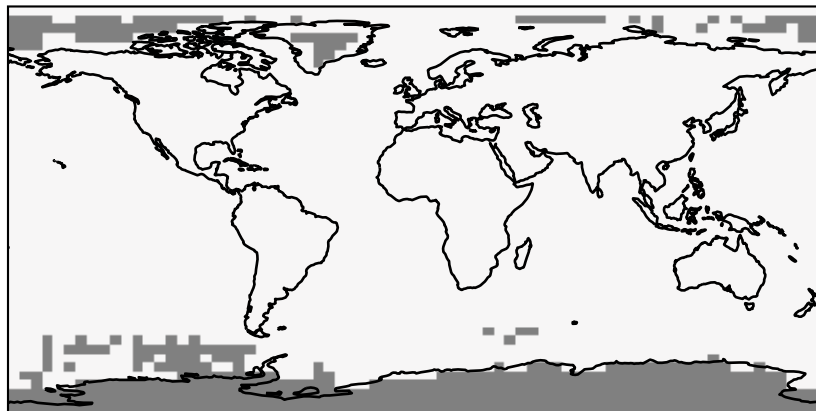
Difference
Dev - Ref, Dynamic Range



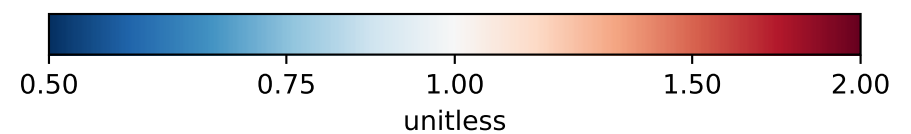
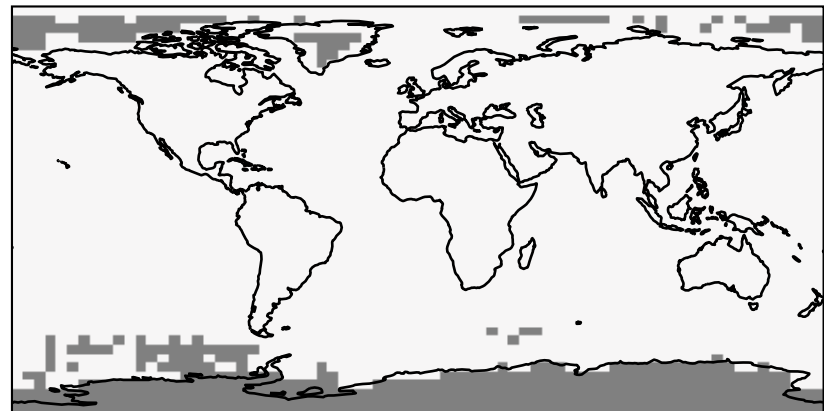
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

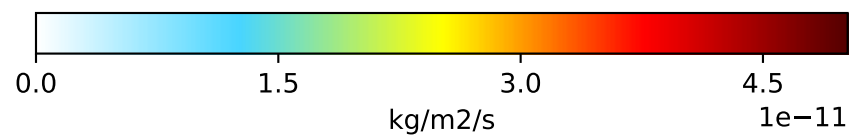
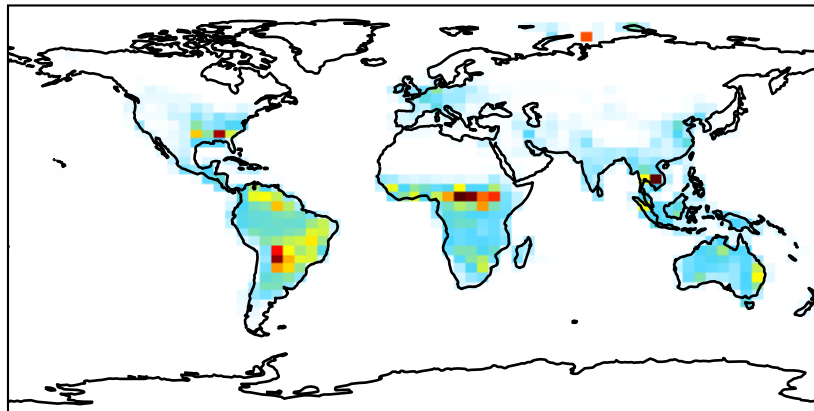


Ratio
Dev/Ref, Fixed Range

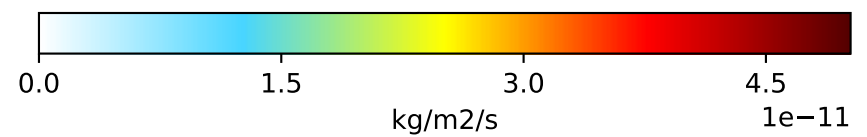
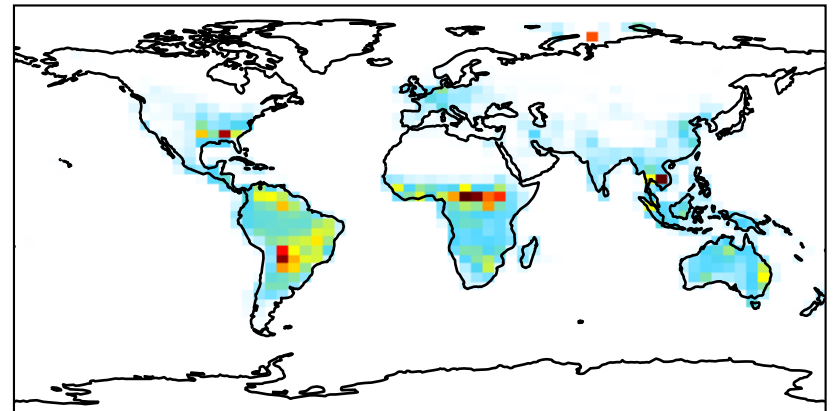


EmisC2H4_Total (Jan2019)

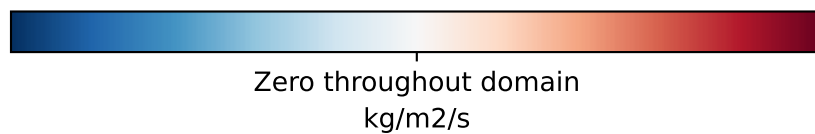
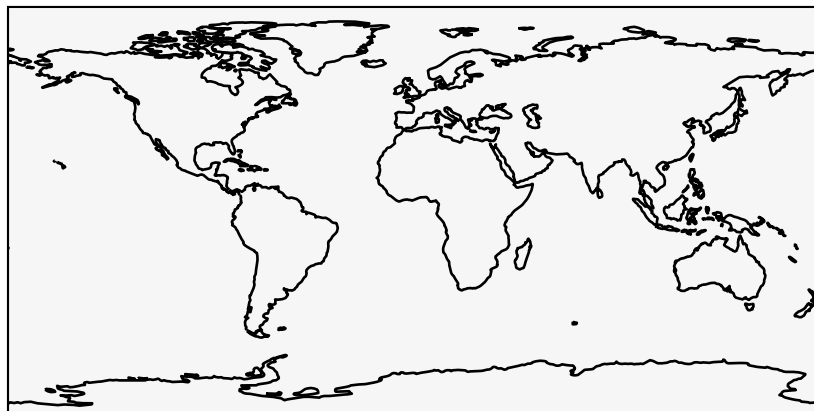
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



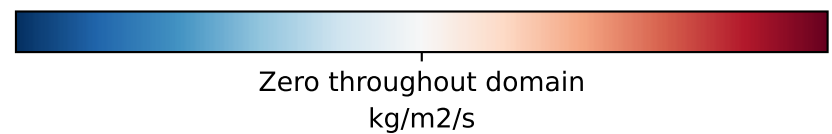
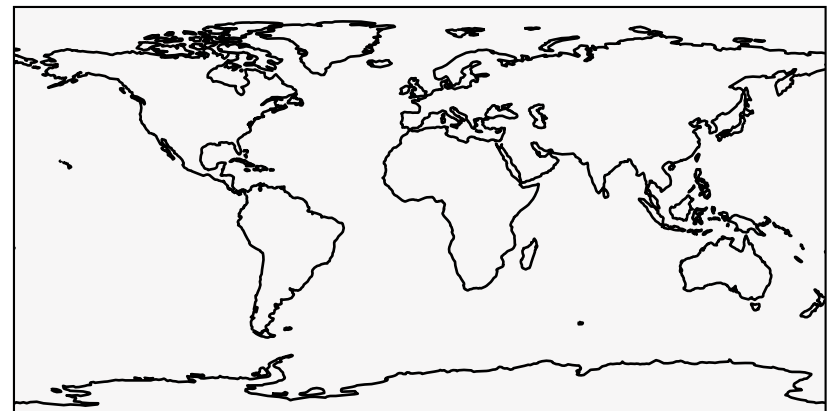
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



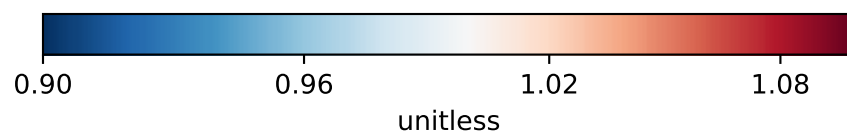
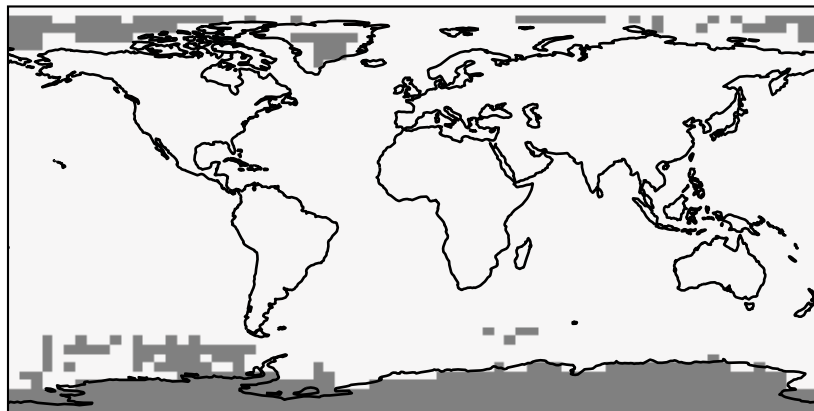
Difference
Dev - Ref, Dynamic Range



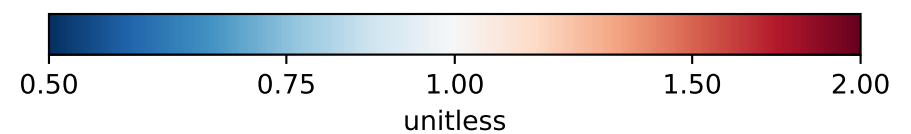
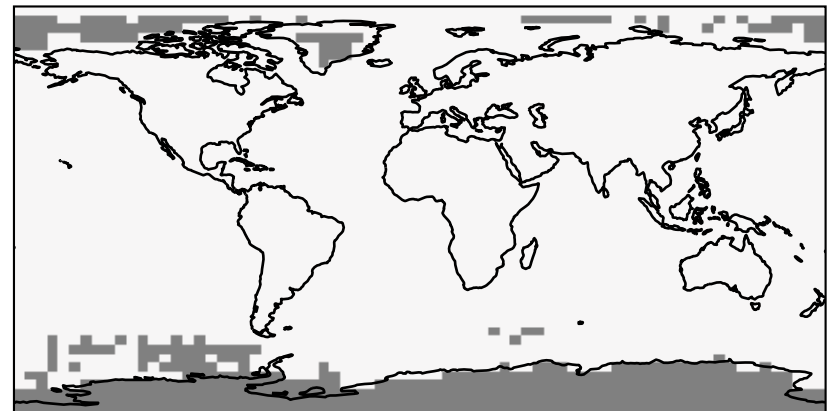
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

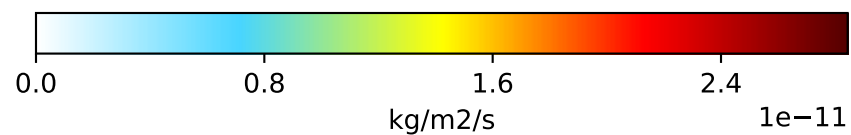
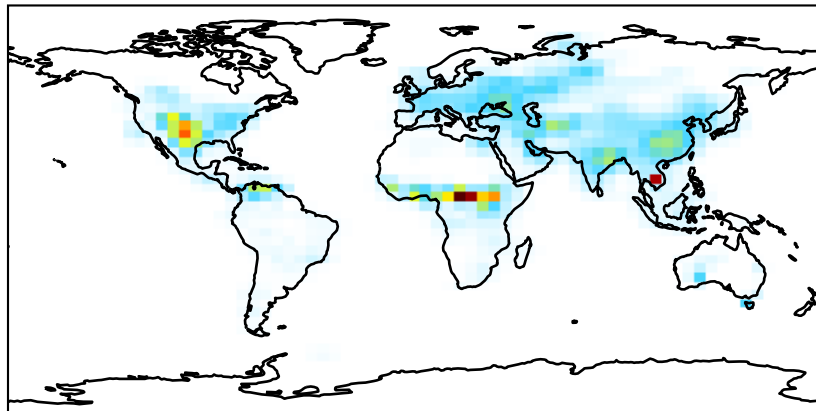


Ratio
Dev/Ref, Fixed Range

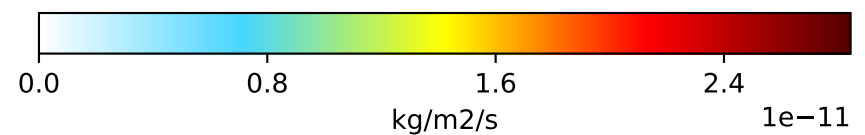
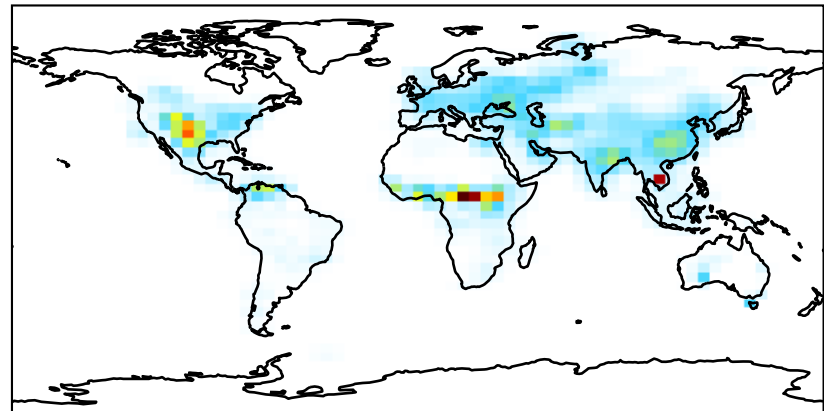


EmisC2H6_Total (Jan2019)

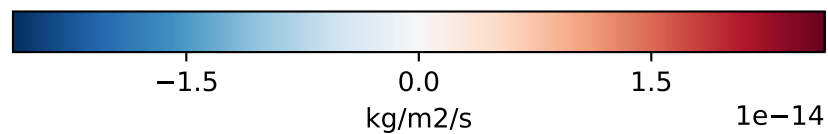
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



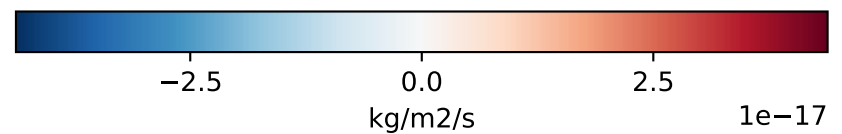
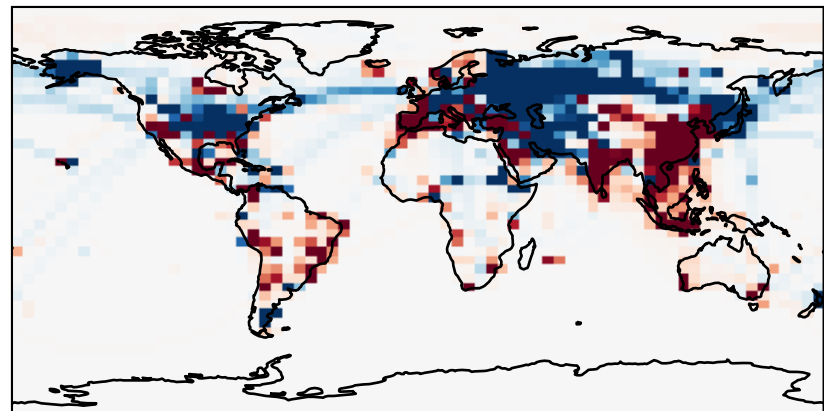
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



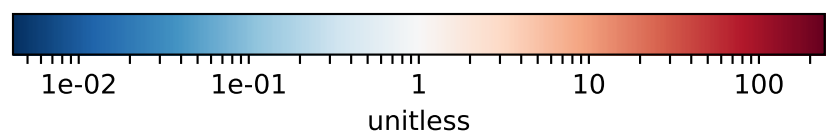
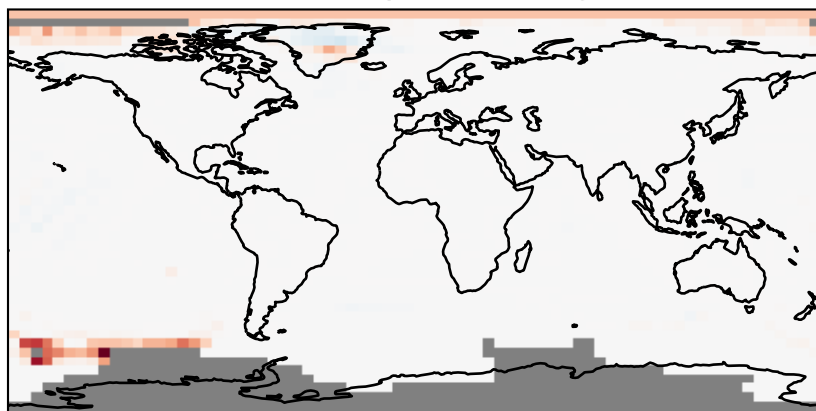
Difference
Dev - Ref, Dynamic Range



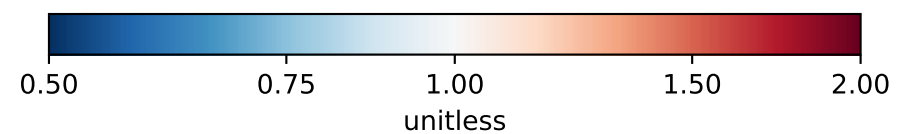
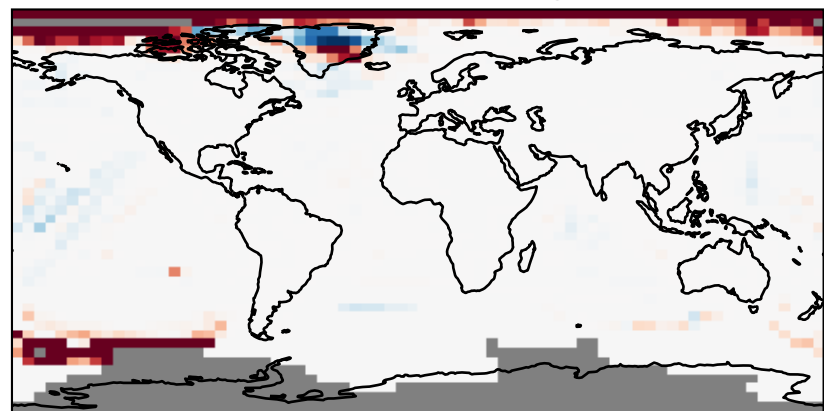
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

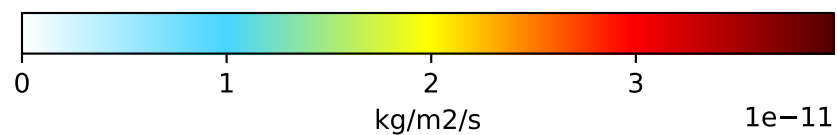
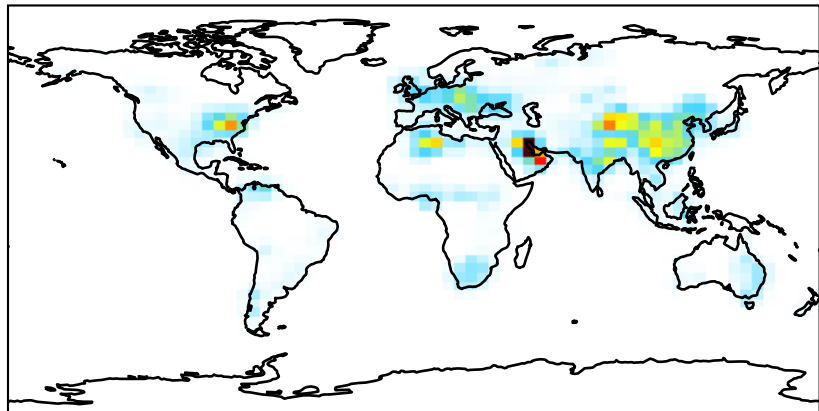


Ratio
Dev/Ref, Fixed Range

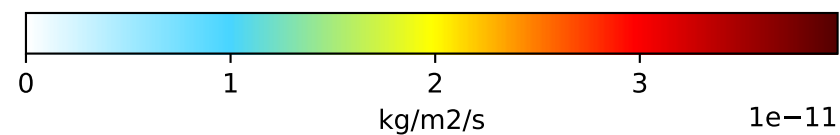
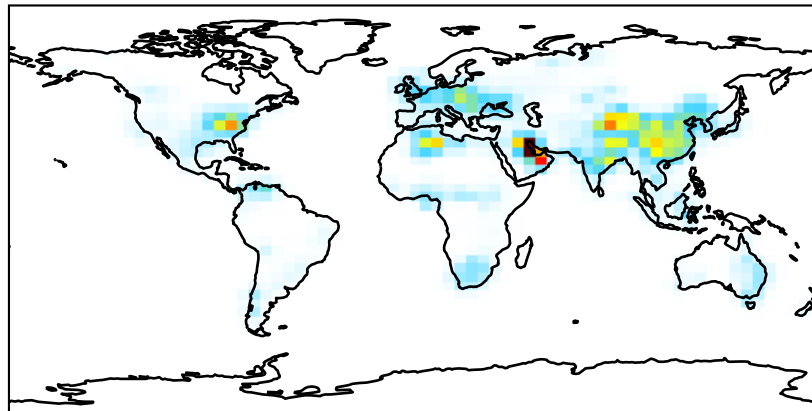


EmisC3H8_Total (Jan2019)

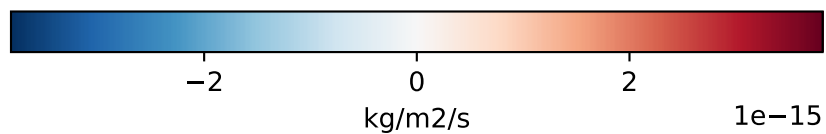
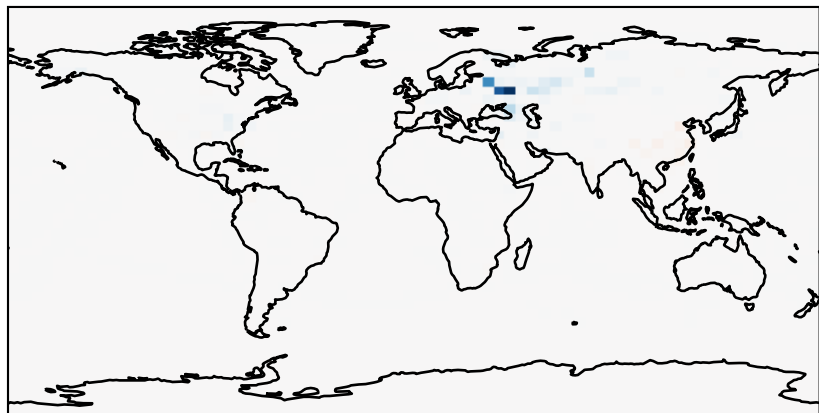
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



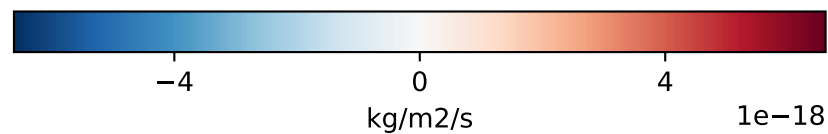
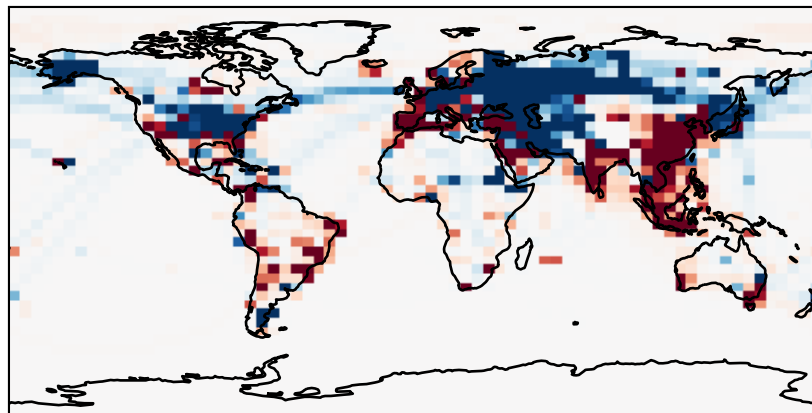
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



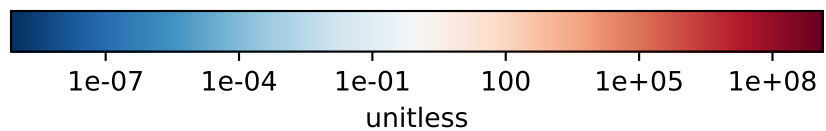
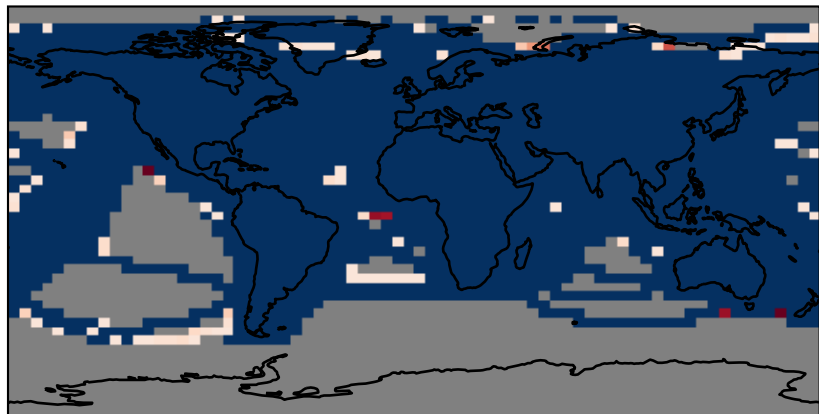
Difference
Dev - Ref, Dynamic Range



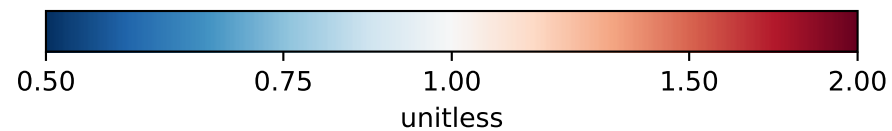
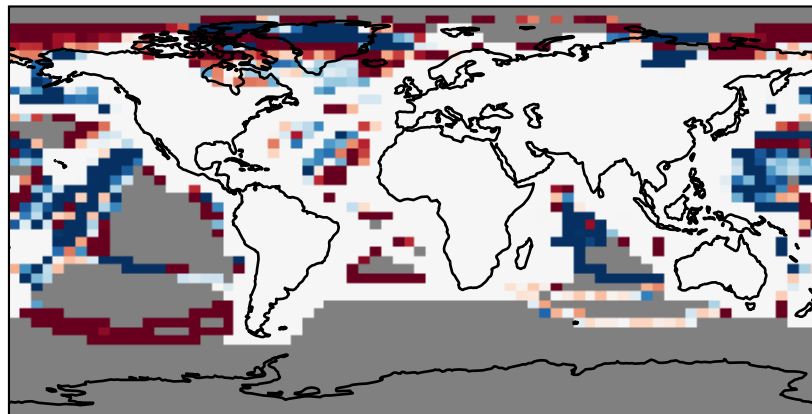
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

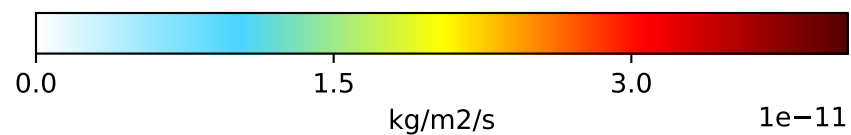
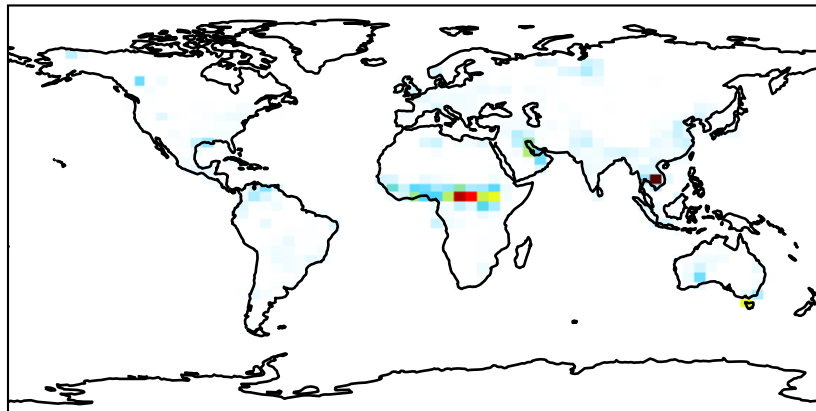


Ratio
Dev/Ref, Fixed Range

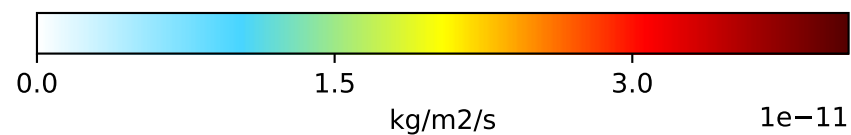
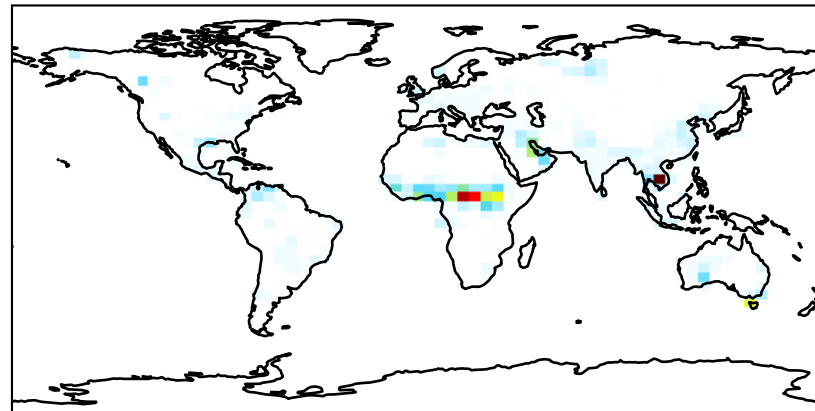


EmisCH2O_Total (Jan2019)

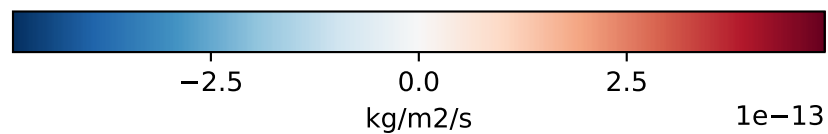
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



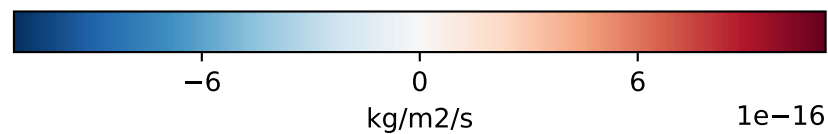
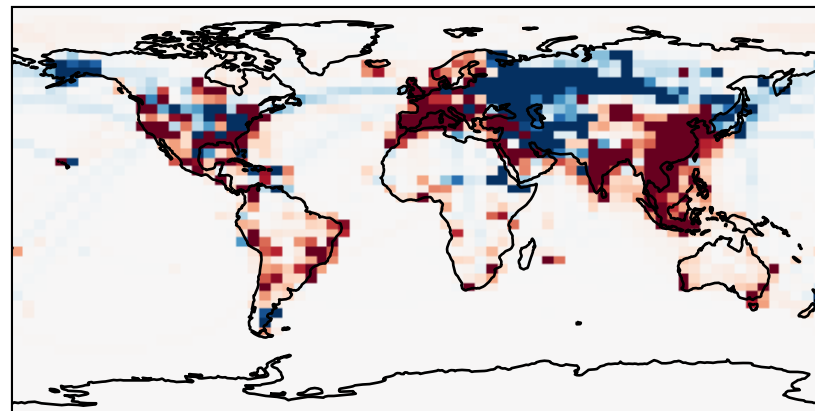
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



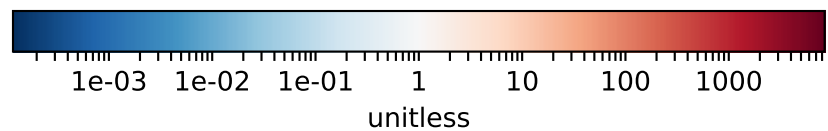
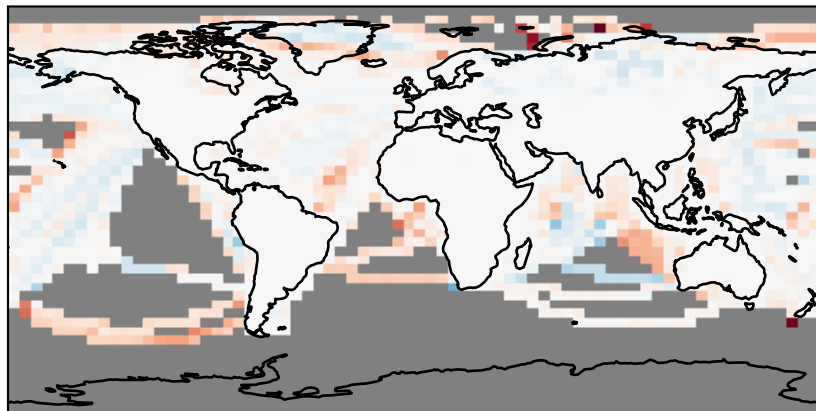
Difference
Dev - Ref, Dynamic Range



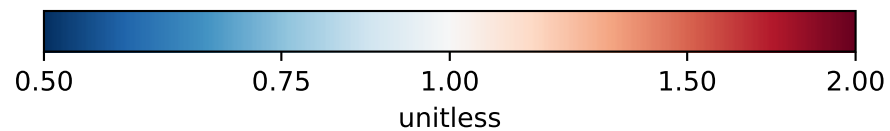
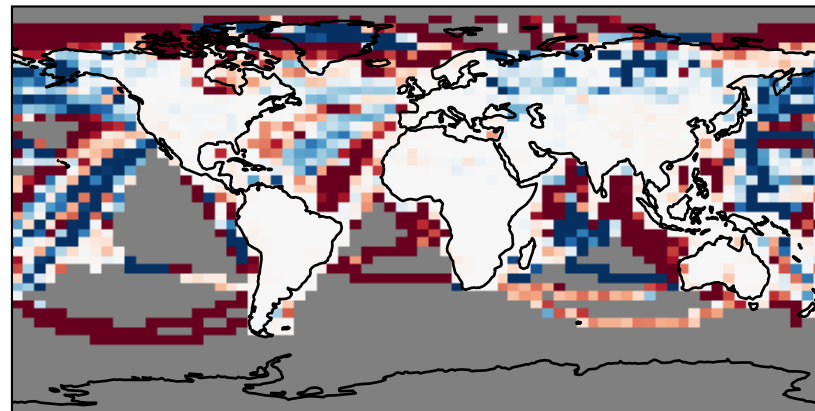
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

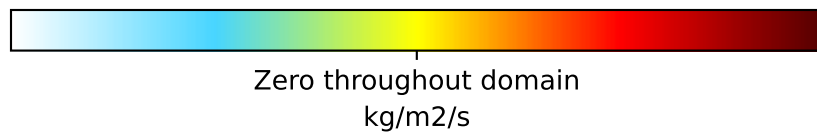
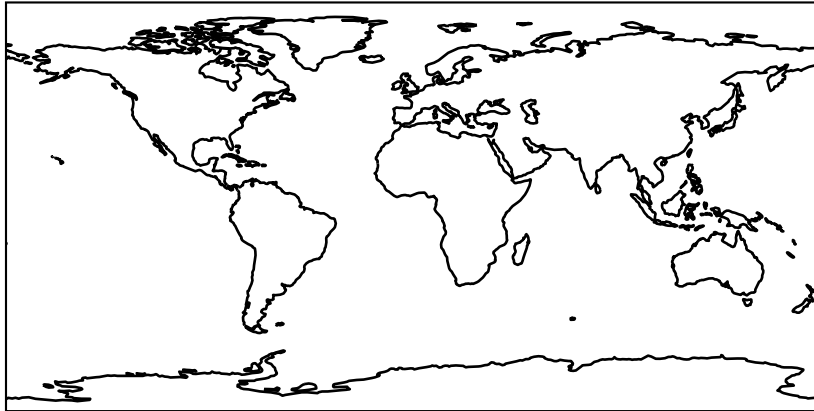


Ratio
Dev/Ref, Fixed Range

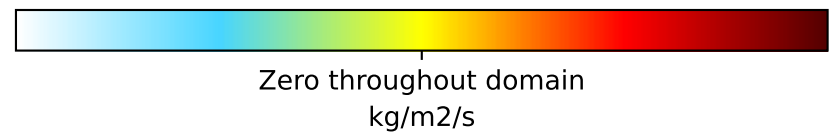
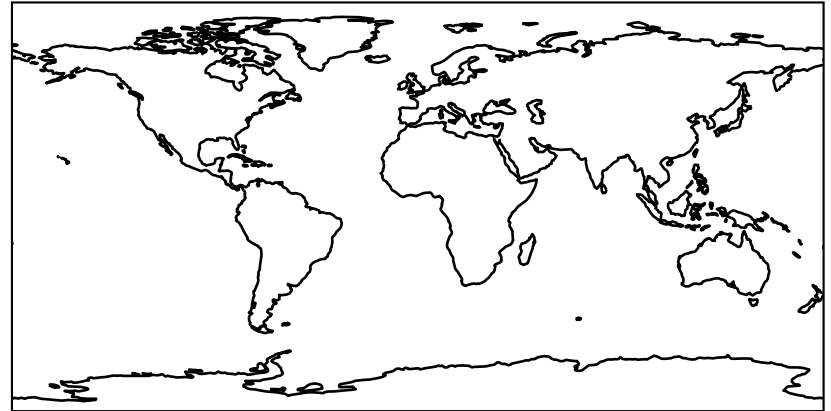


EmisCl2_Total (Jan2019)

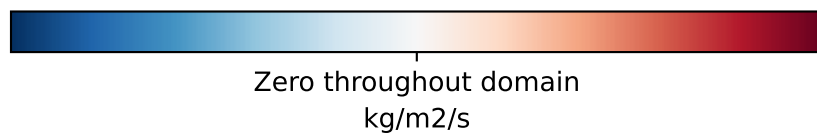
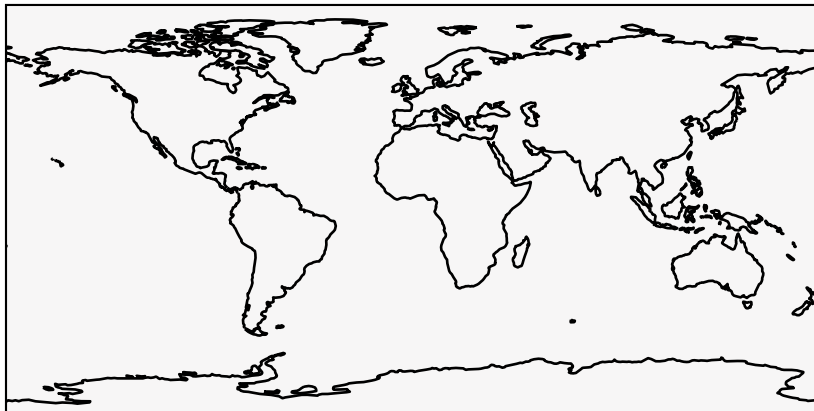
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



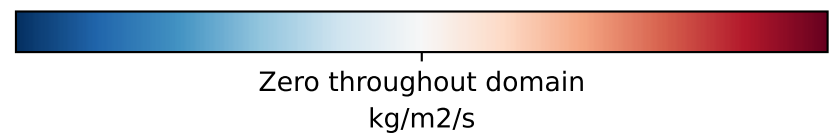
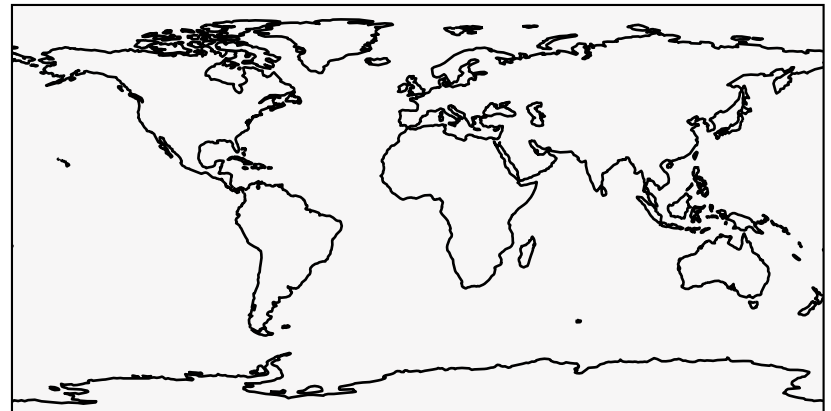
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



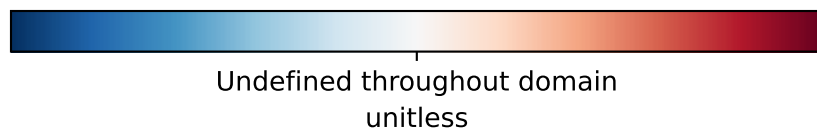
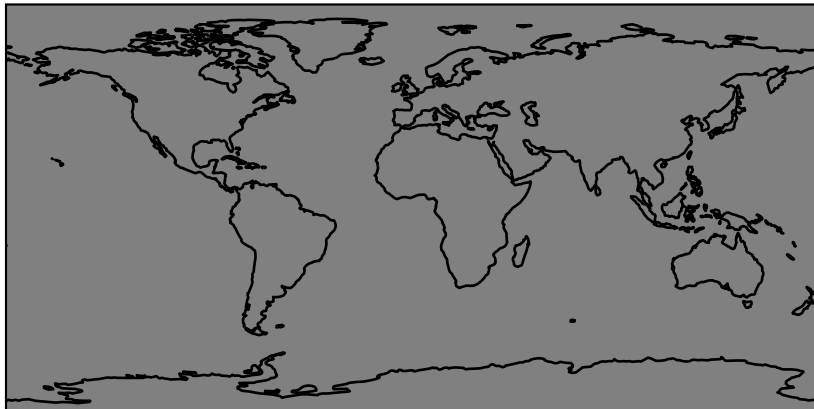
Difference
Dev - Ref, Dynamic Range



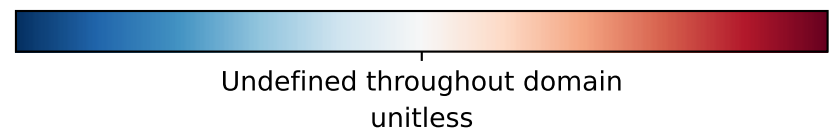
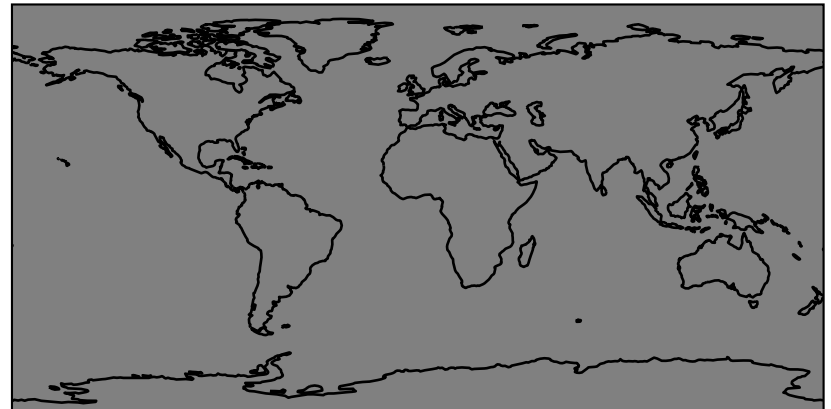
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

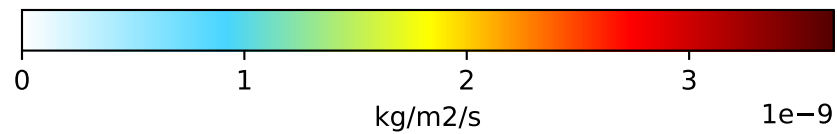
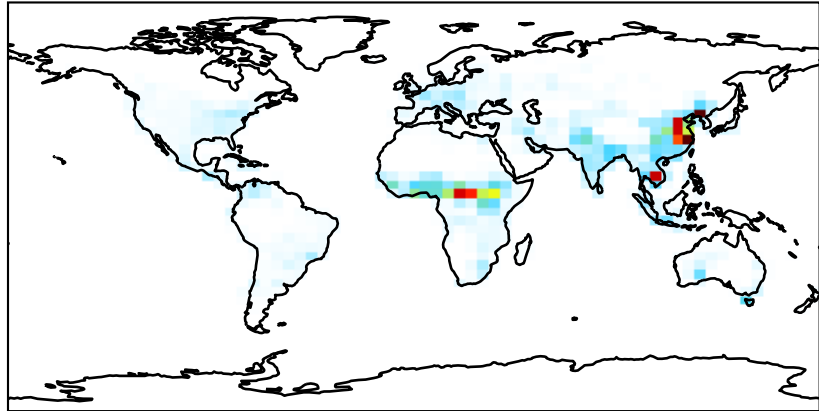


Ratio
Dev/Ref, Fixed Range

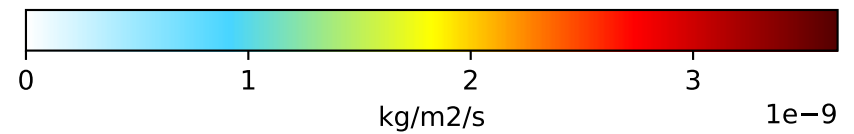
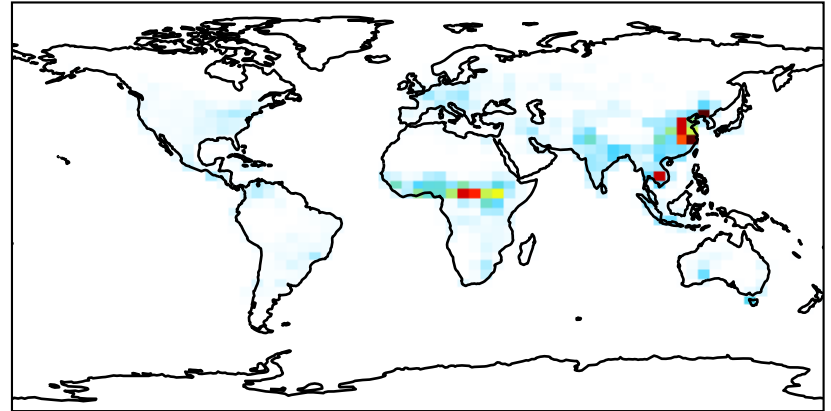


EmisCO_Total (Jan2019)

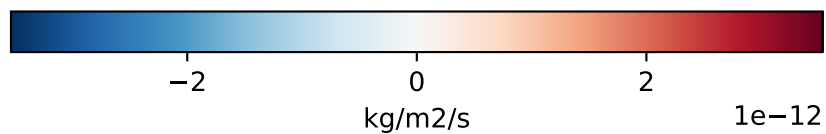
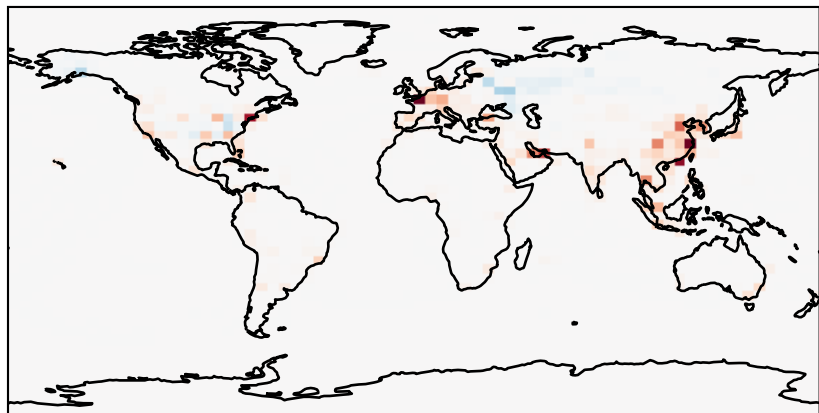
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



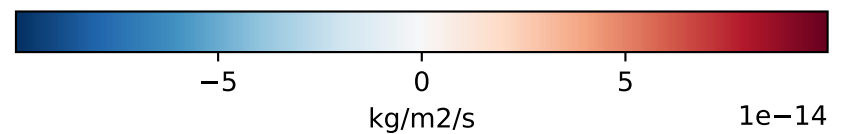
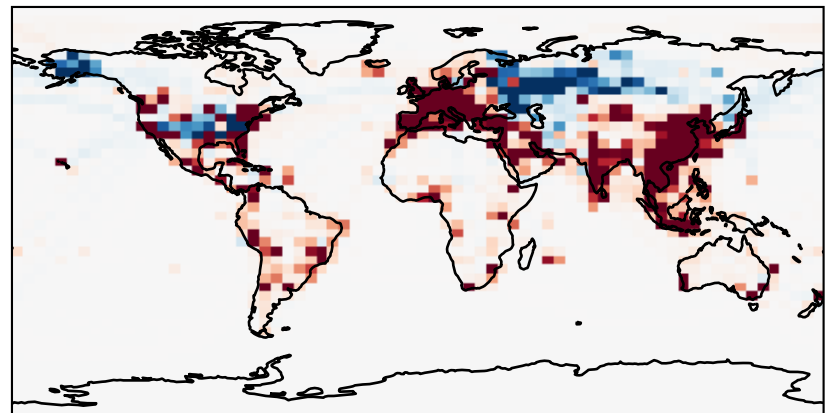
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



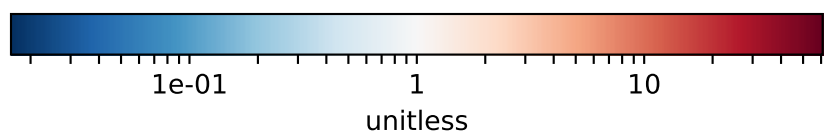
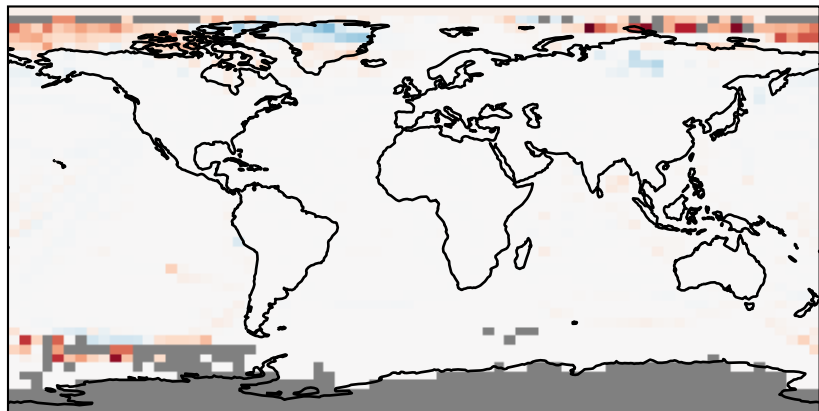
Difference
Dev - Ref, Dynamic Range



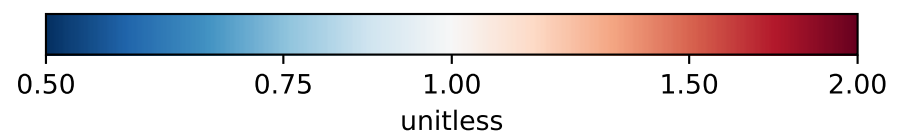
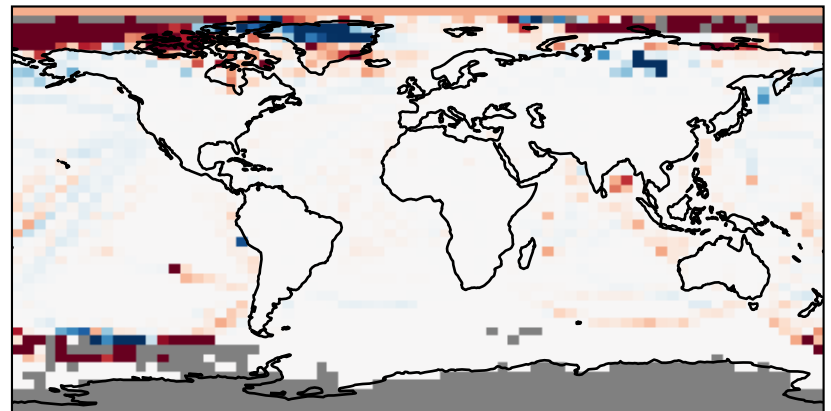
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

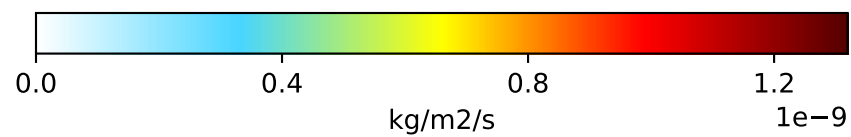
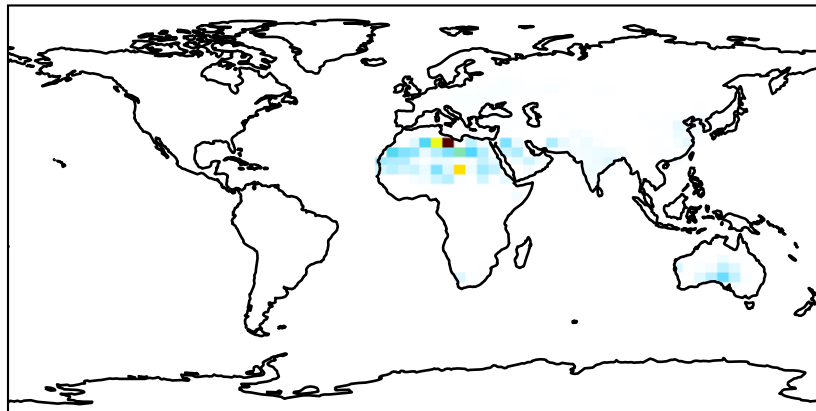


Ratio
Dev/Ref, Fixed Range

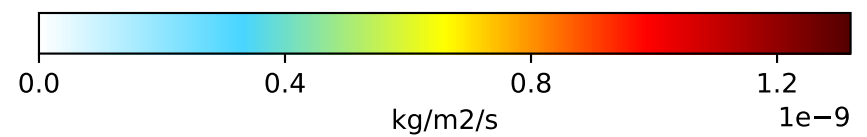
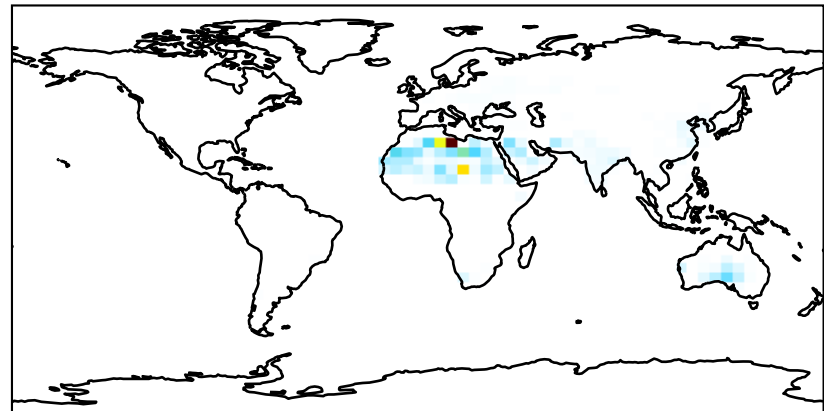


EmisDST1_Total (Jan2019)

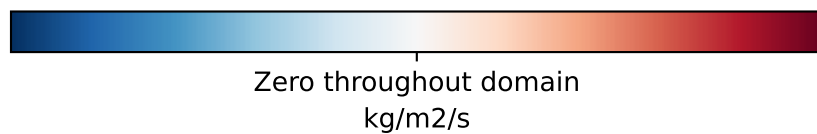
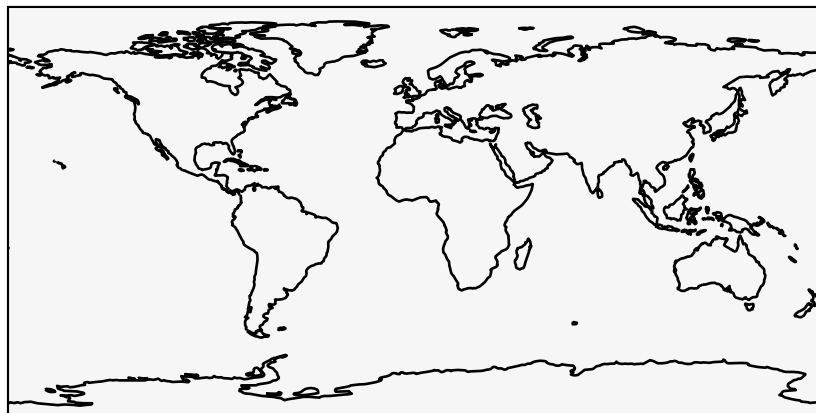
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



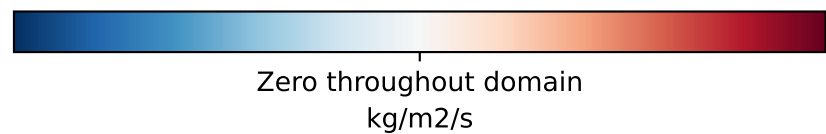
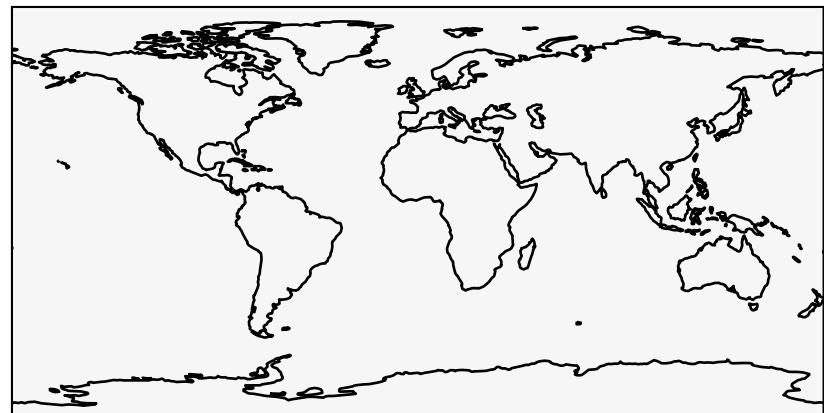
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



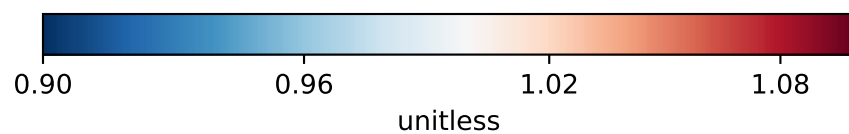
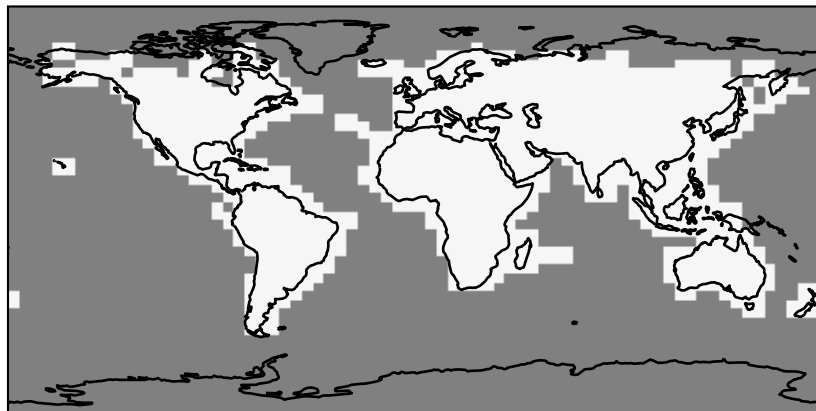
Difference
Dev - Ref, Dynamic Range



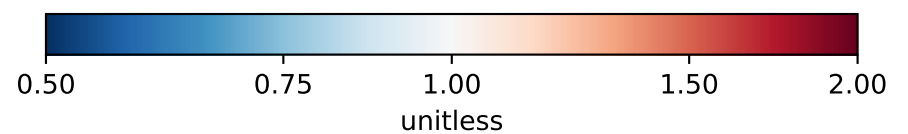
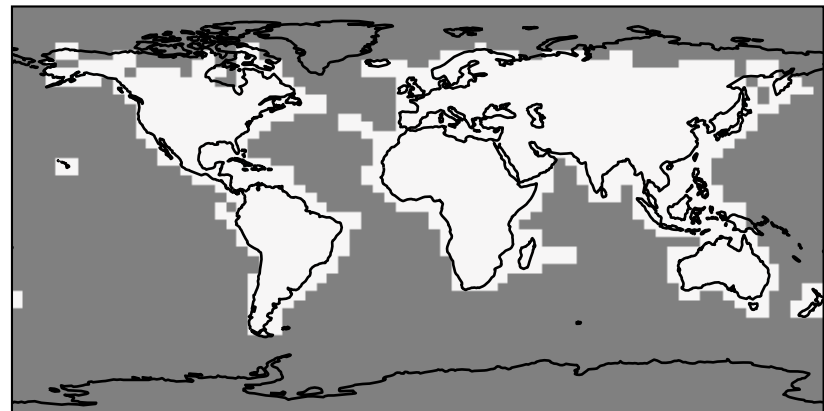
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

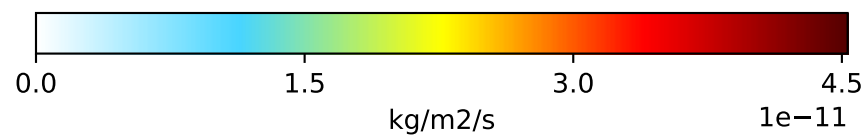
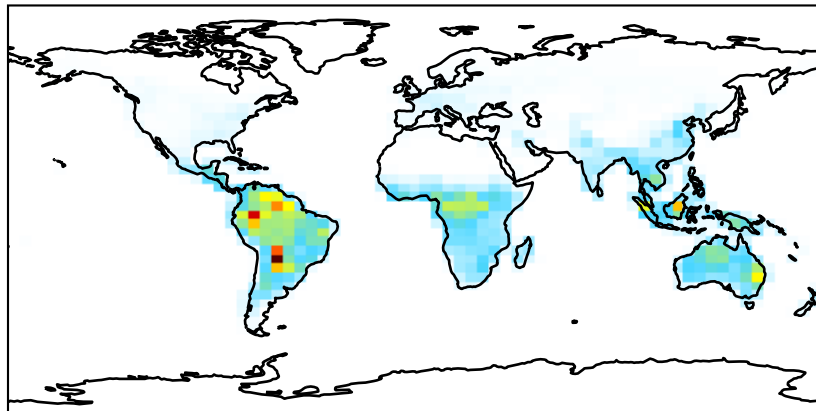


Ratio
Dev/Ref, Fixed Range

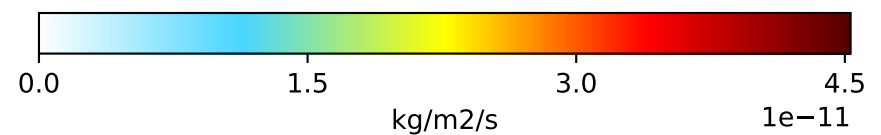
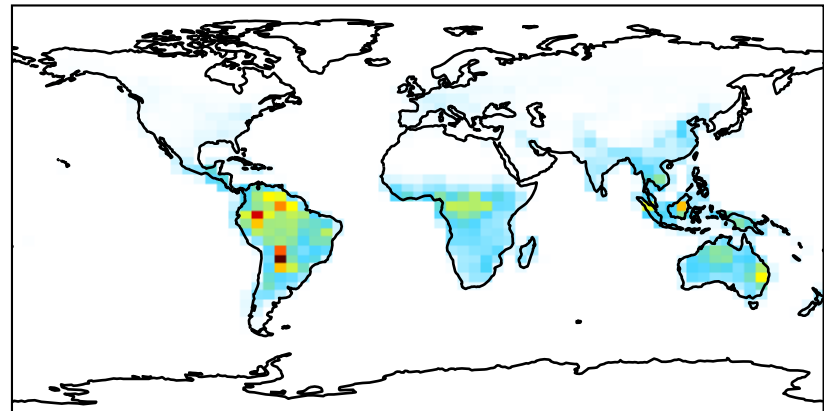


EmisEOH_Total (Jan2019)

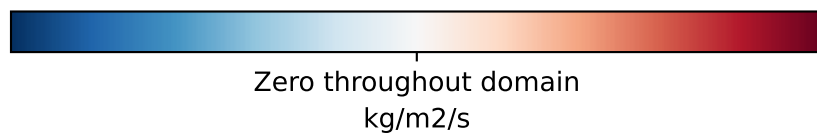
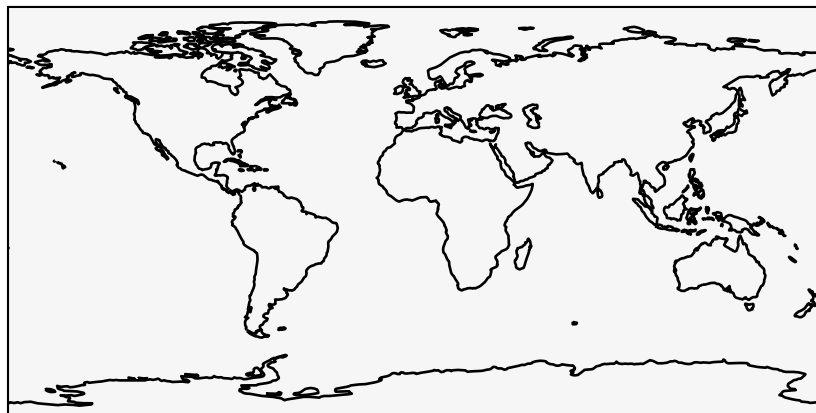
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



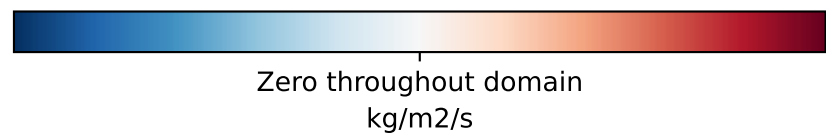
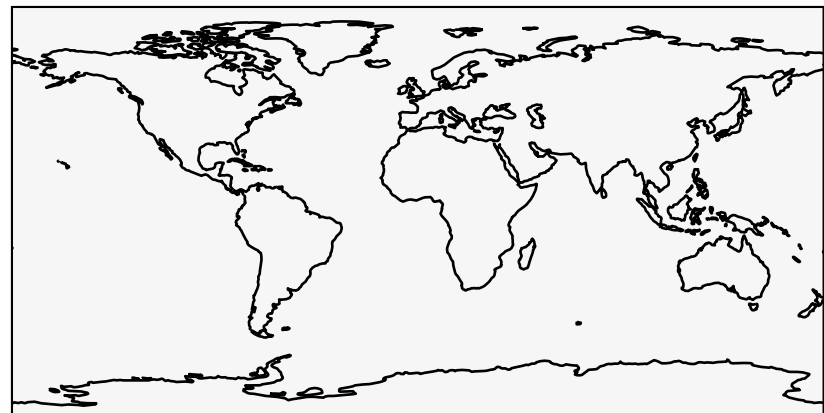
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



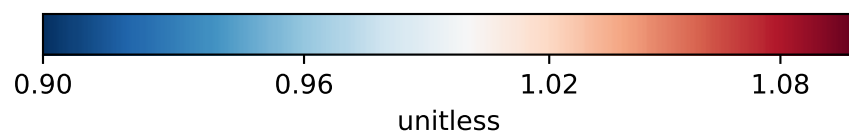
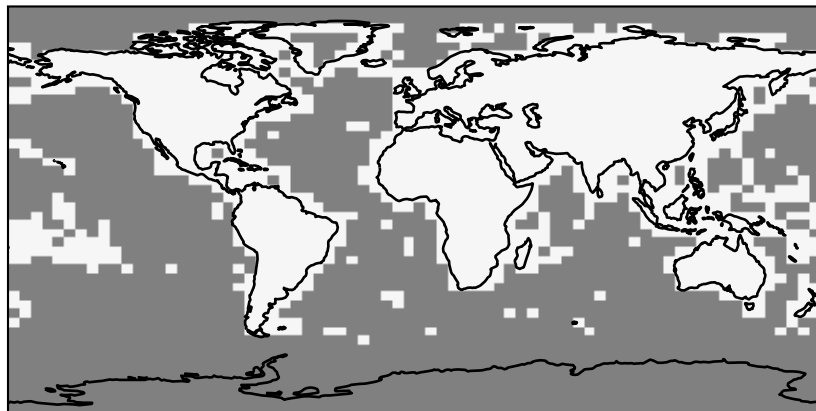
Difference
Dev - Ref, Dynamic Range



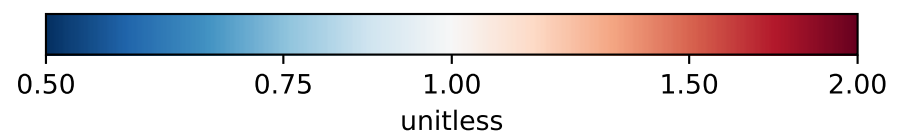
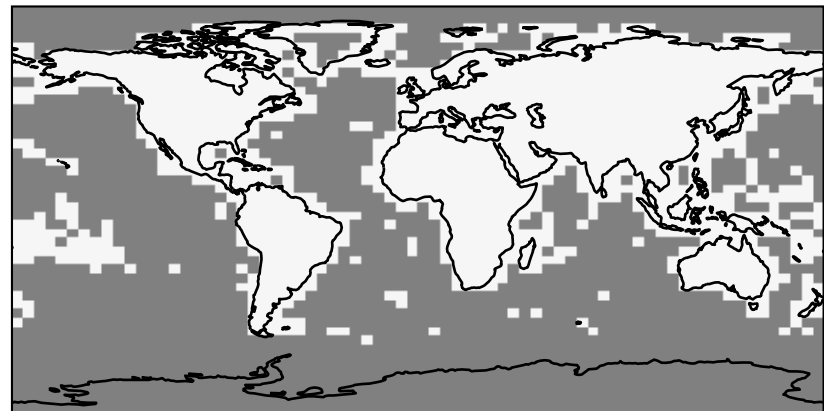
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

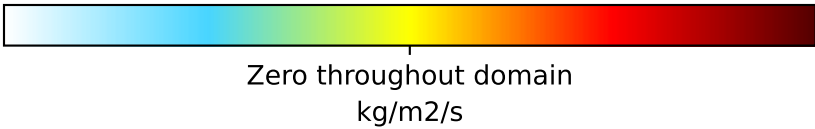
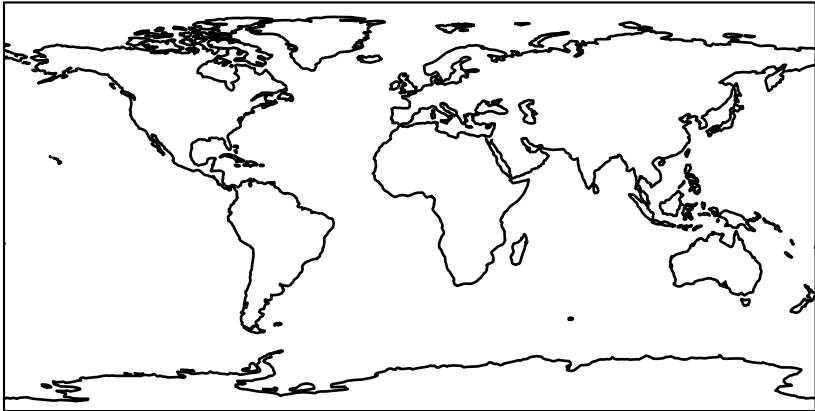


Ratio
Dev/Ref, Fixed Range

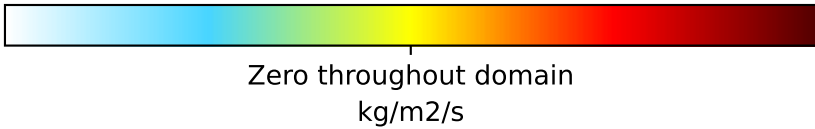
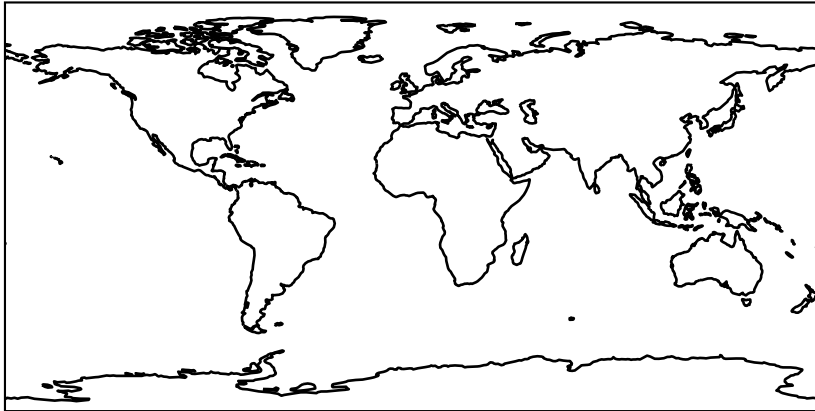


EmisGLYC_Total (Jan2019)

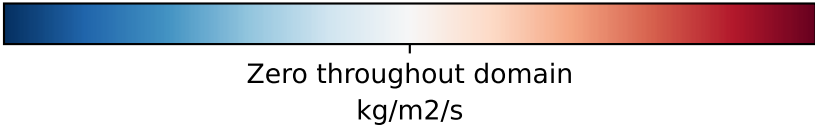
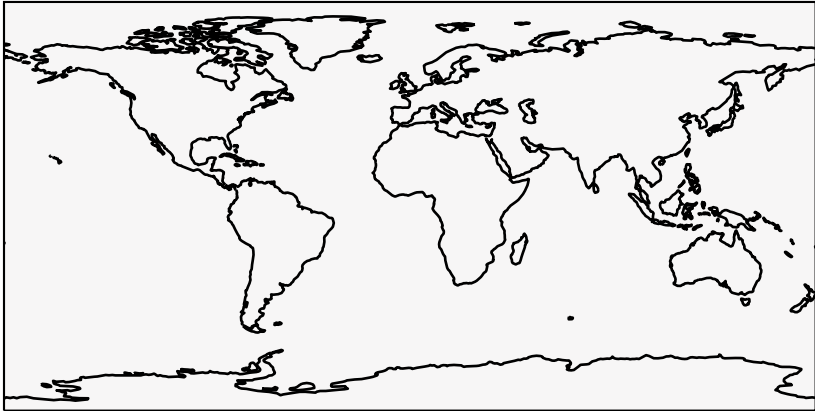
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



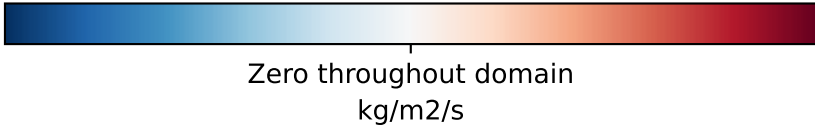
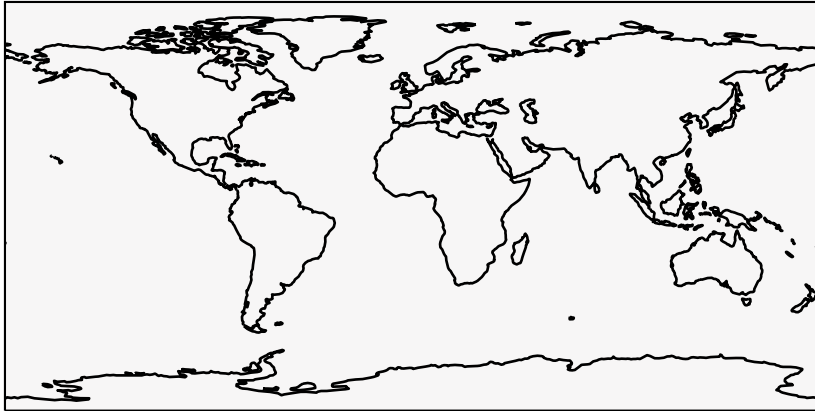
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



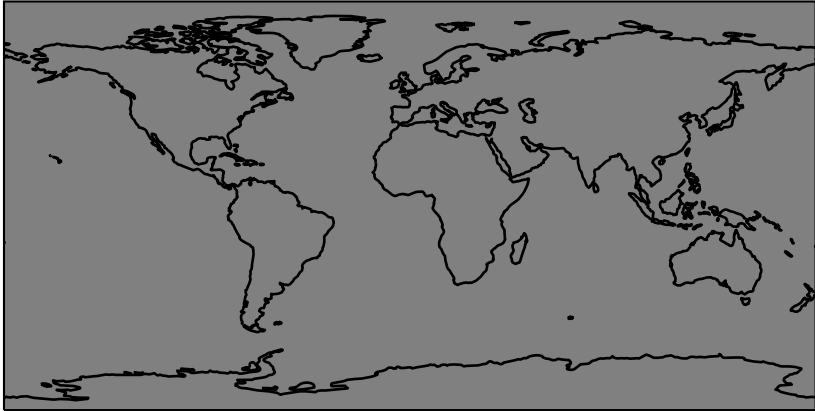
Difference
Dev - Ref, Dynamic Range



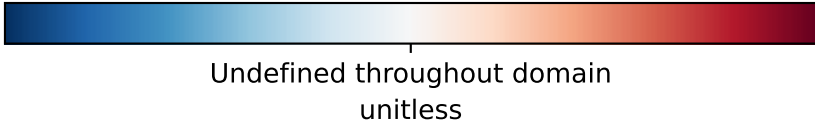
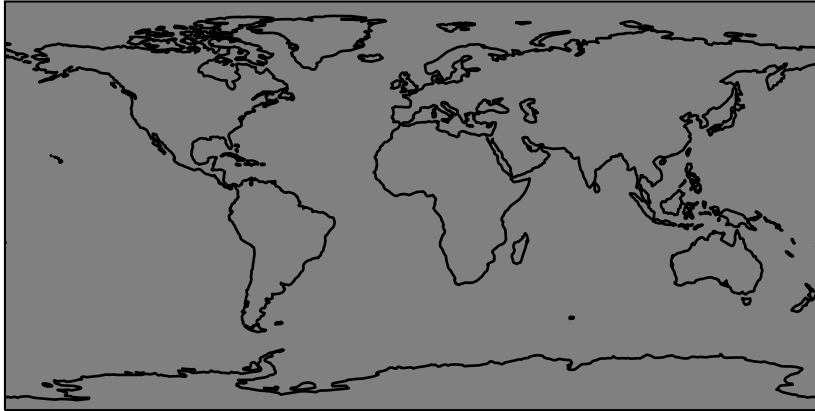
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

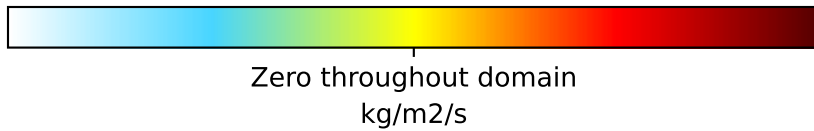
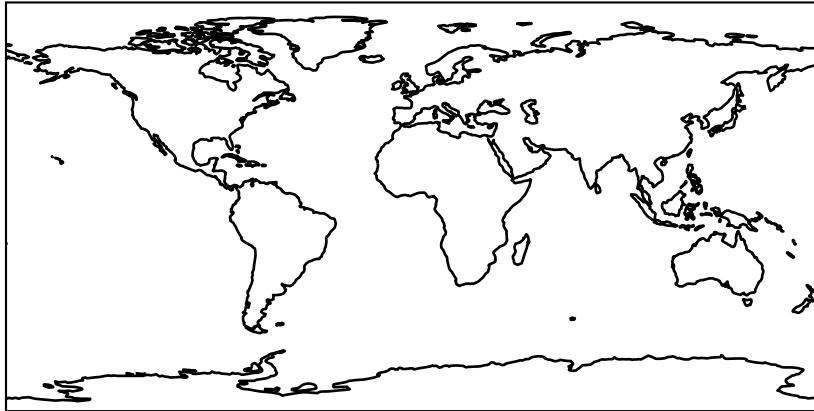


Ratio
Dev/Ref, Fixed Range

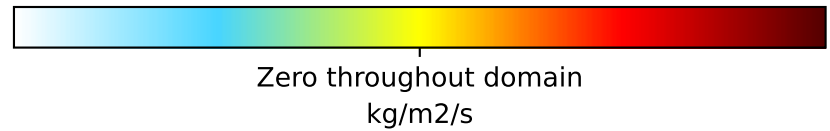
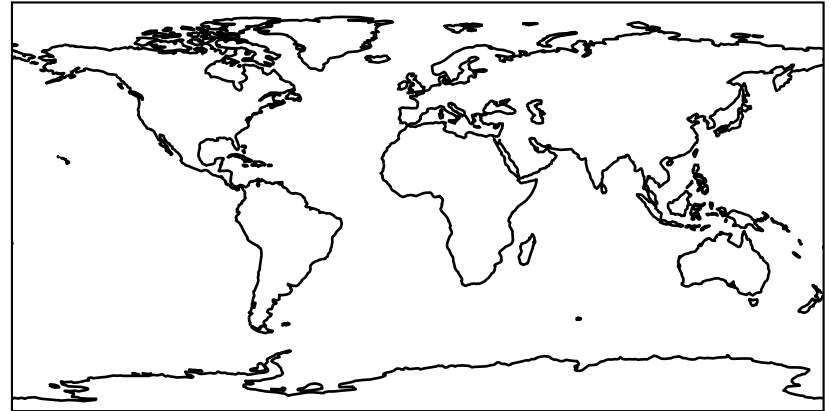


EmisHAC_Total (Jan2019)

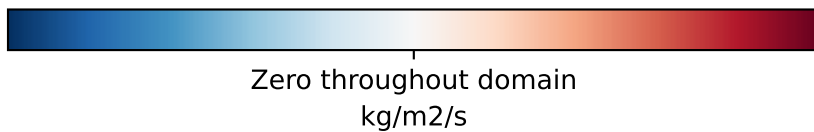
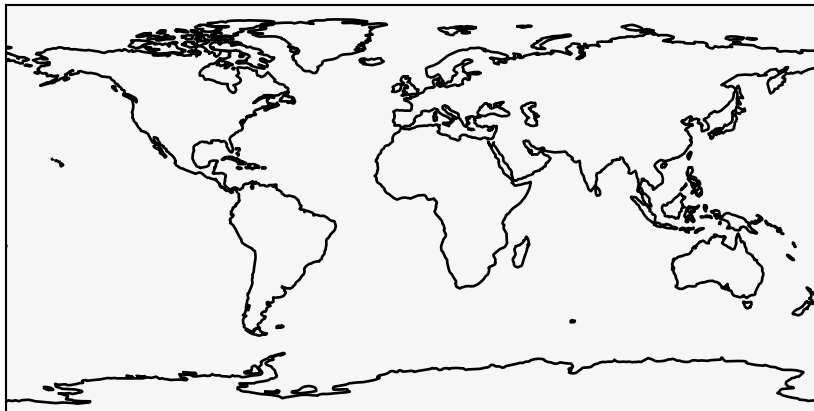
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



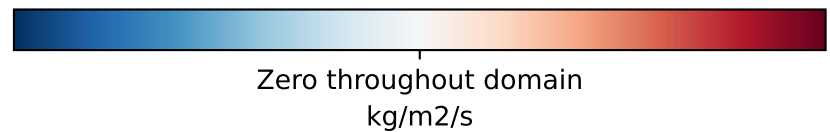
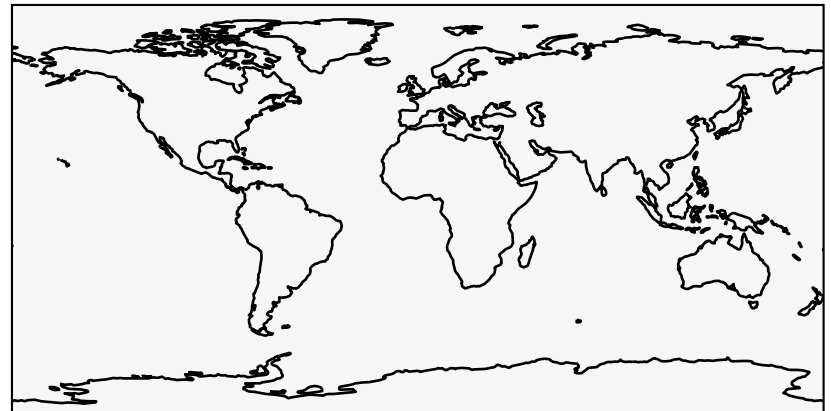
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



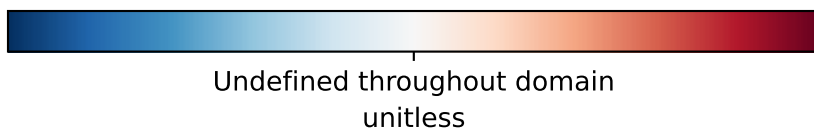
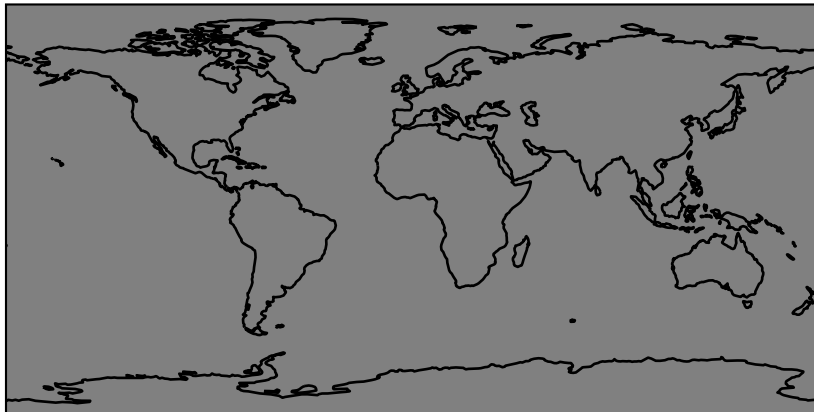
Difference
Dev - Ref, Dynamic Range



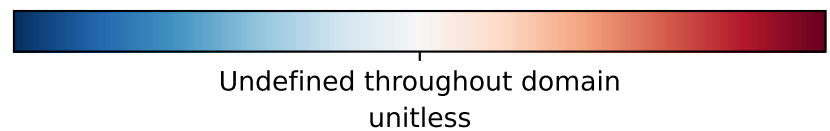
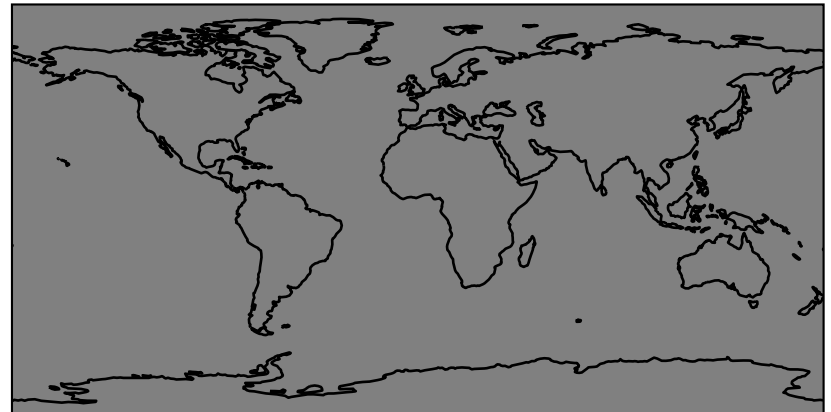
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

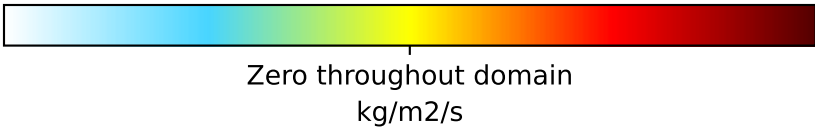


Ratio
Dev/Ref, Fixed Range

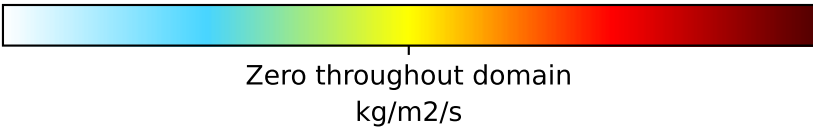


EmisHCl_Total (Jan2019)

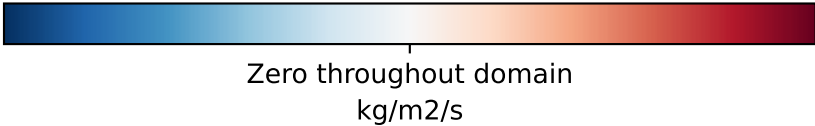
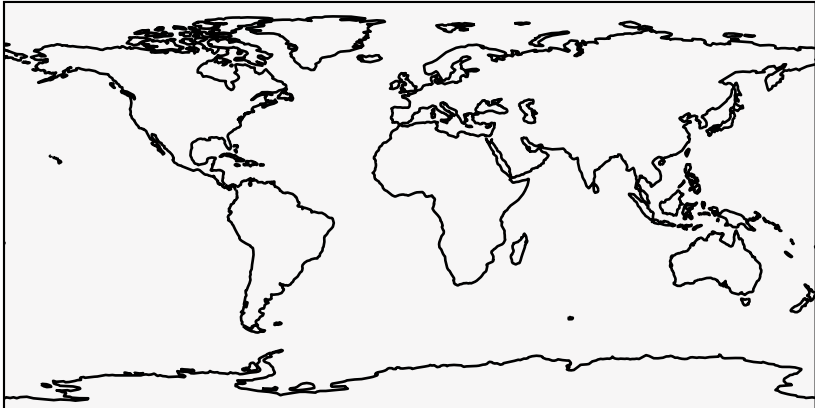
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



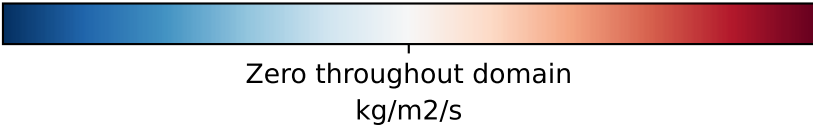
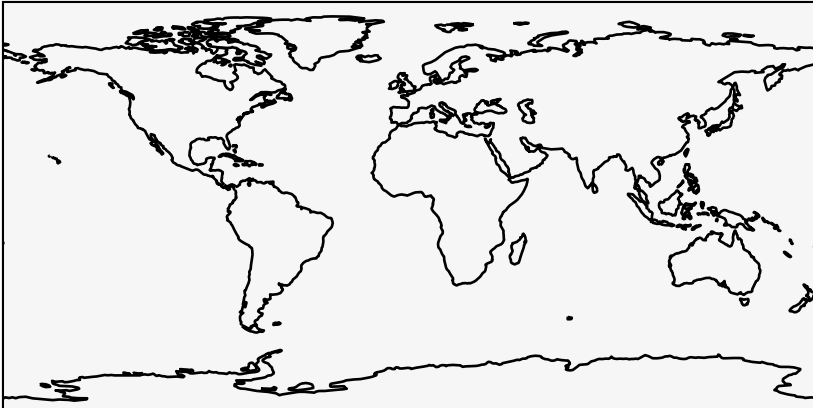
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



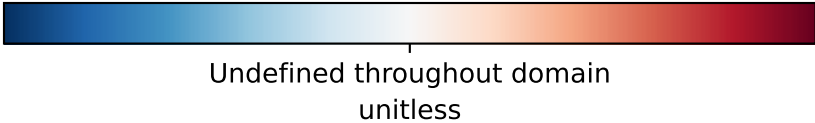
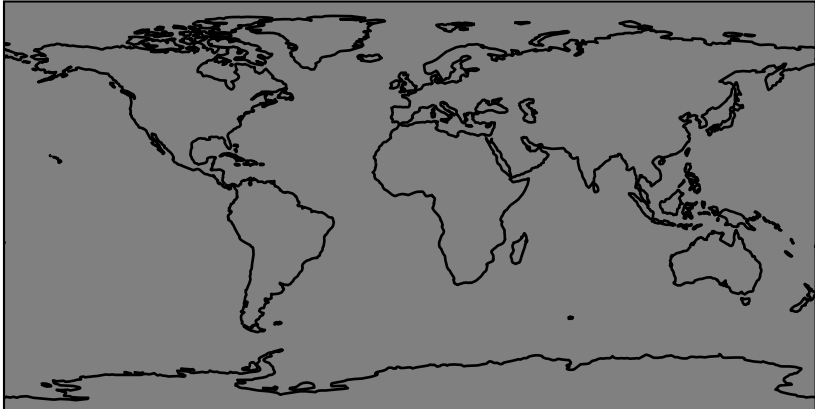
Difference
Dev - Ref, Dynamic Range



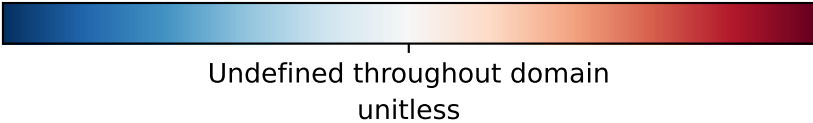
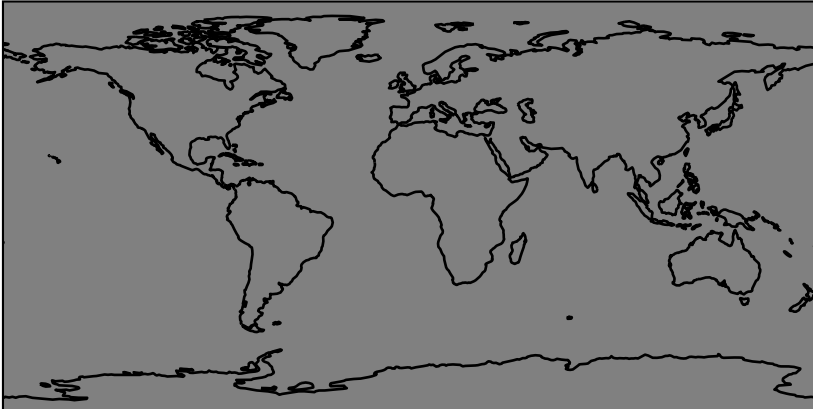
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

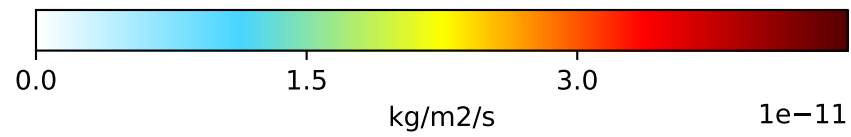
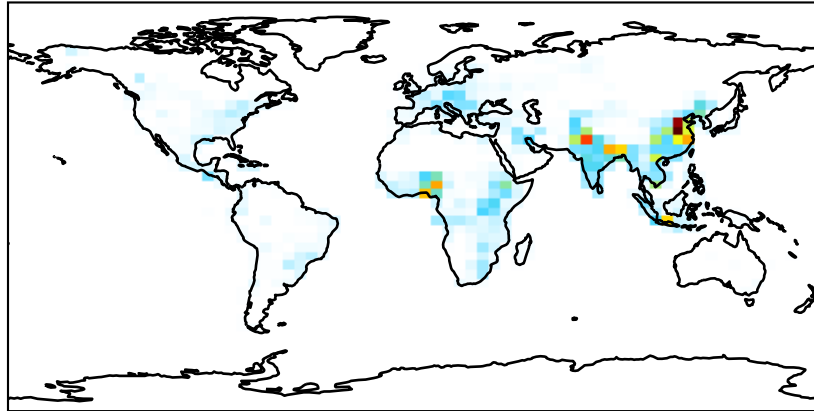


Ratio
Dev/Ref, Fixed Range

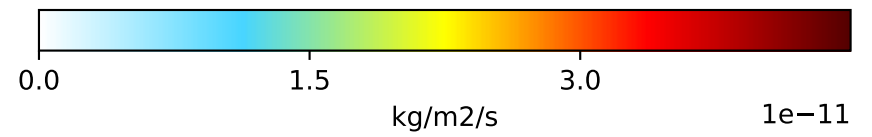
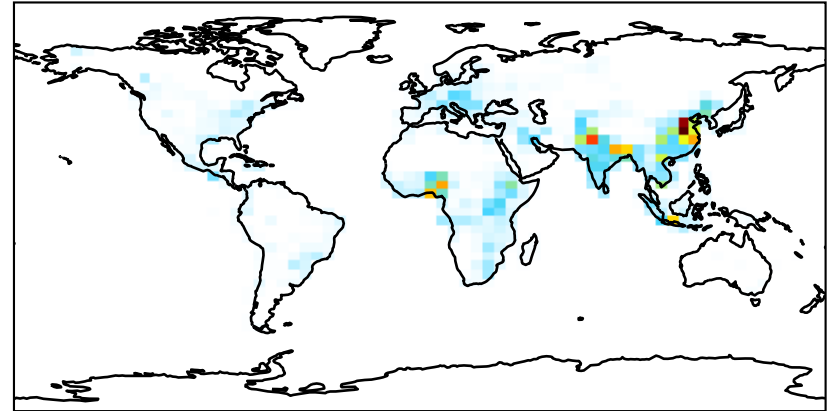


EmisHCOOH_Total (Jan2019)

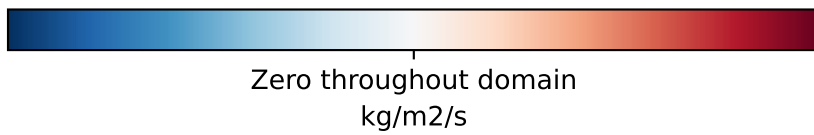
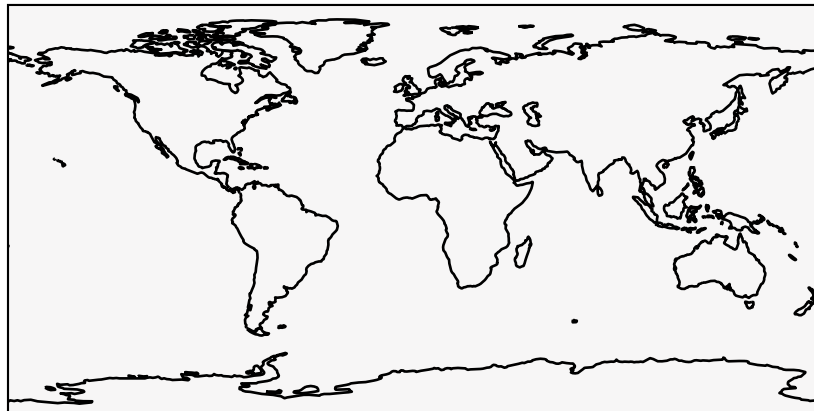
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



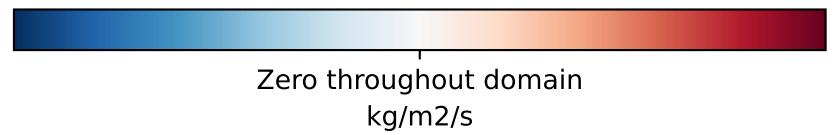
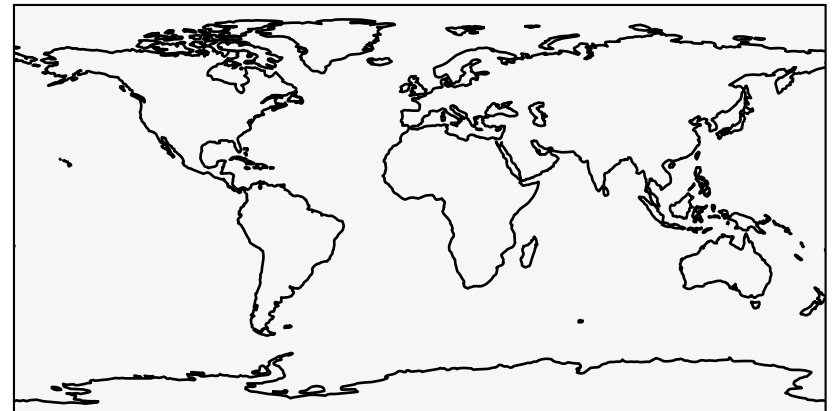
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



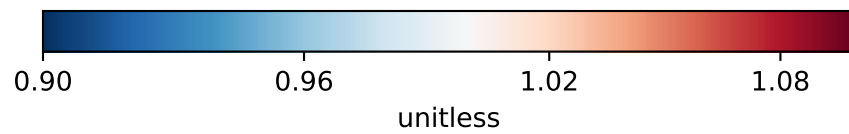
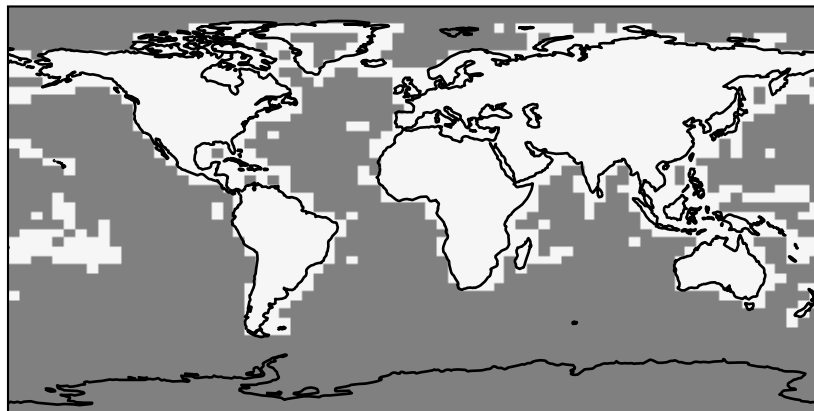
Difference
Dev - Ref, Dynamic Range



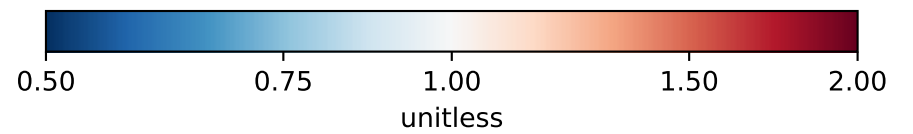
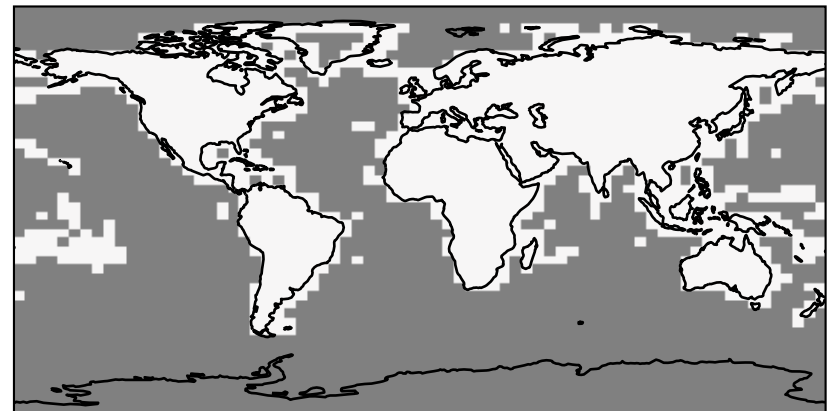
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

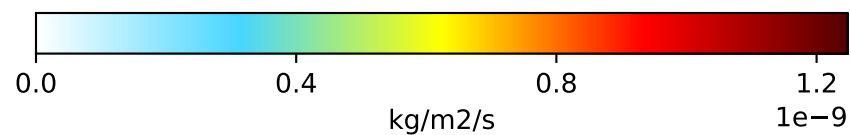
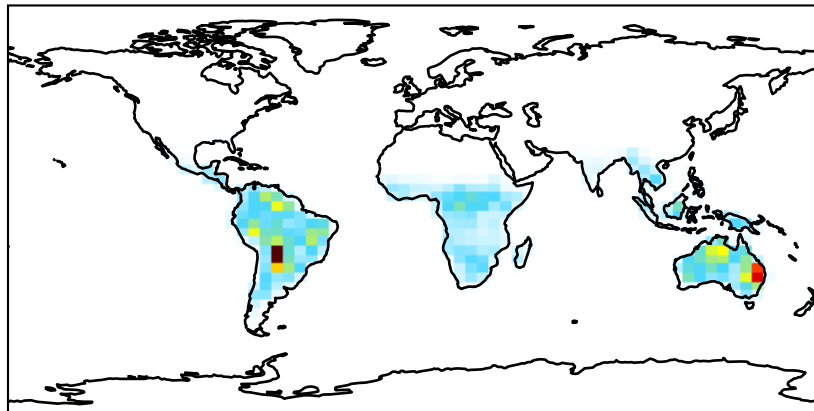


Ratio
Dev/Ref, Fixed Range

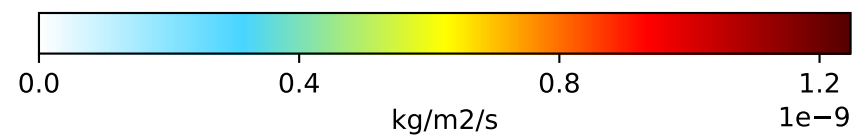
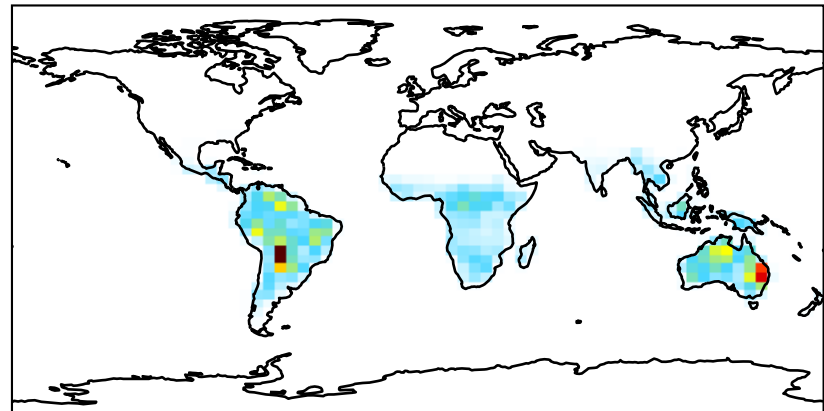


EmisISOP_Total (Jan2019)

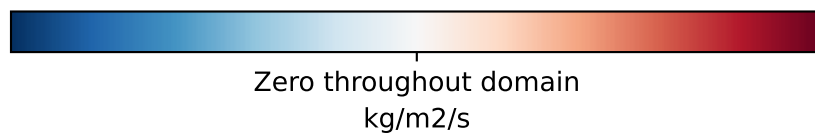
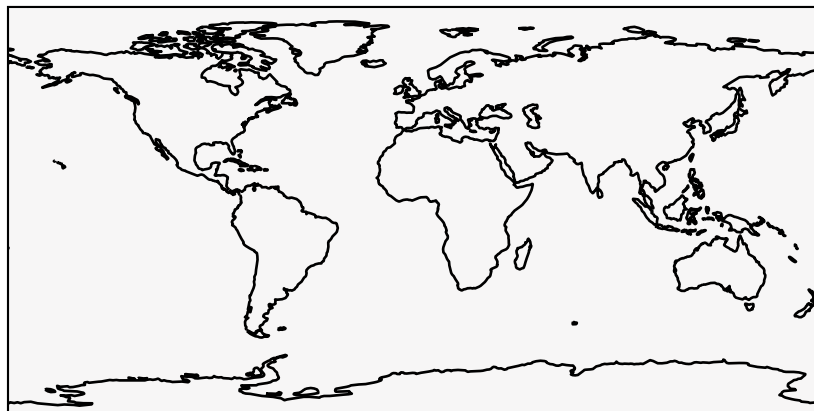
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



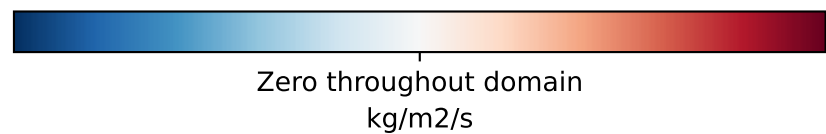
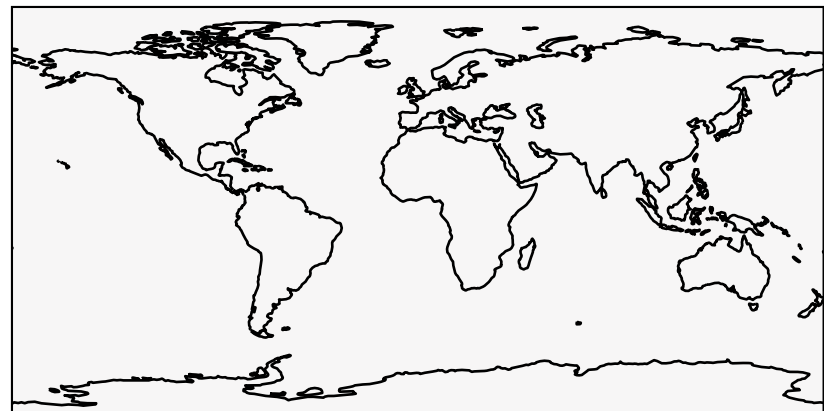
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



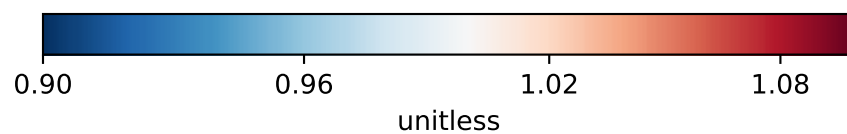
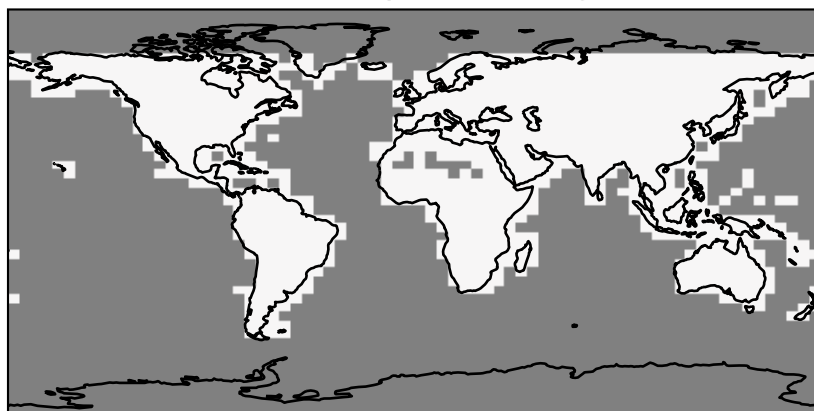
Difference
Dev - Ref, Dynamic Range



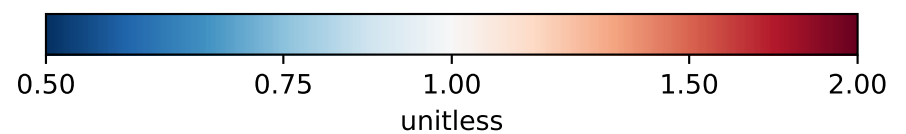
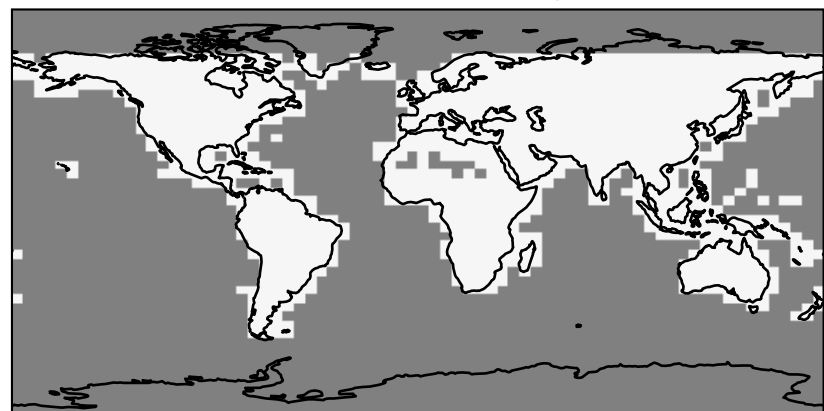
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

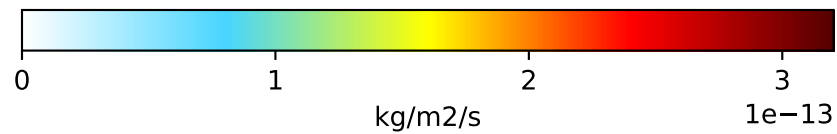
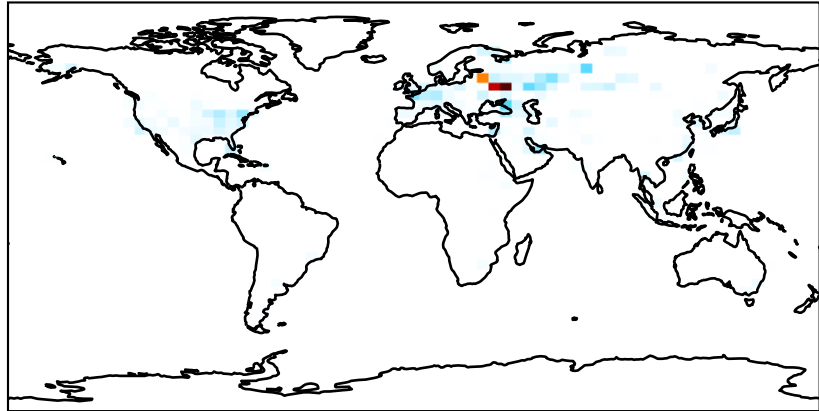


Ratio
Dev/Ref, Fixed Range

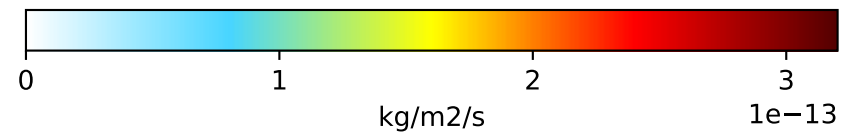
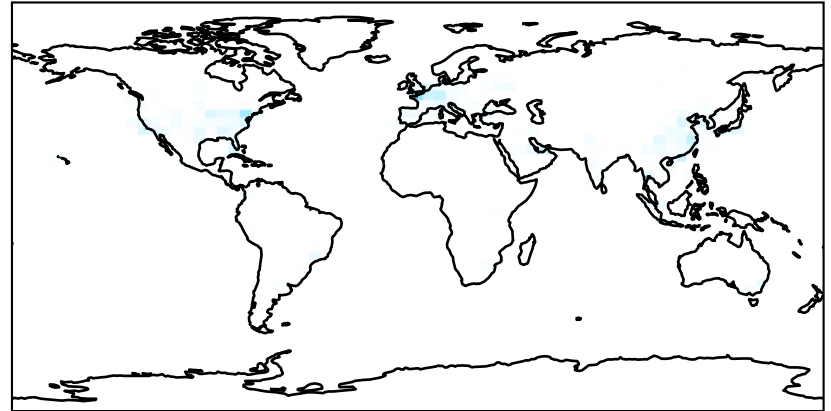


EmisMACR_Total (Jan2019)

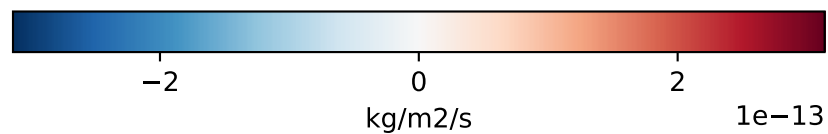
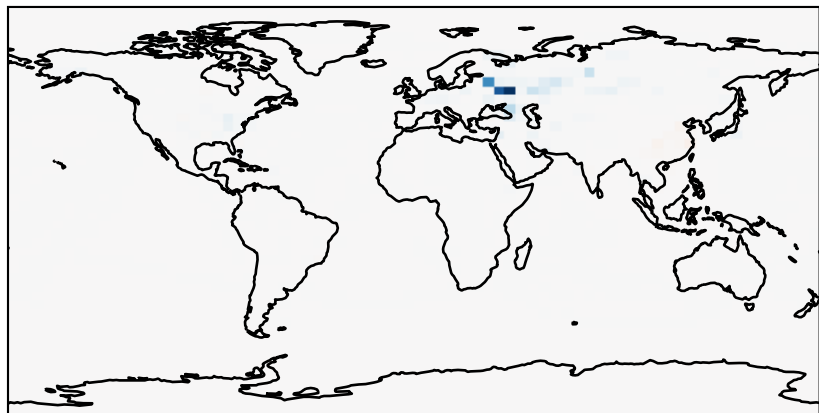
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



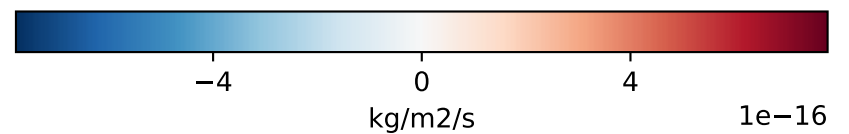
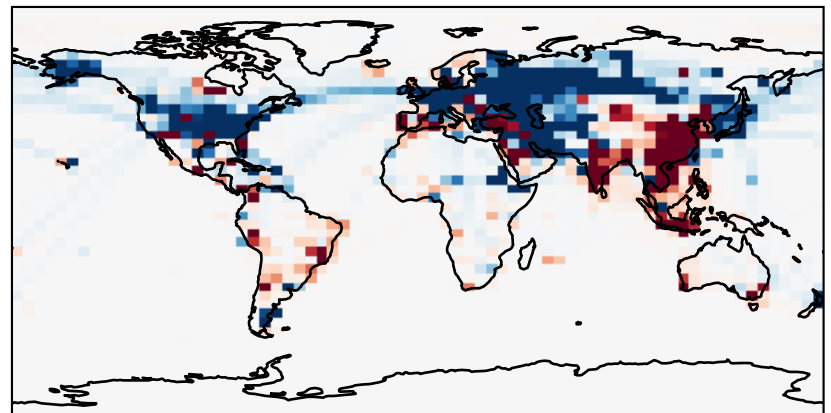
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



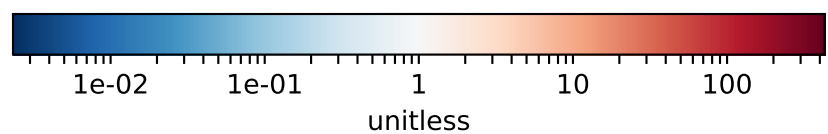
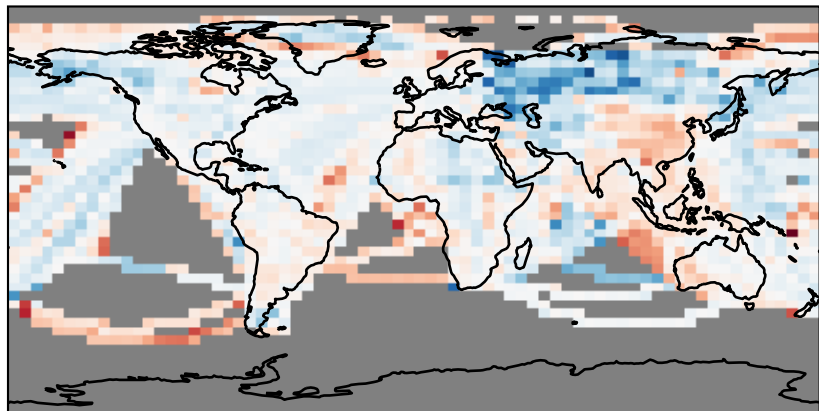
Difference
Dev - Ref, Dynamic Range



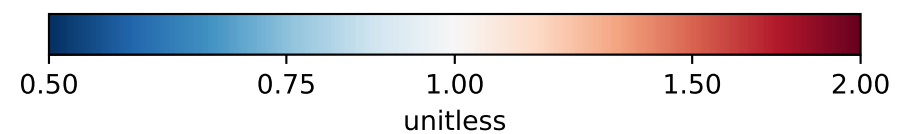
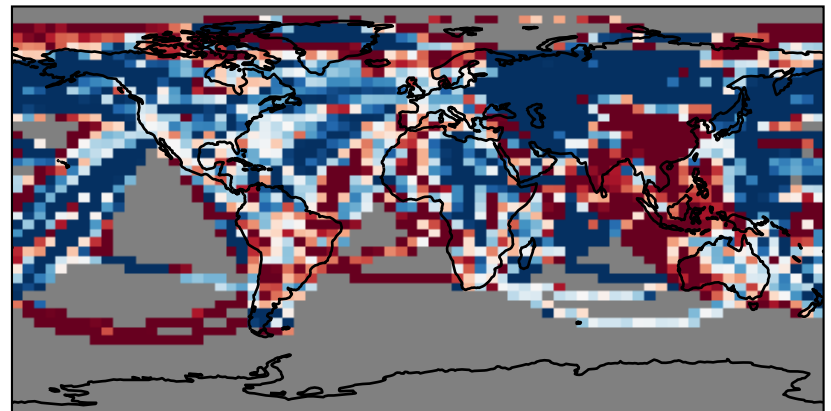
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

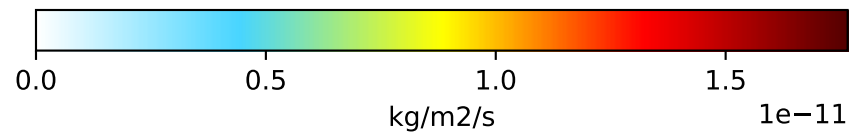
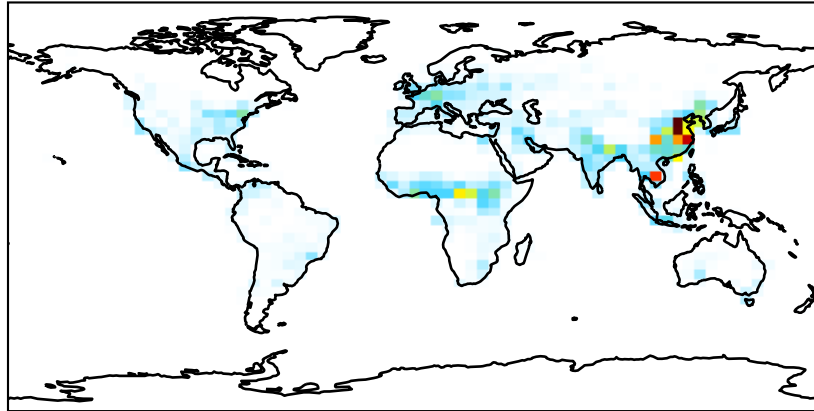


Ratio
Dev/Ref, Fixed Range

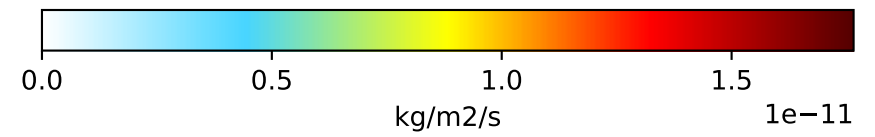
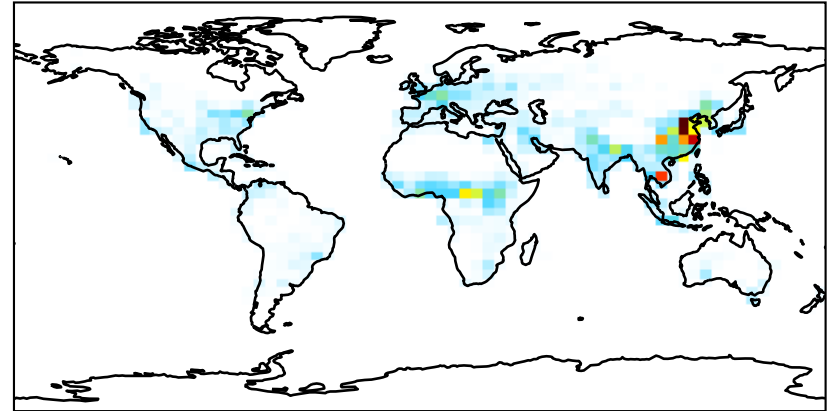


EmisMEK_Total (Jan2019)

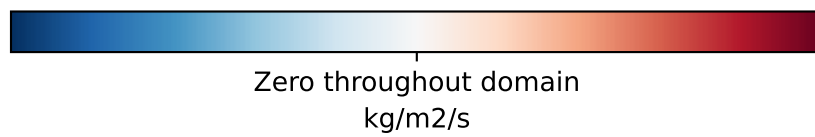
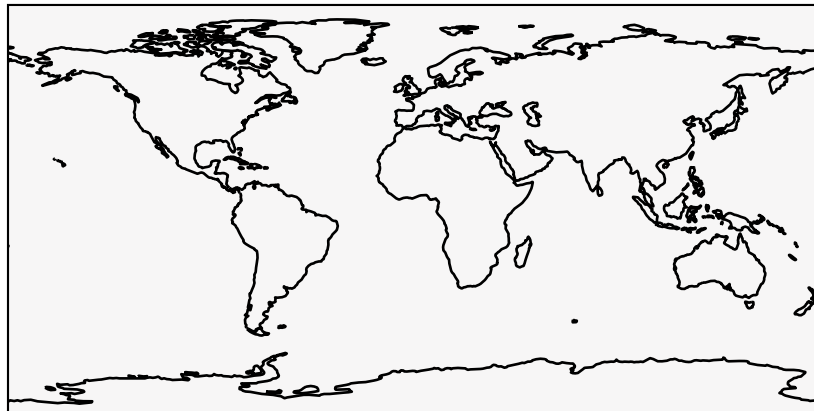
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



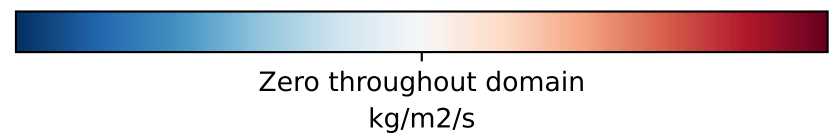
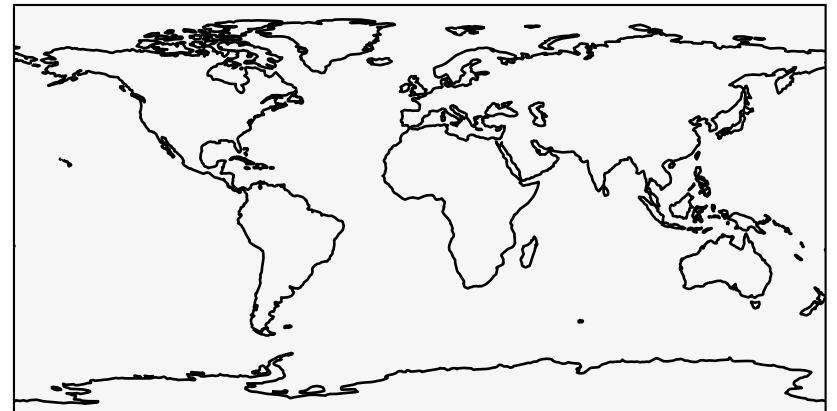
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



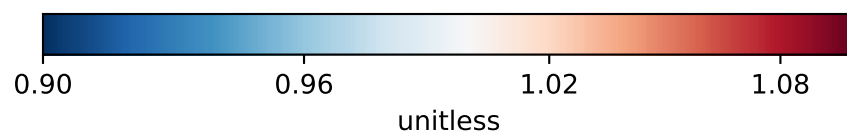
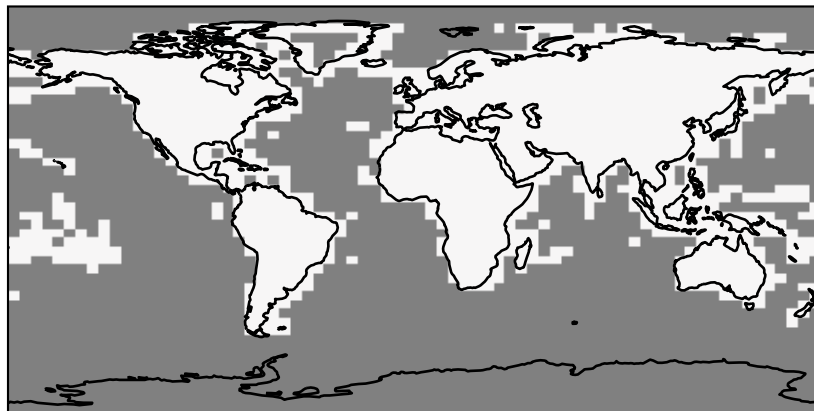
Difference
Dev - Ref, Dynamic Range



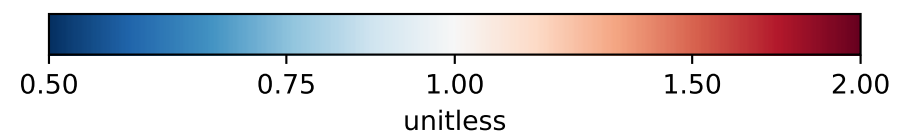
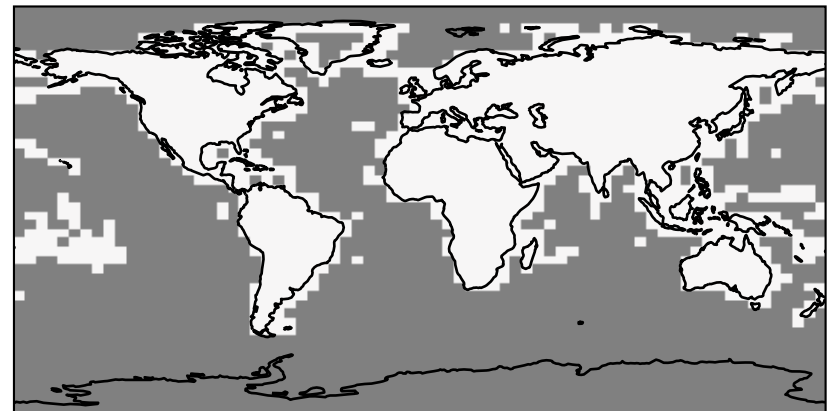
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

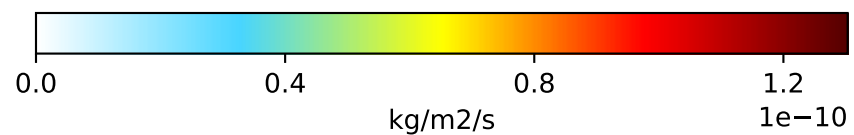
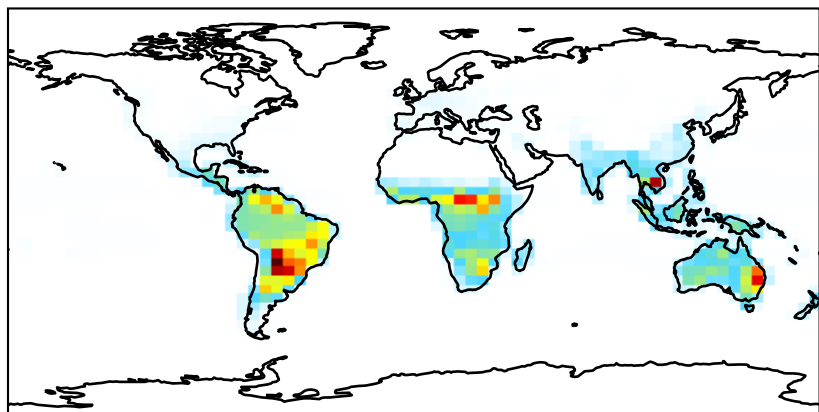


Ratio
Dev/Ref, Fixed Range

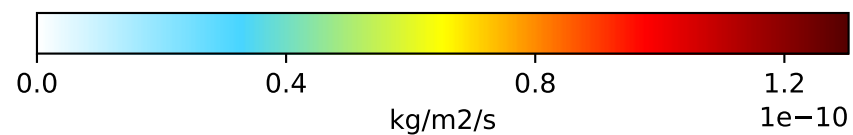
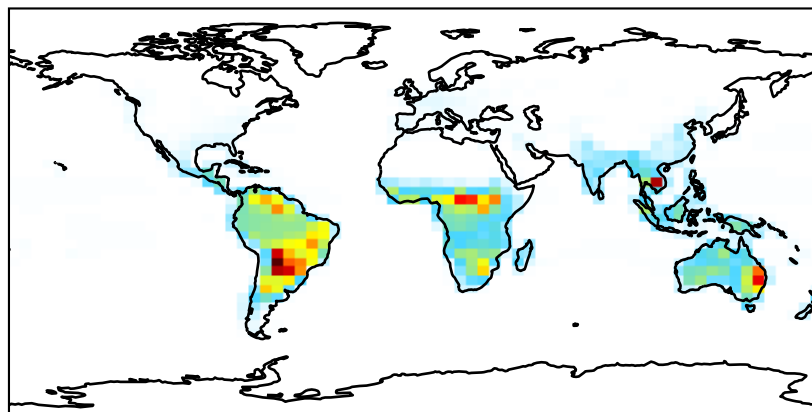


EmisMOH_Total (Jan2019)

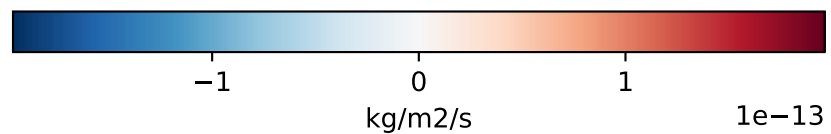
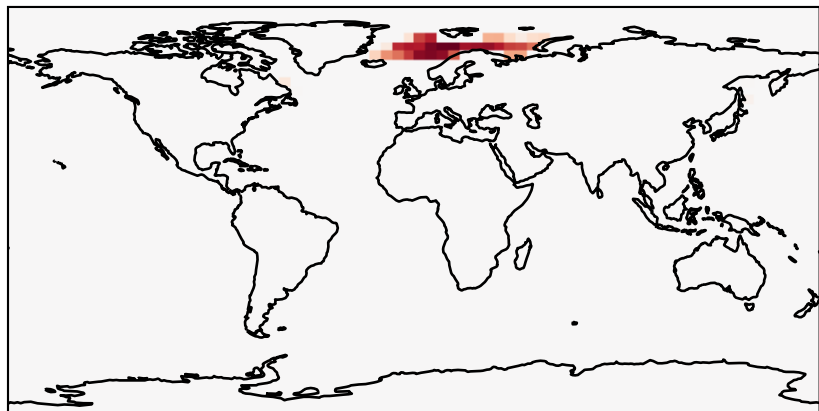
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



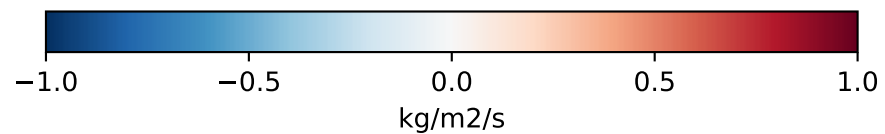
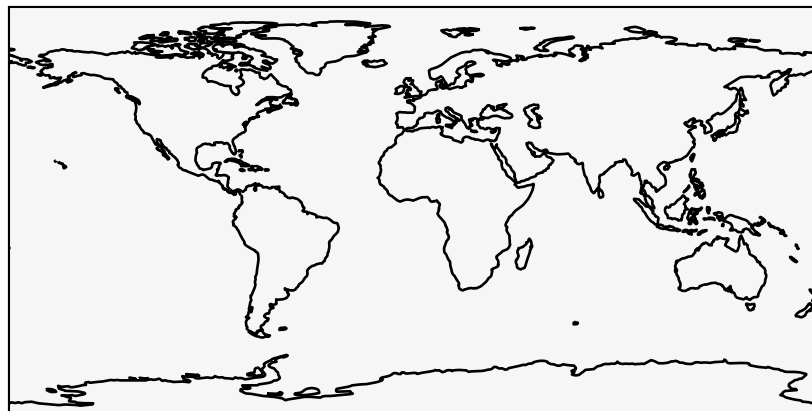
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



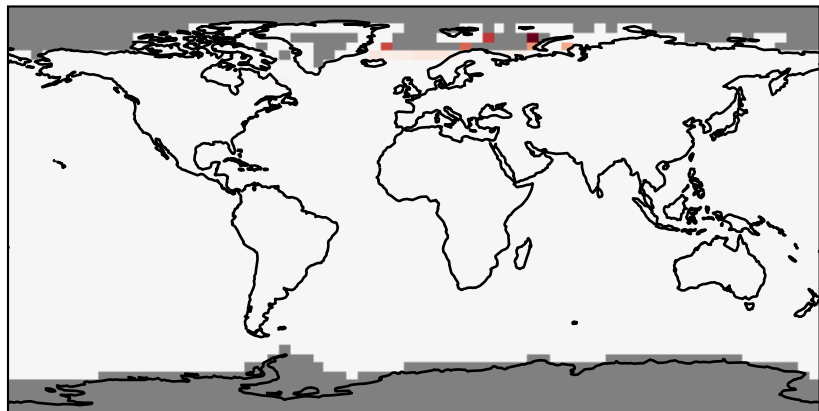
Difference
Dev - Ref, Dynamic Range



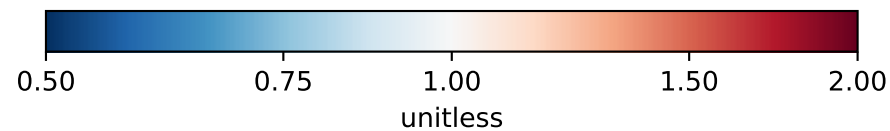
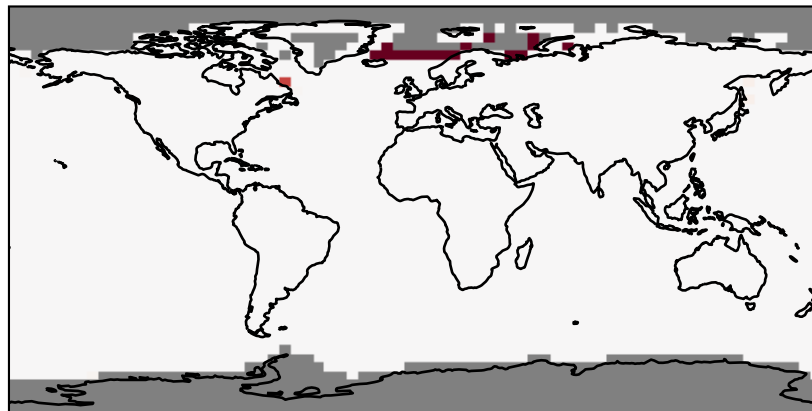
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

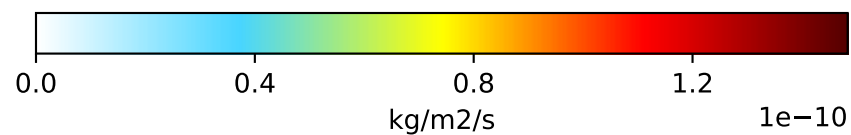
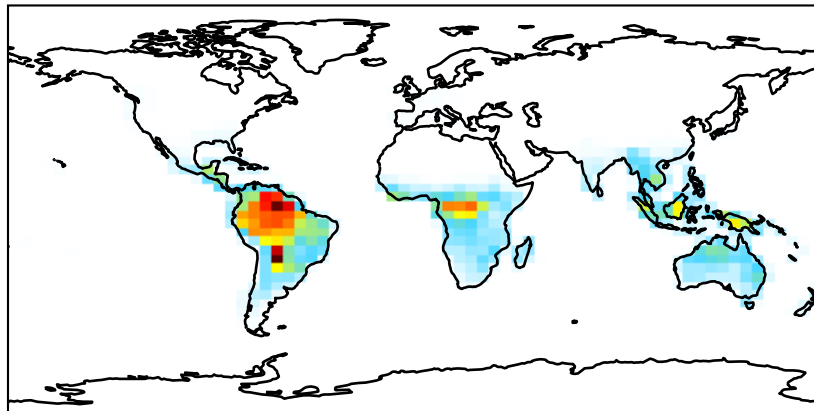


Ratio
Dev/Ref, Fixed Range

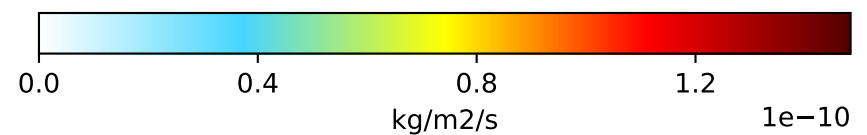
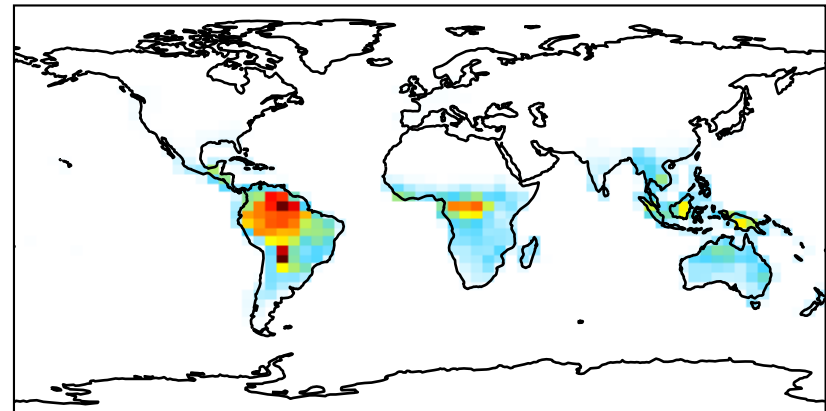


EmisMTPA_Total (Jan2019)

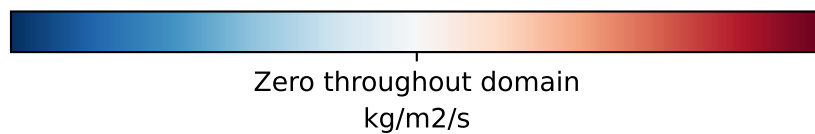
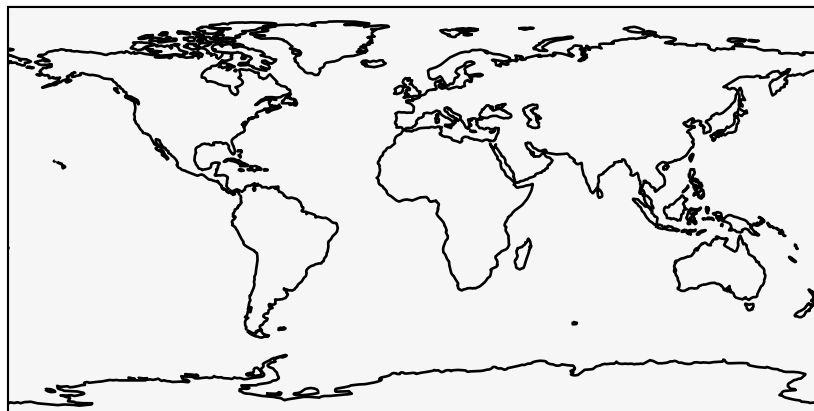
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



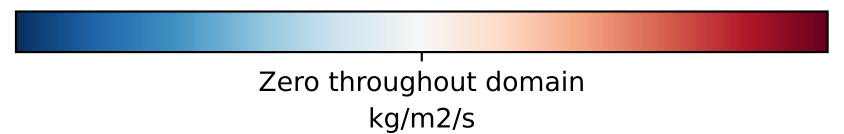
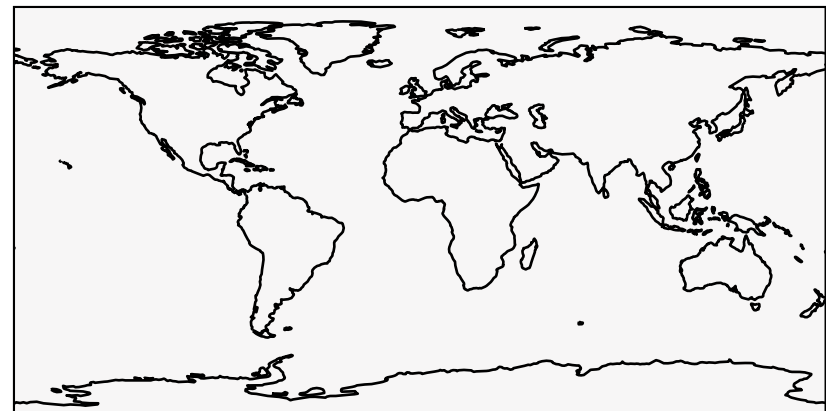
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



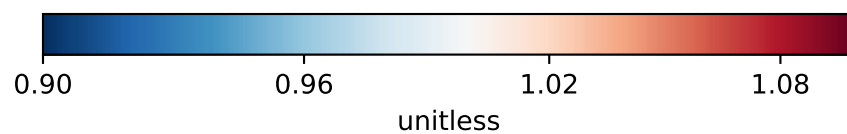
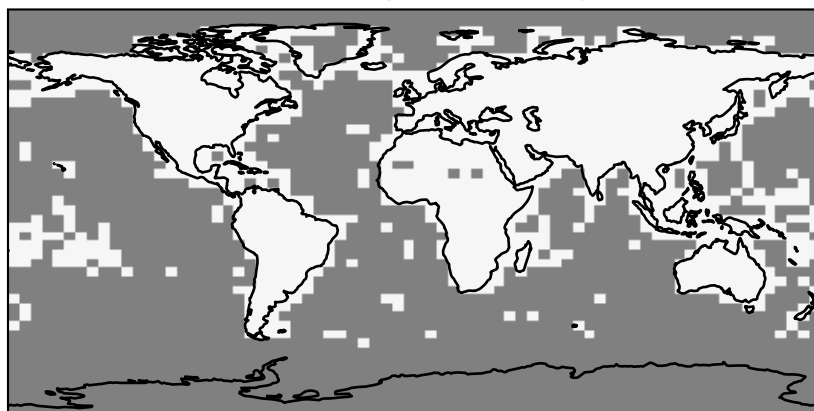
Difference
Dev - Ref, Dynamic Range



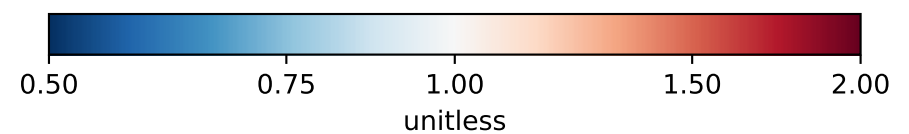
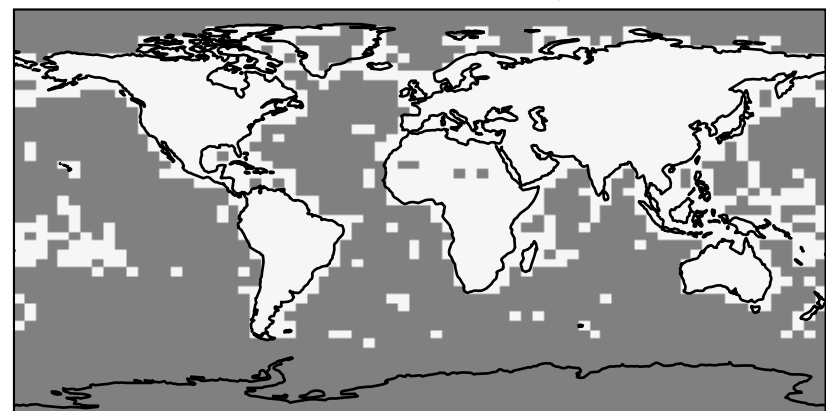
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

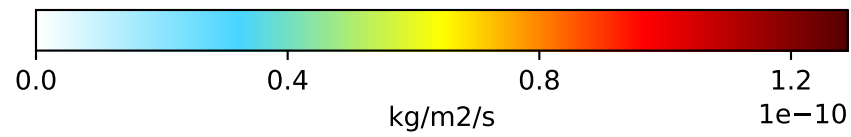
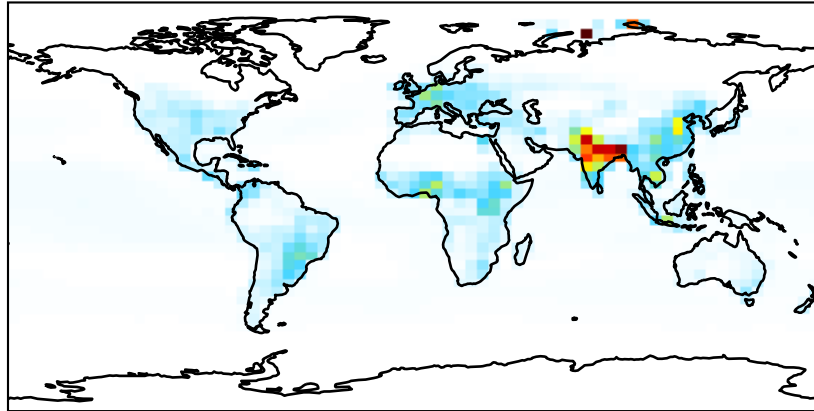


Ratio
Dev/Ref, Fixed Range

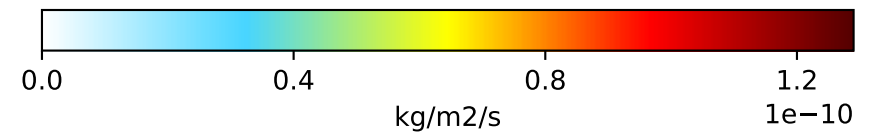
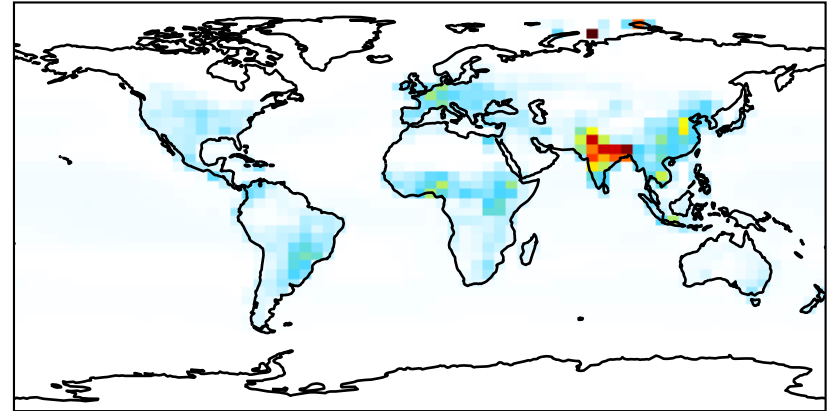


EmisNH3_Total (Jan2019)

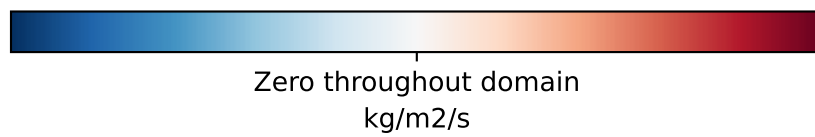
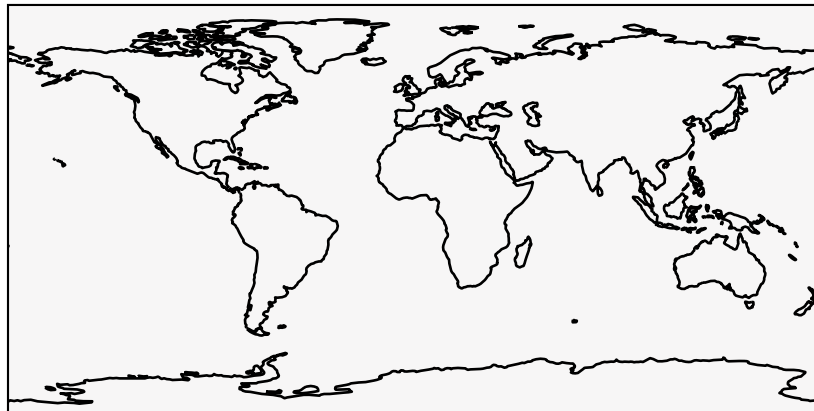
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



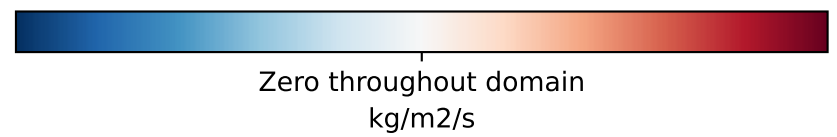
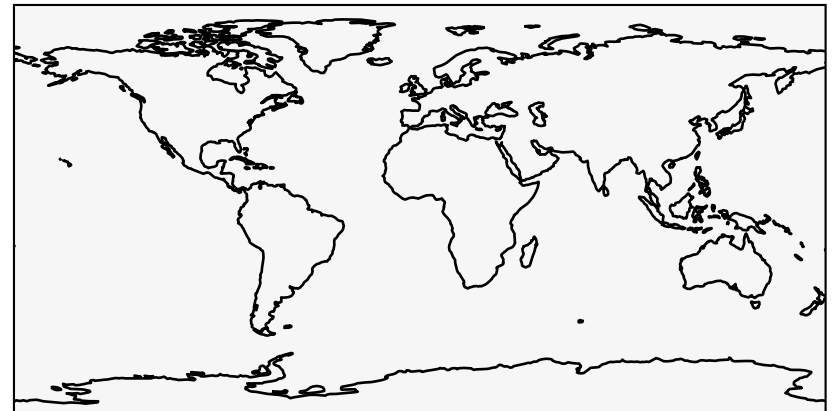
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



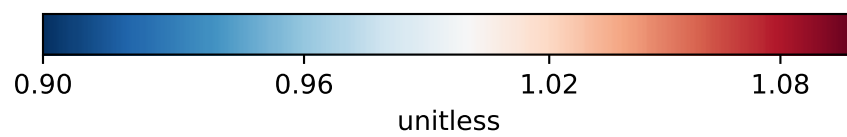
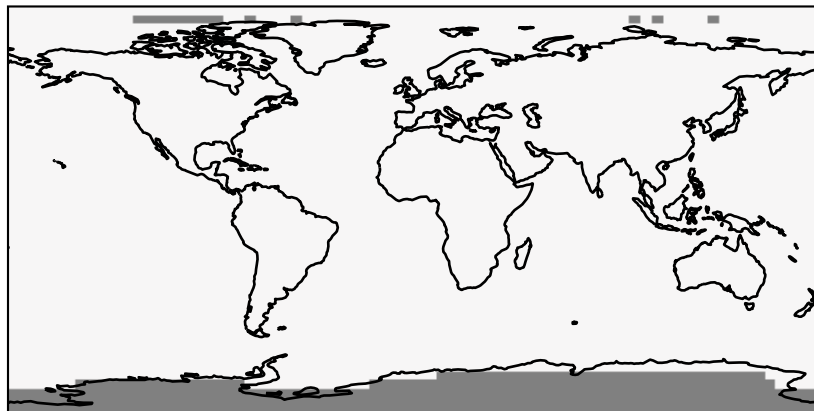
Difference
Dev - Ref, Dynamic Range



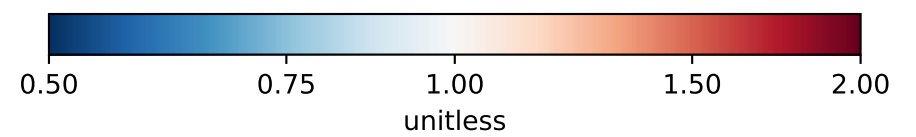
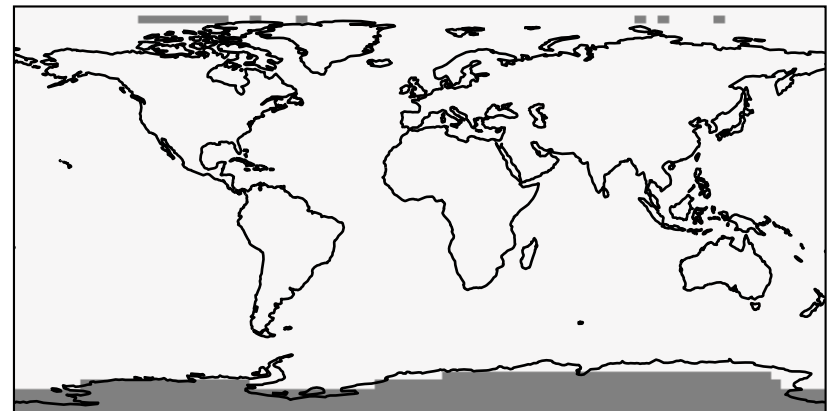
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

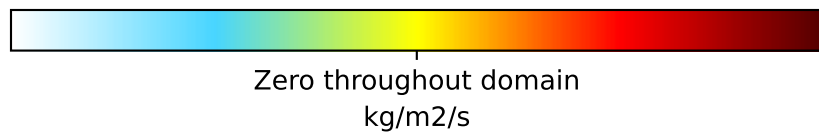
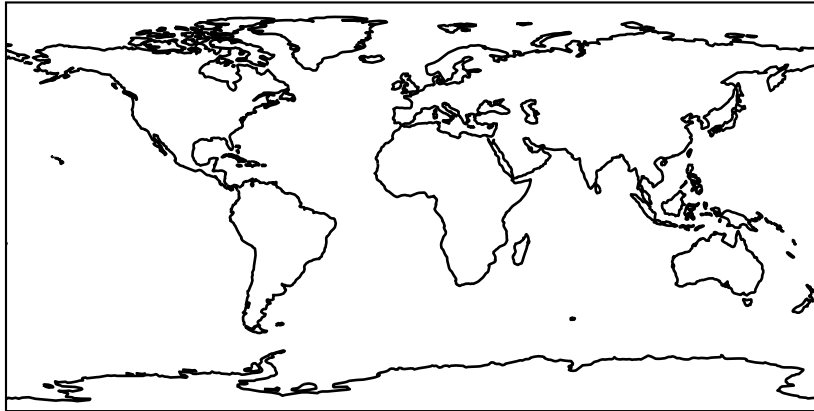


Ratio
Dev/Ref, Fixed Range

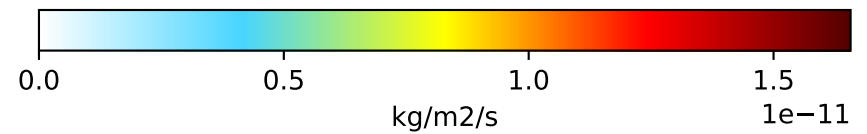
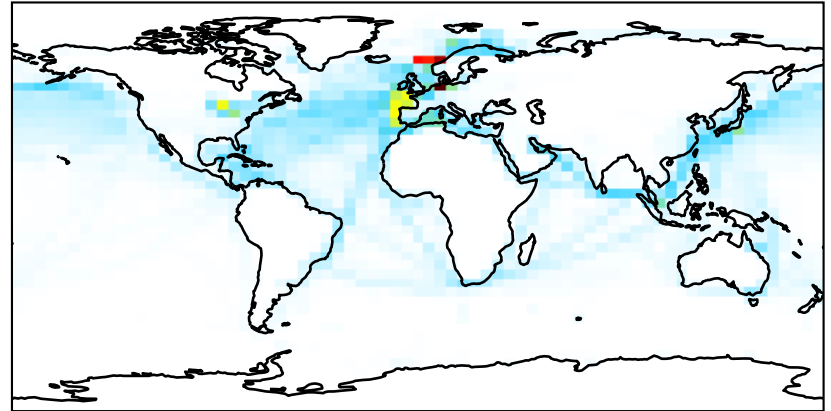


EmisNO2_Total (Jan2019)

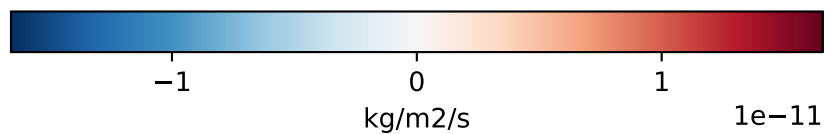
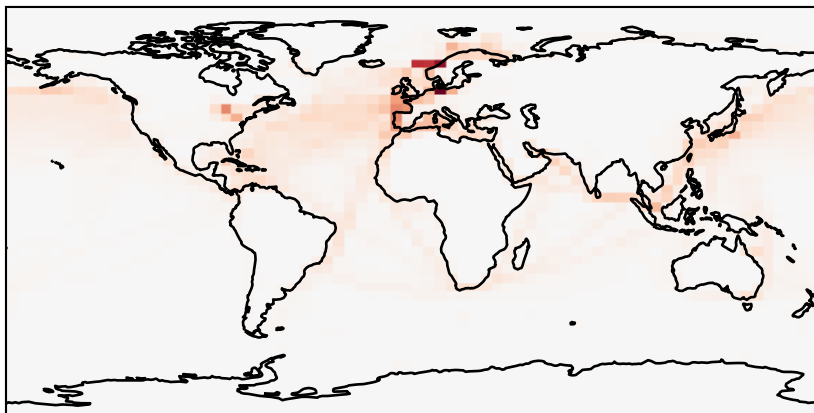
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



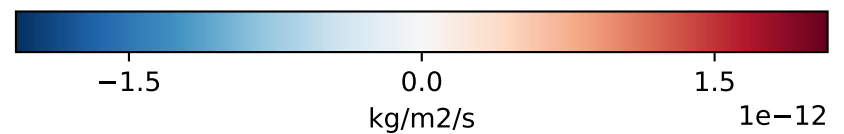
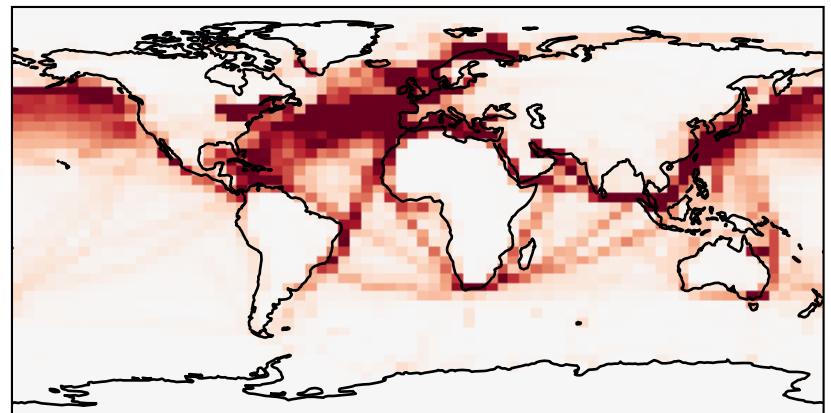
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



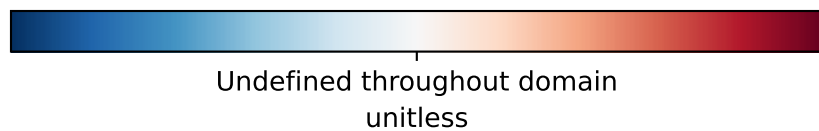
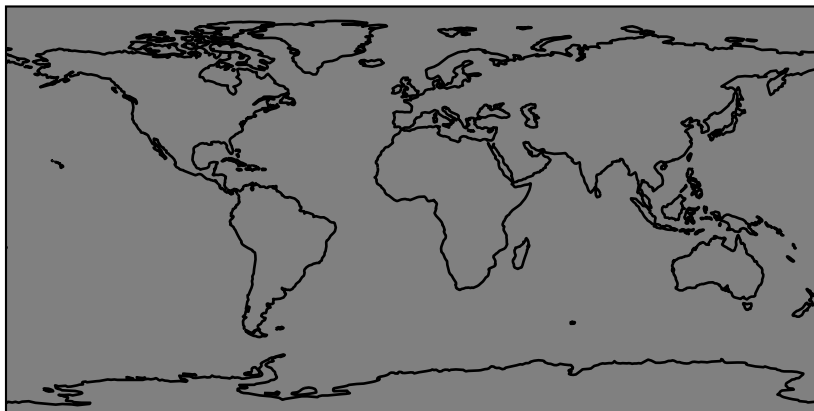
Difference
Dev - Ref, Dynamic Range



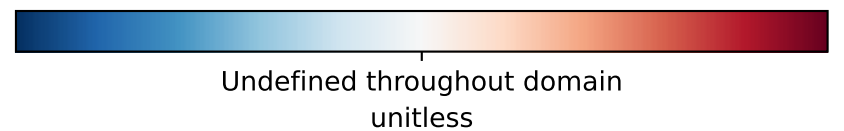
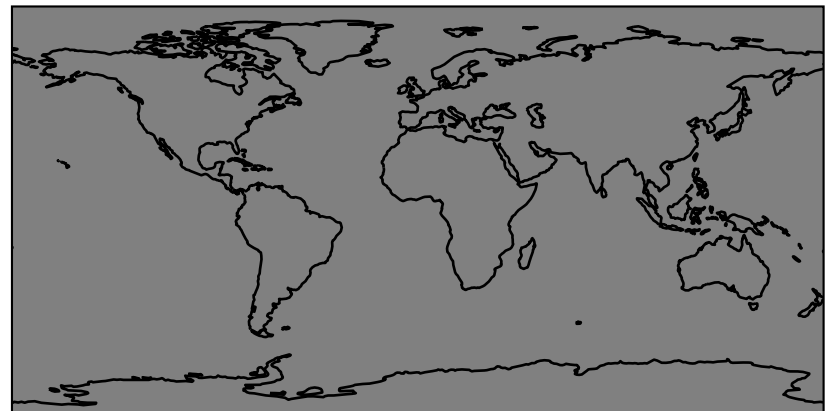
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

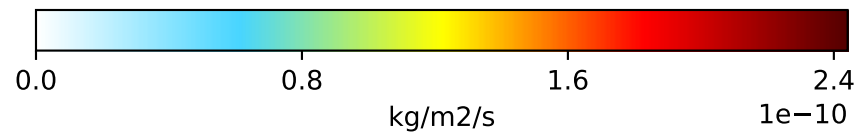
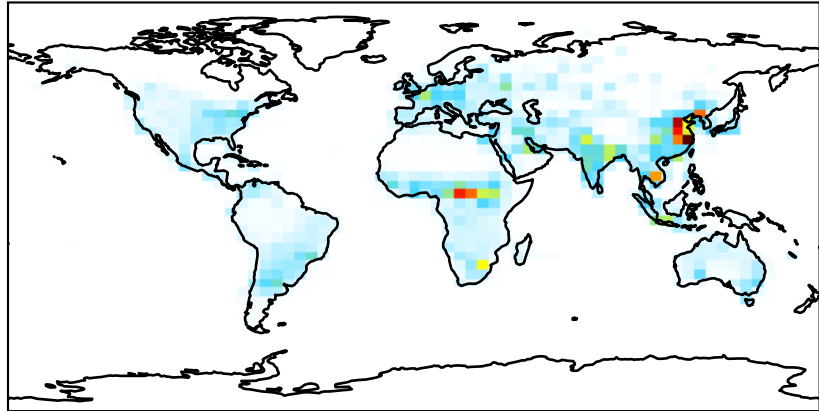


Ratio
Dev/Ref, Fixed Range

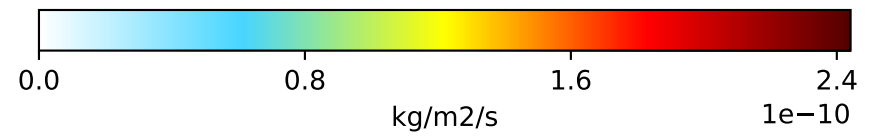
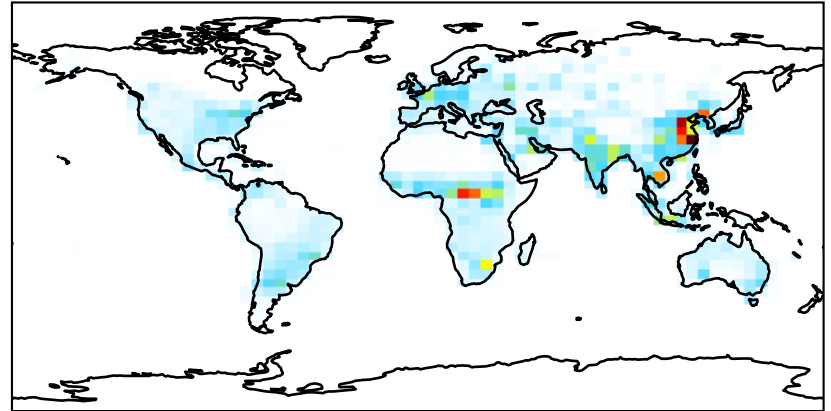


EmisNO_Total (Jan2019)

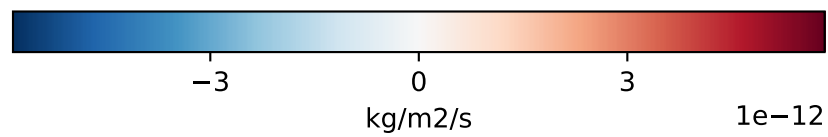
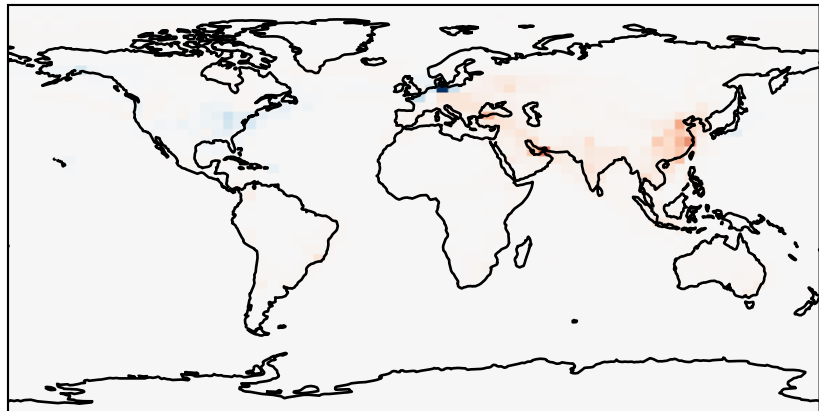
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



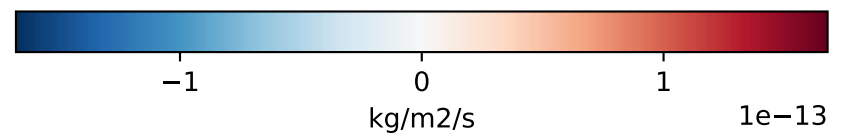
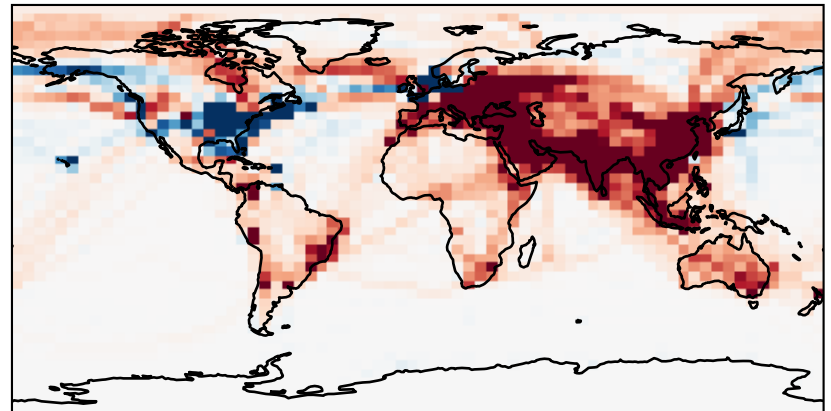
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



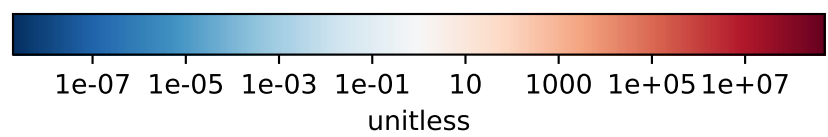
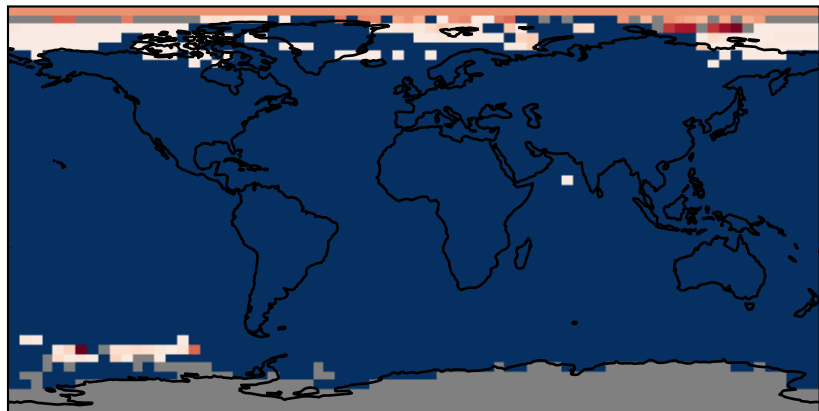
Difference
Dev - Ref, Dynamic Range



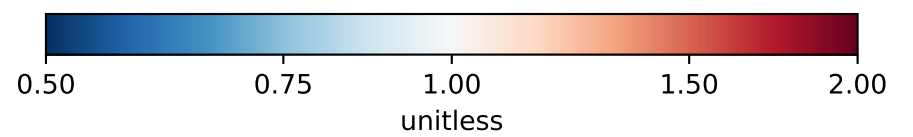
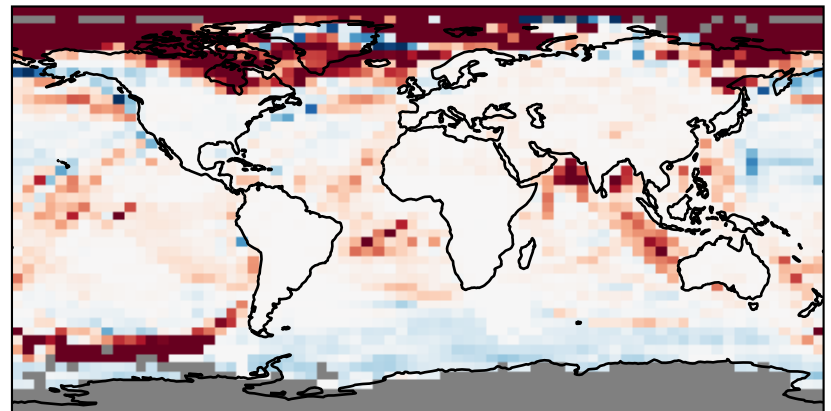
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

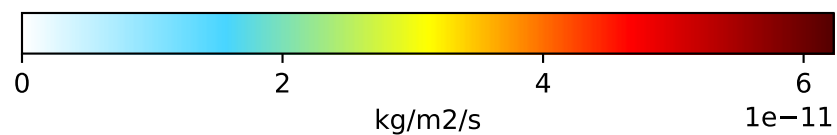
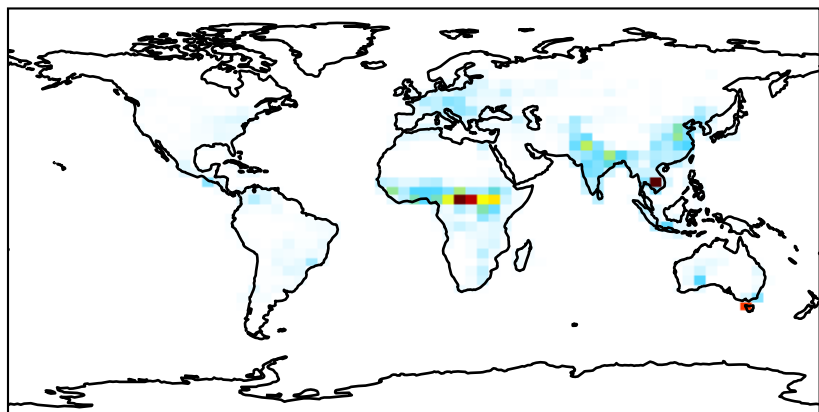


Ratio
Dev/Ref, Fixed Range

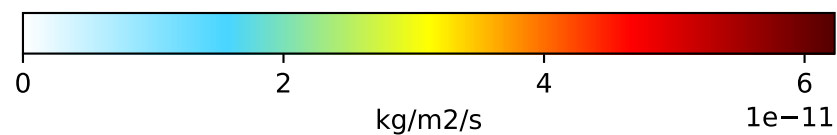
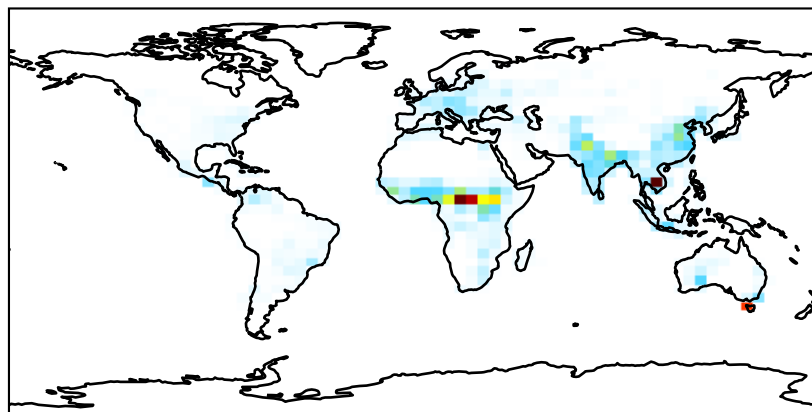


EmisOCPI_Total (Jan2019)

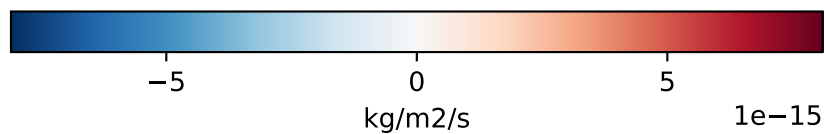
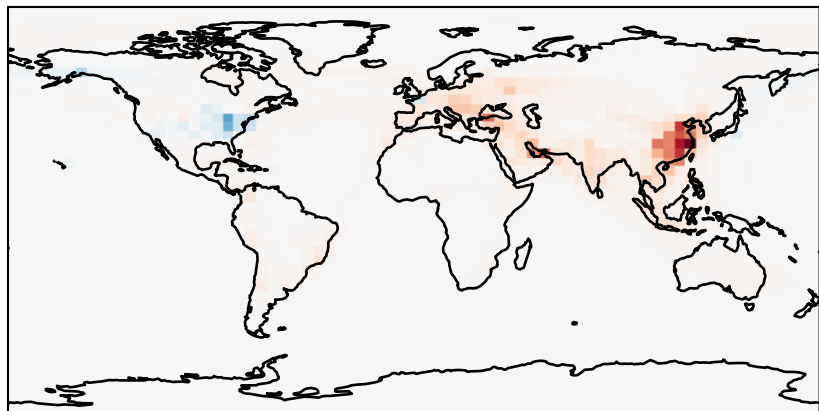
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



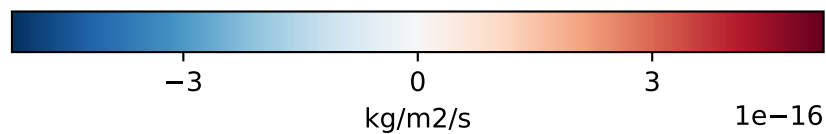
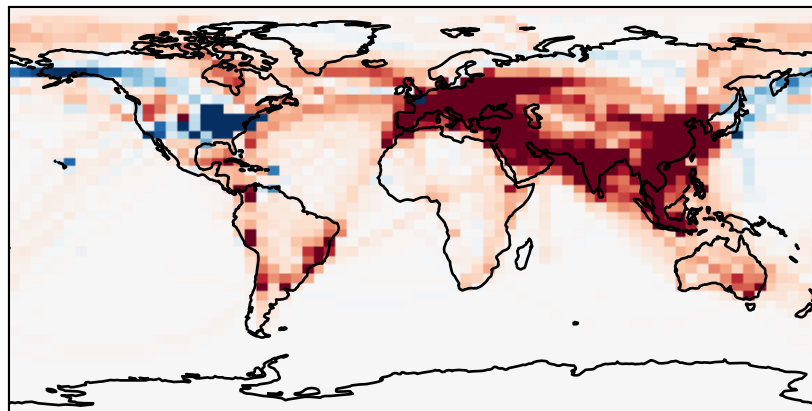
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



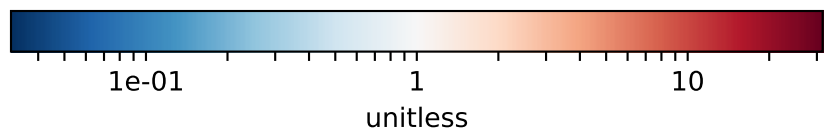
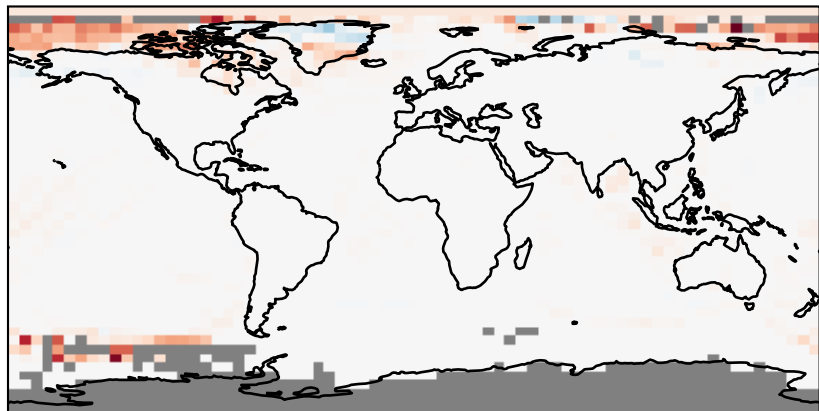
Difference
Dev - Ref, Dynamic Range



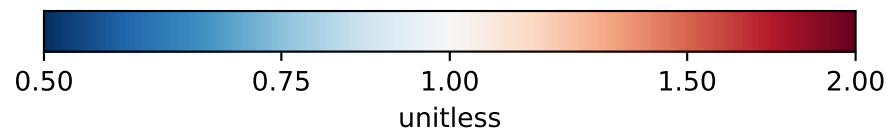
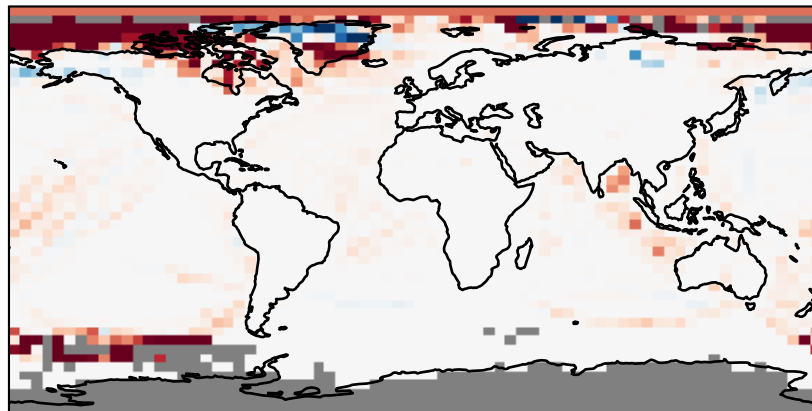
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

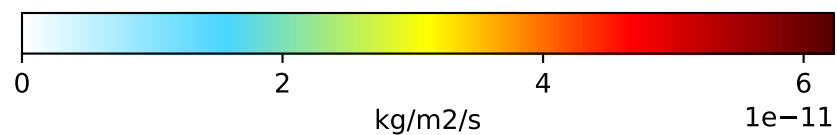
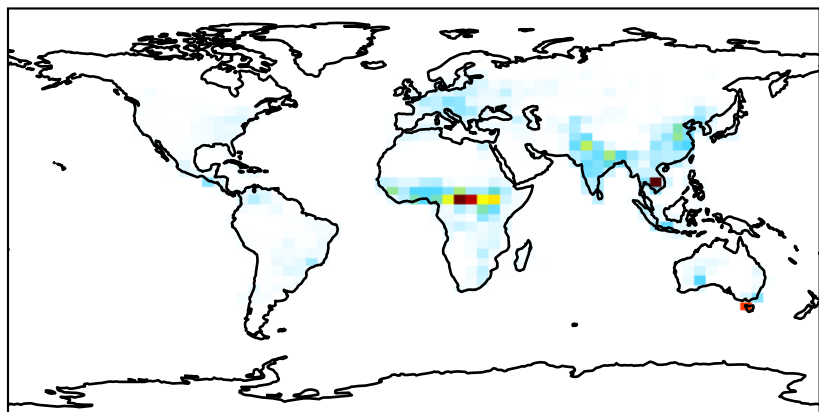


Ratio
Dev/Ref, Fixed Range

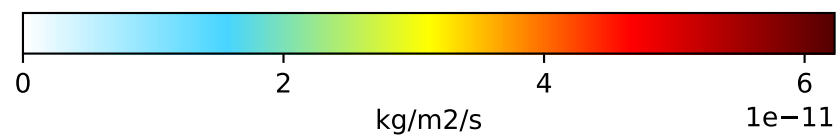
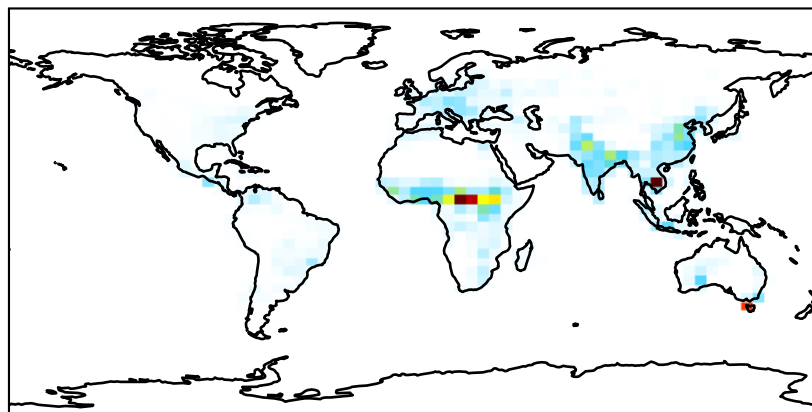


EmisOCPO_Total (Jan2019)

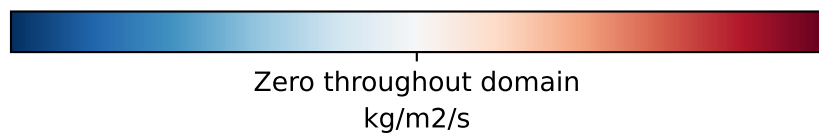
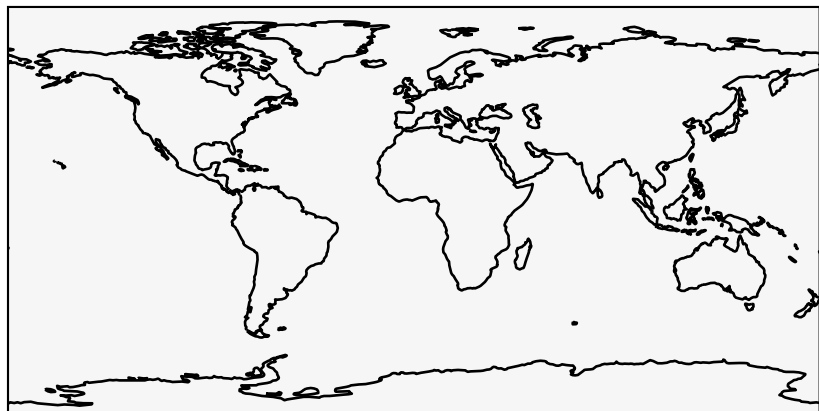
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



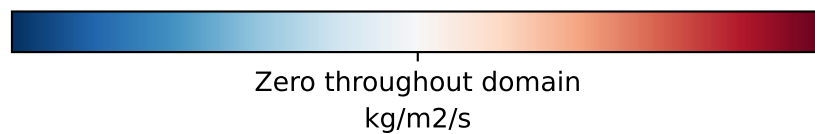
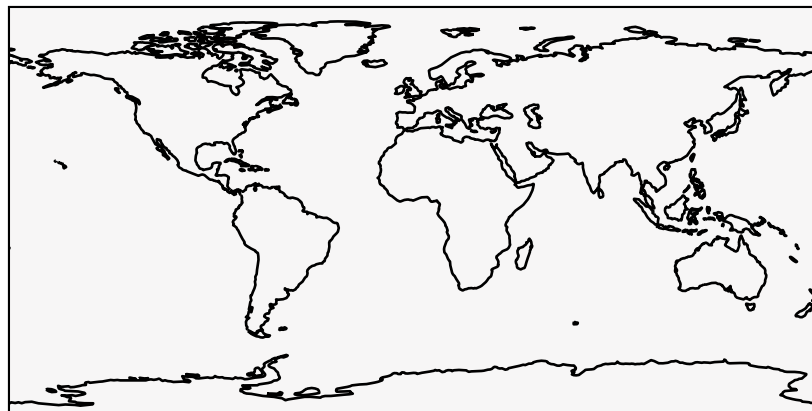
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



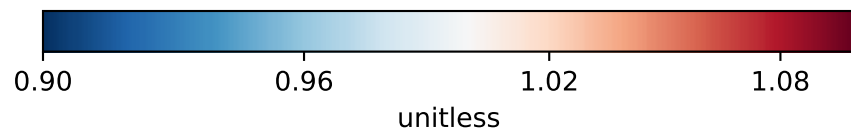
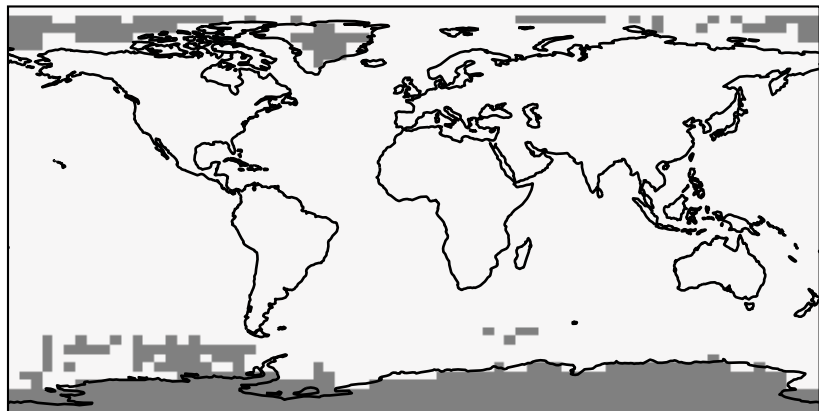
Difference
Dev - Ref, Dynamic Range



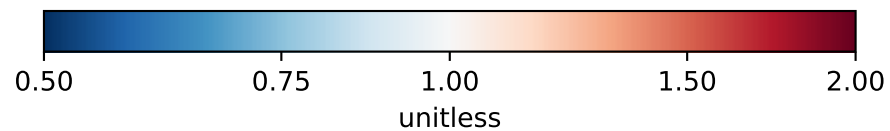
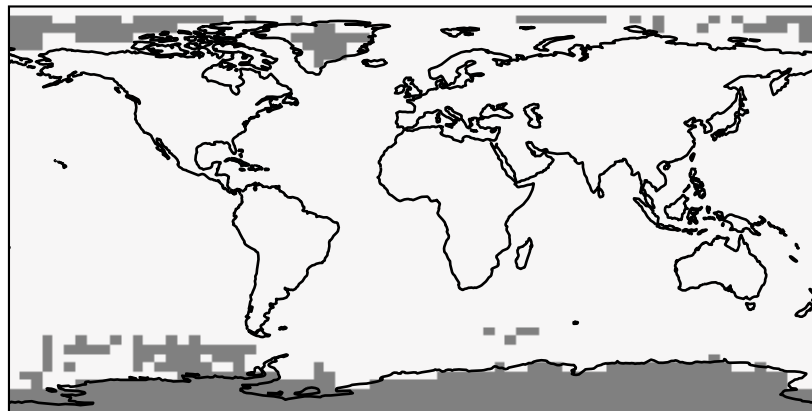
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

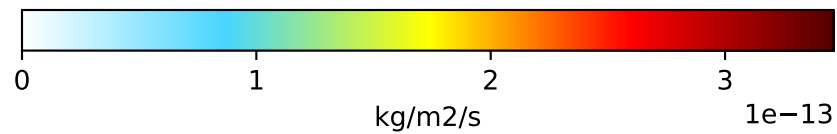
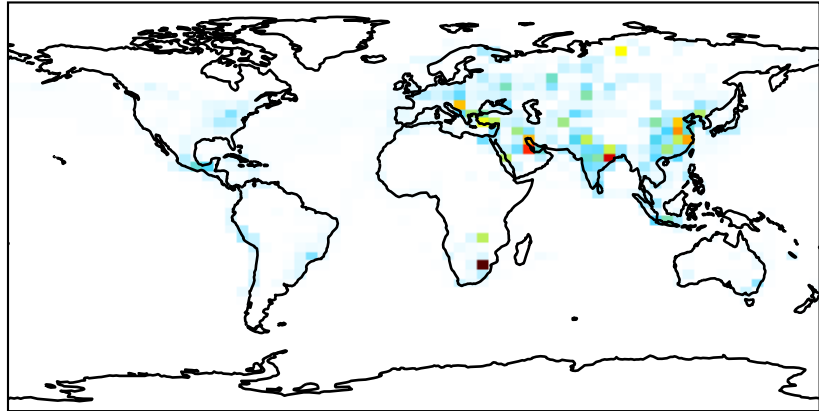


Ratio
Dev/Ref, Fixed Range

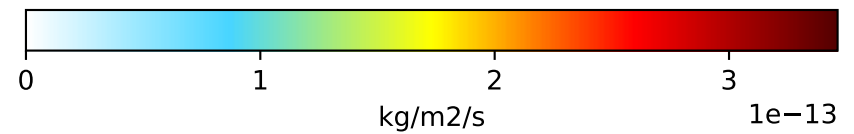
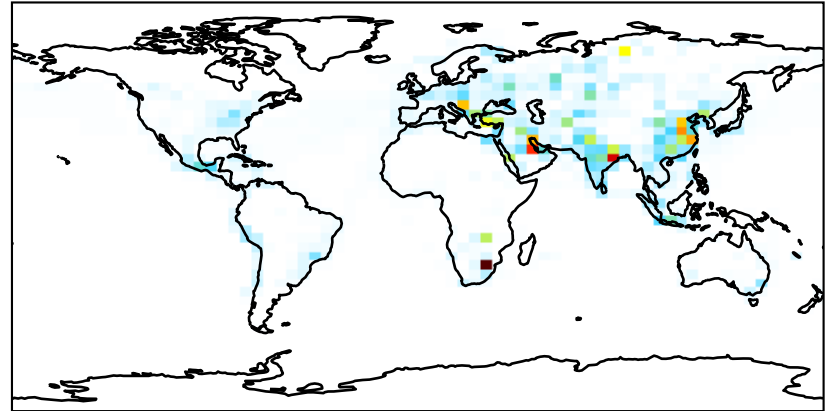


EmispFe_Total (Jan2019)

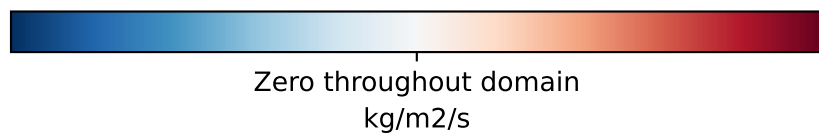
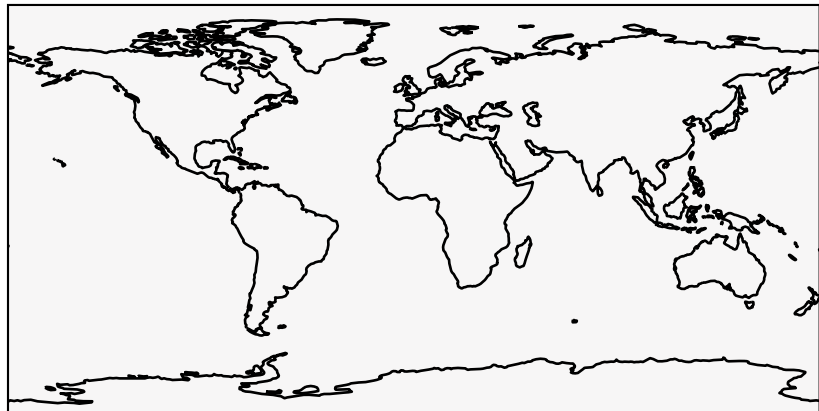
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



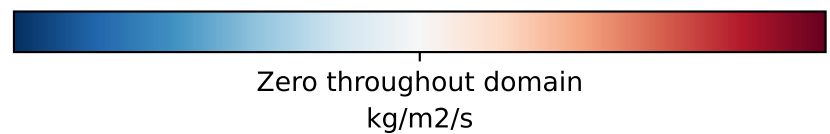
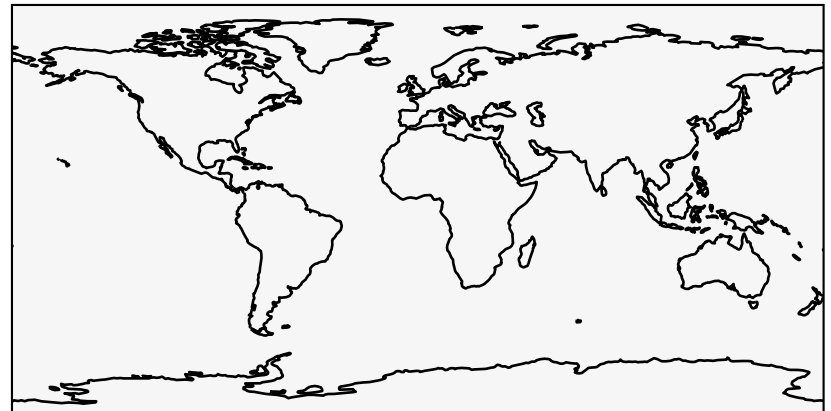
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



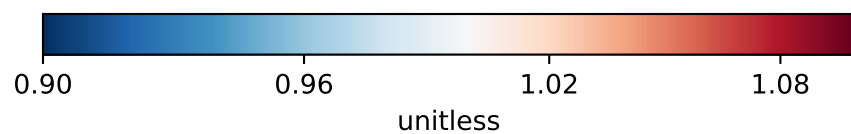
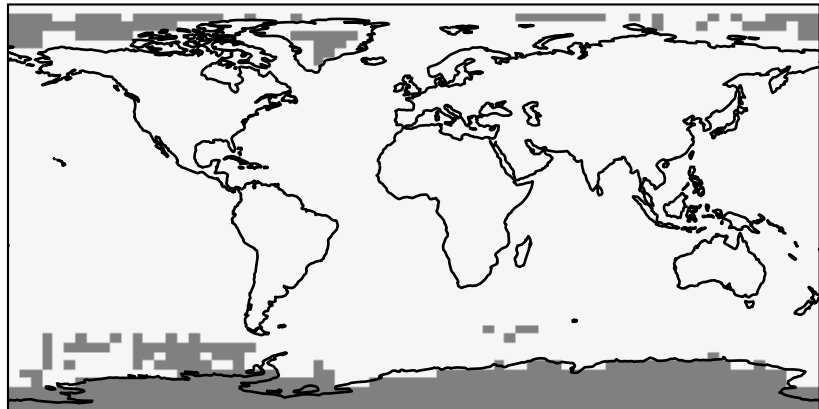
Difference
Dev - Ref, Dynamic Range



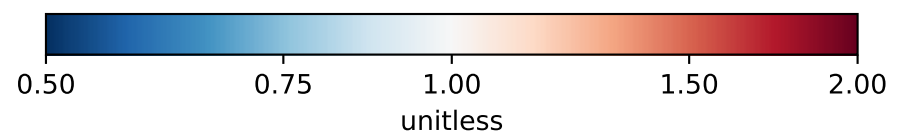
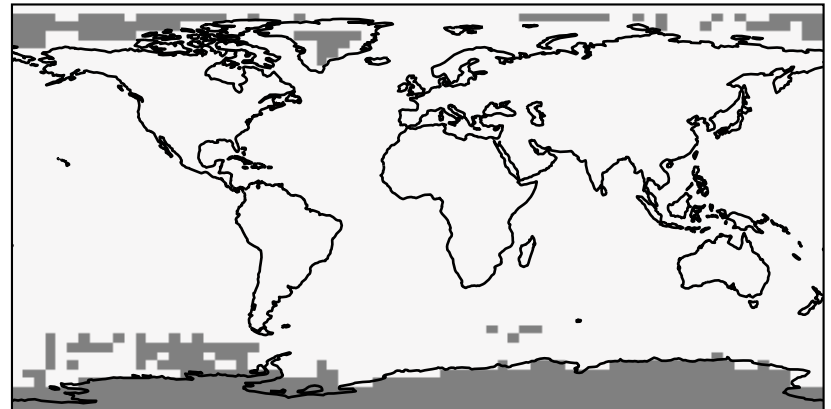
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

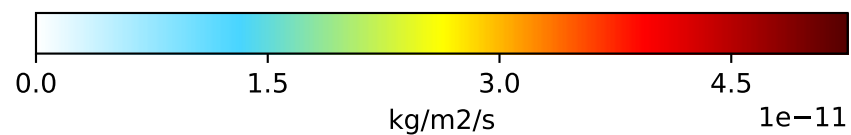
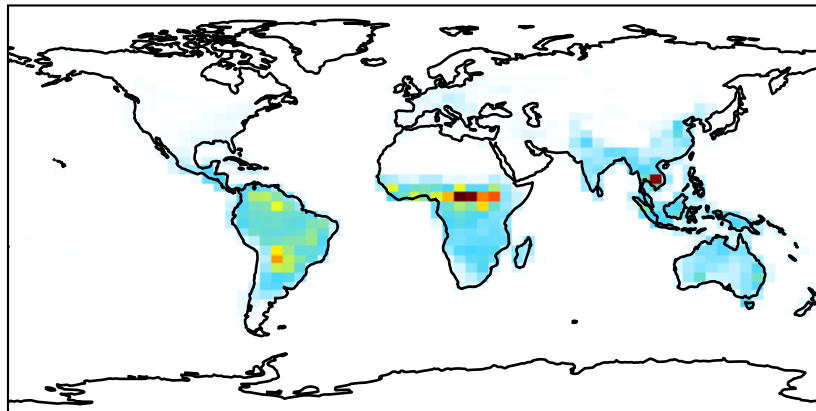


Ratio
Dev/Ref, Fixed Range

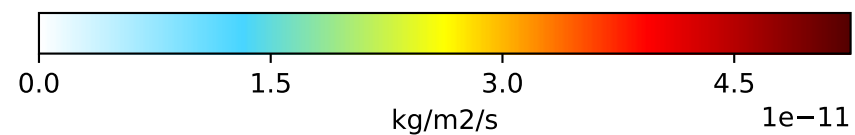
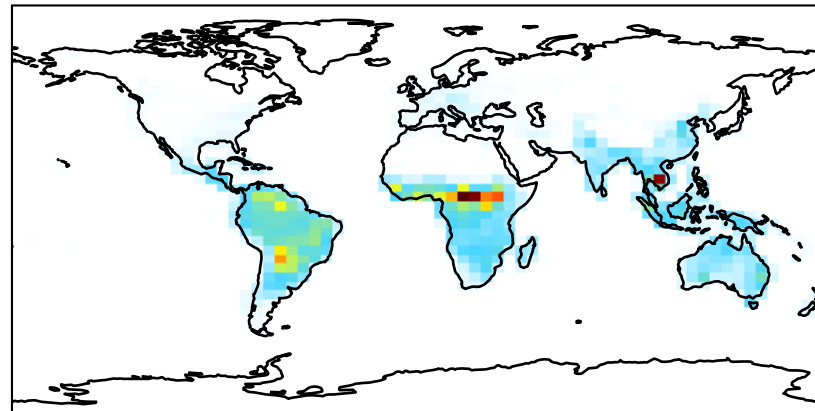


EmisPRPE_Total (Jan2019)

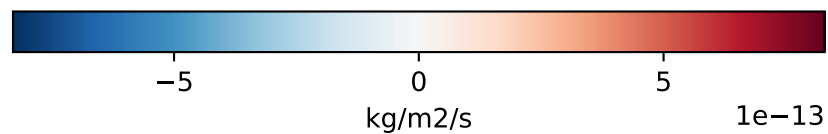
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



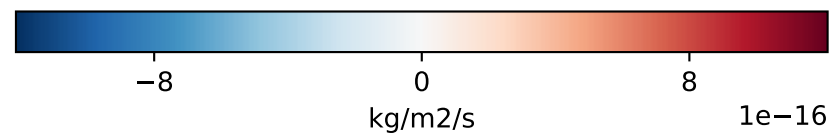
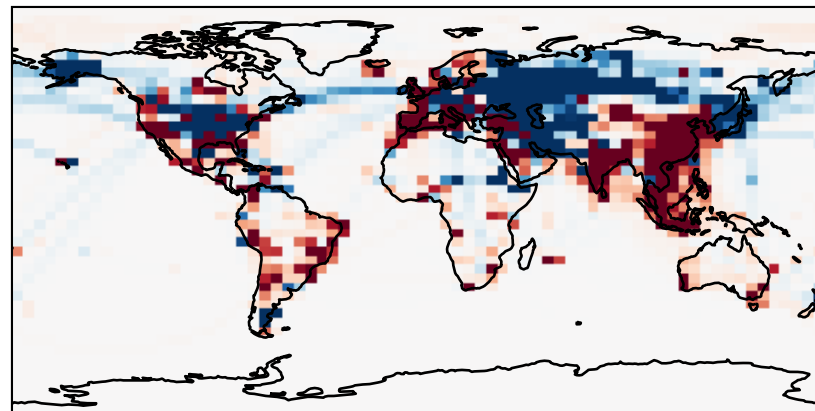
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



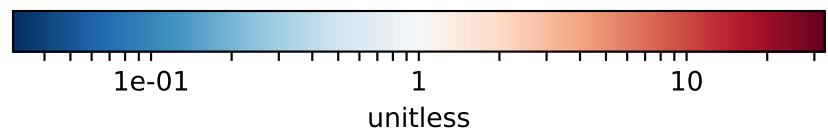
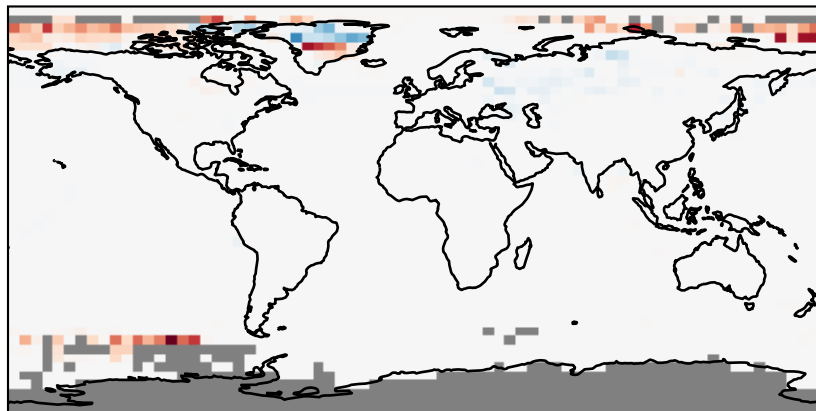
Difference
Dev - Ref, Dynamic Range



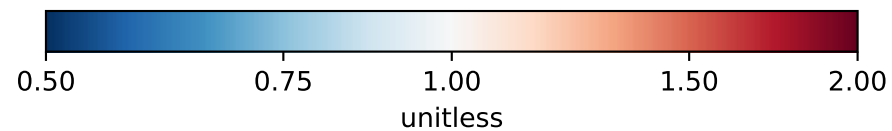
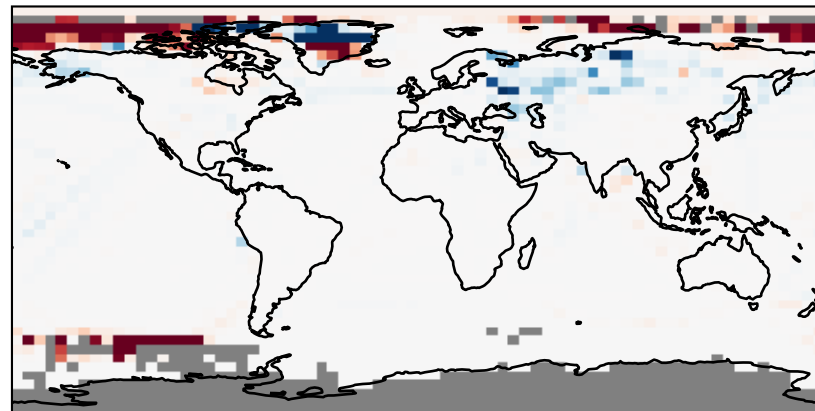
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

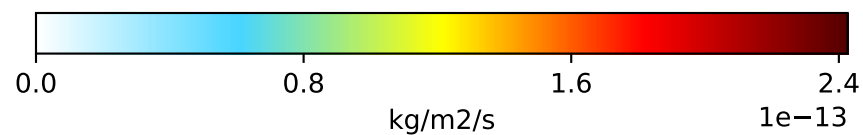
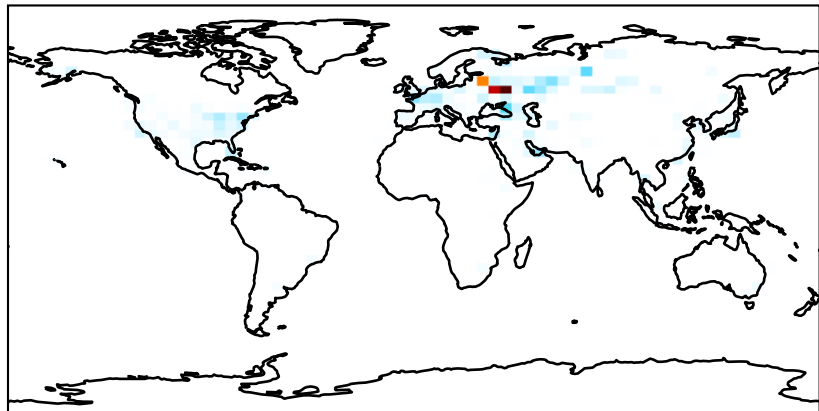


Ratio
Dev/Ref, Fixed Range

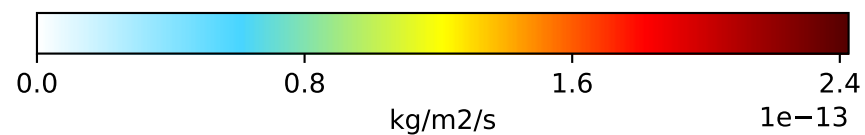


EmisRCHO_Total (Jan2019)

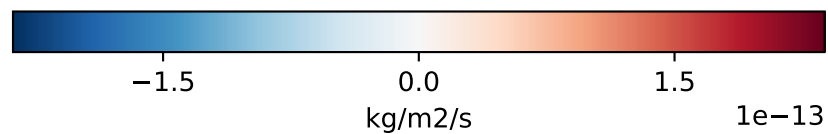
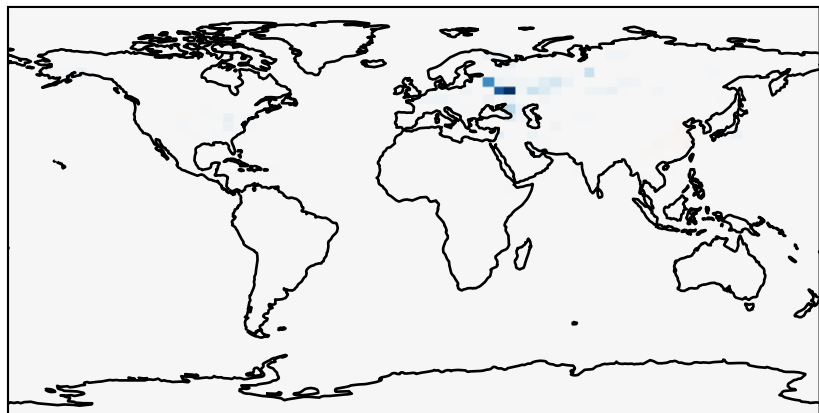
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



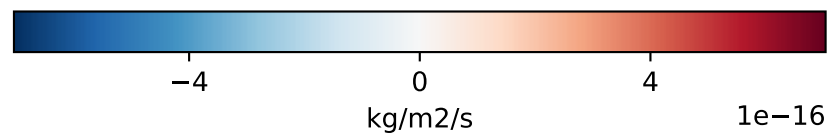
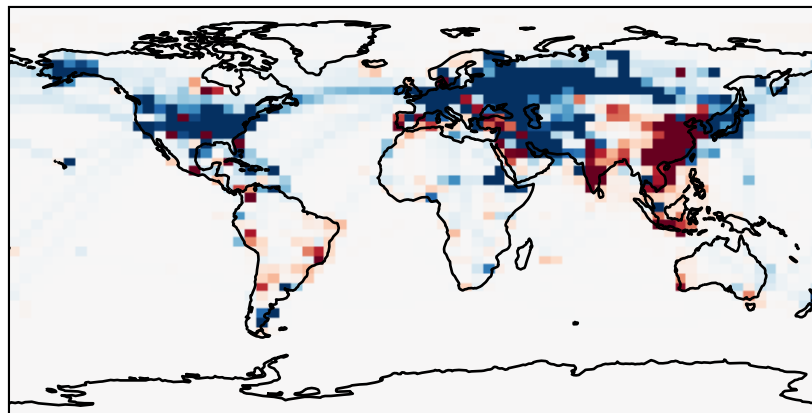
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



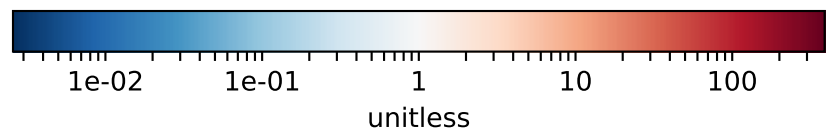
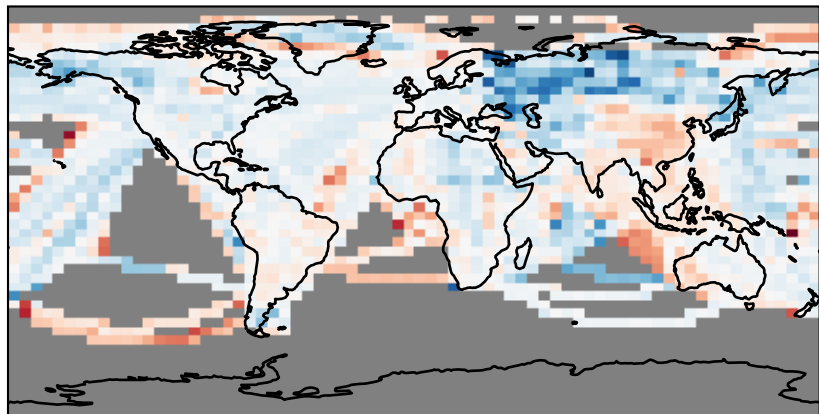
Difference
Dev - Ref, Dynamic Range



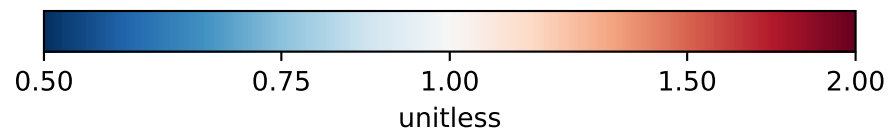
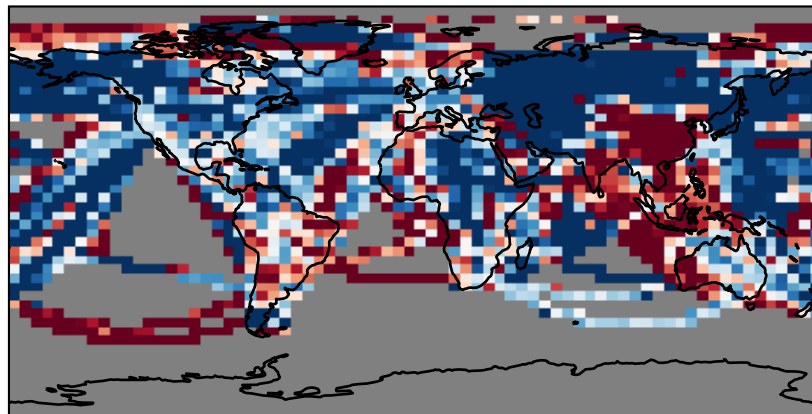
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

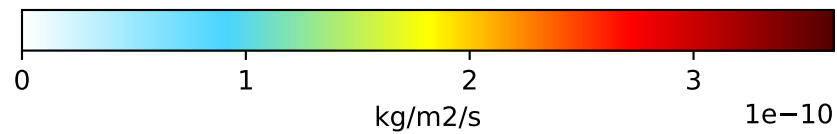
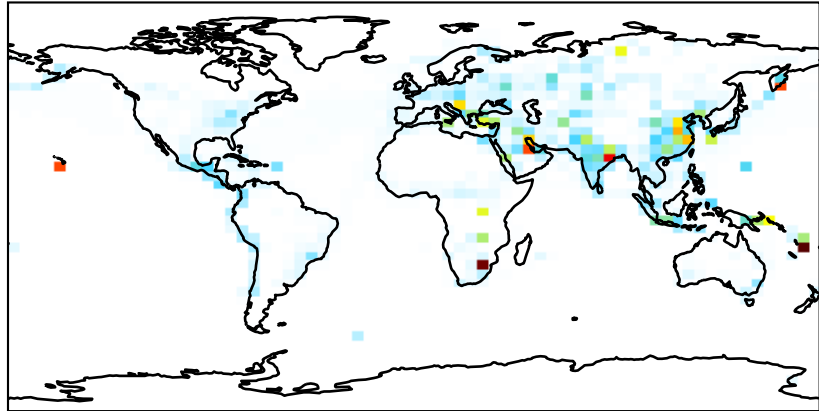


Ratio
Dev/Ref, Fixed Range

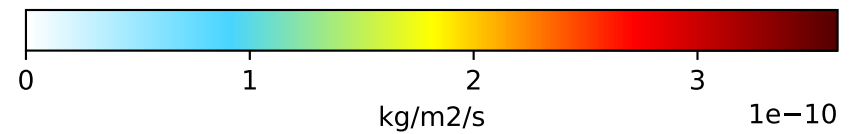
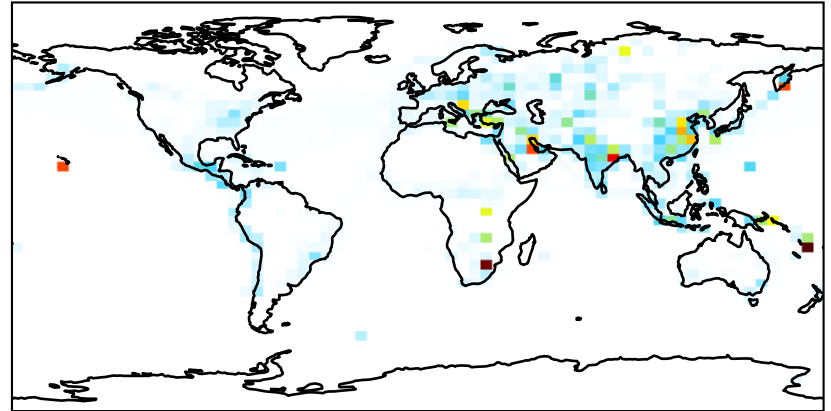


EmisSO2_Total (Jan2019)

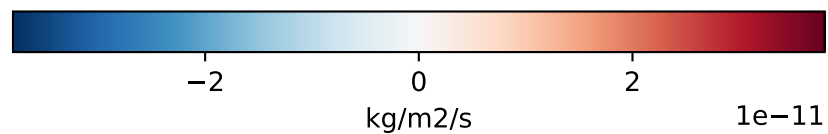
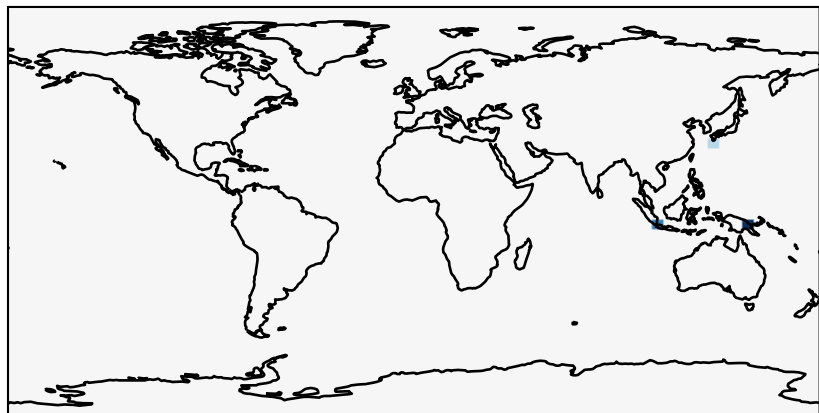
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



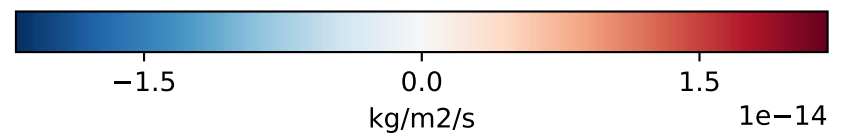
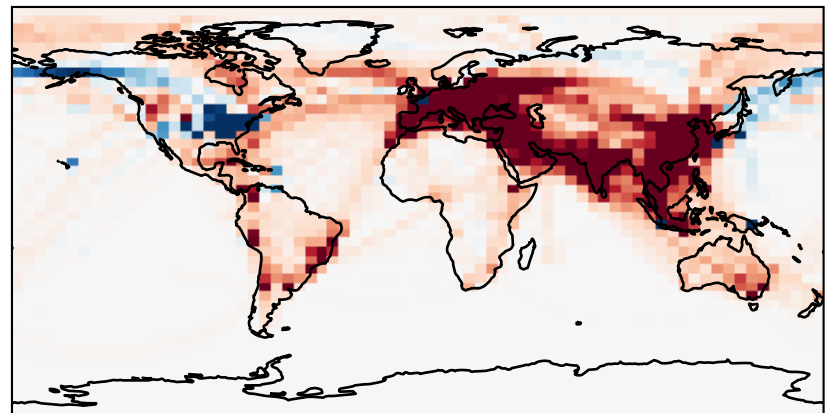
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



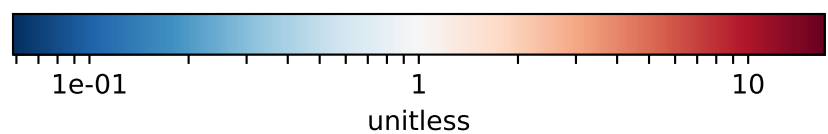
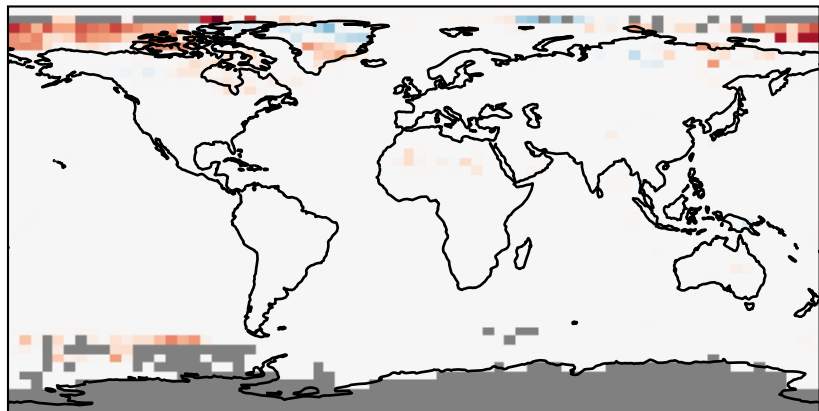
Difference
Dev - Ref, Dynamic Range



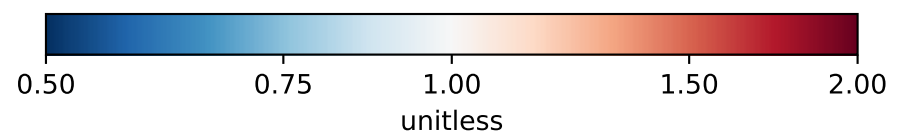
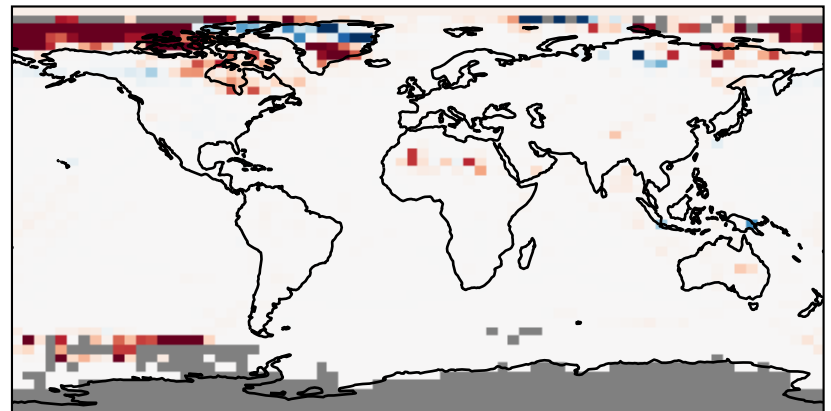
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

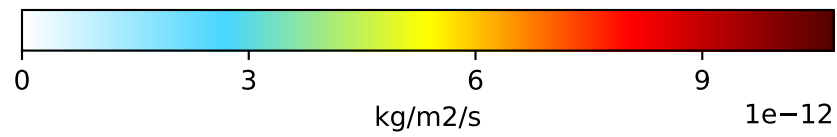
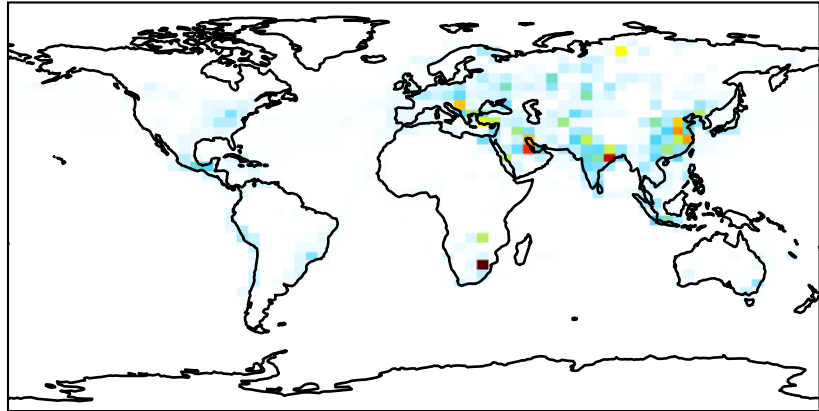


Ratio
Dev/Ref, Fixed Range

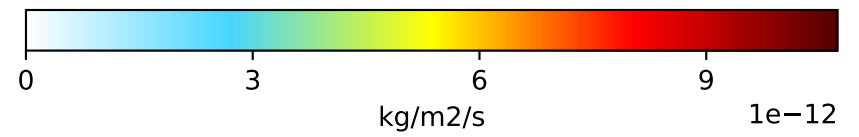
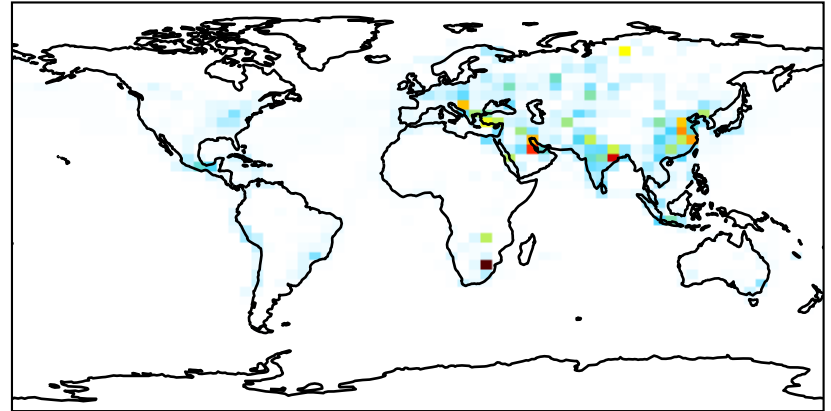


EmisSO4_Total (Jan2019)

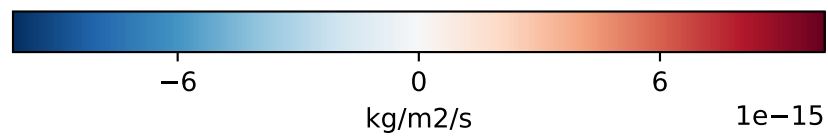
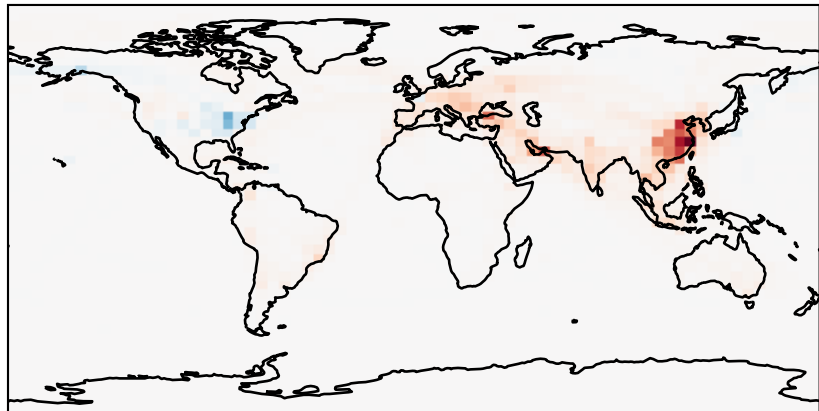
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



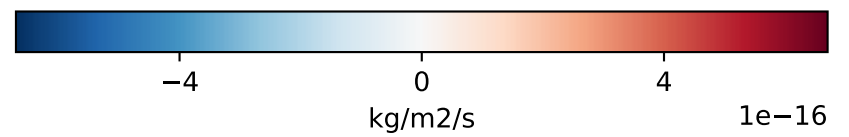
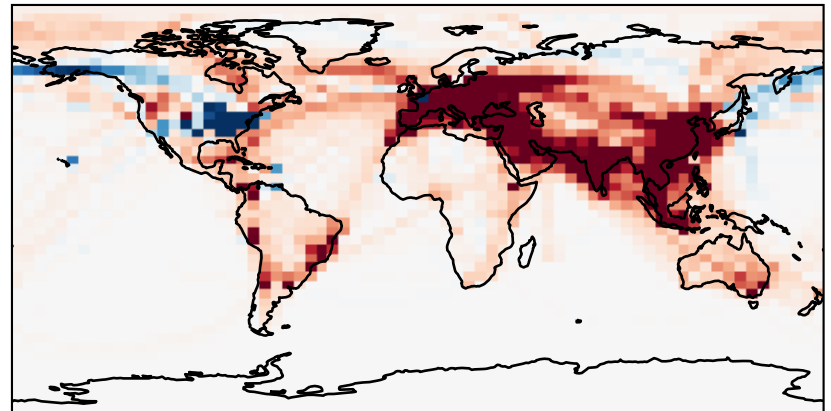
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



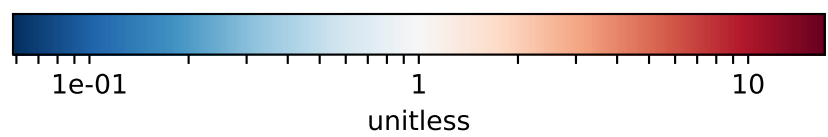
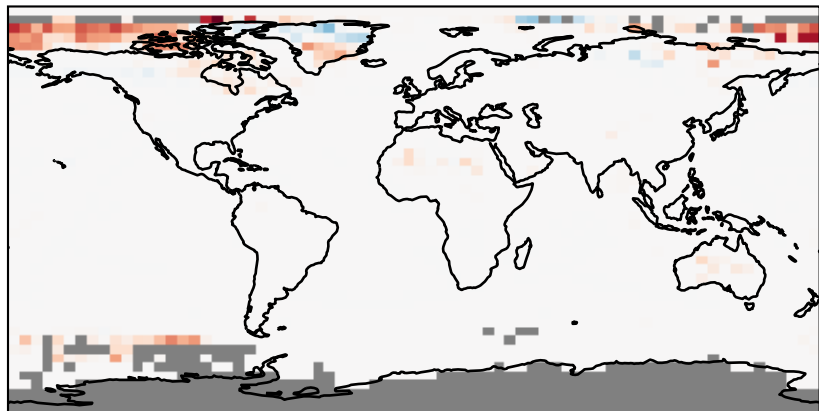
Difference
Dev - Ref, Dynamic Range



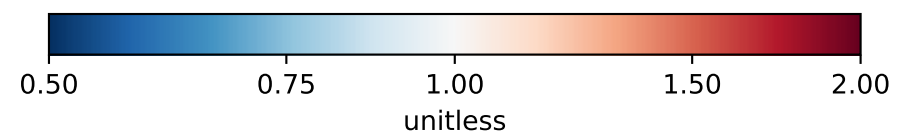
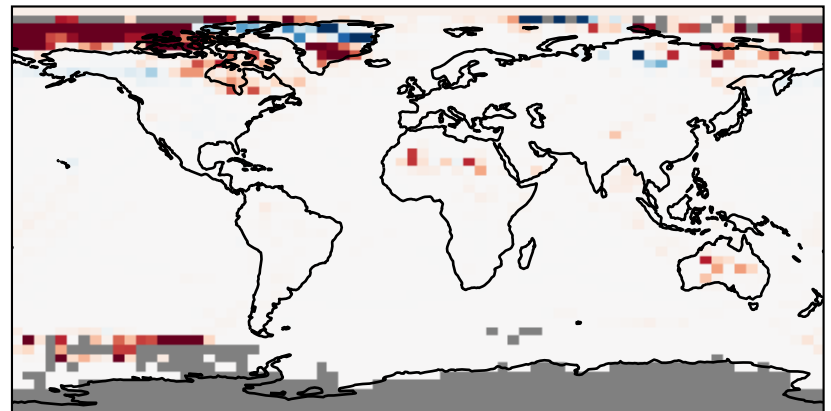
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

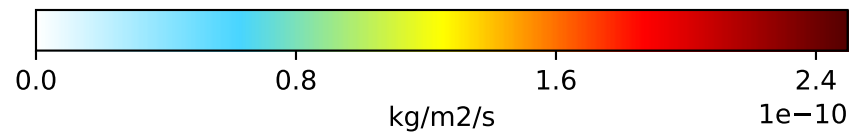
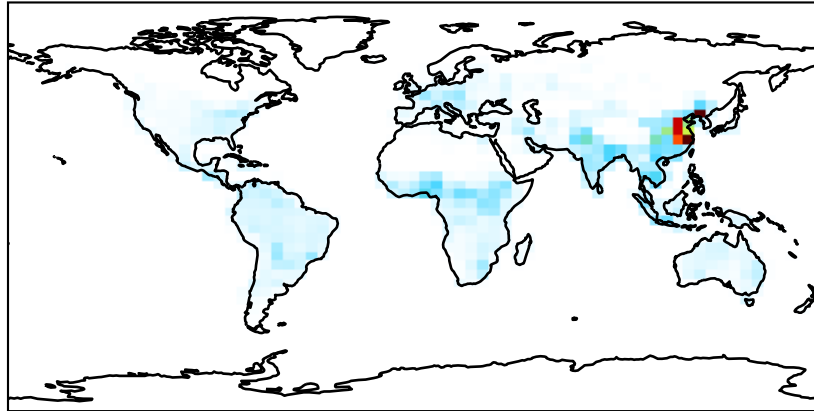


Ratio
Dev/Ref, Fixed Range

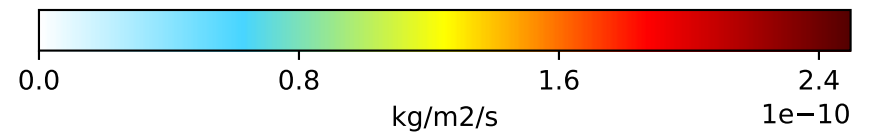
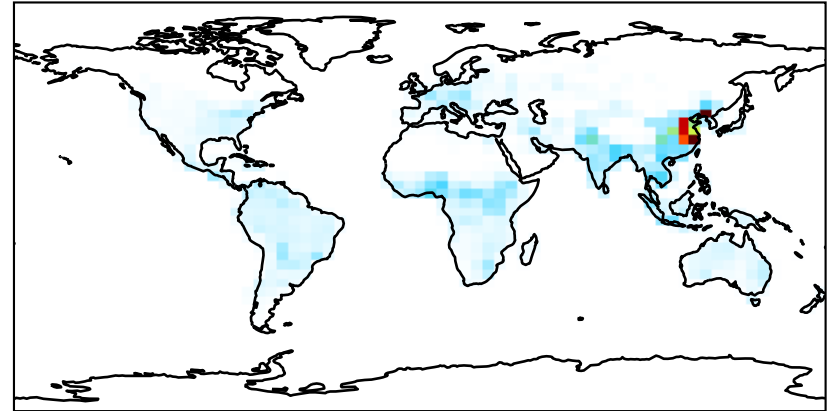


EmisSOAP_Total (Jan2019)

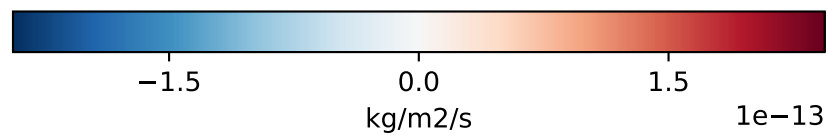
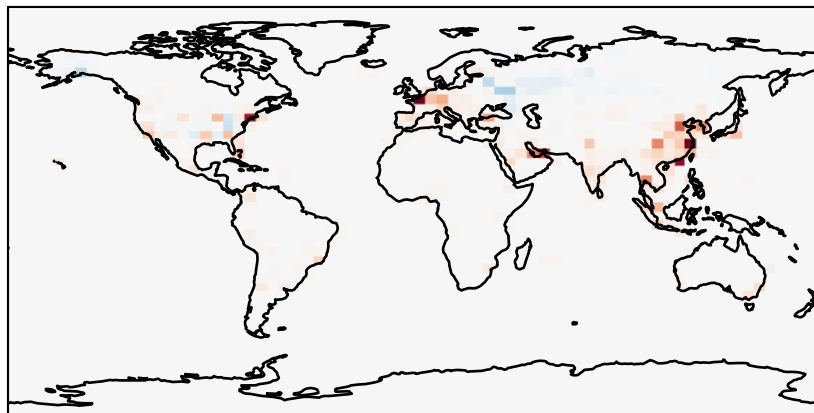
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



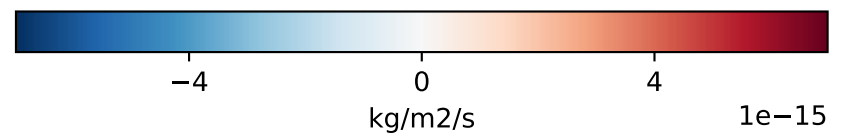
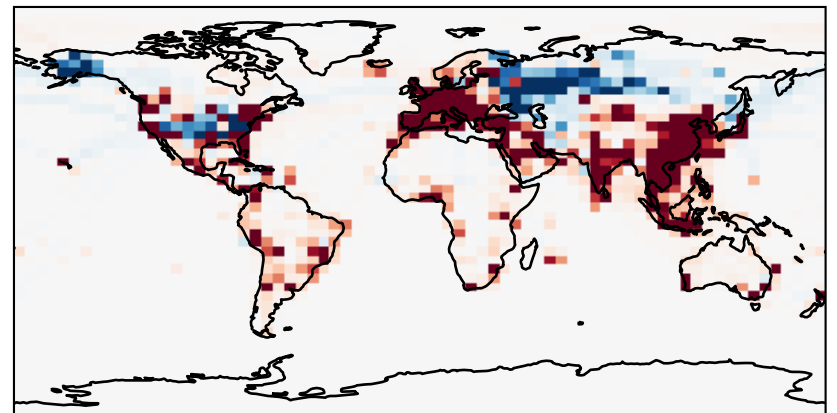
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



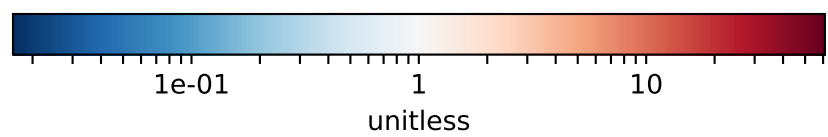
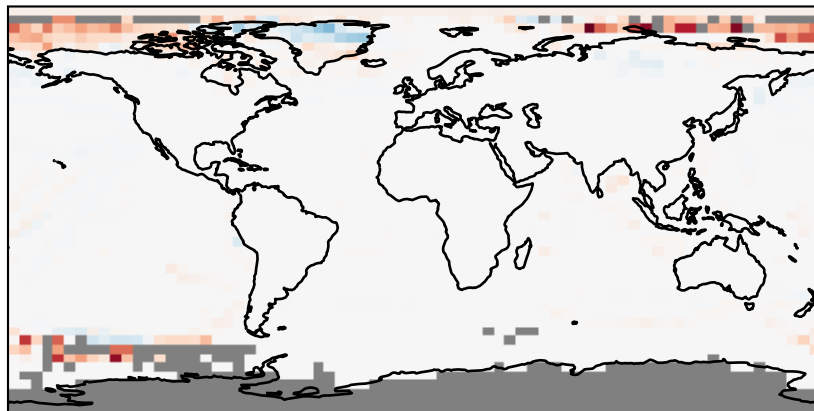
Difference
Dev - Ref, Dynamic Range



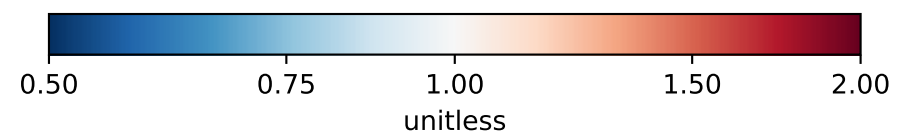
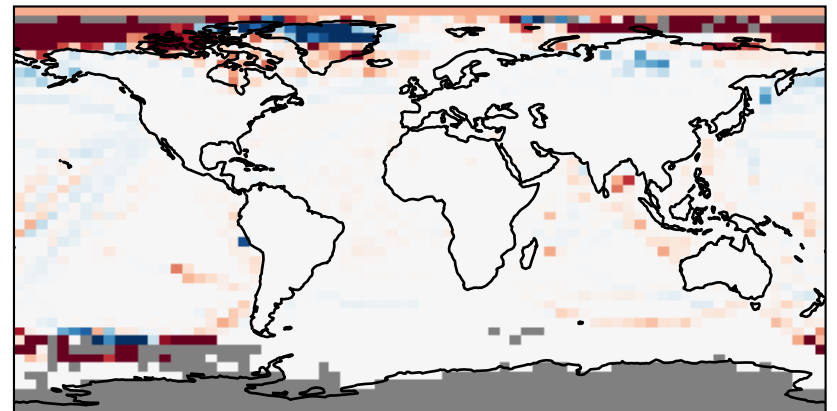
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

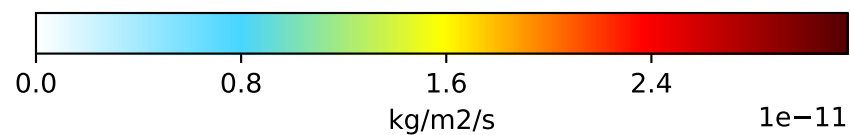
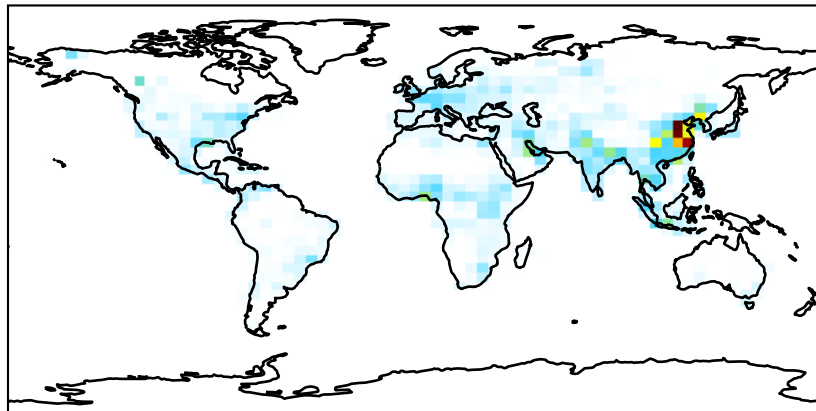


Ratio
Dev/Ref, Fixed Range

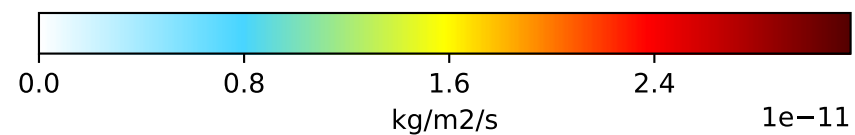
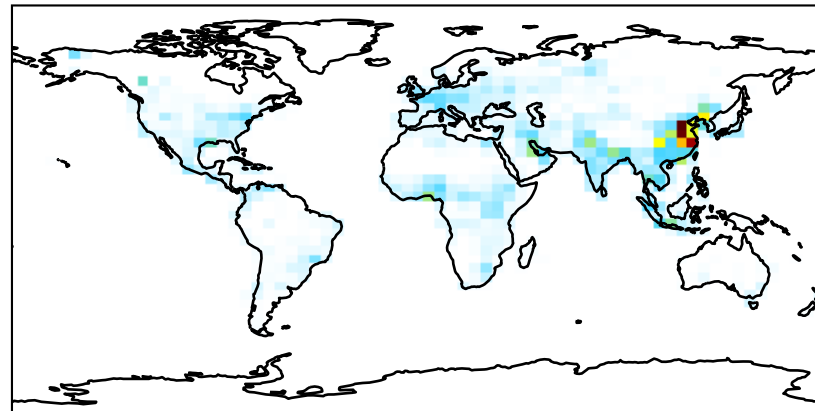


EmisTOLU_Total (Jan2019)

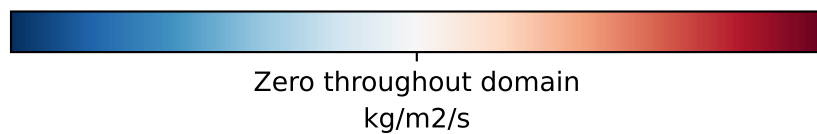
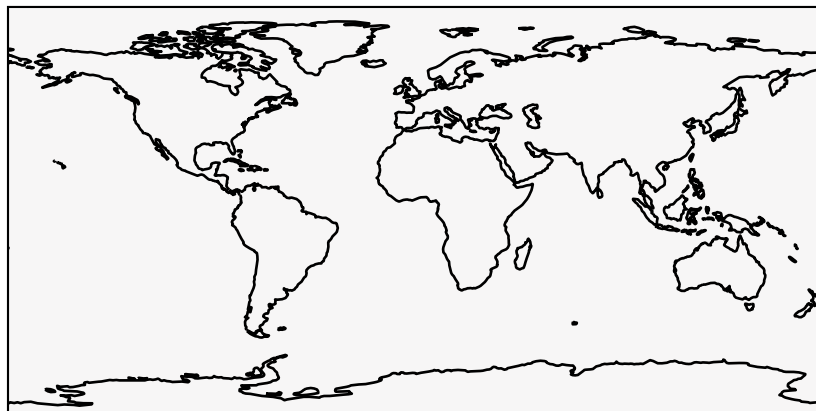
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



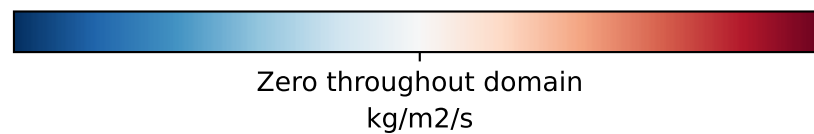
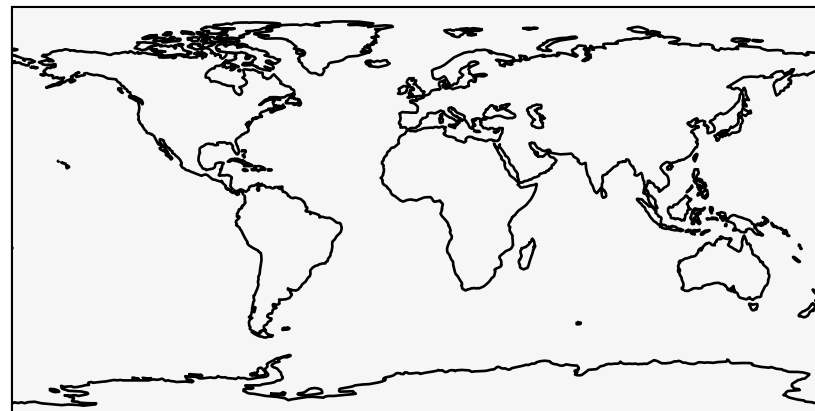
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



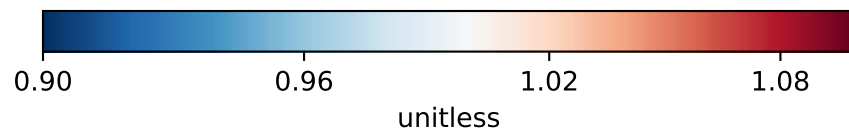
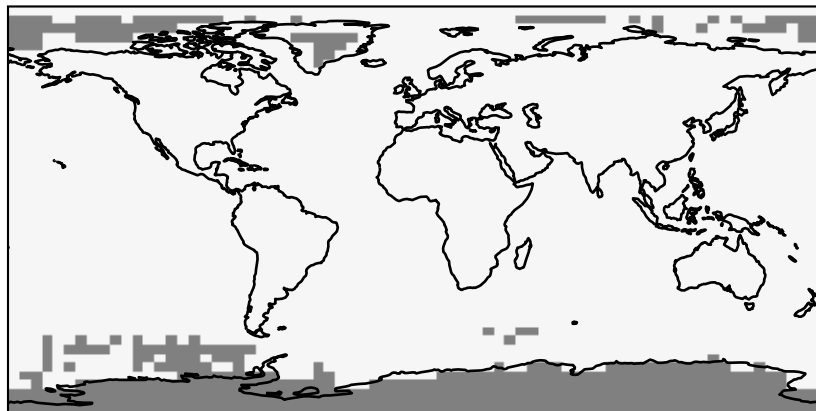
Difference
Dev - Ref, Dynamic Range



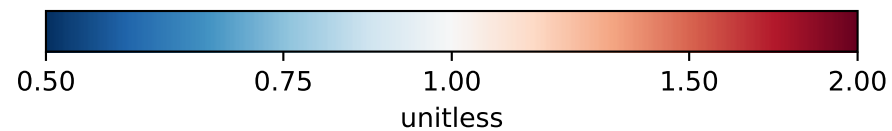
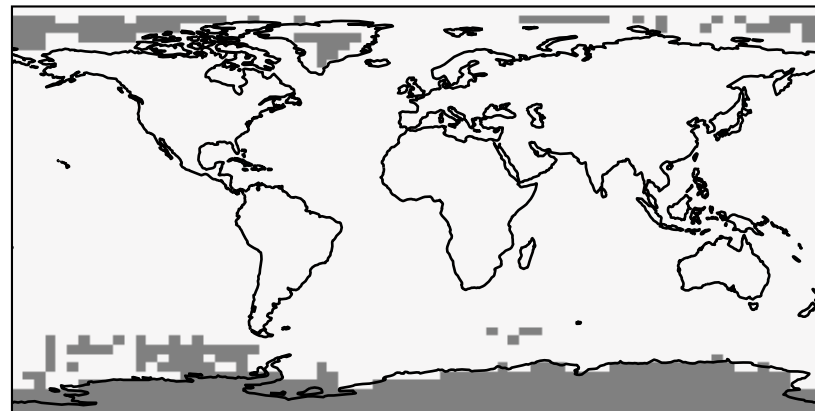
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

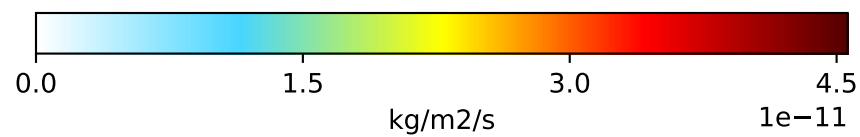
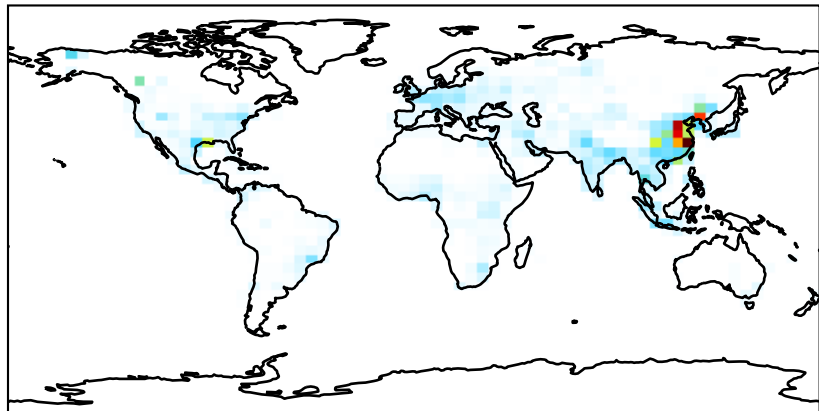


Ratio
Dev/Ref, Fixed Range

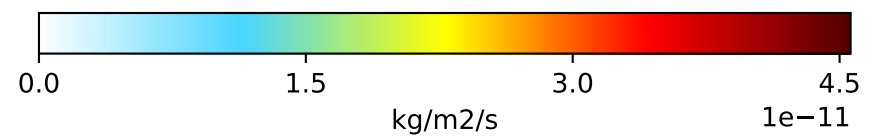
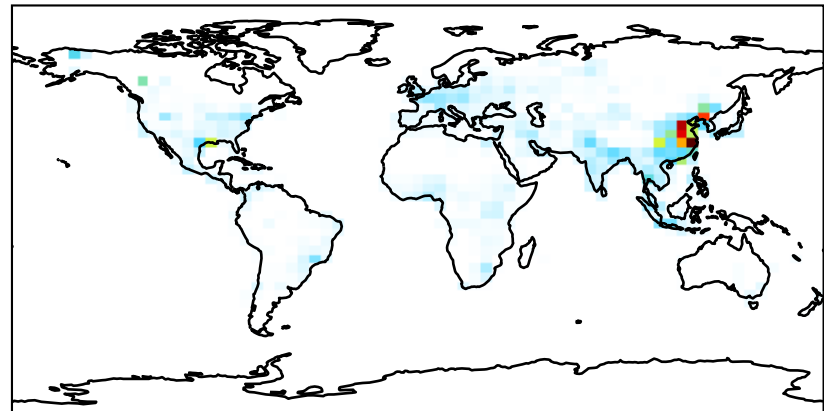


EmisXYLE_Total (Jan2019)

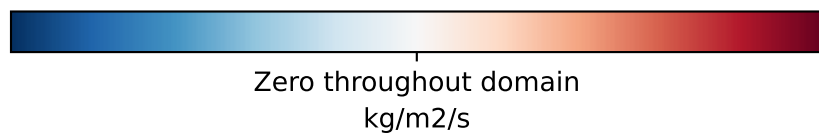
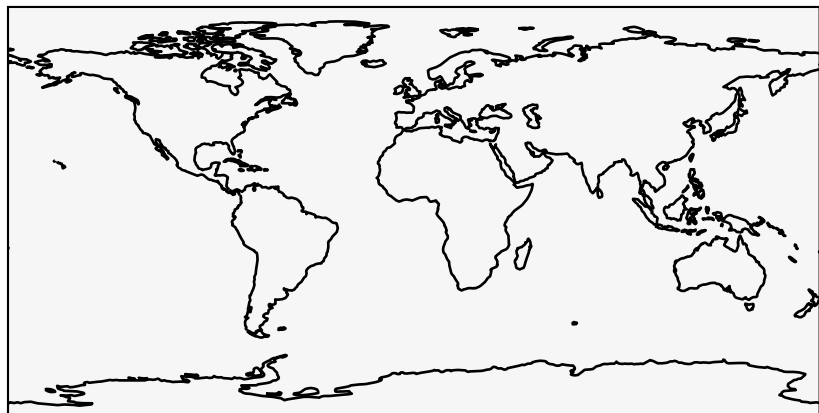
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



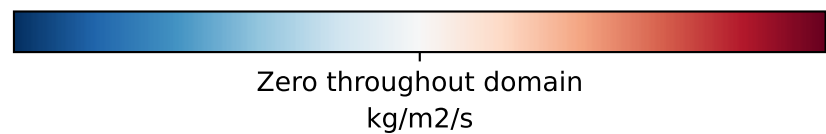
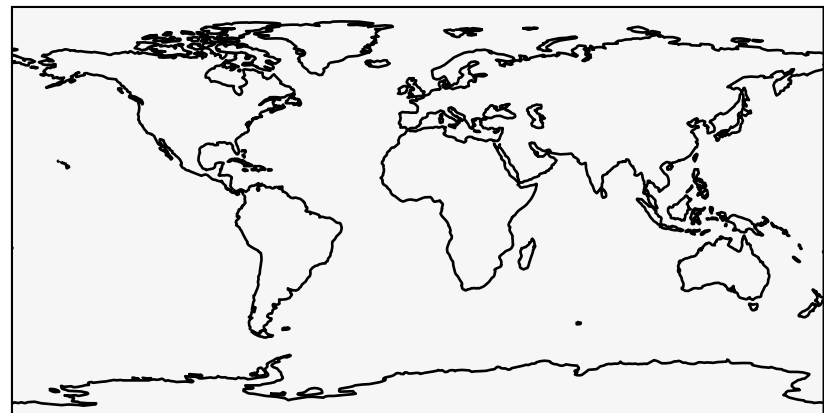
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



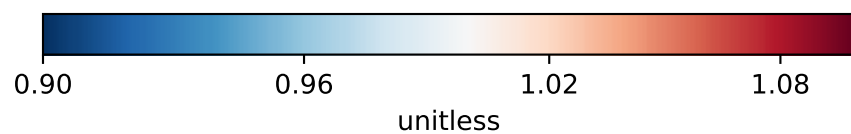
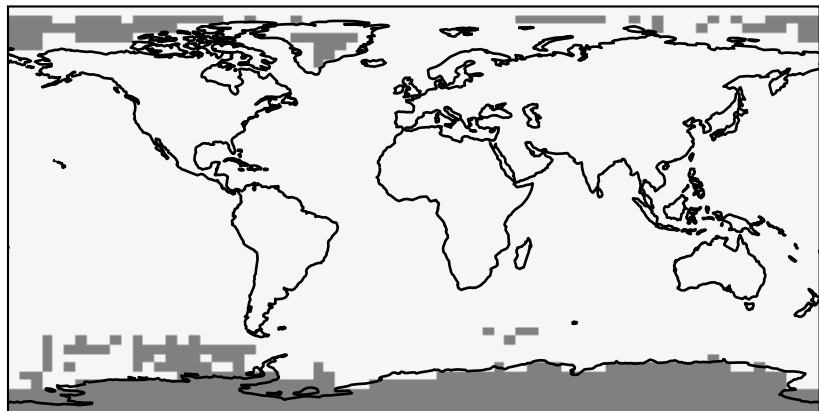
Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range



Ratio
Dev/Ref, Fixed Range

