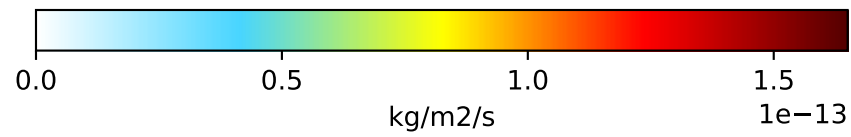
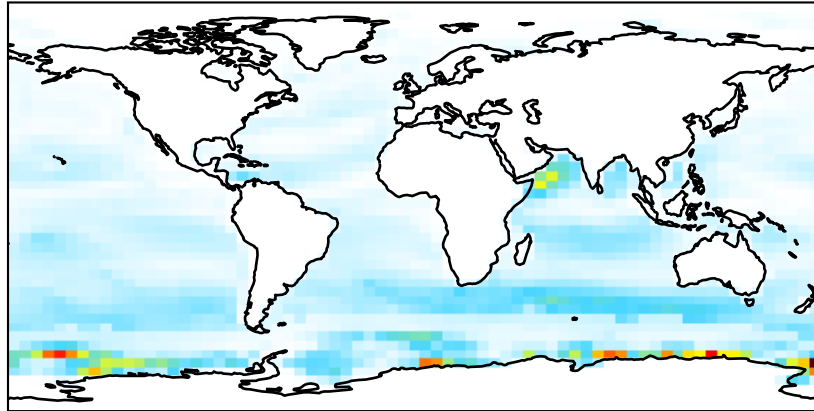
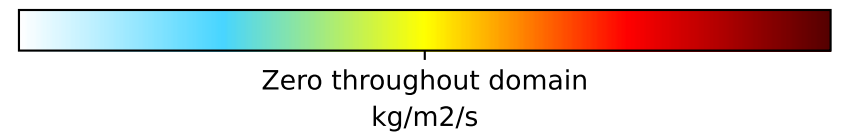
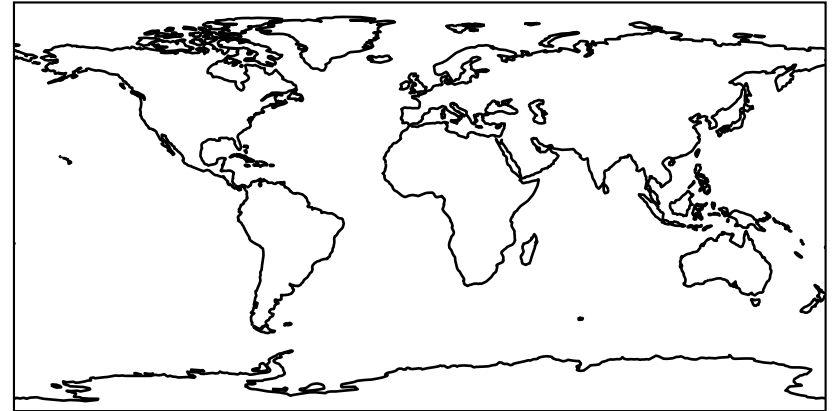


EmisBrSALA_Natural (Jul2019)

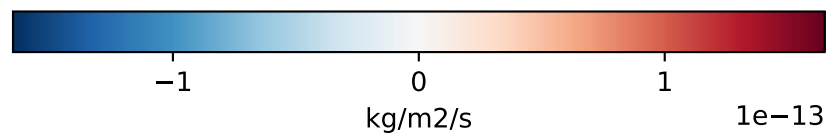
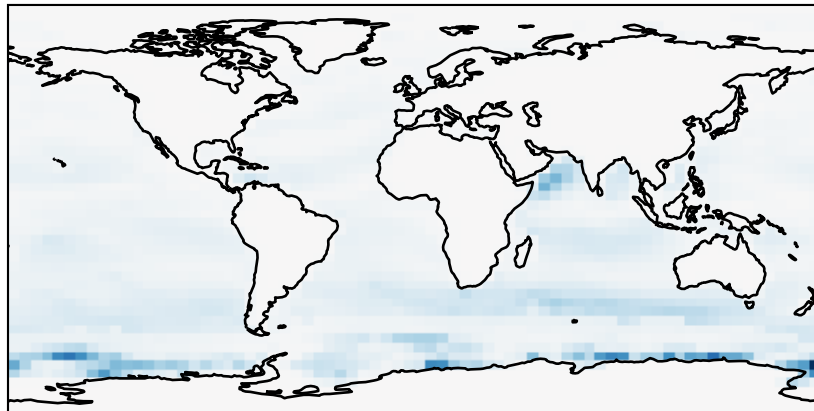
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



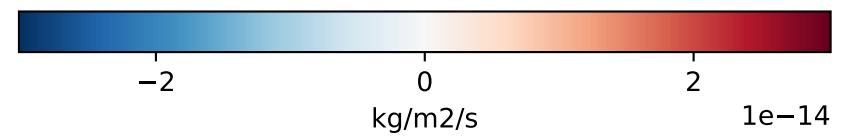
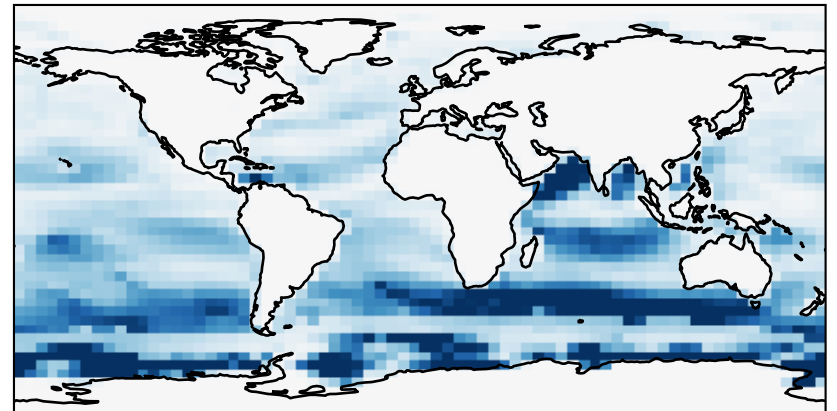
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



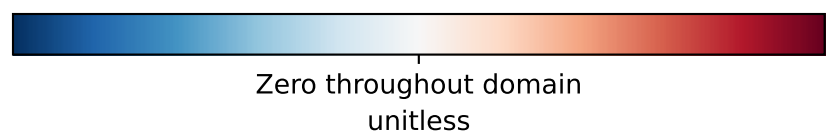
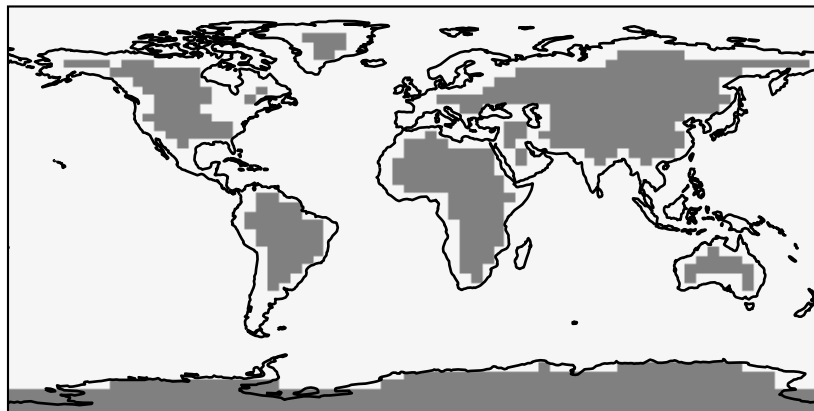
Difference
Dev - Ref, Dynamic Range



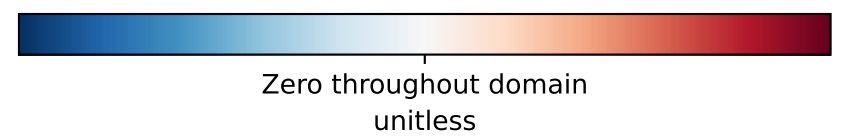
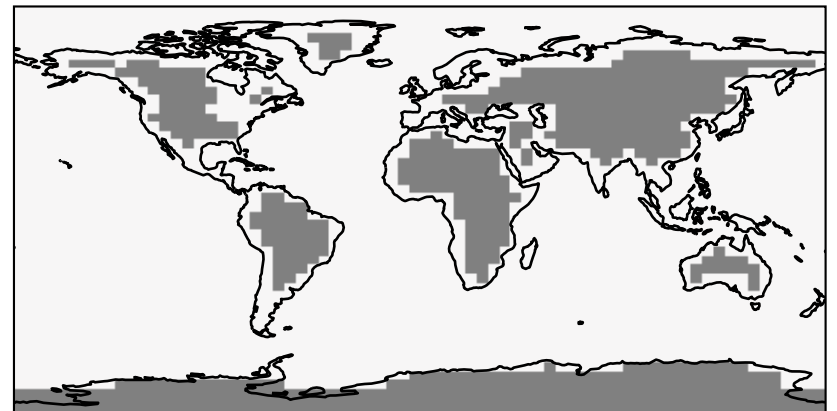
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

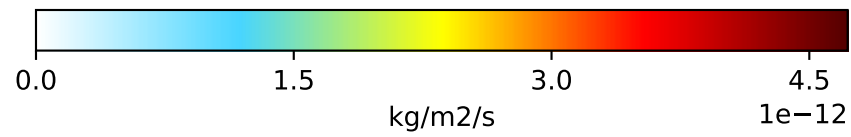
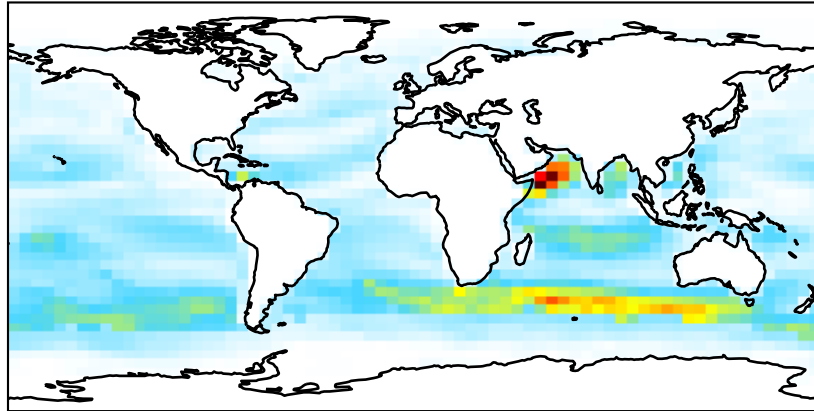


Ratio
Dev/Ref, Fixed Range

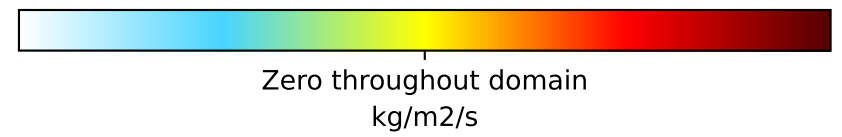
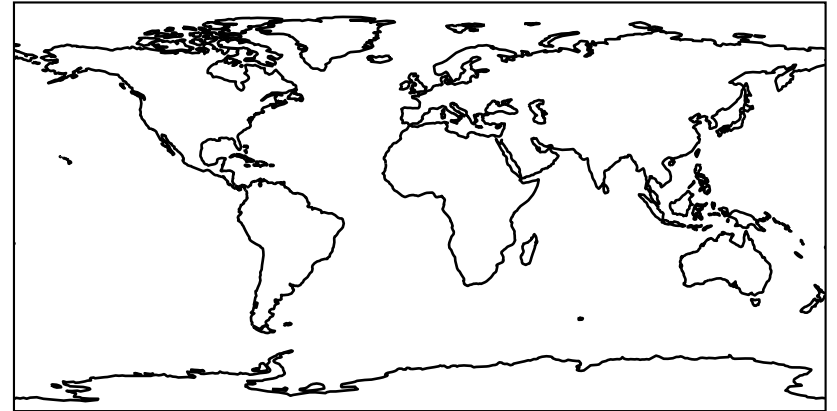


EmisBrSALC_Natural (Jul2019)

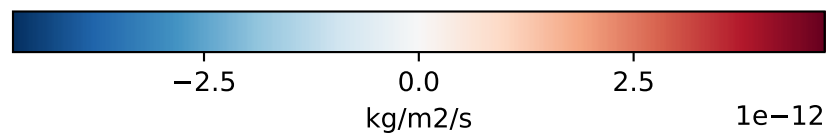
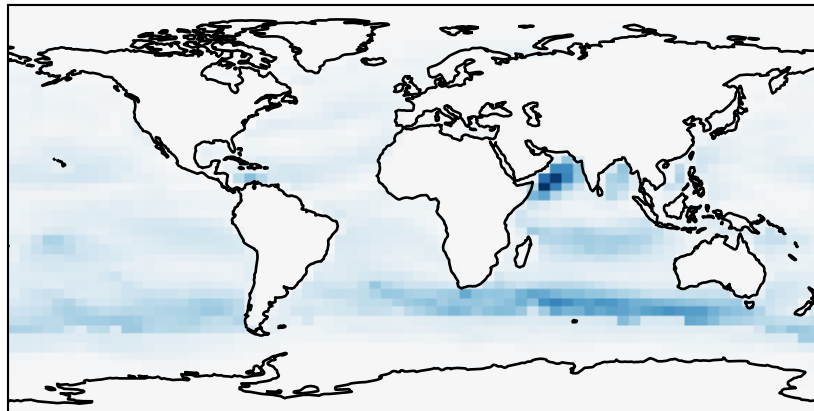
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



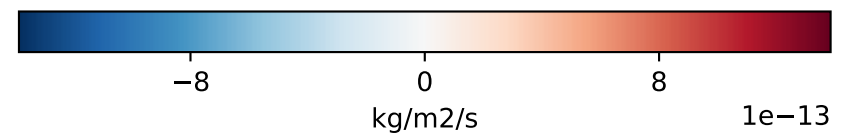
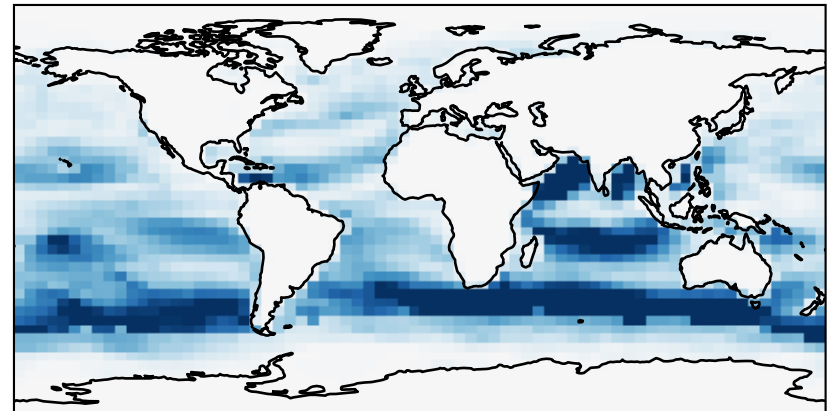
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



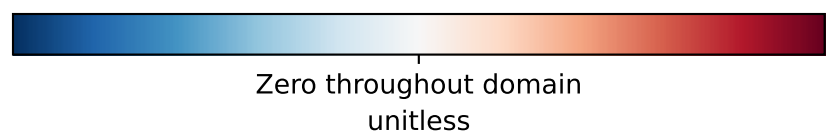
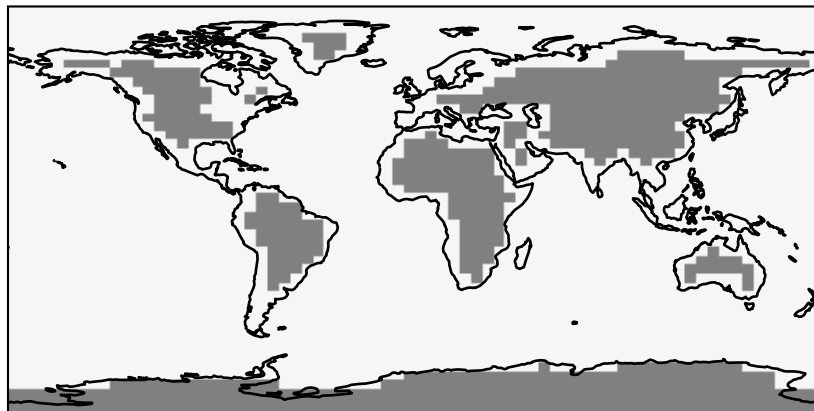
Difference
Dev - Ref, Dynamic Range



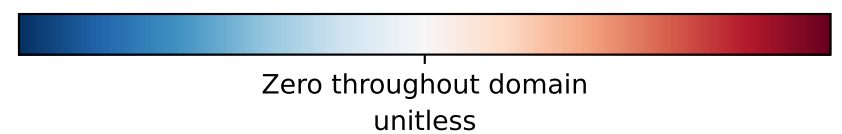
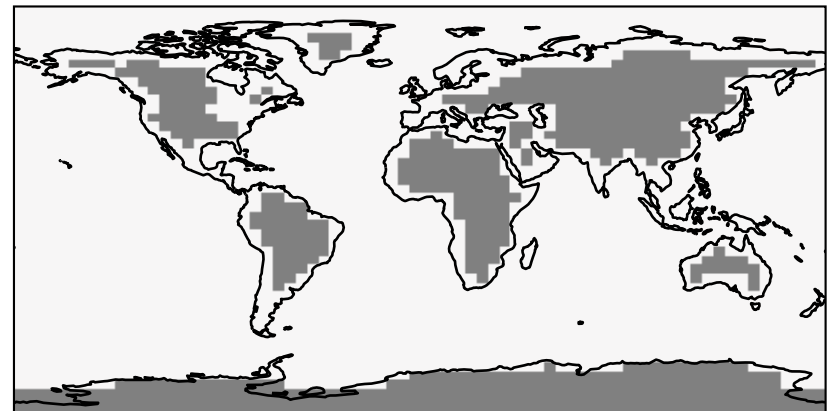
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

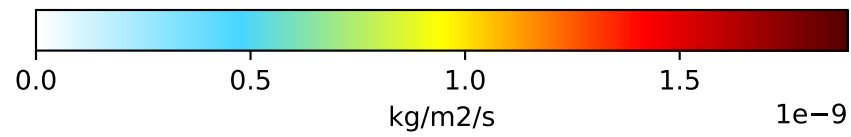
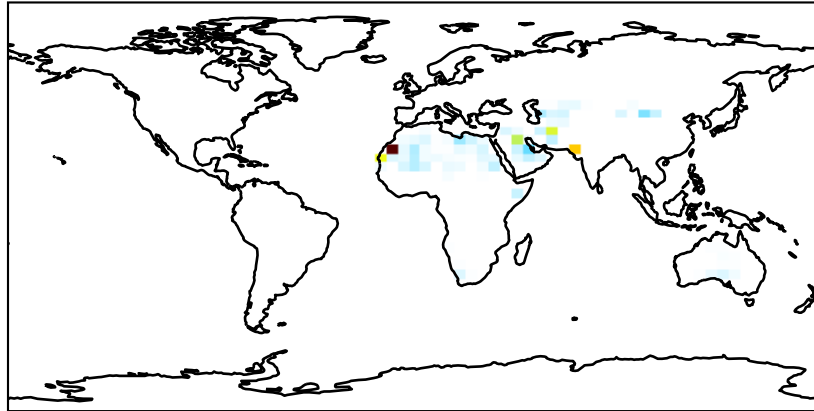


Ratio
Dev/Ref, Fixed Range

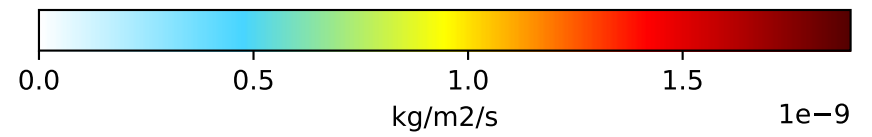
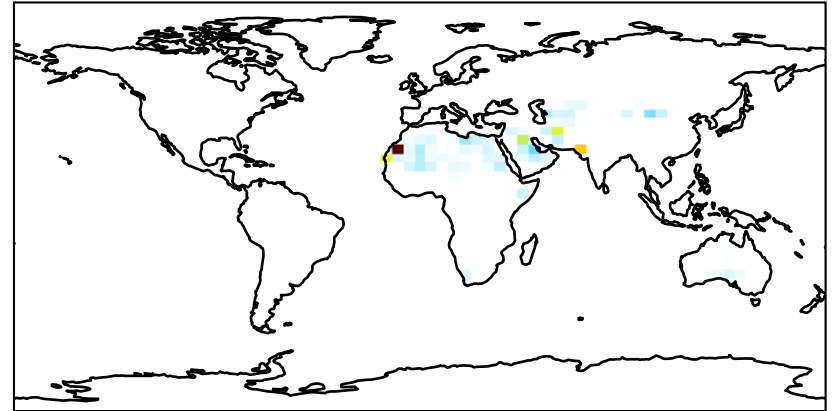


EmisDST1_Natural (Jul2019)

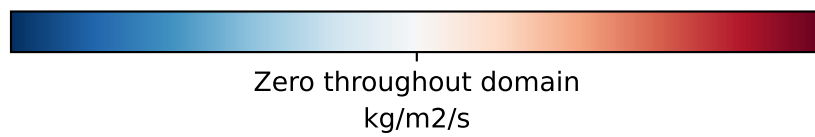
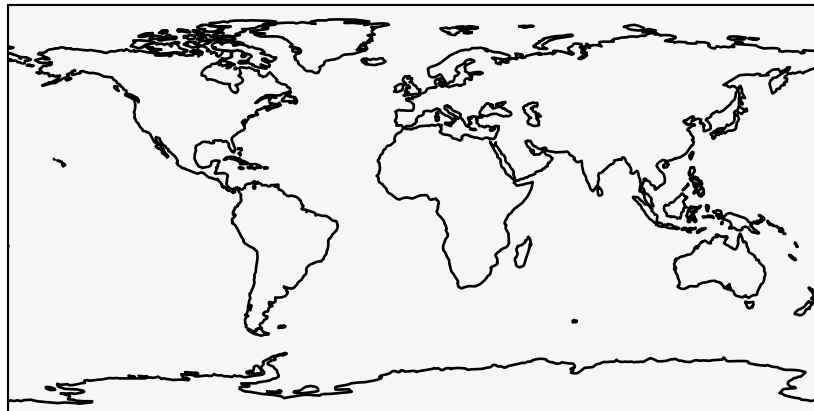
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



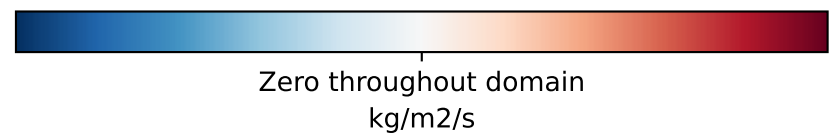
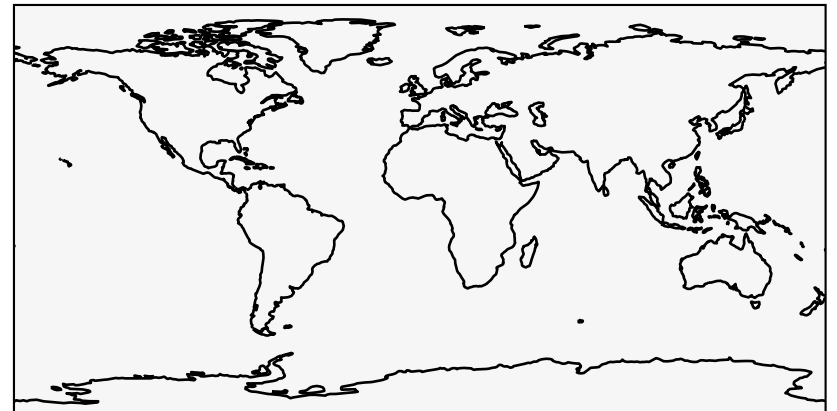
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



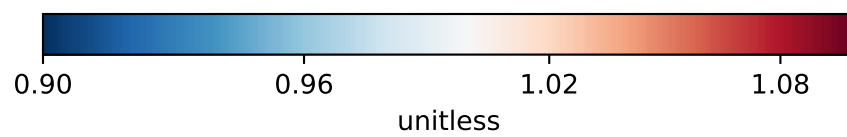
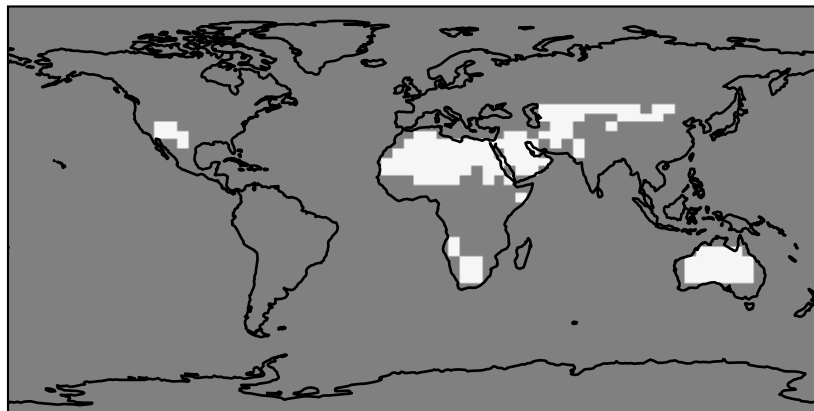
Difference
Dev - Ref, Dynamic Range



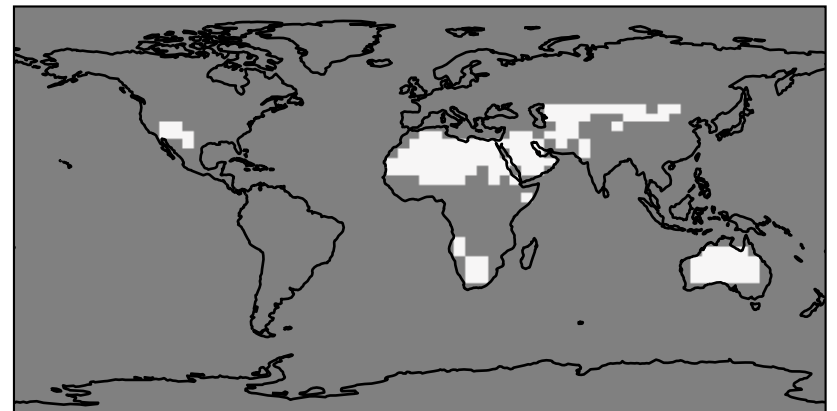
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

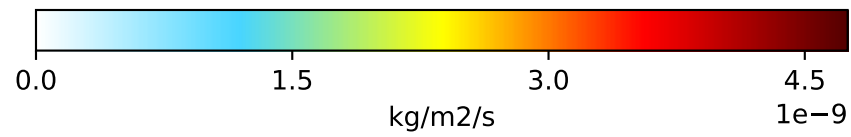
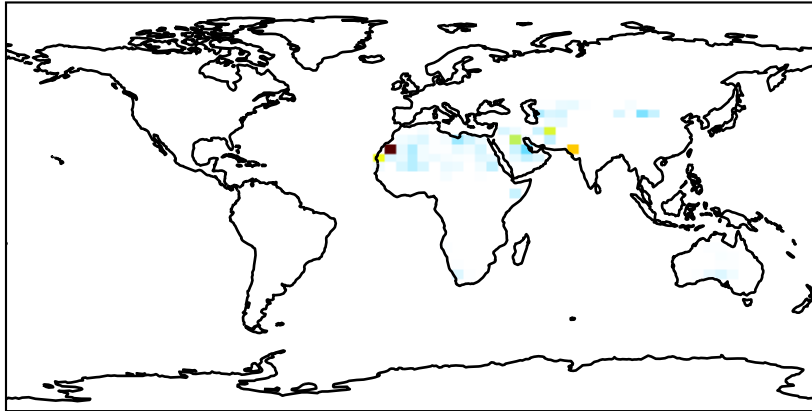


Ratio
Dev/Ref, Fixed Range

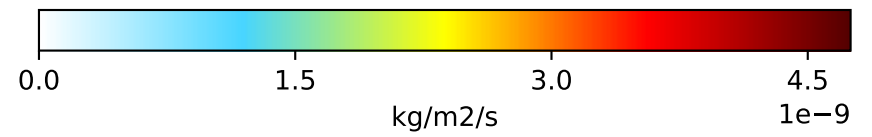
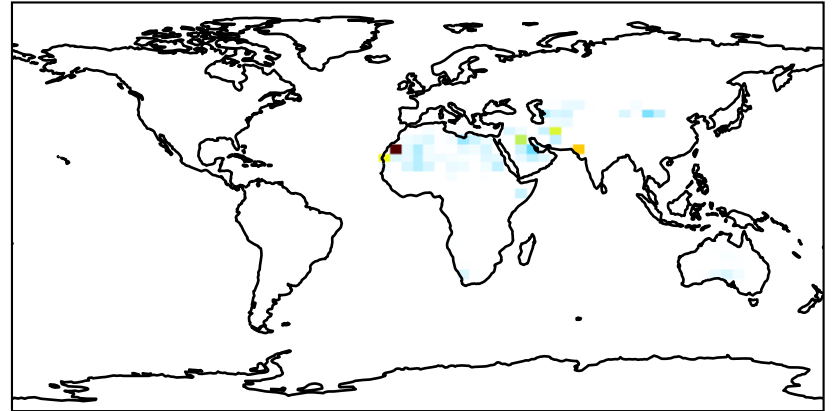


EmisDST2_Natural (Jul2019)

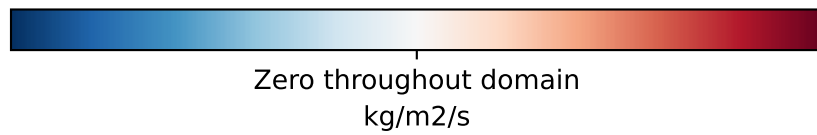
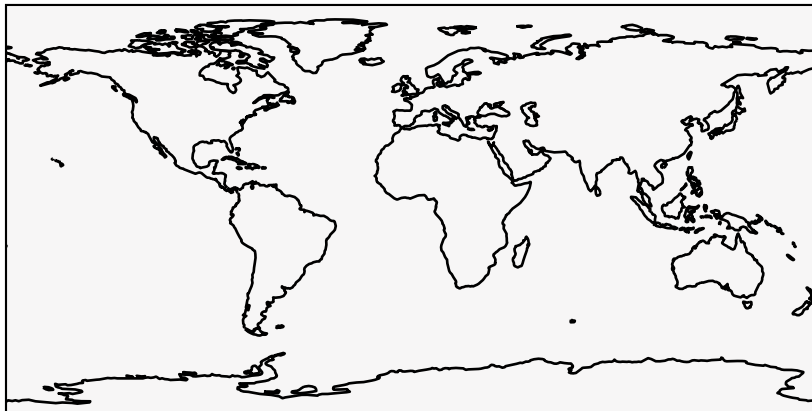
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



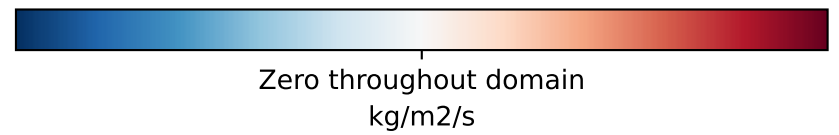
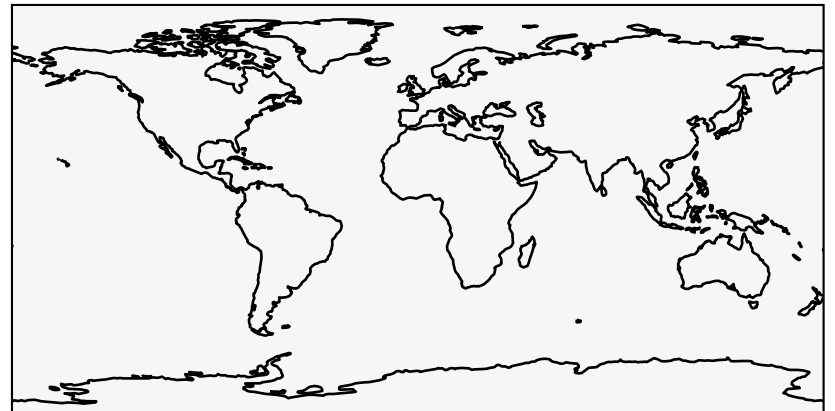
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



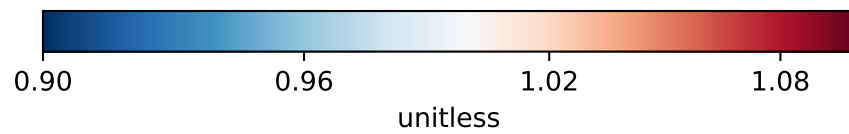
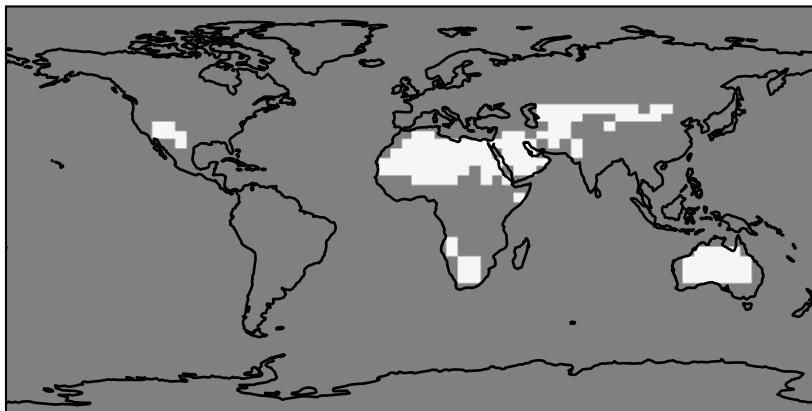
Difference
Dev - Ref, Dynamic Range



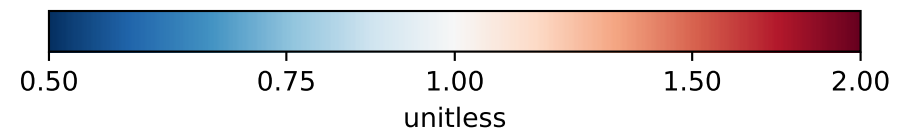
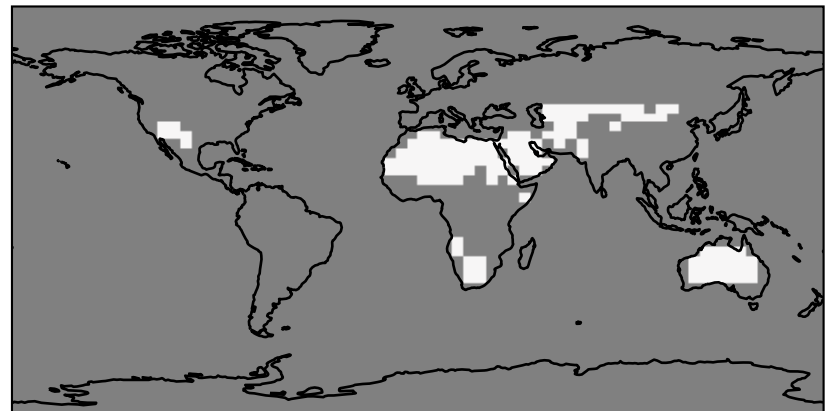
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

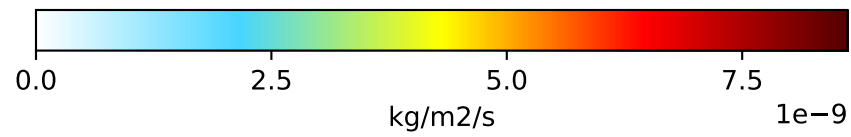
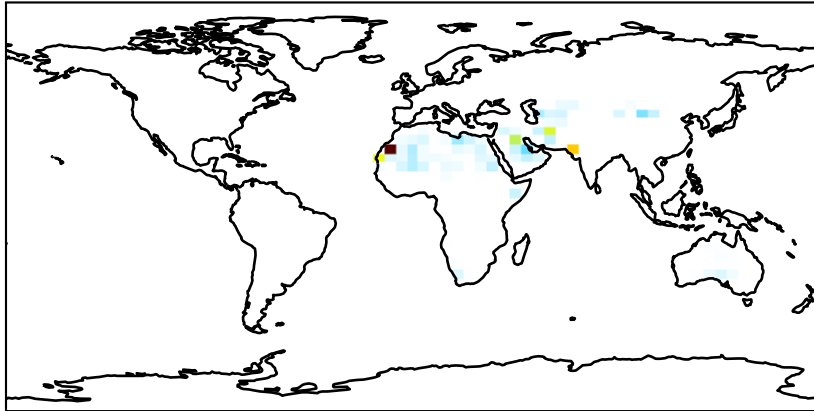


Ratio
Dev/Ref, Fixed Range

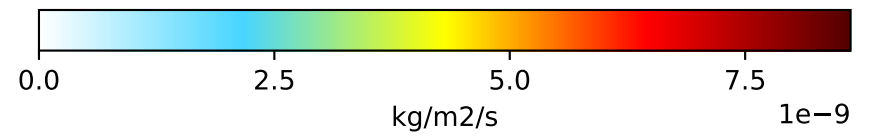
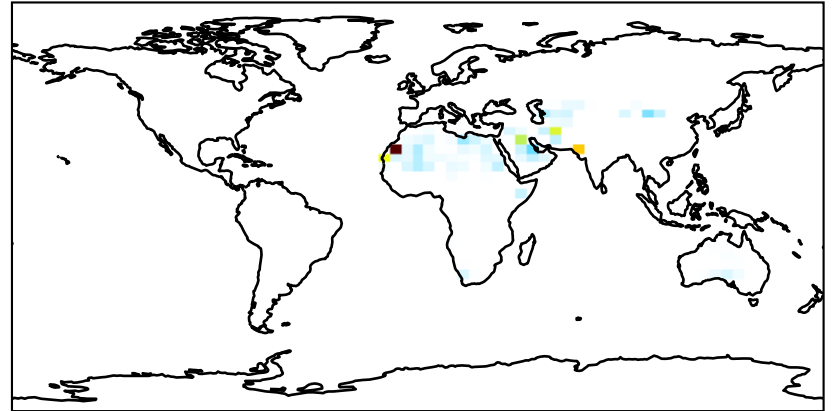


EmisDST3_Natural (Jul2019)

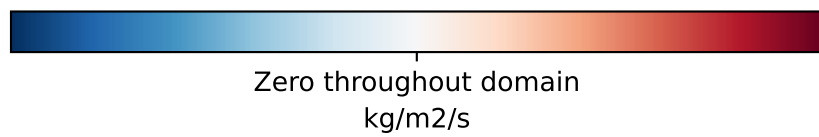
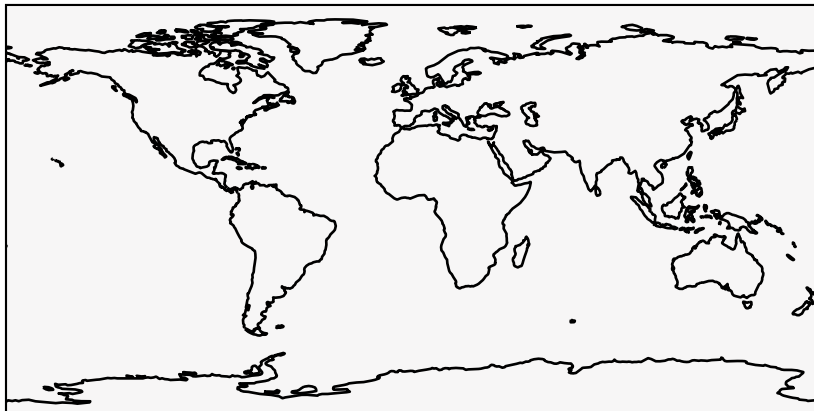
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



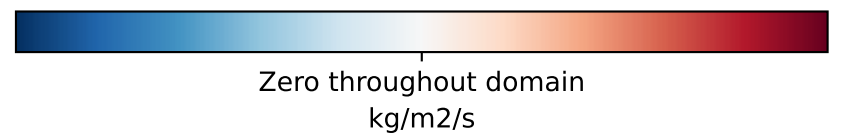
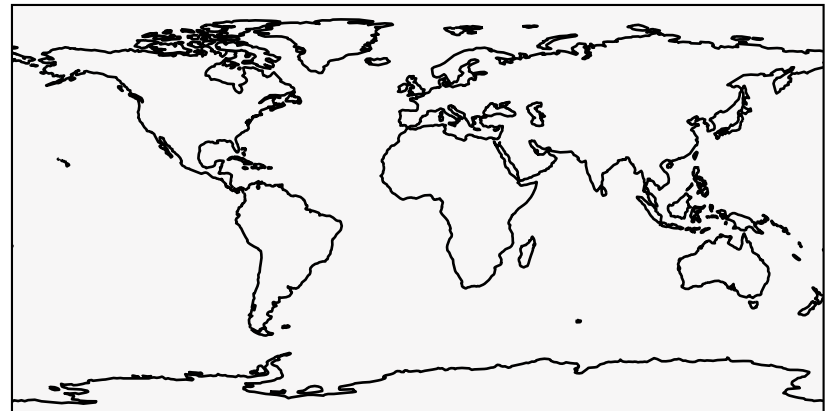
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



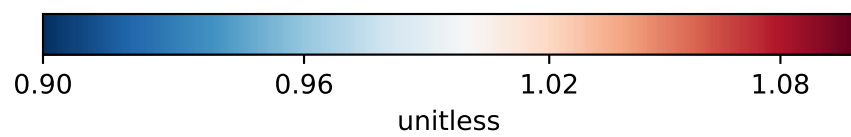
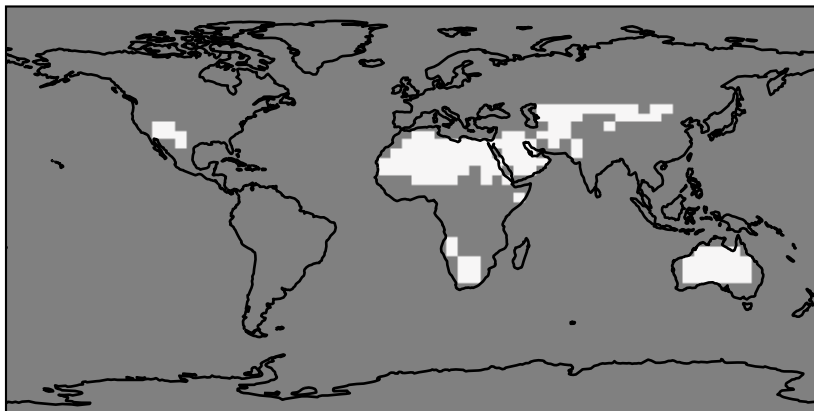
Difference
Dev - Ref, Dynamic Range



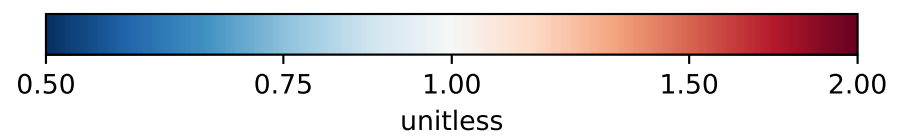
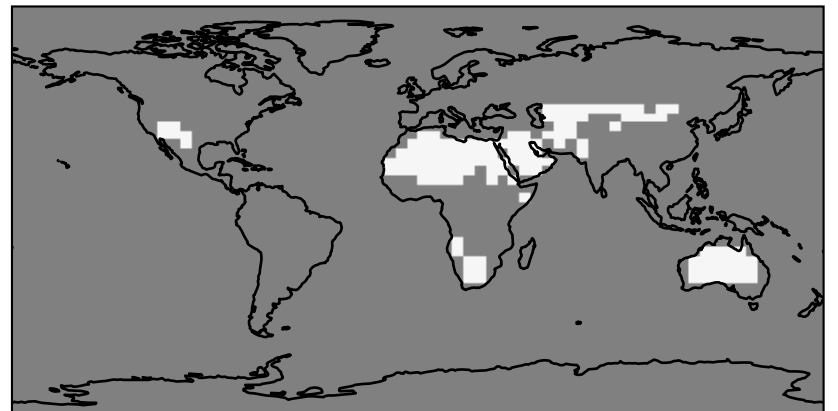
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

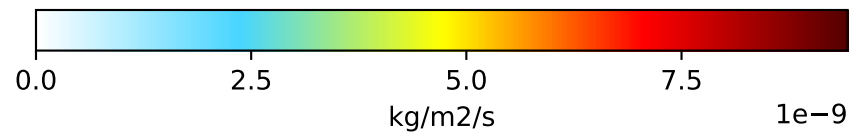
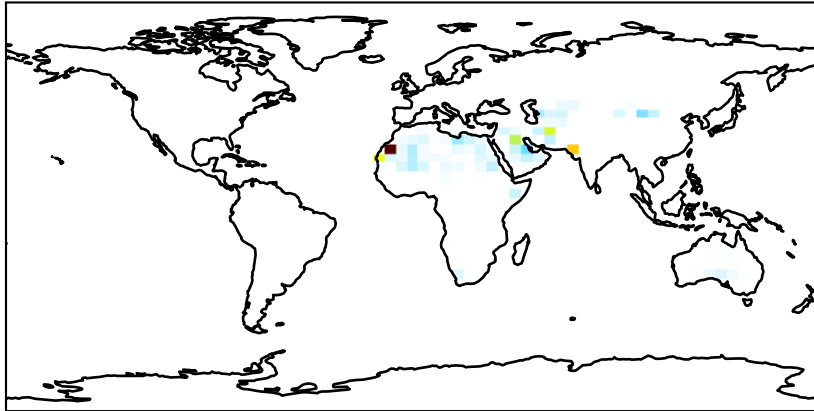


Ratio
Dev/Ref, Fixed Range

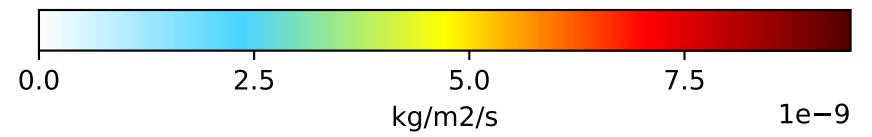
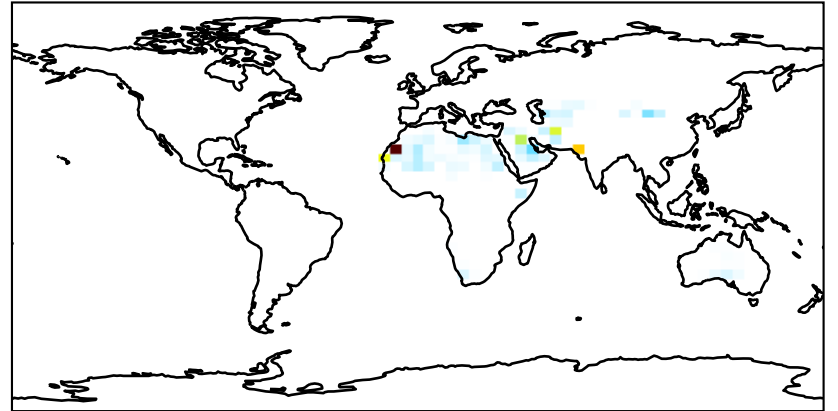


EmisDST4_Natural (Jul2019)

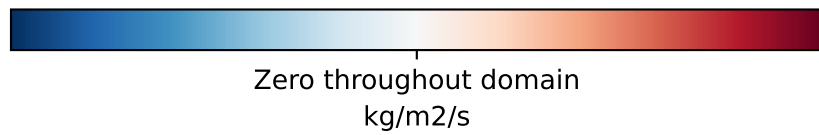
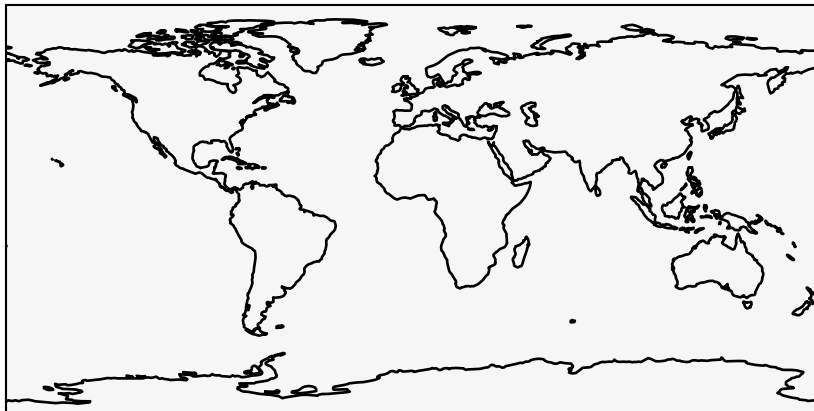
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



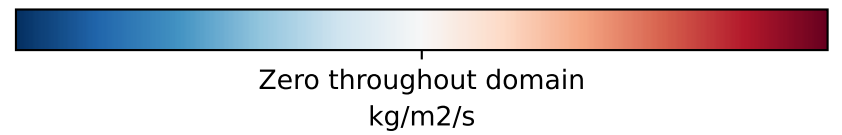
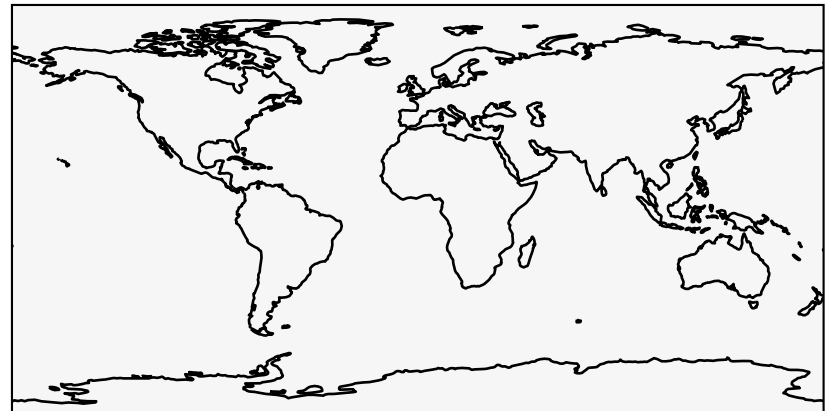
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



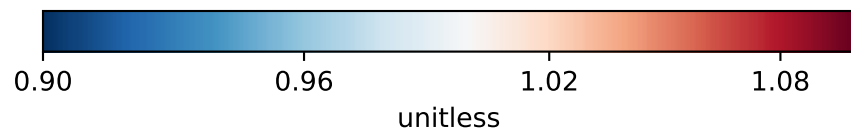
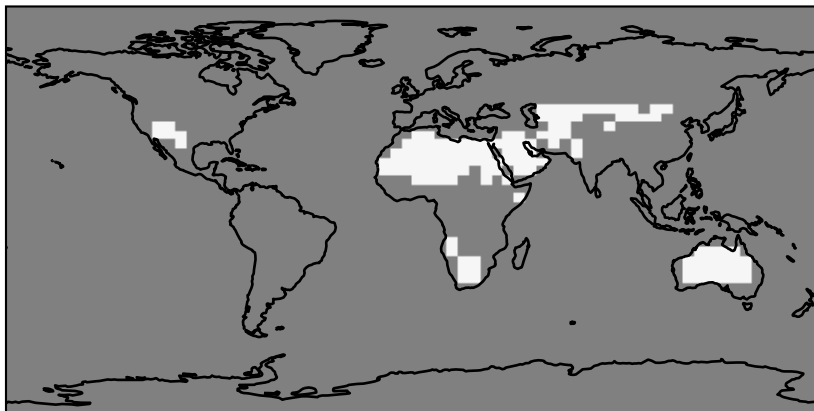
Difference
Dev - Ref, Dynamic Range



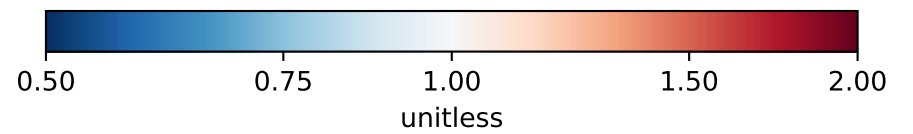
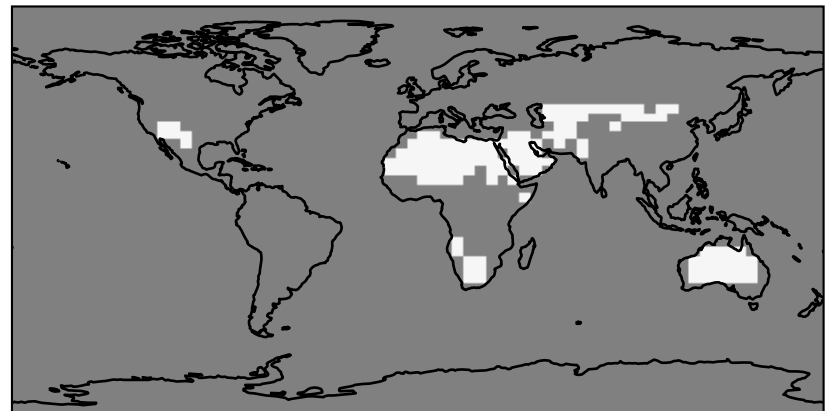
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

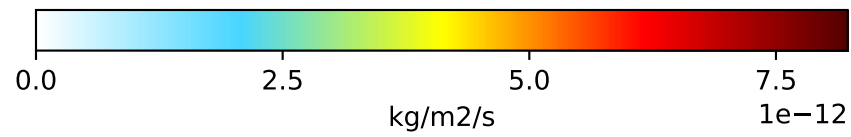
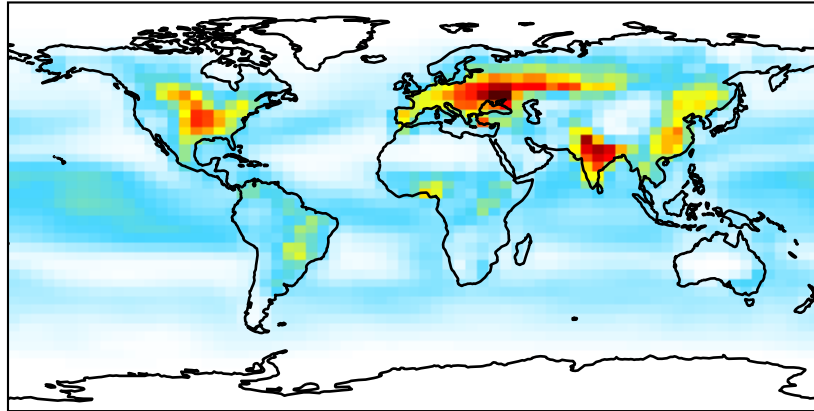


Ratio
Dev/Ref, Fixed Range

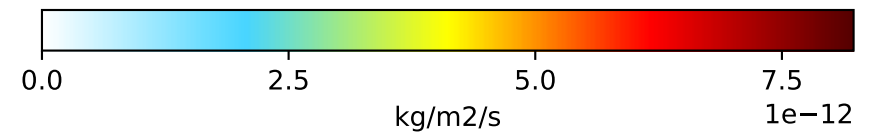
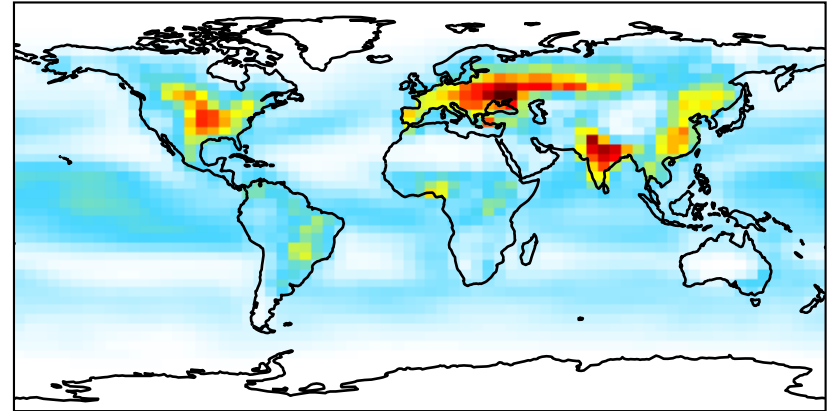


EmisNH3_Natural (Jul2019)

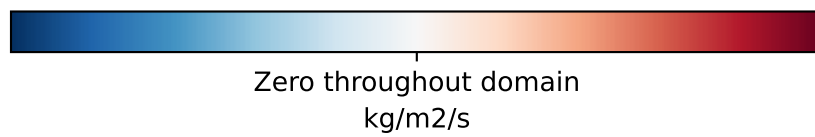
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



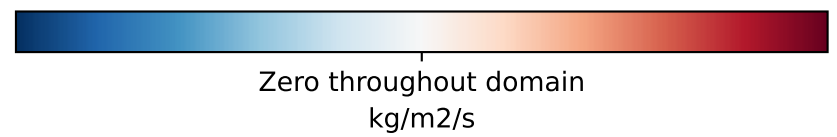
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



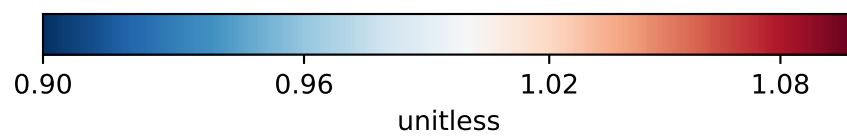
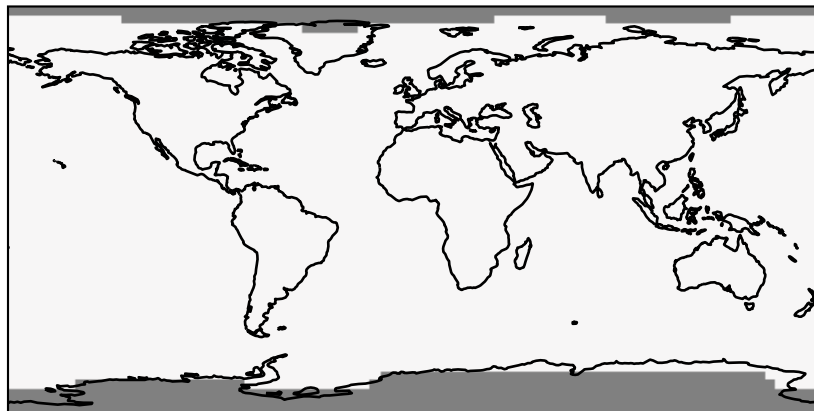
Difference
Dev - Ref, Dynamic Range



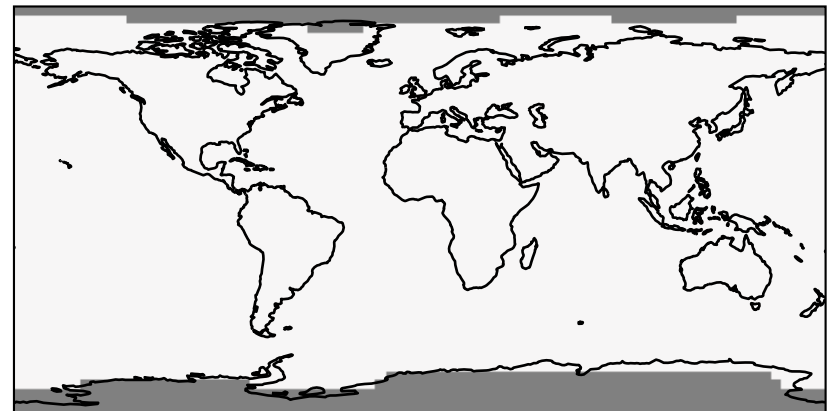
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

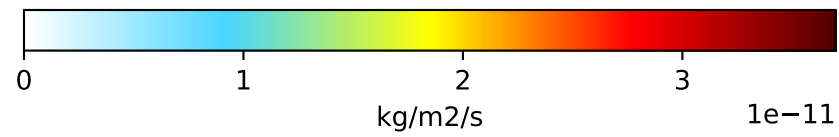
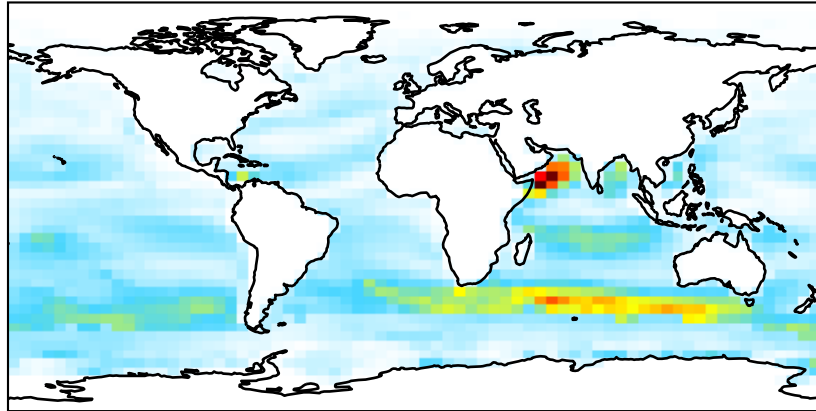


Ratio
Dev/Ref, Fixed Range

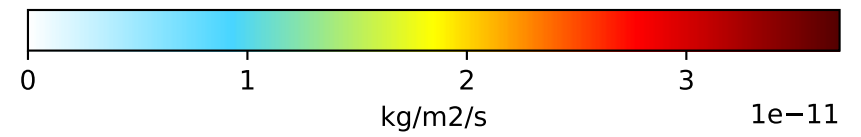
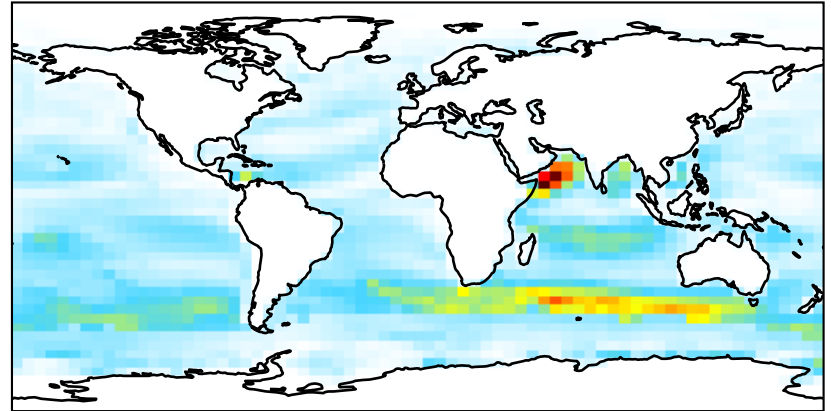


EmisSALA_Natural (Jul2019)

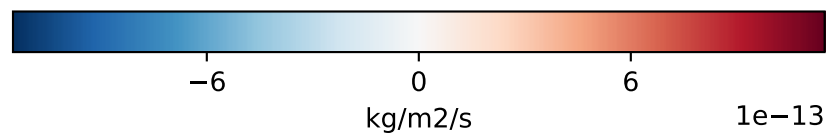
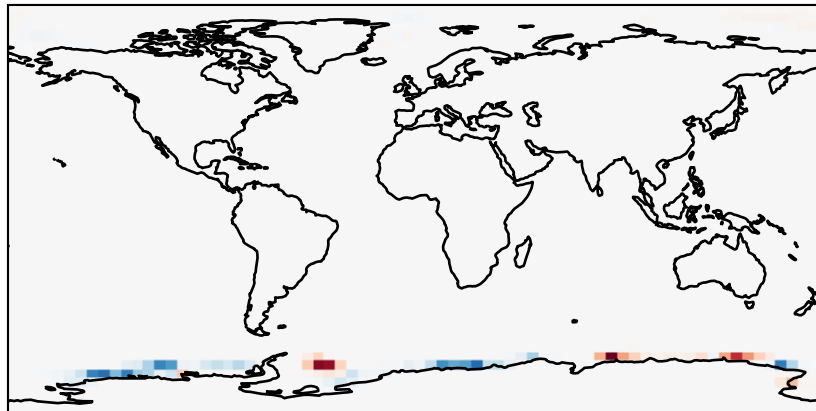
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



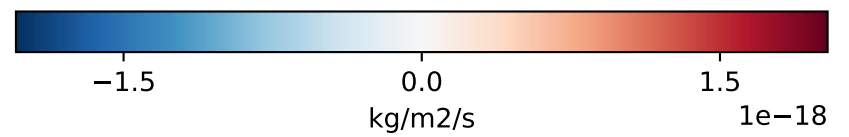
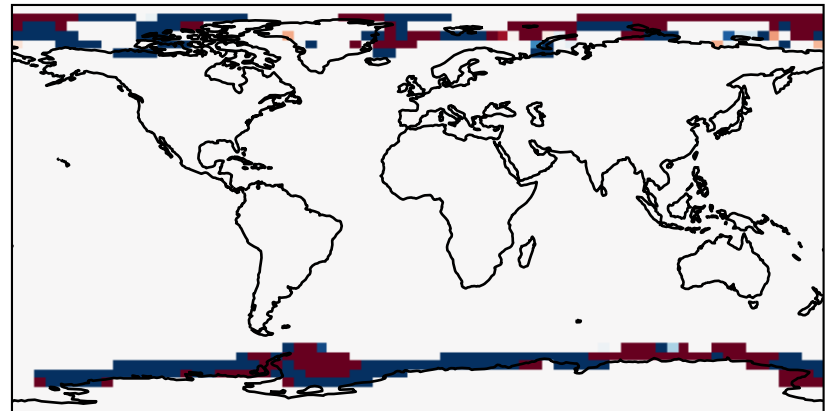
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



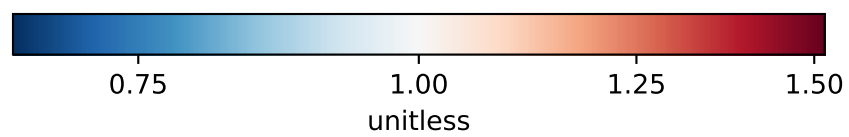
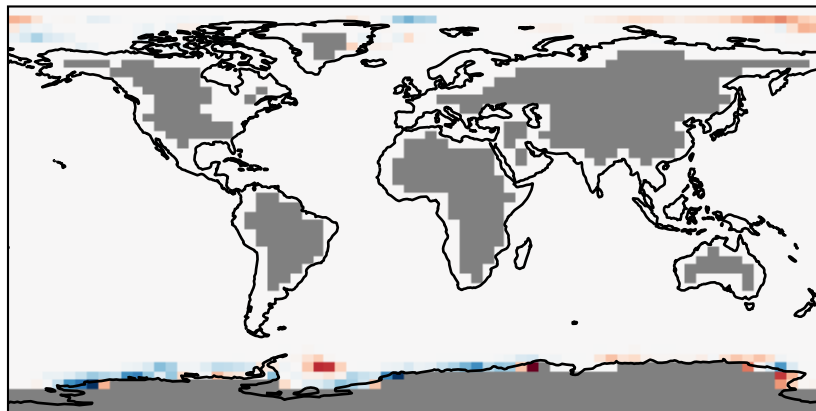
Difference
Dev - Ref, Dynamic Range



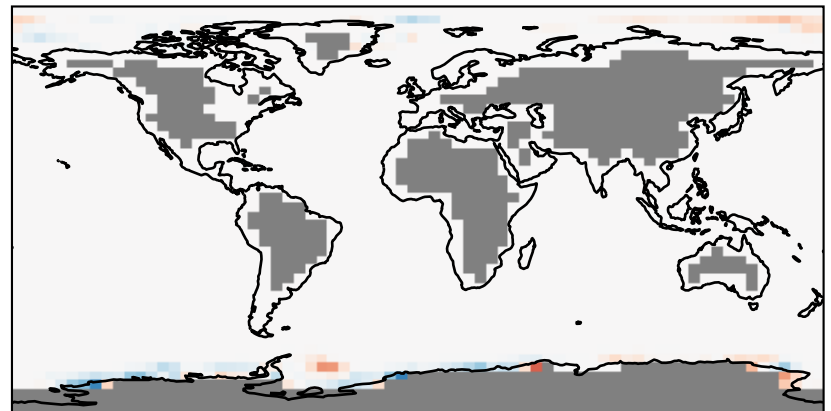
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

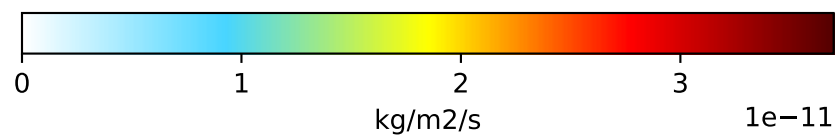
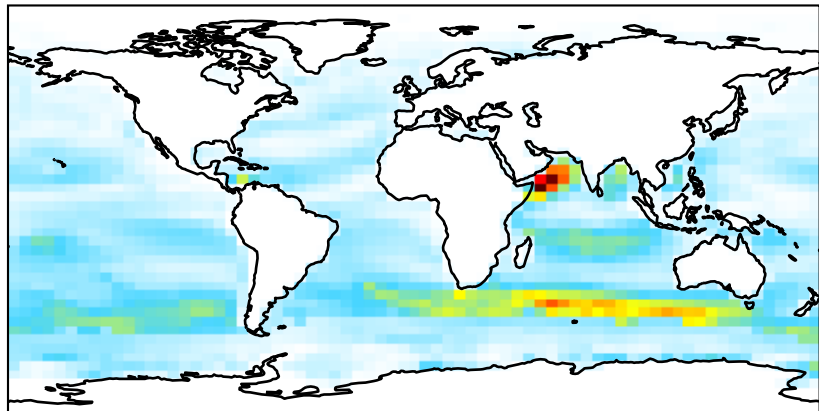


Ratio
Dev/Ref, Fixed Range

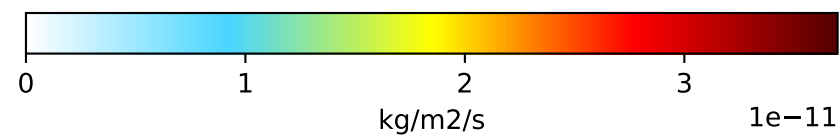
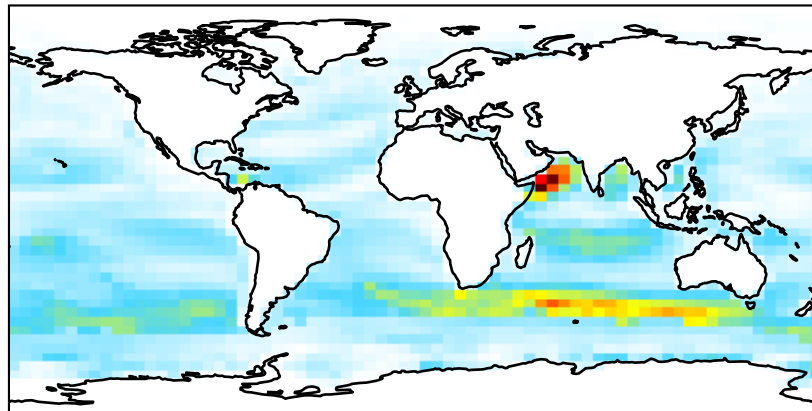


EmisSALAAL_Natural (Jul2019)

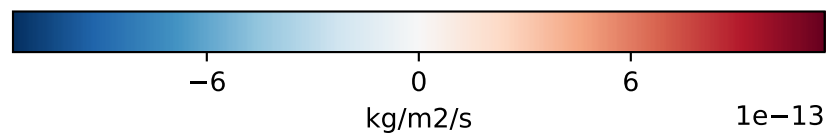
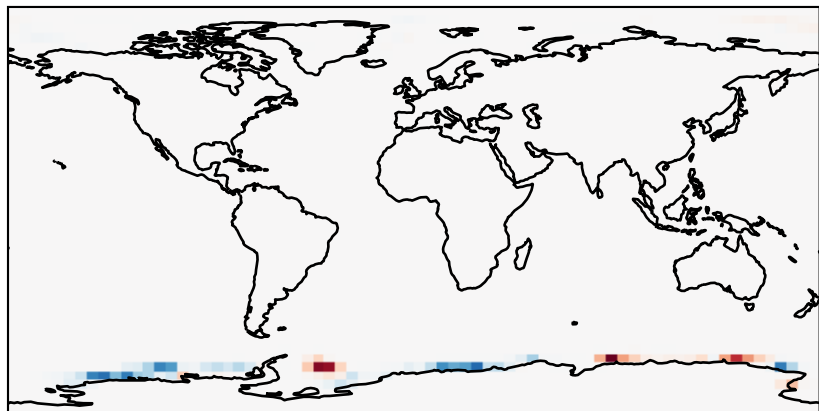
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



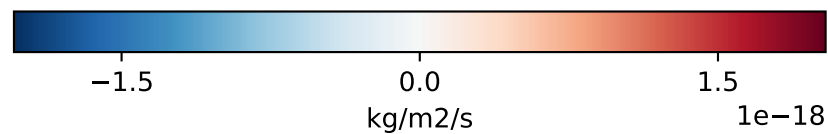
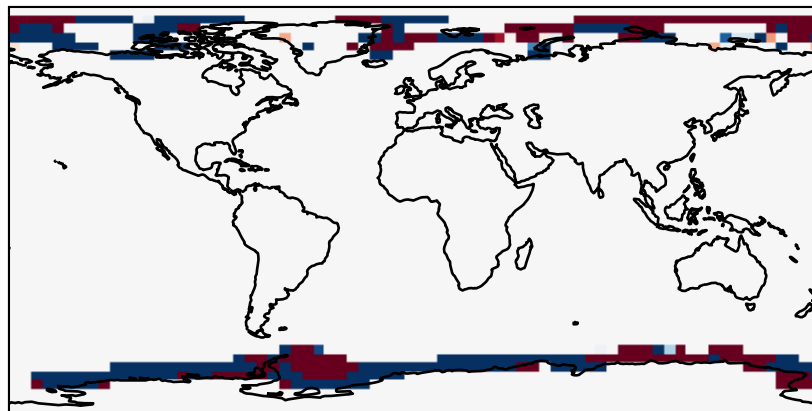
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



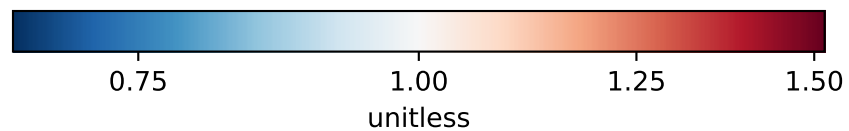
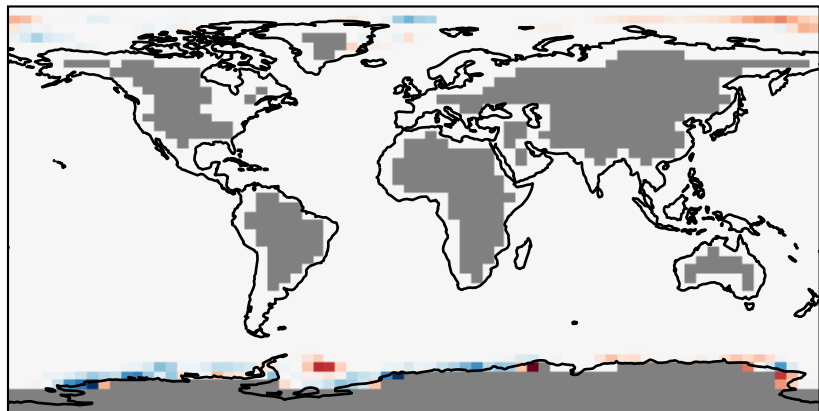
Difference
Dev - Ref, Dynamic Range



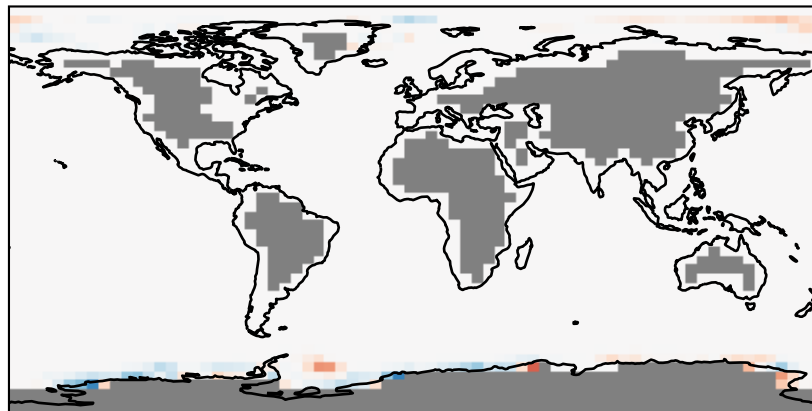
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

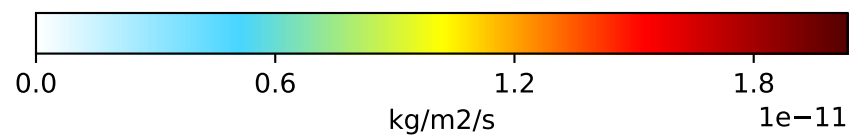
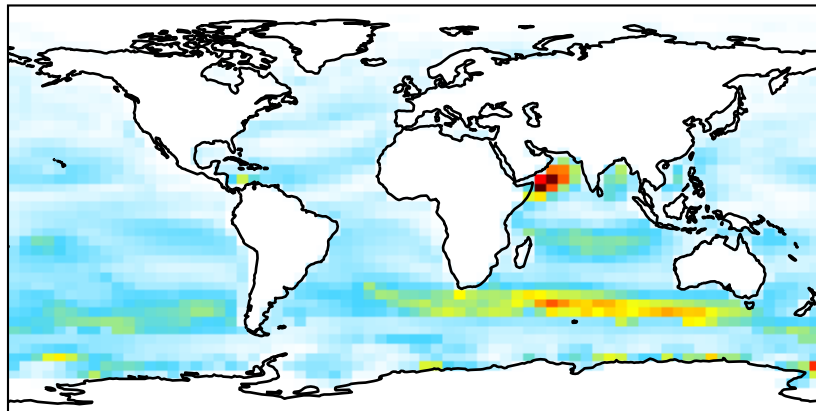


Ratio
Dev/Ref, Fixed Range

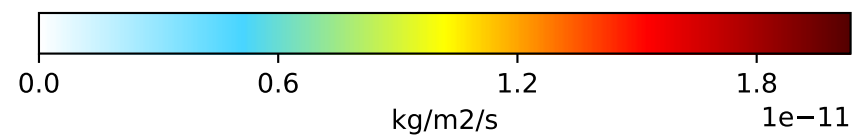
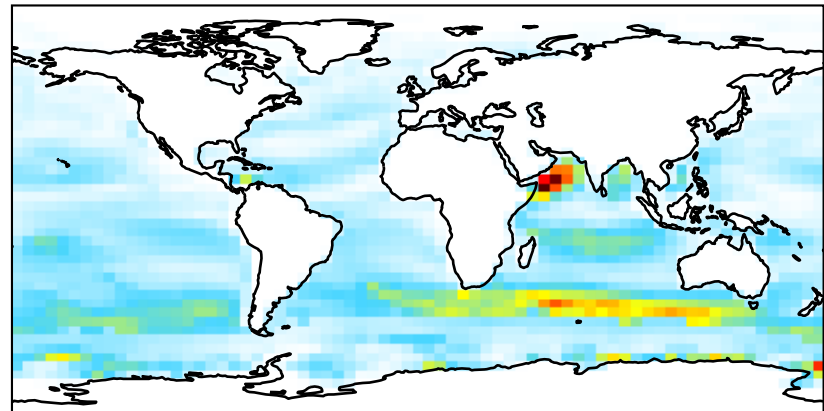


EmisSALACL_Natural (Jul2019)

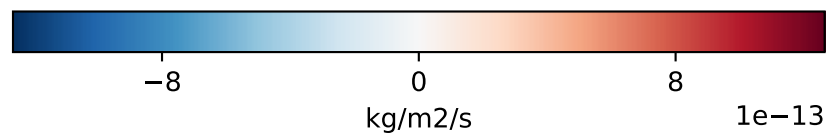
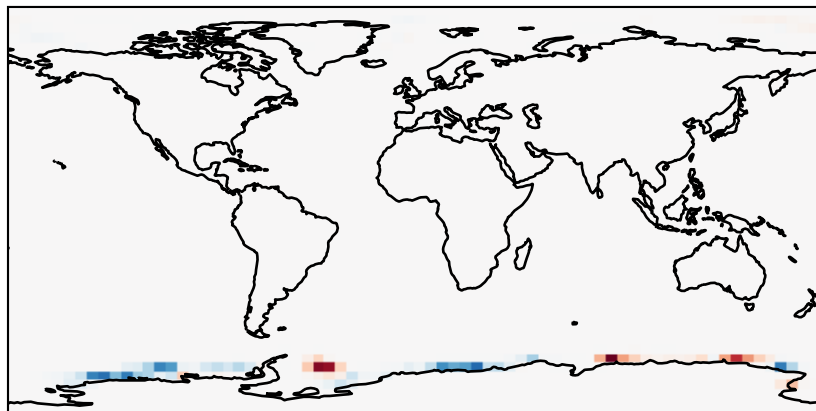
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



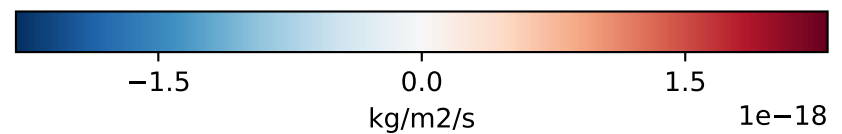
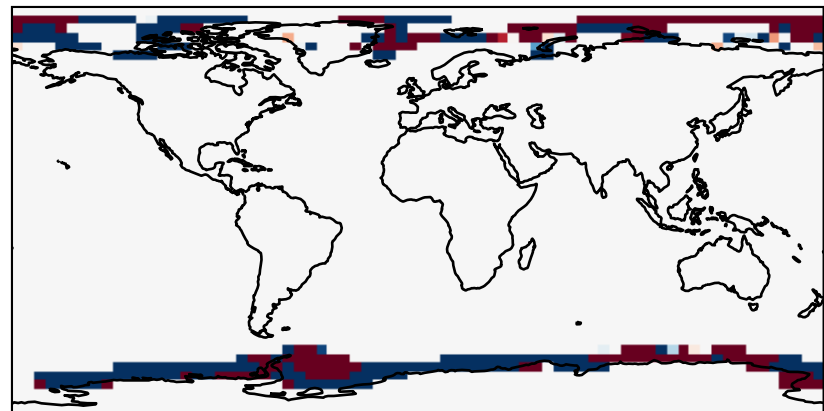
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



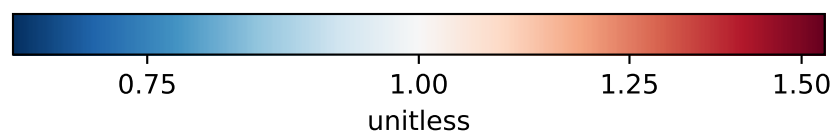
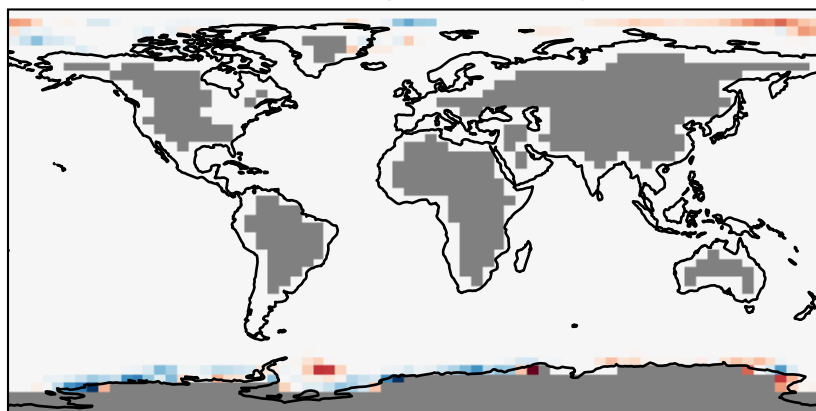
Difference
Dev - Ref, Dynamic Range



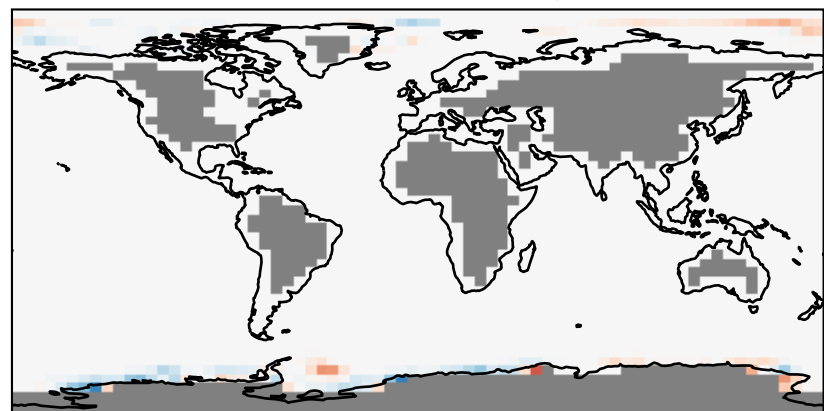
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

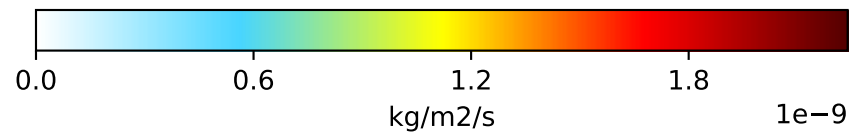
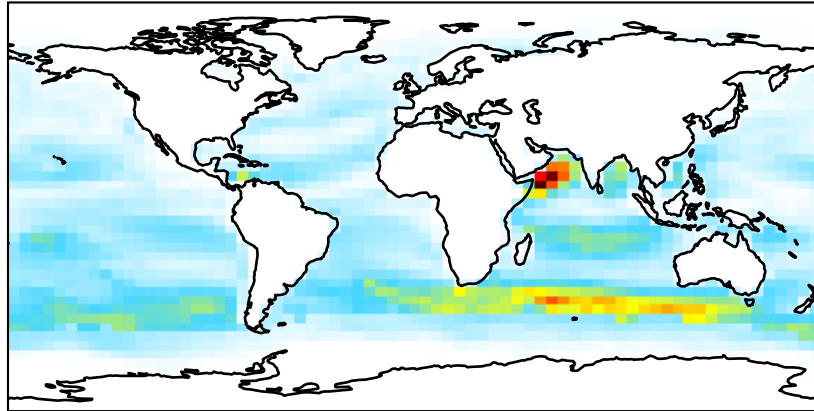


Ratio
Dev/Ref, Fixed Range

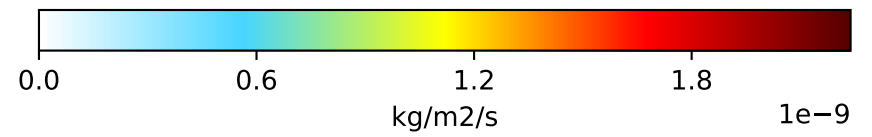
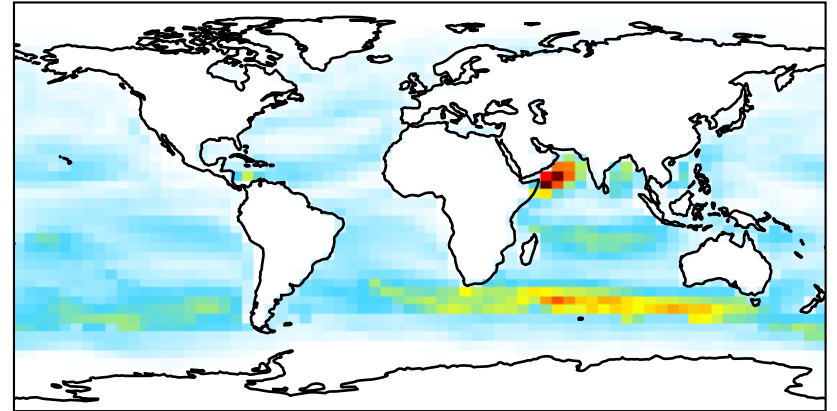


EmisSALC_Natural (Jul2019)

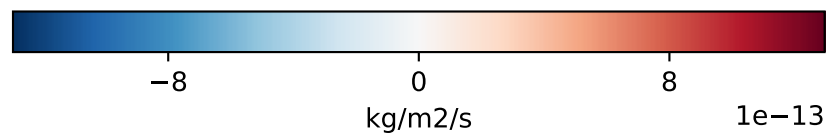
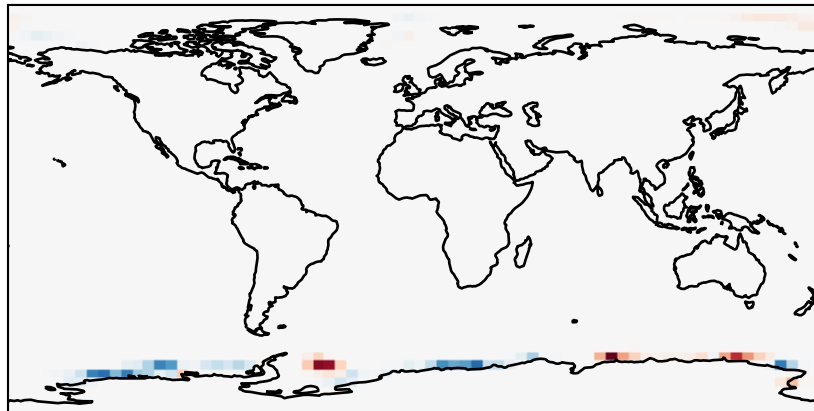
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



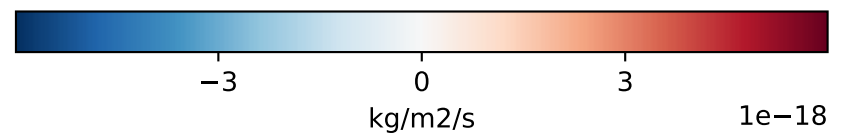
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



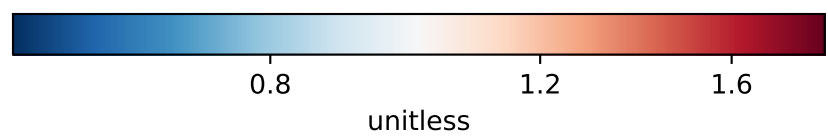
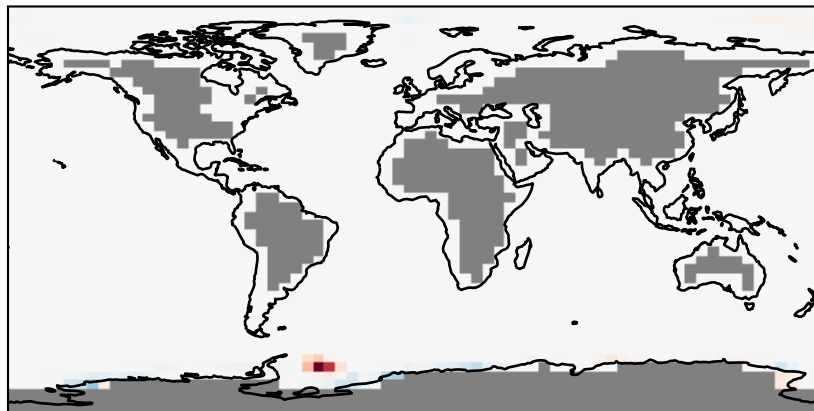
Difference
Dev - Ref, Dynamic Range



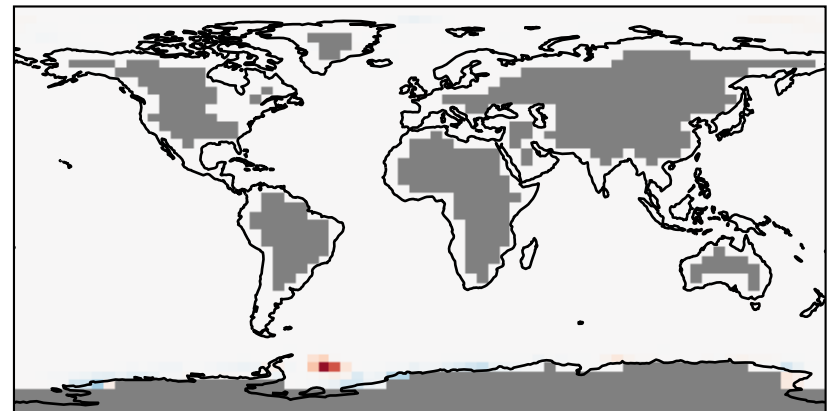
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

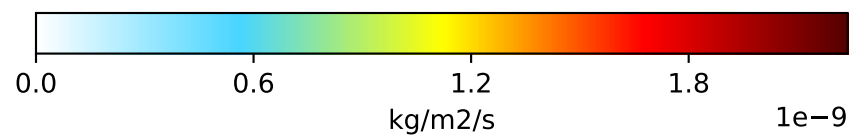
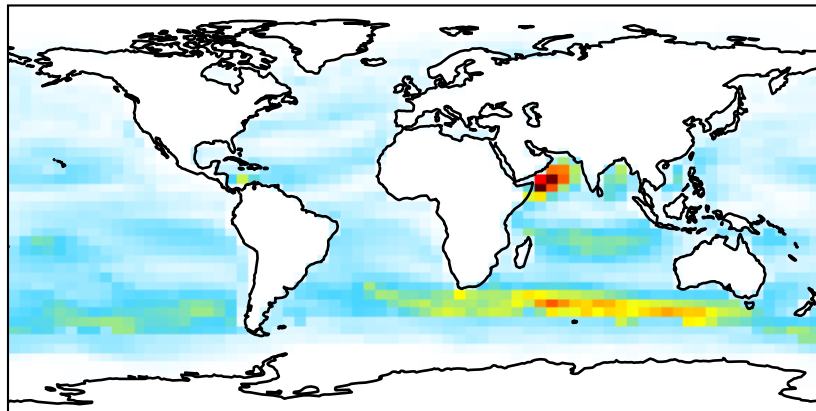


Ratio
Dev/Ref, Fixed Range

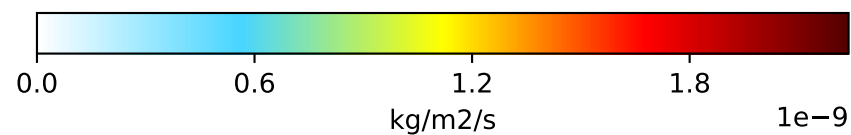
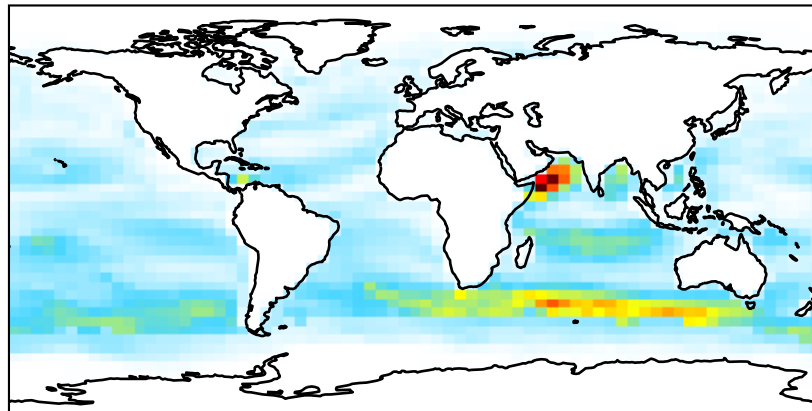


EmisSALCAL_Natural (Jul2019)

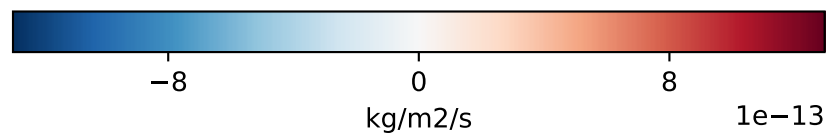
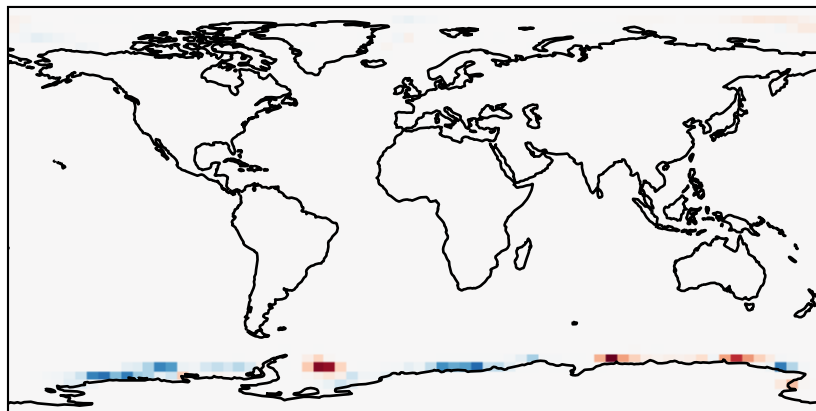
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



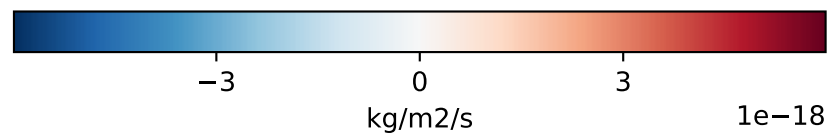
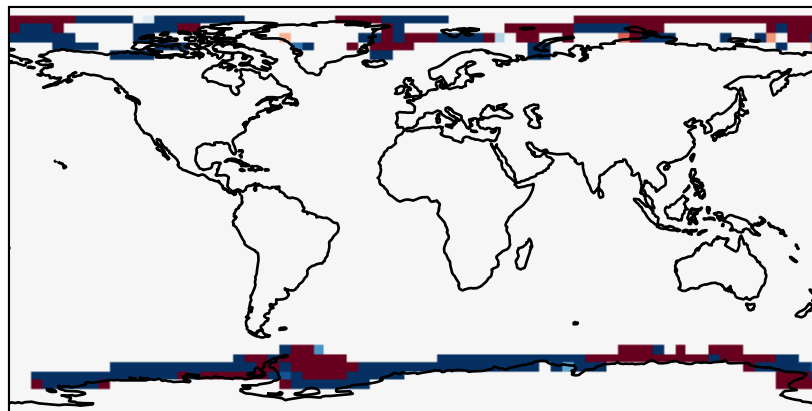
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



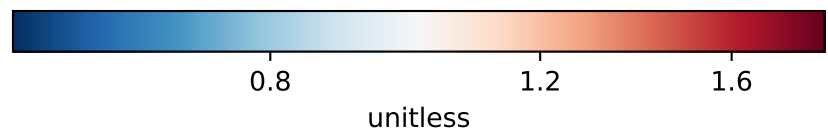
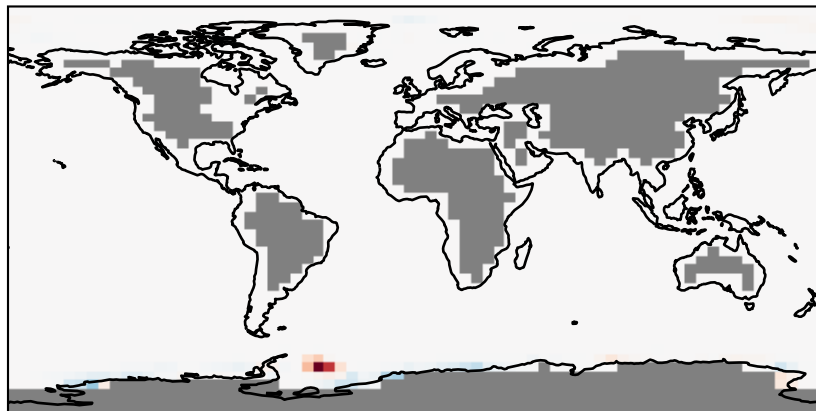
Difference
Dev - Ref, Dynamic Range



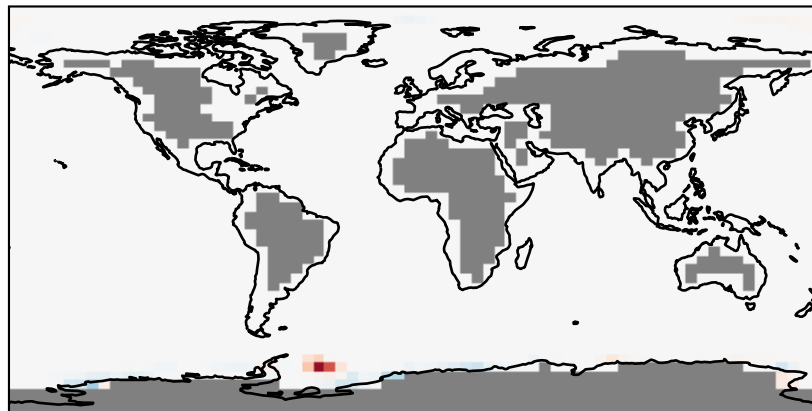
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

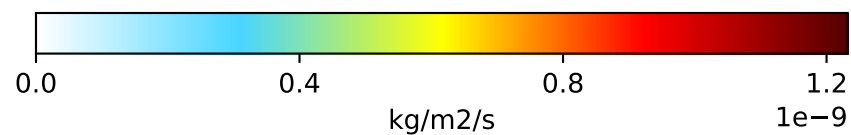
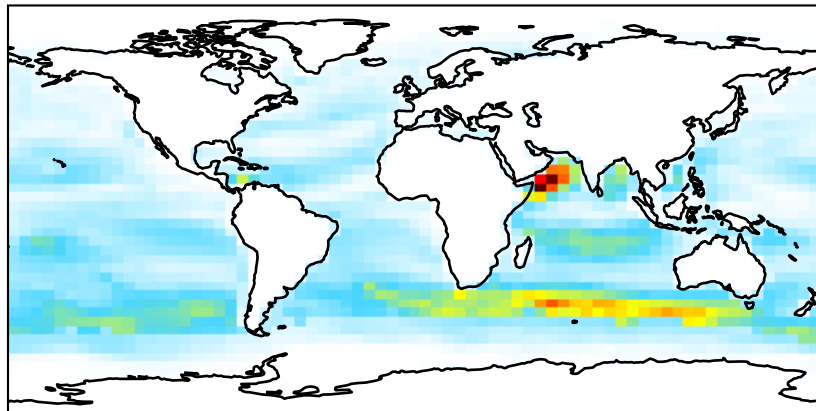


Ratio
Dev/Ref, Fixed Range

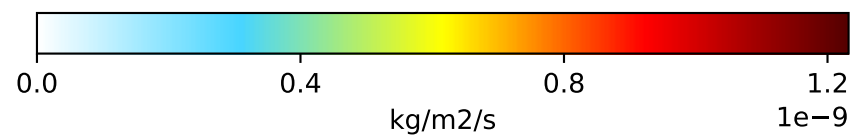
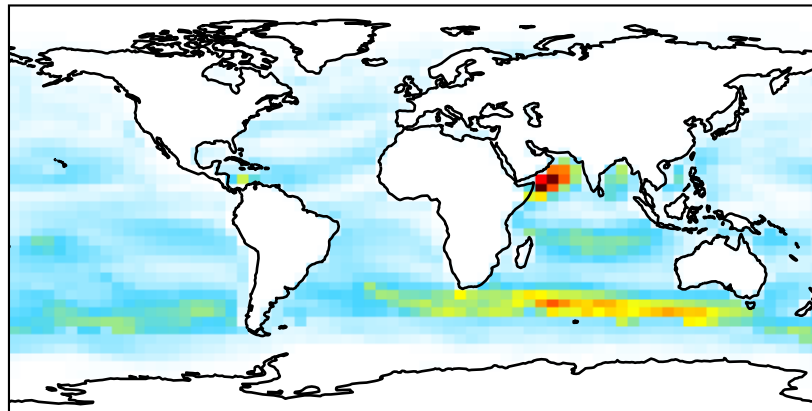


EmisSALCCL_Natural (Jul2019)

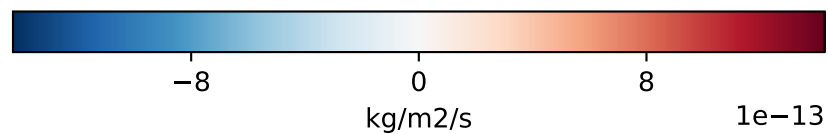
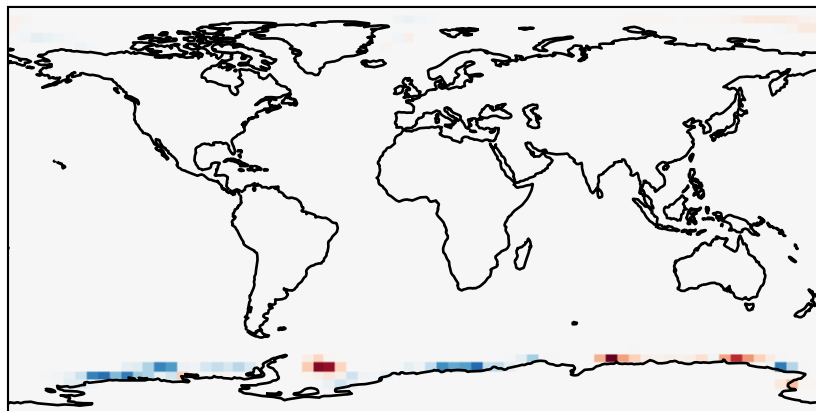
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



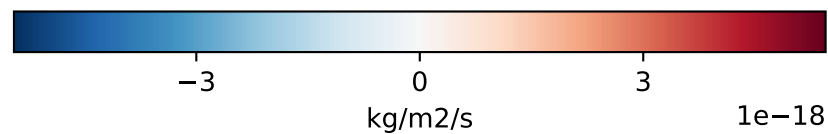
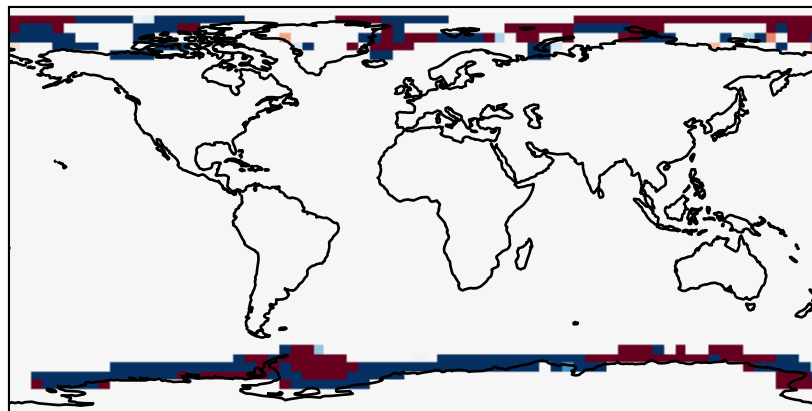
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



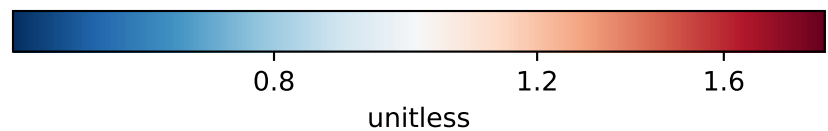
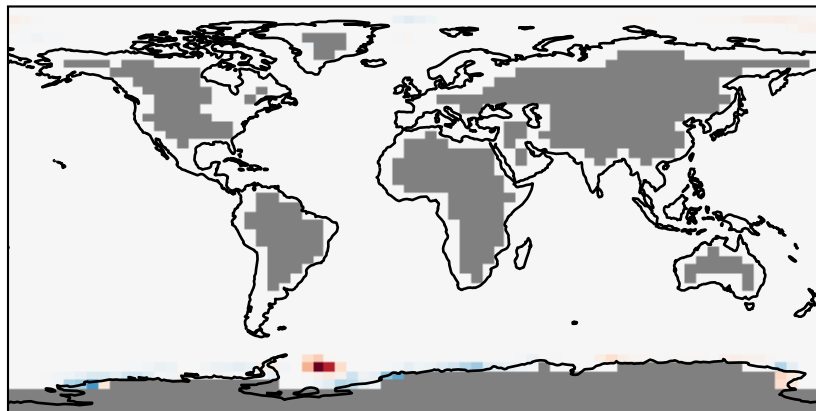
Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range



Ratio
Dev/Ref, Fixed Range

