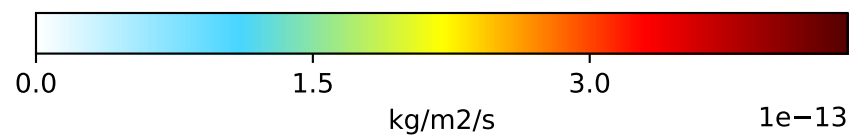
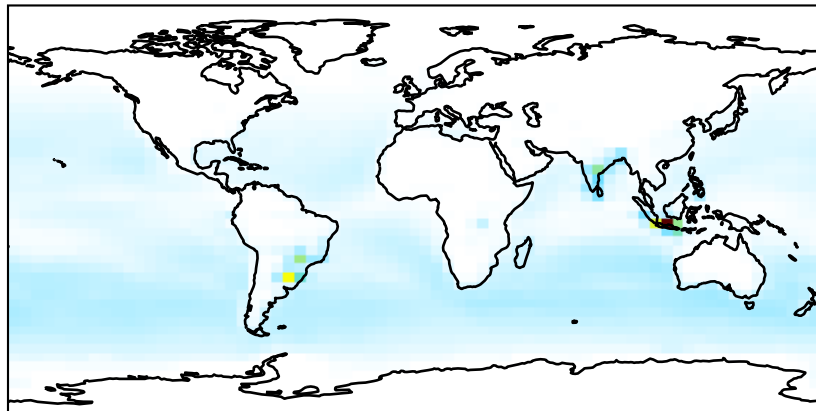
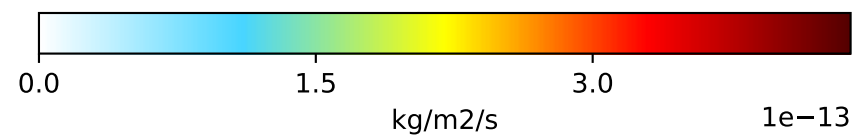
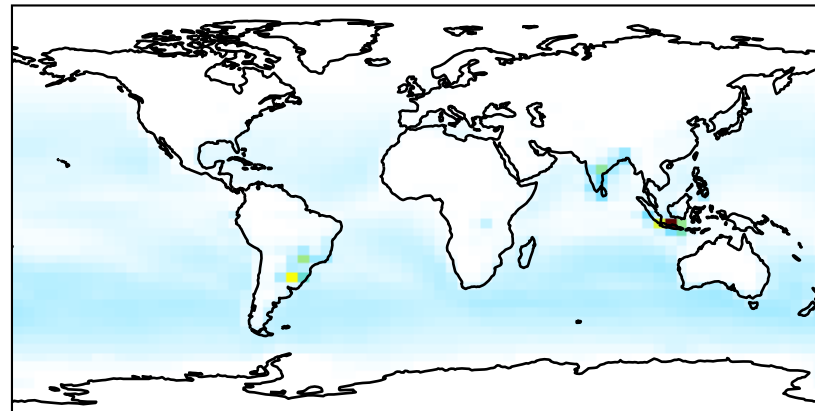


EmisCH3I_Ocean (Jan2019)

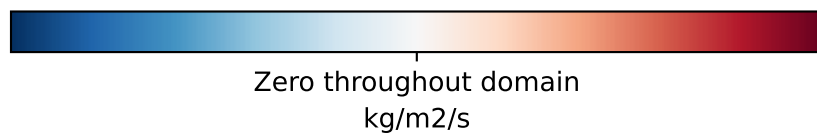
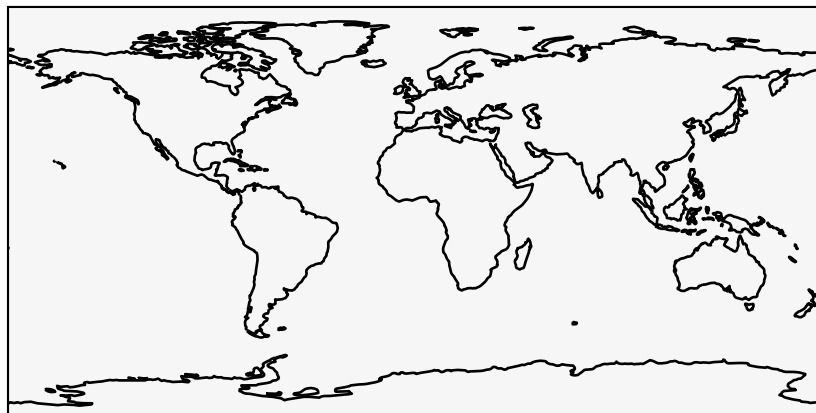
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



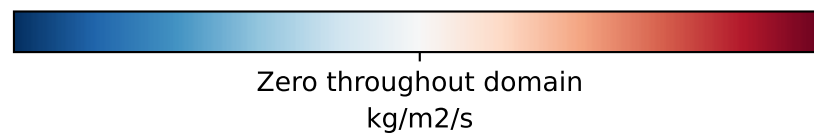
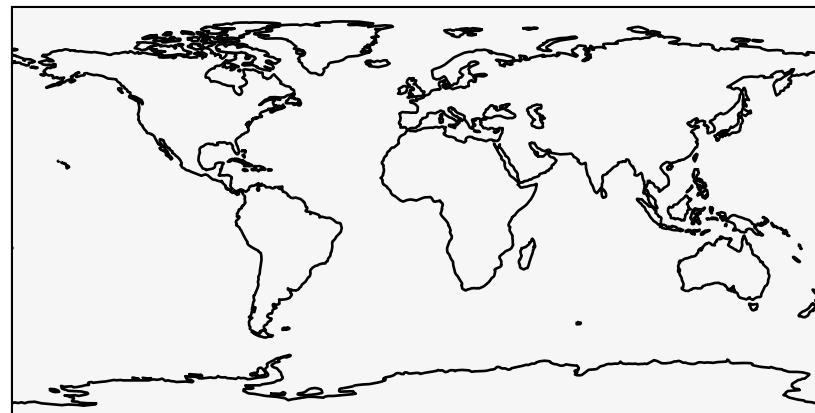
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



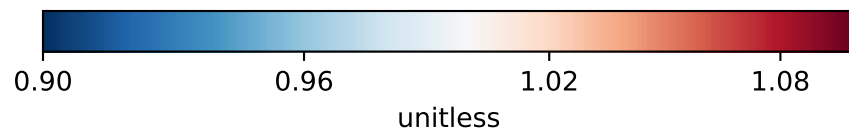
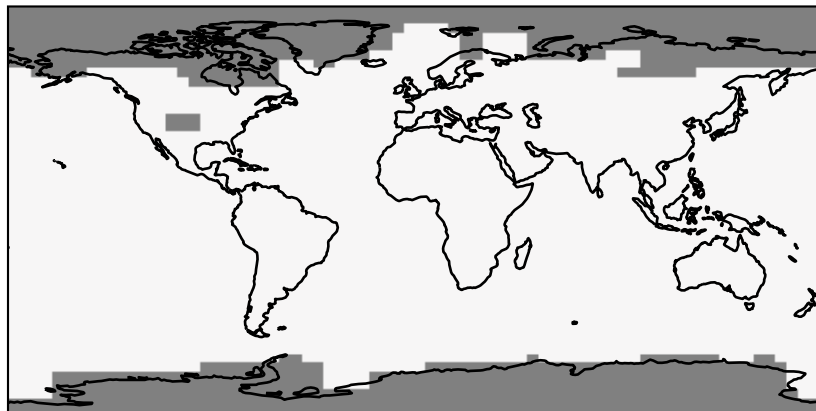
Difference
Dev - Ref, Dynamic Range



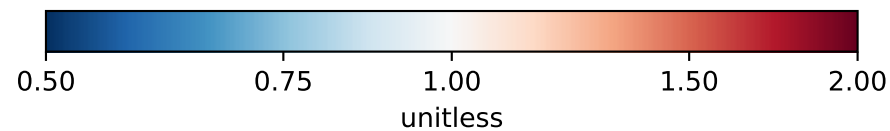
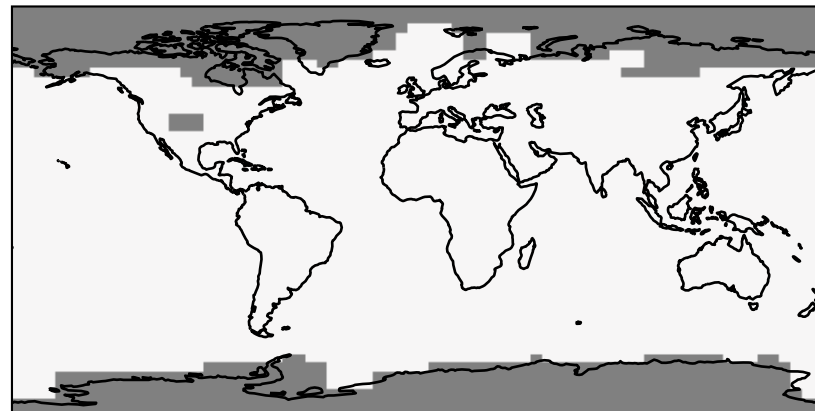
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

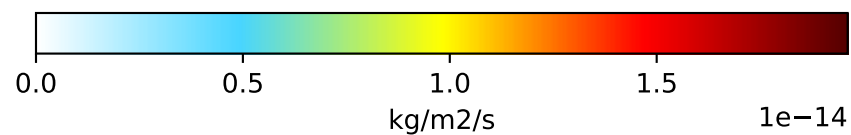
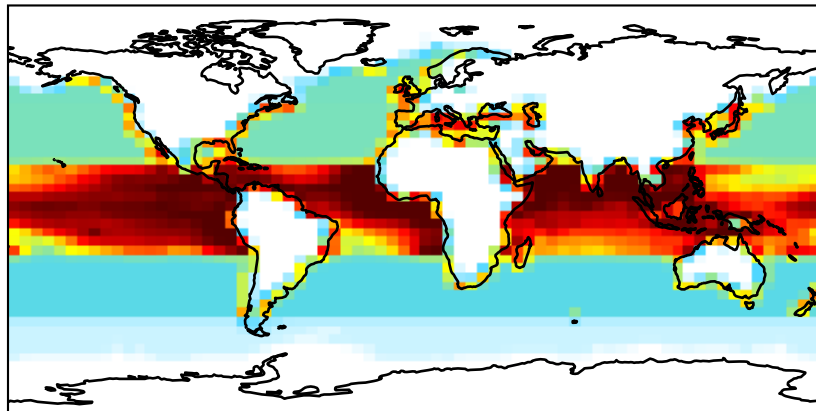


Ratio
Dev/Ref, Fixed Range

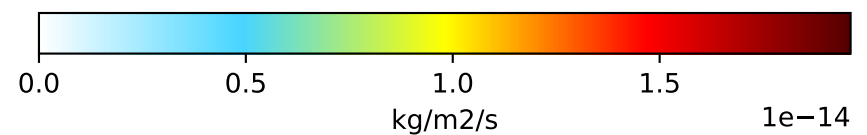
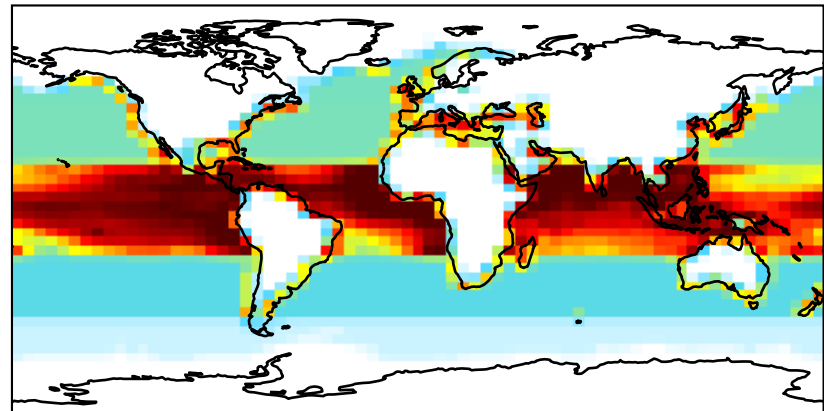


EmisCH2I2_Ocean (Jan2019)

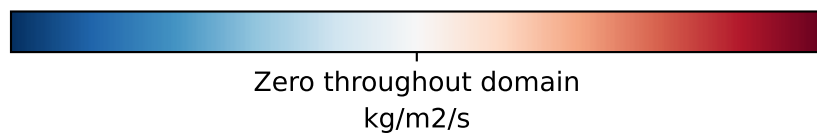
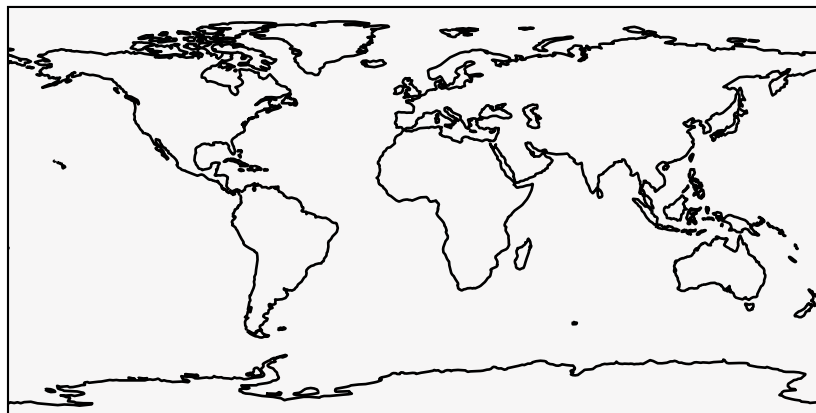
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



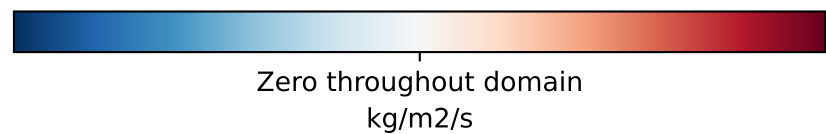
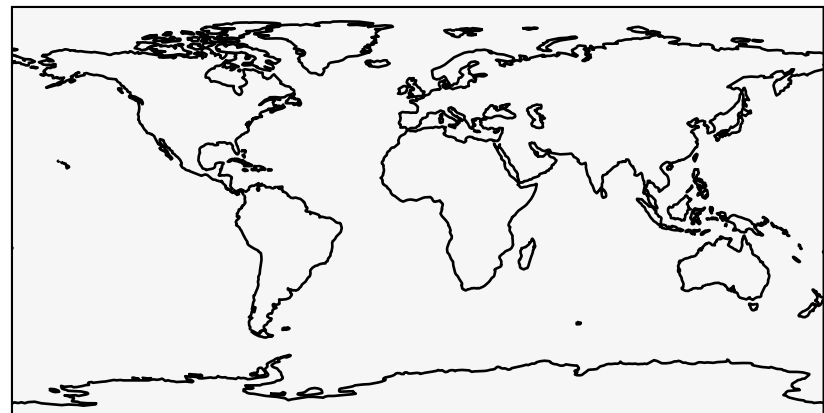
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



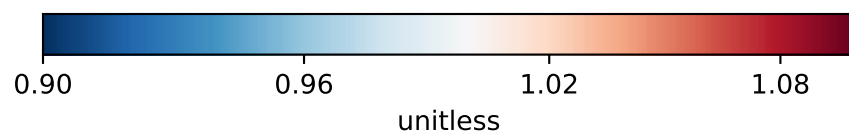
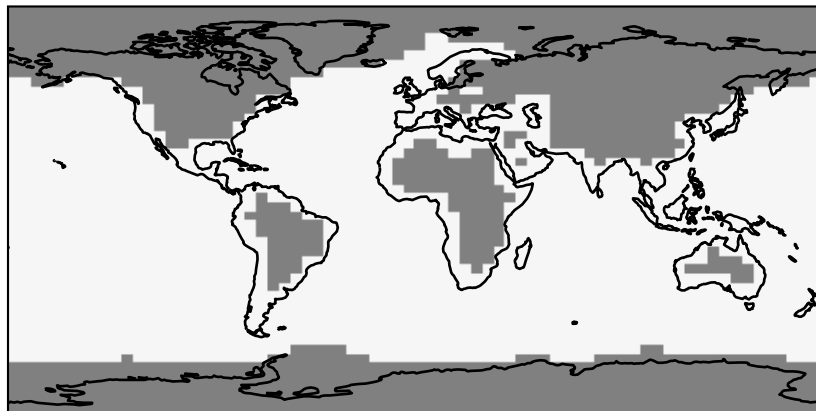
Difference
Dev - Ref, Dynamic Range



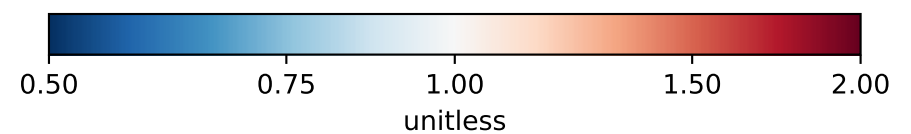
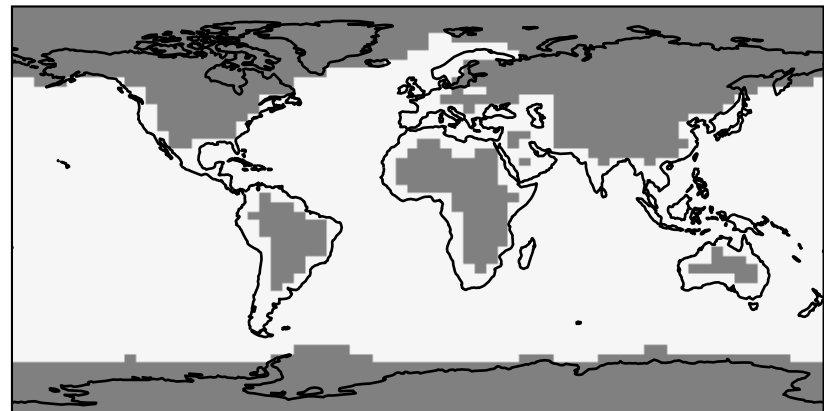
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

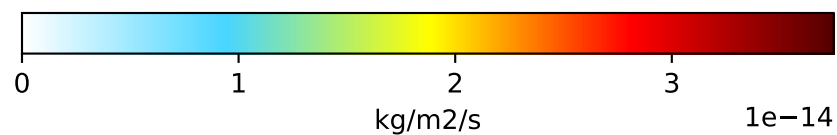
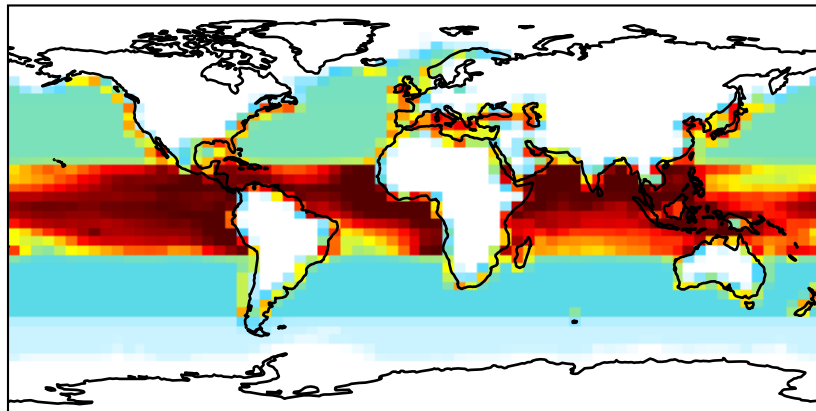


Ratio
Dev/Ref, Fixed Range

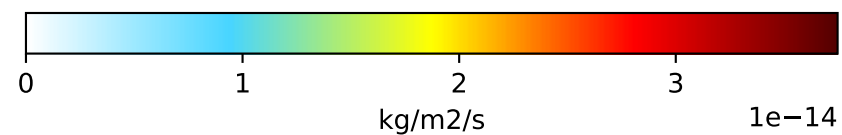
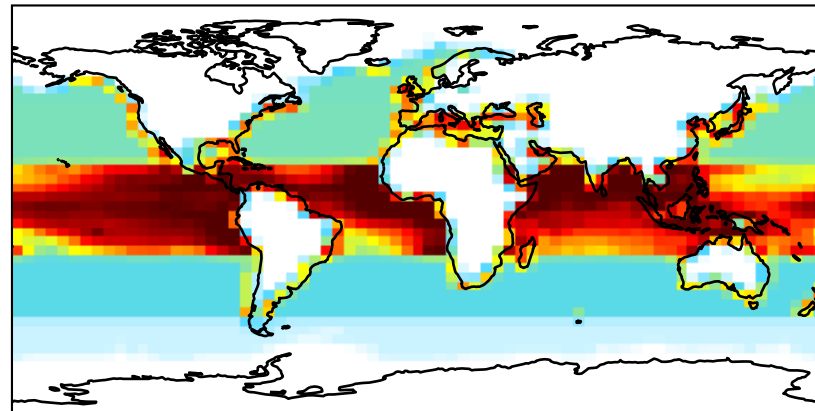


EmisCH2ICl_Ocean (Jan2019)

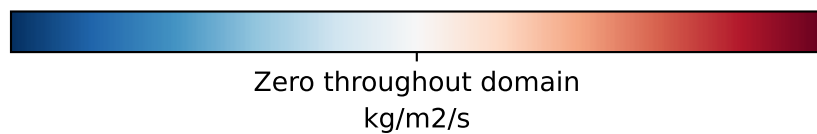
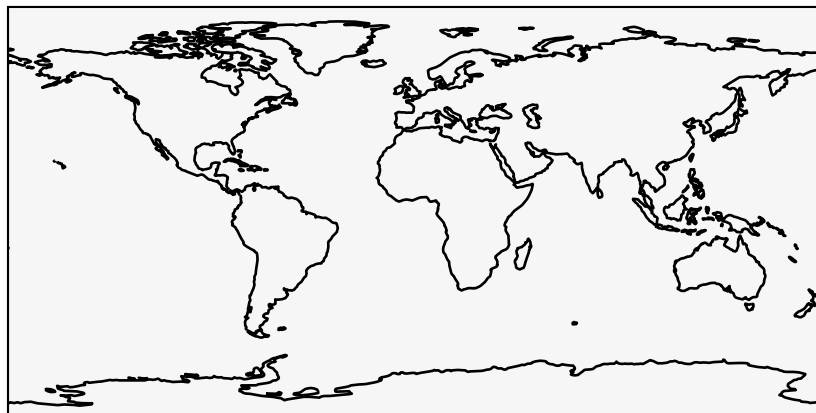
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



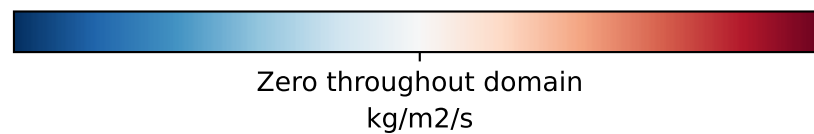
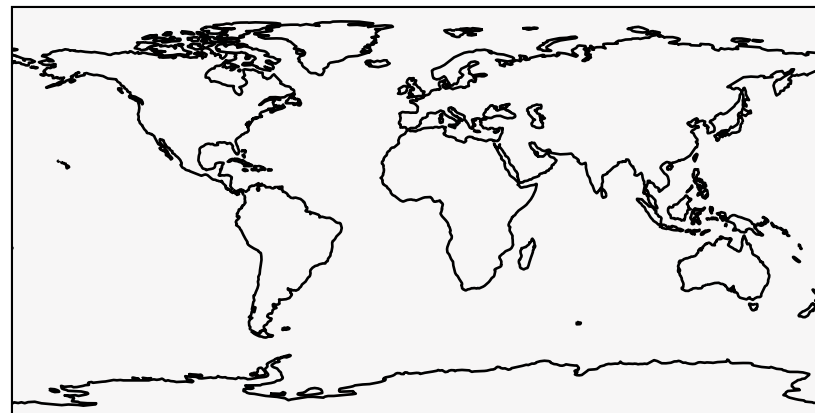
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



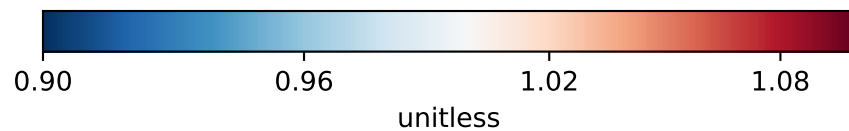
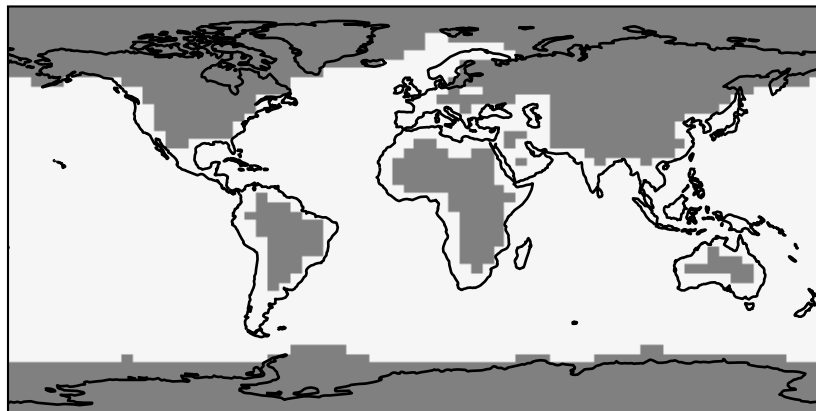
Difference
Dev - Ref, Dynamic Range



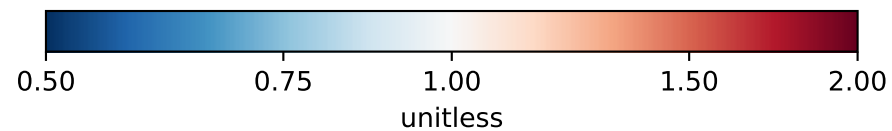
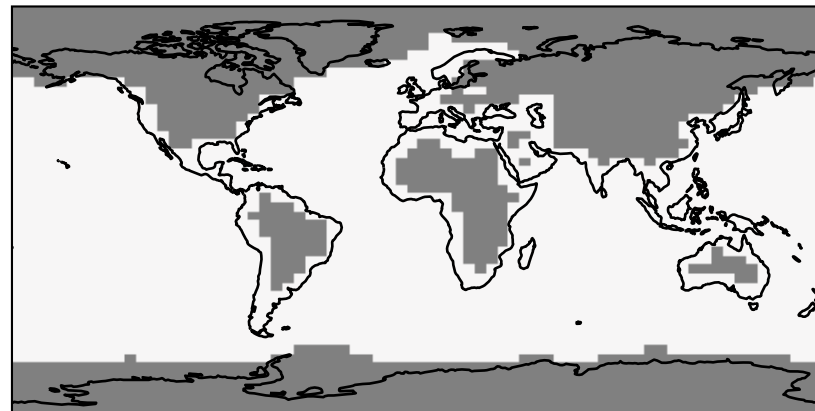
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

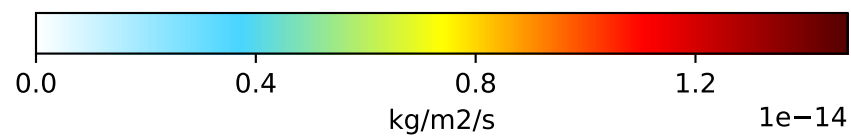
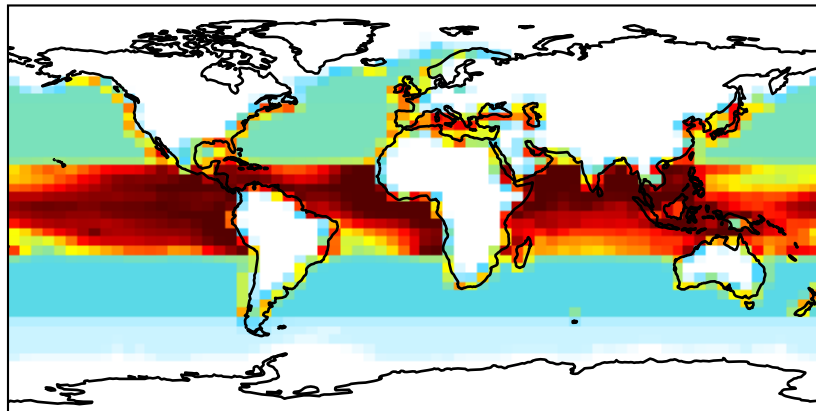


Ratio
Dev/Ref, Fixed Range

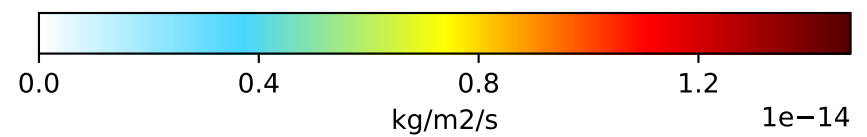
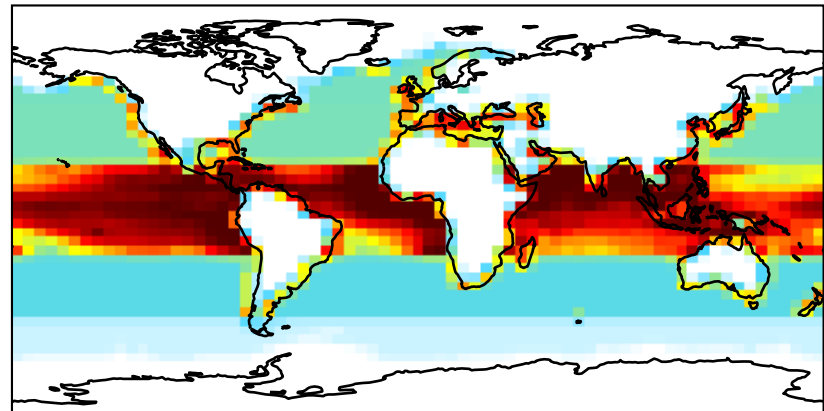


EmisCH2IBr_Ocean (Jan2019)

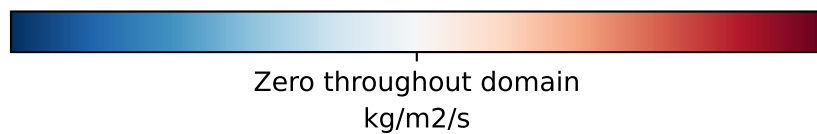
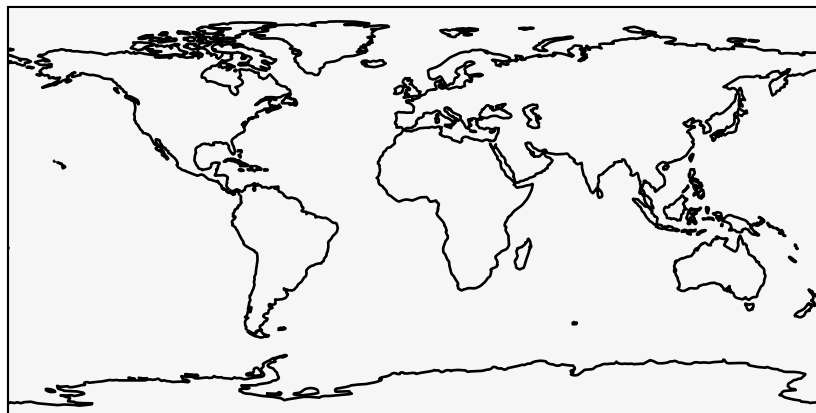
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



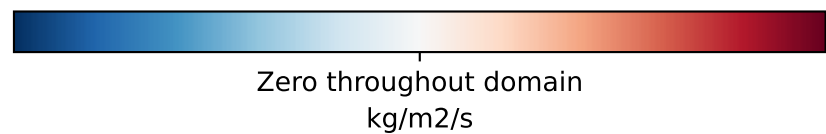
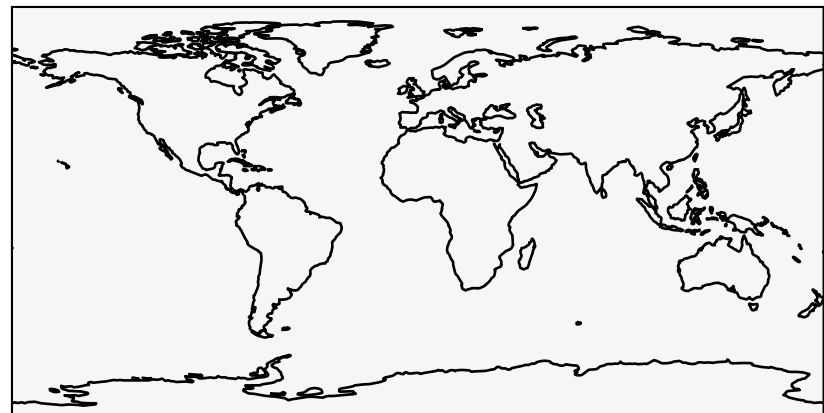
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



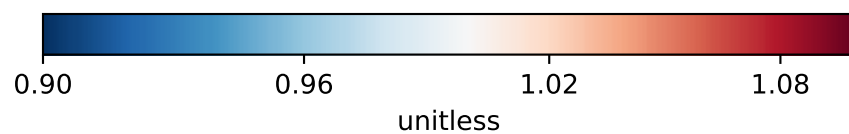
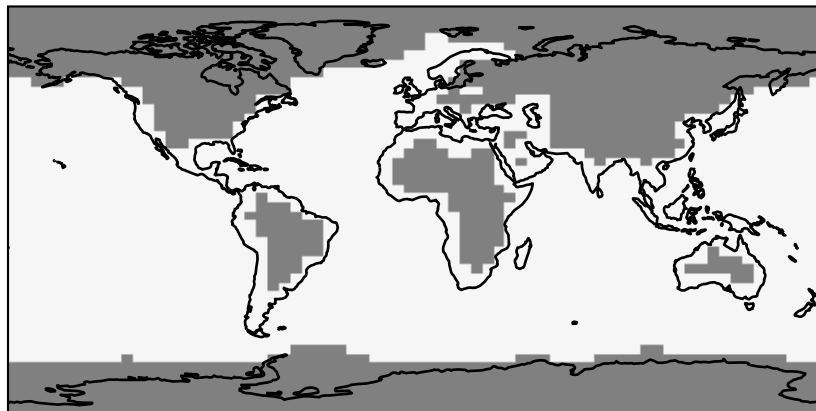
Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range



Ratio
Dev/Ref, Fixed Range

