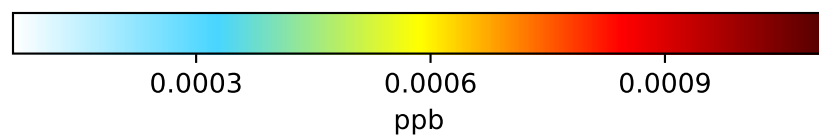
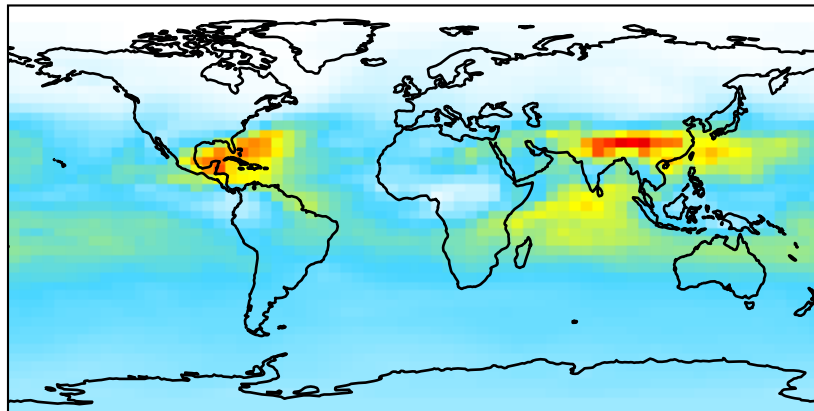
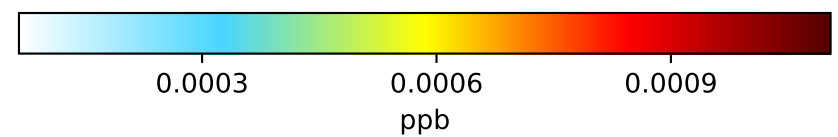
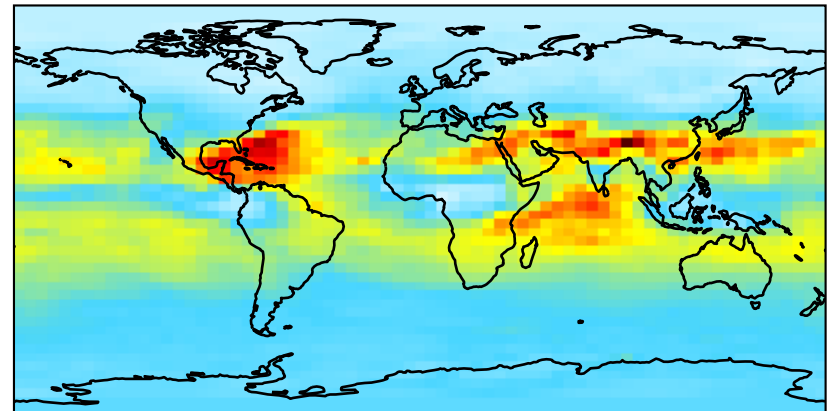


# SpeciesConc\_ly (Jul2019)

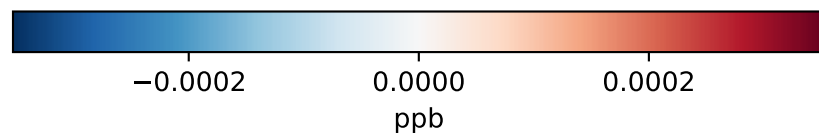
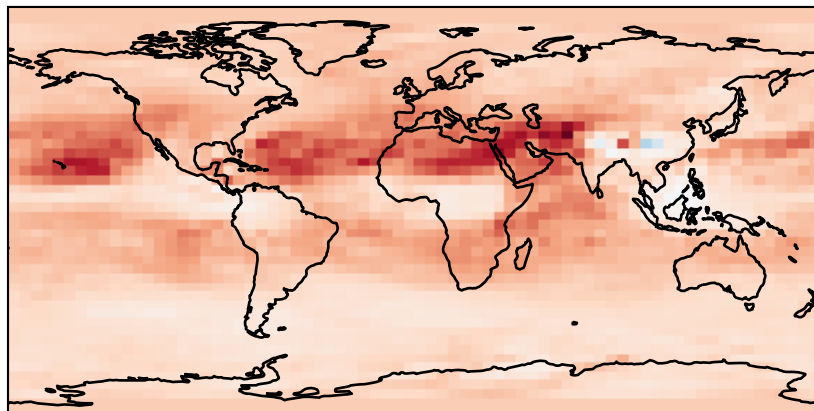
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



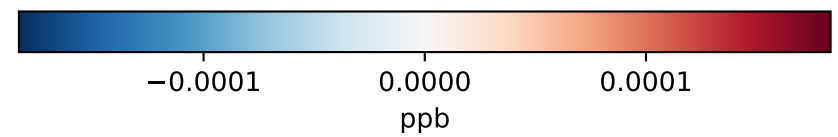
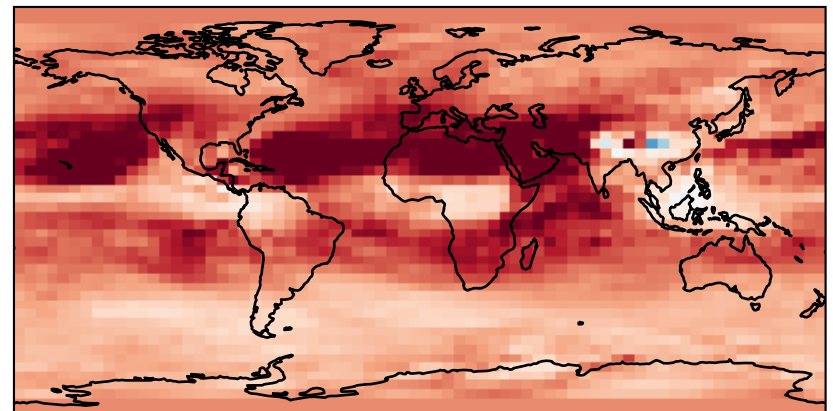
GCC\_13.4.0-rc.3 (Dev)  
4.0x5.0



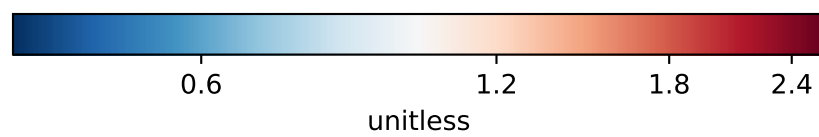
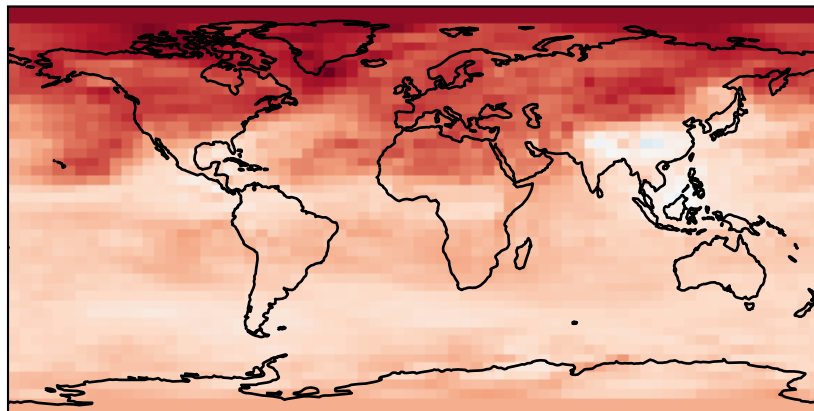
Difference  
Dev - Ref, Dynamic Range



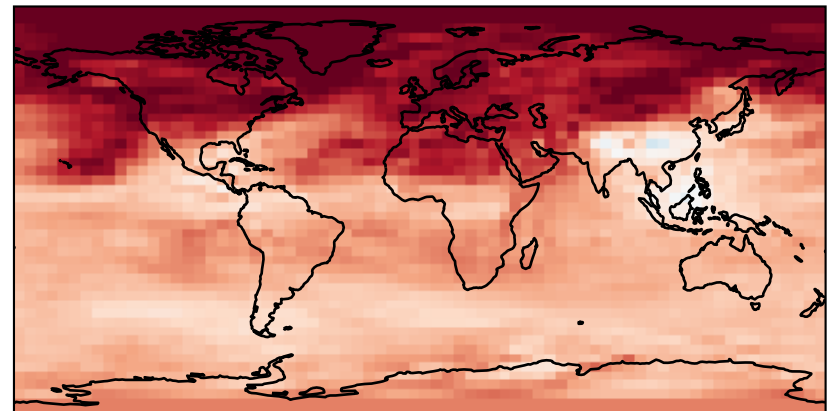
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

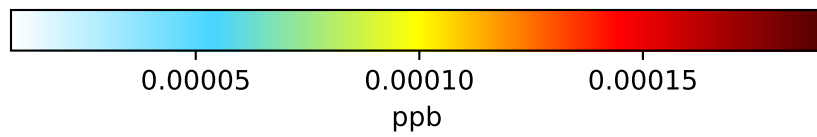
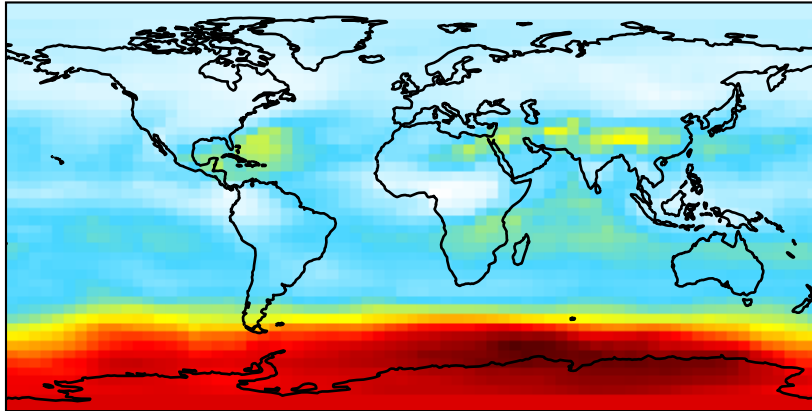


Ratio  
Dev/Ref, Fixed Range

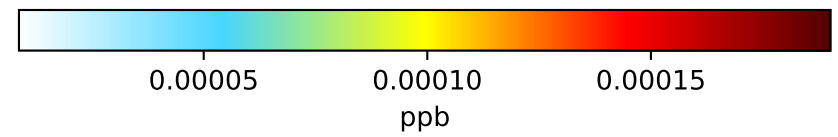
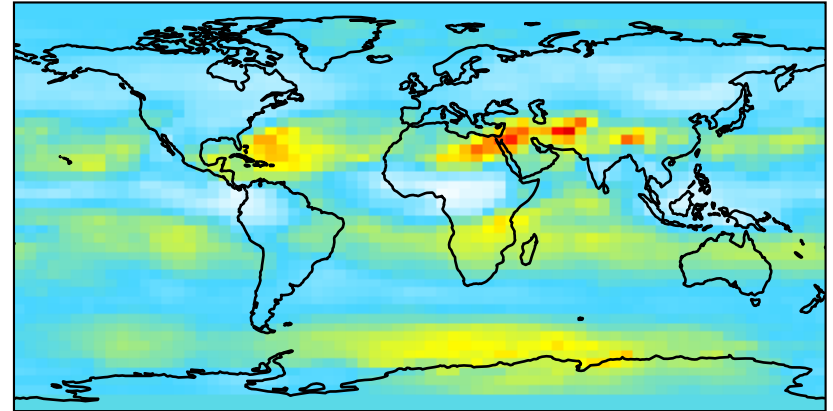


# SpeciesConc\_IxOy (Jul2019)

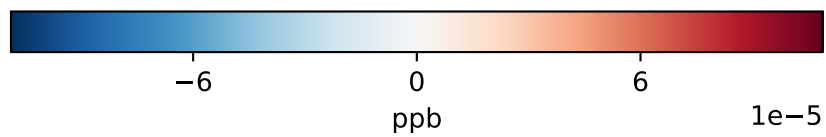
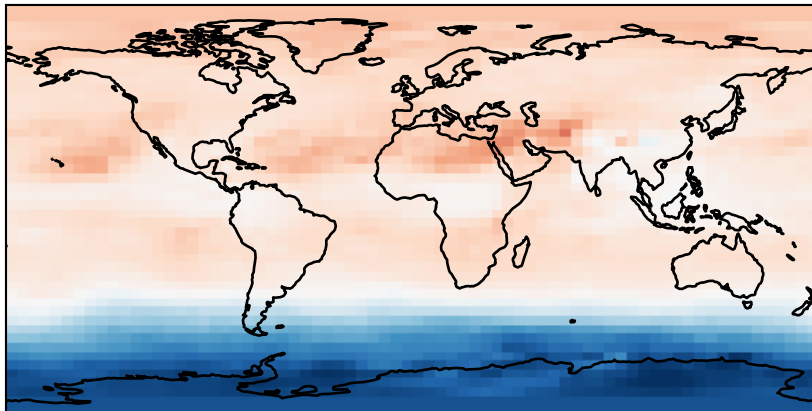
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



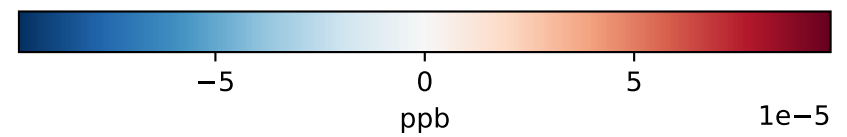
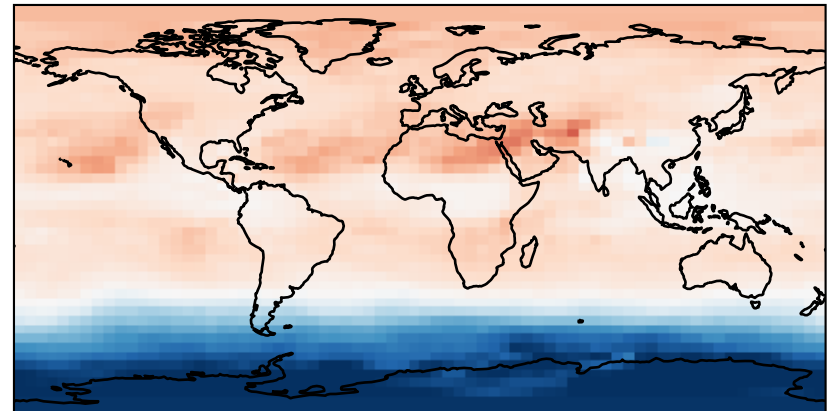
GCC\_13.4.0-rc.3 (Dev)  
4.0x5.0



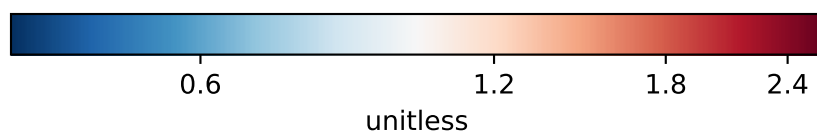
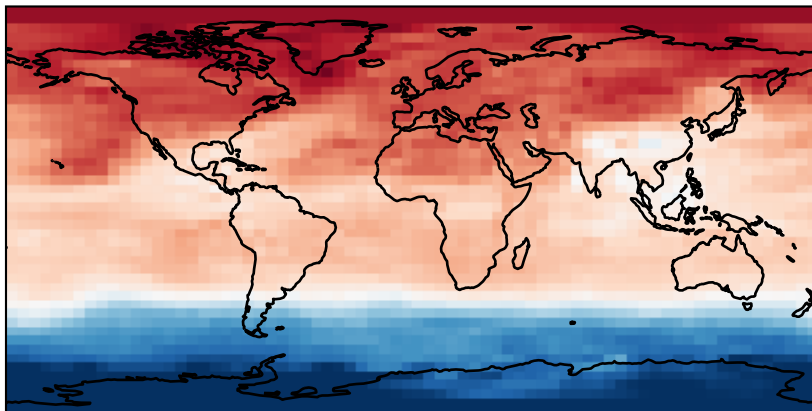
Difference  
Dev - Ref, Dynamic Range



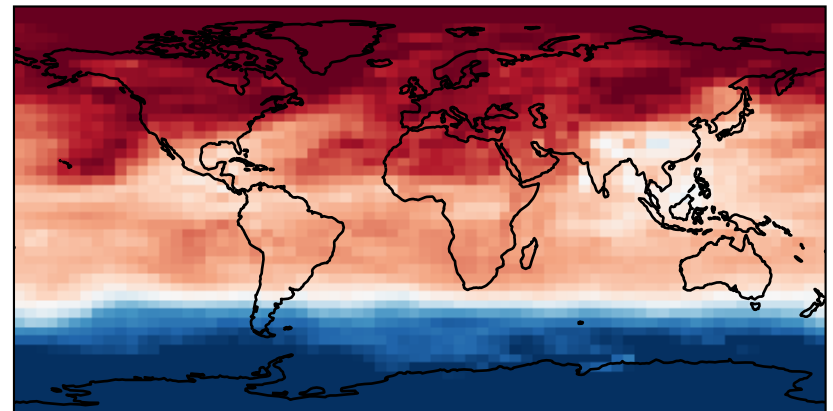
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

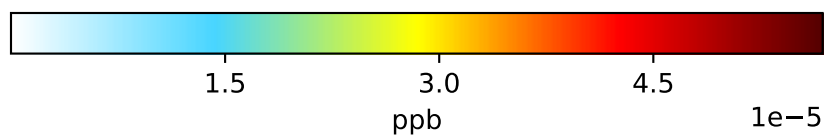
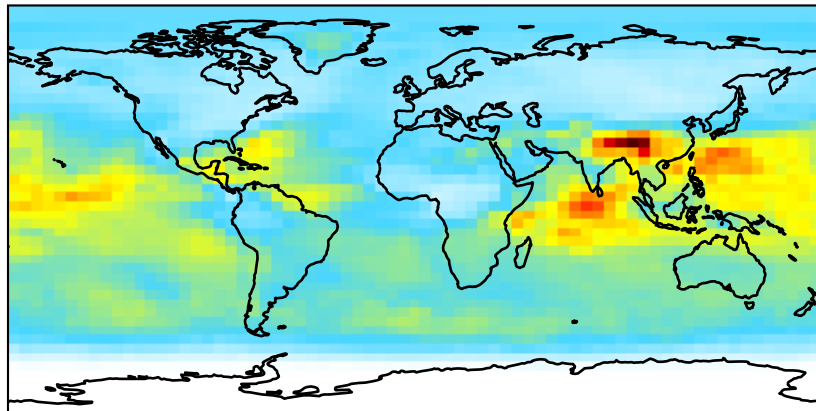


Ratio  
Dev/Ref, Fixed Range

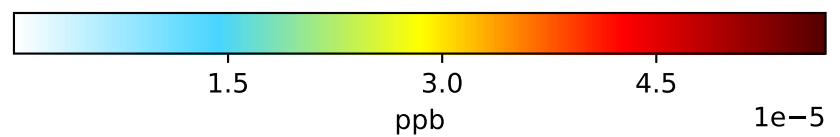
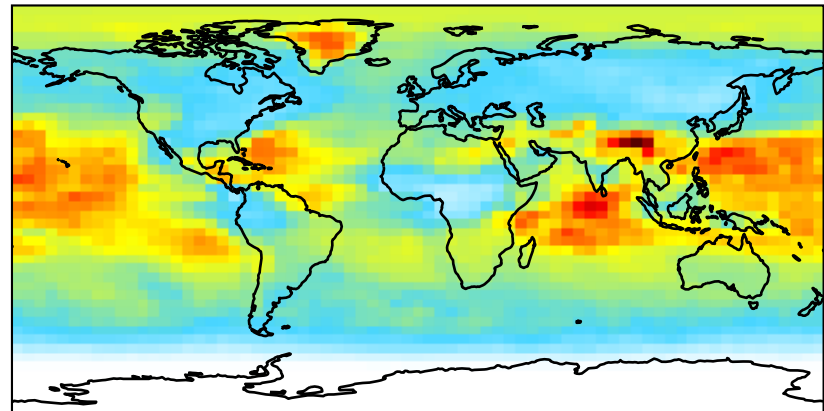


# SpeciesConc\_I (Jul2019)

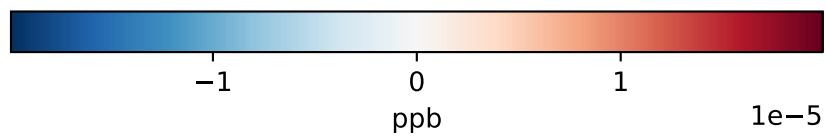
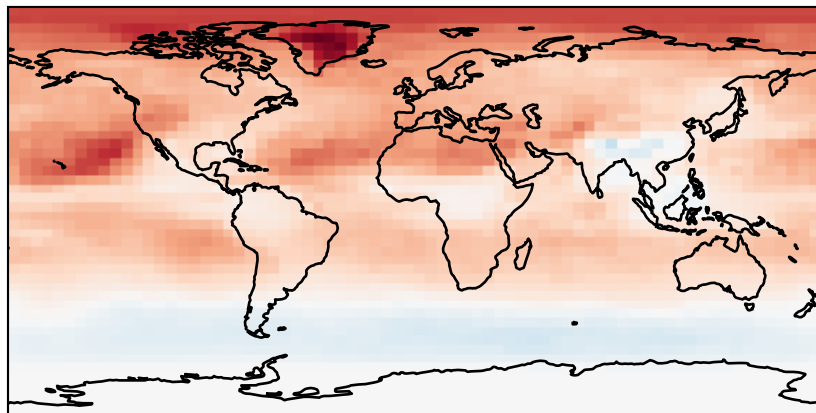
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



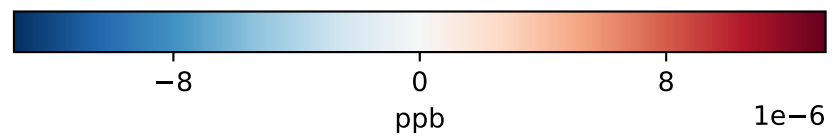
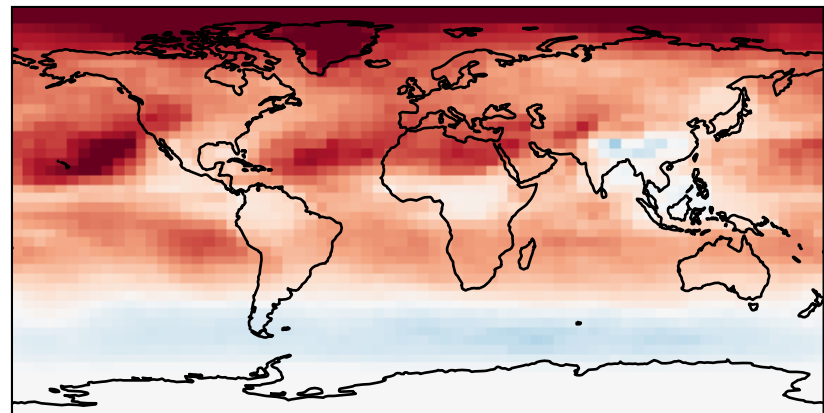
GCC\_13.4.0-rc.3 (Dev)  
4.0x5.0



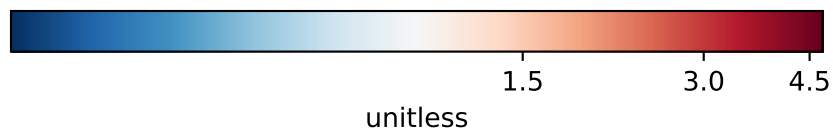
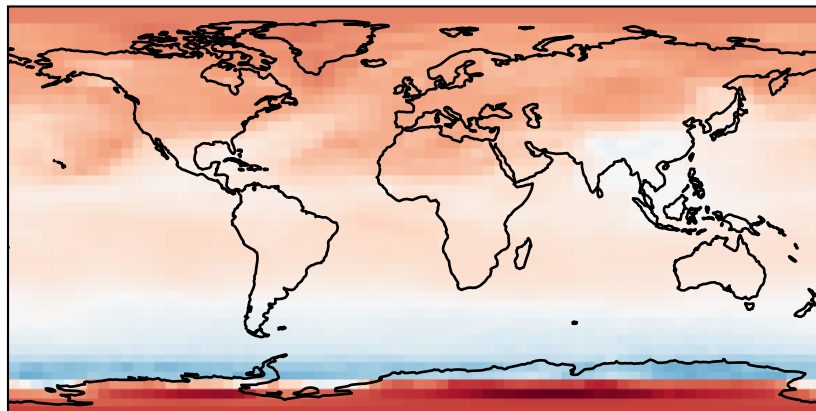
Difference  
Dev - Ref, Dynamic Range



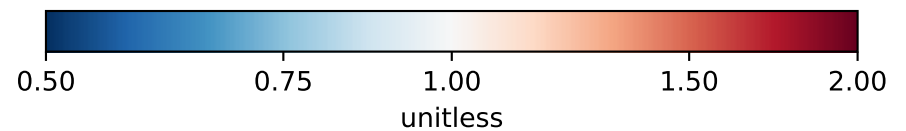
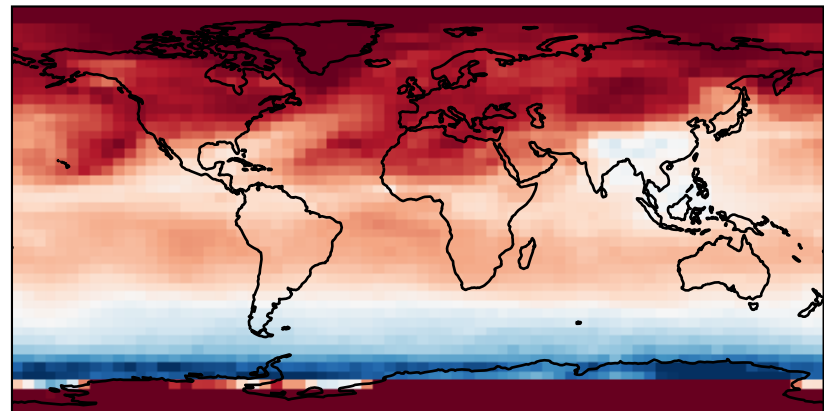
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

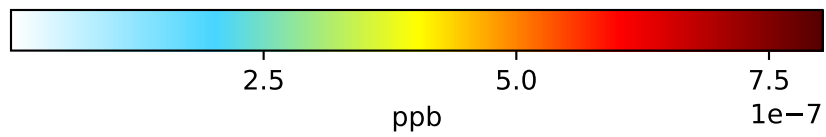
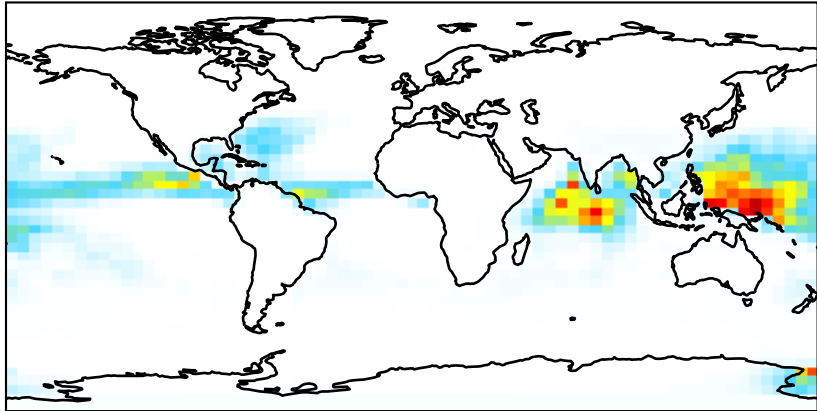


Ratio  
Dev/Ref, Fixed Range

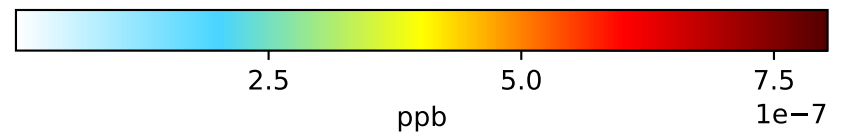
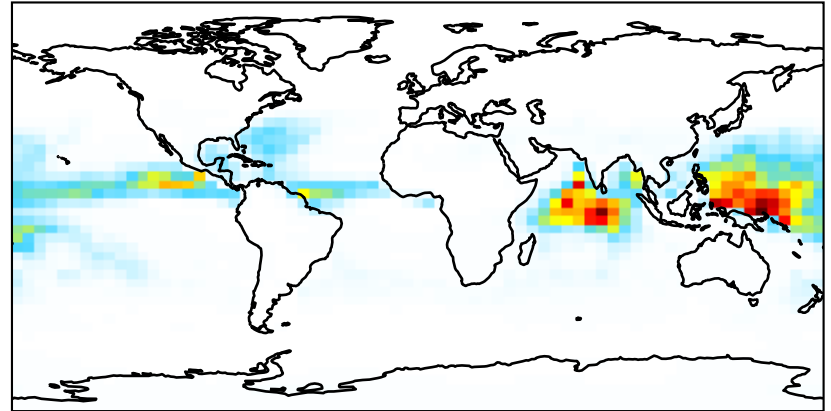


# SpeciesConc\_I2 (Jul2019)

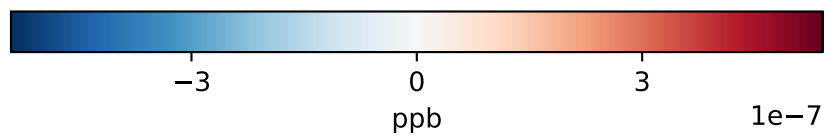
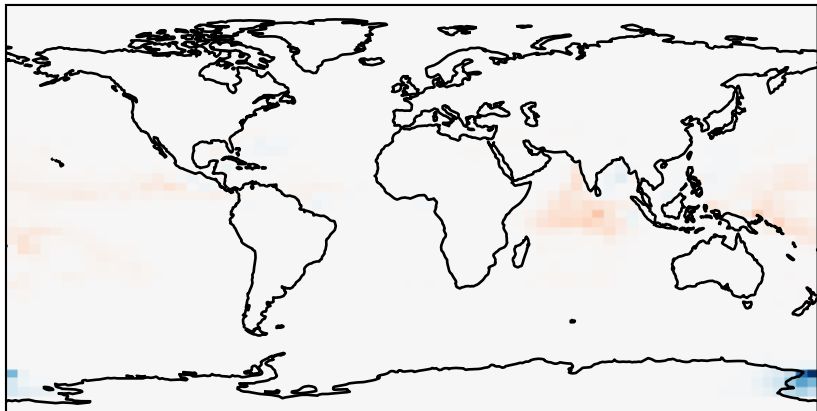
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



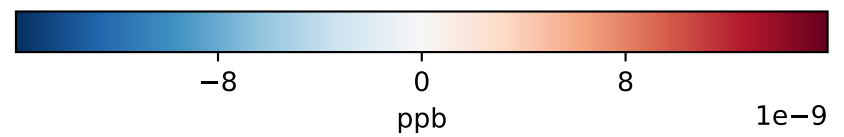
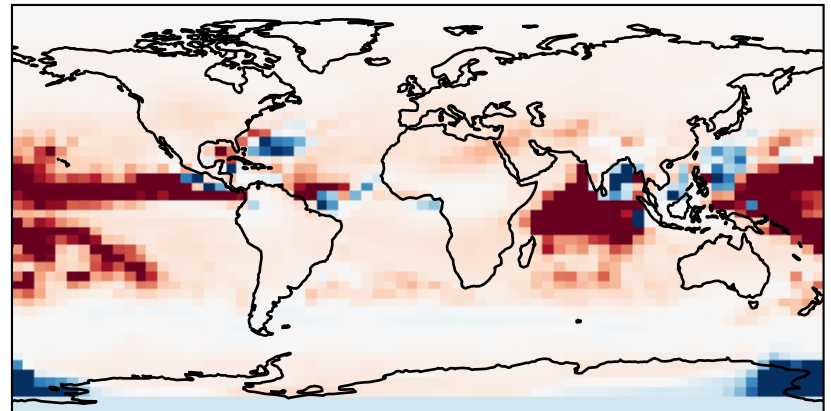
GCC\_13.4.0-rc.3 (Dev)  
4.0x5.0



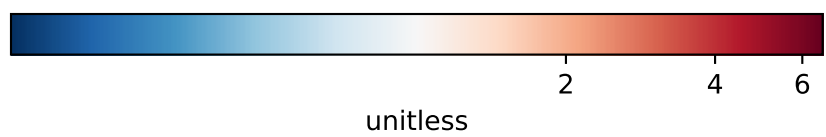
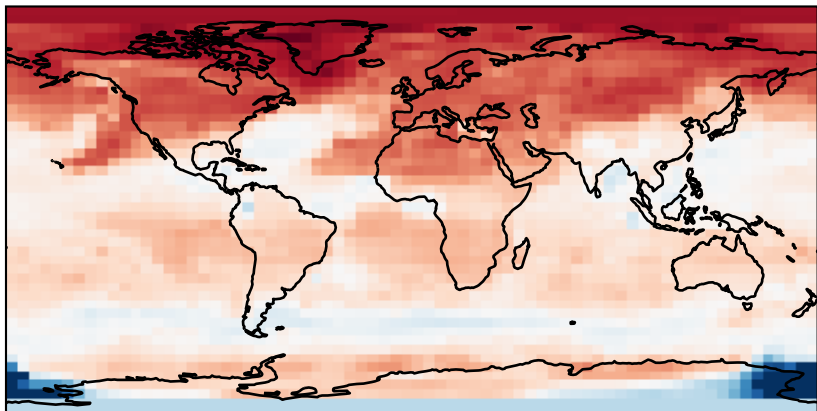
Difference  
Dev - Ref, Dynamic Range



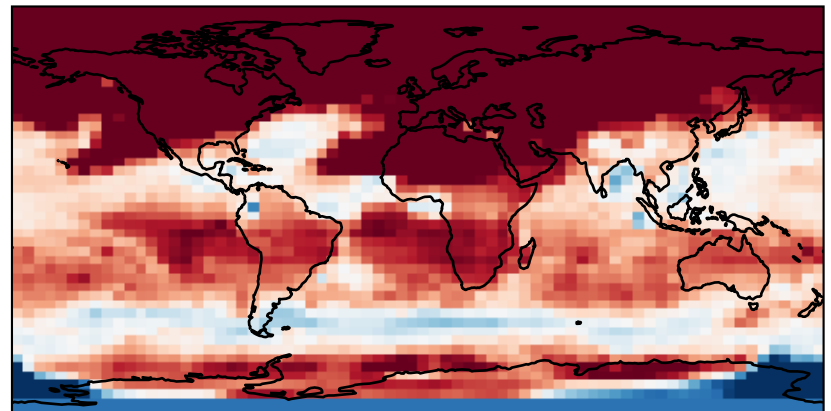
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



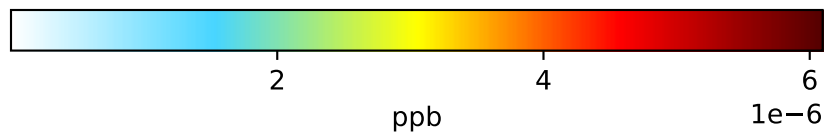
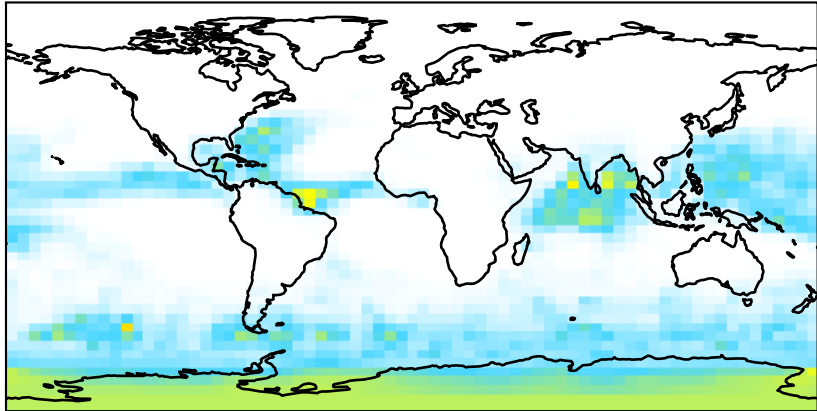
Ratio  
Dev/Ref, Fixed Range



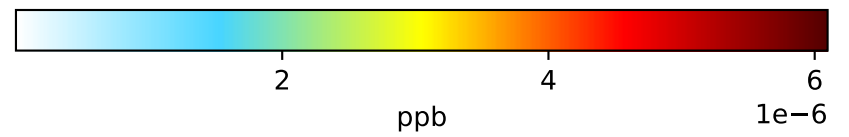
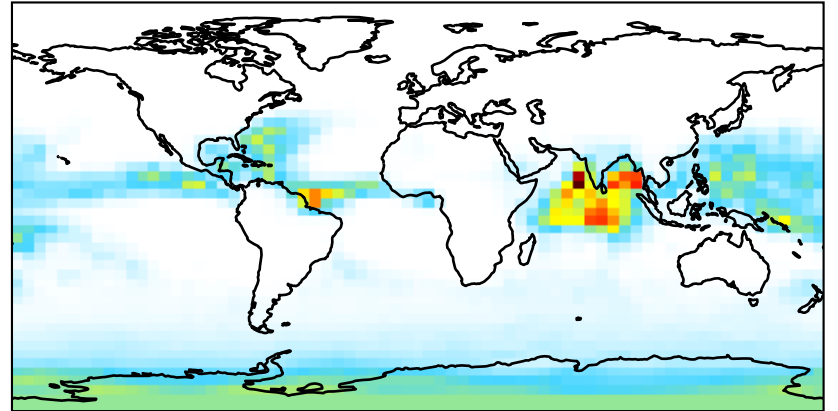


# SpeciesConc\_IBr (Jul2019)

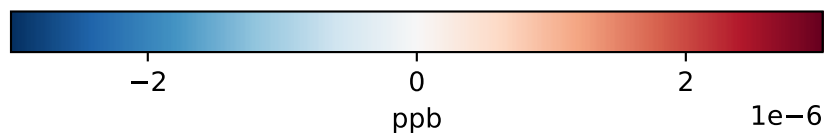
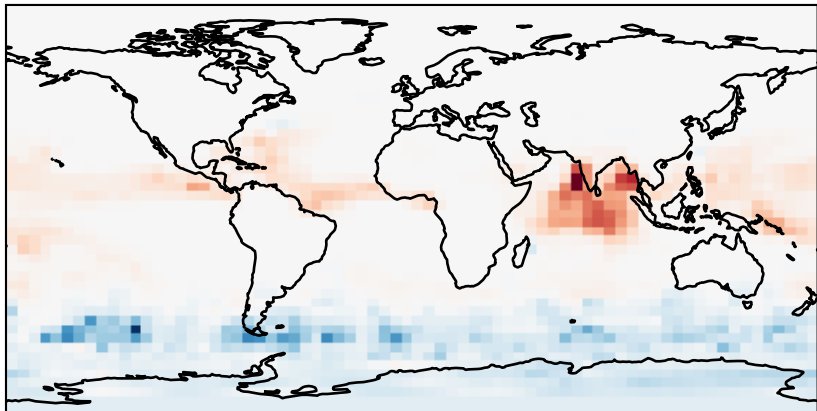
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



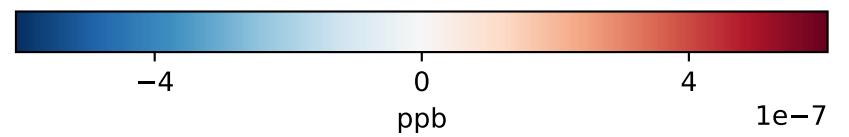
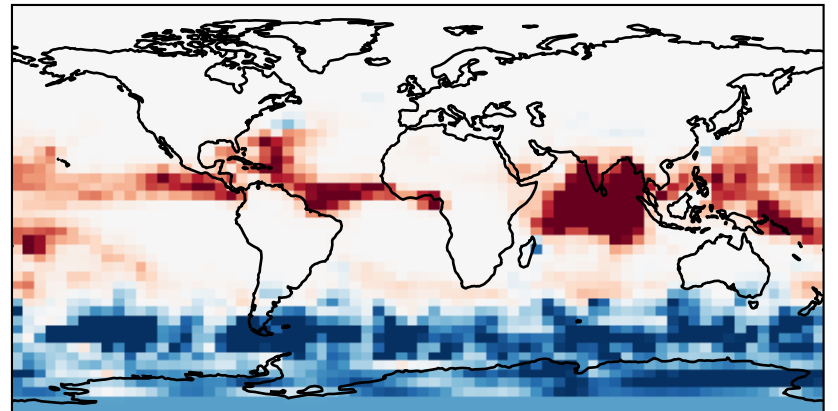
GCC\_13.4.0-rc.3 (Dev)  
4.0x5.0



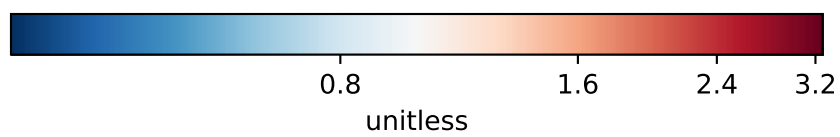
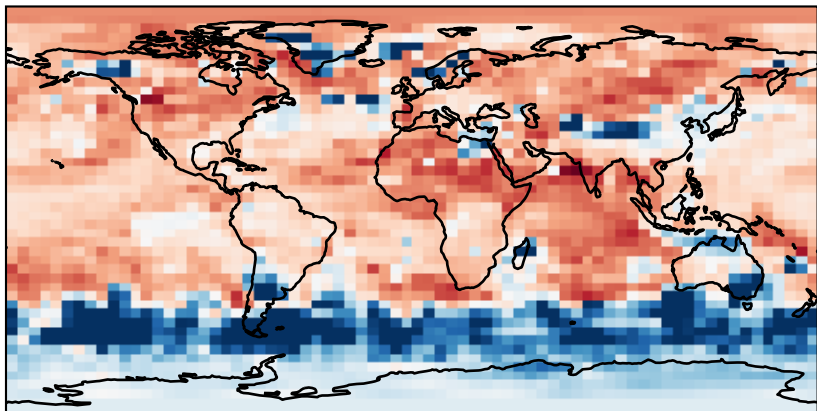
Difference  
Dev - Ref, Dynamic Range



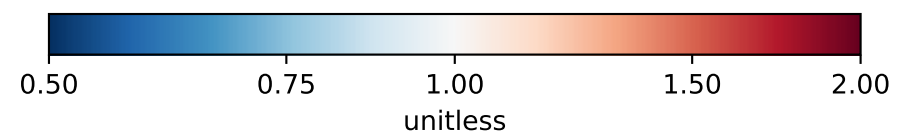
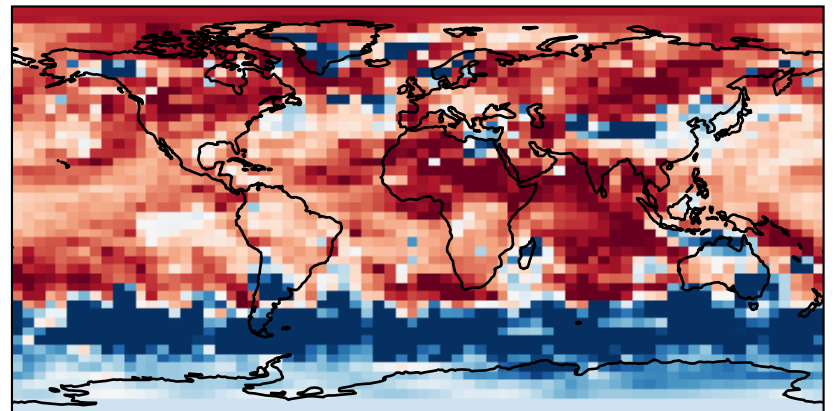
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

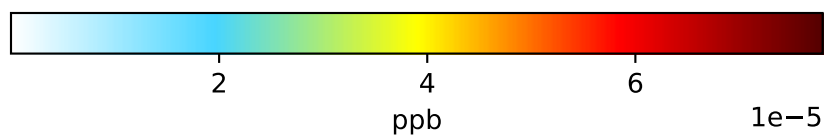
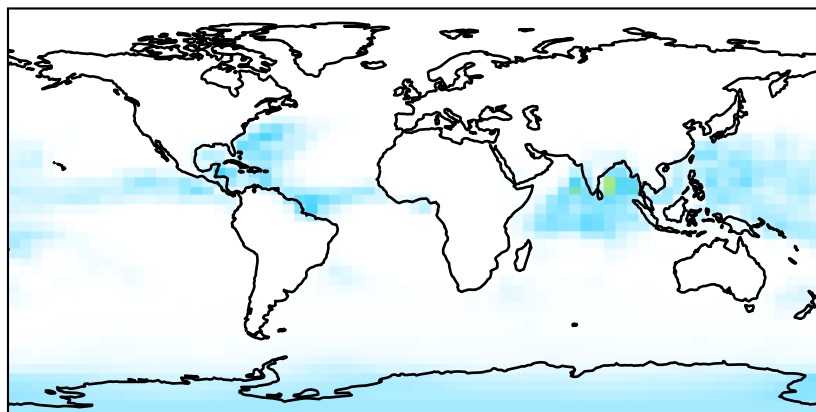


Ratio  
Dev/Ref, Fixed Range

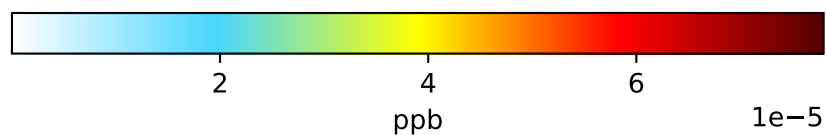
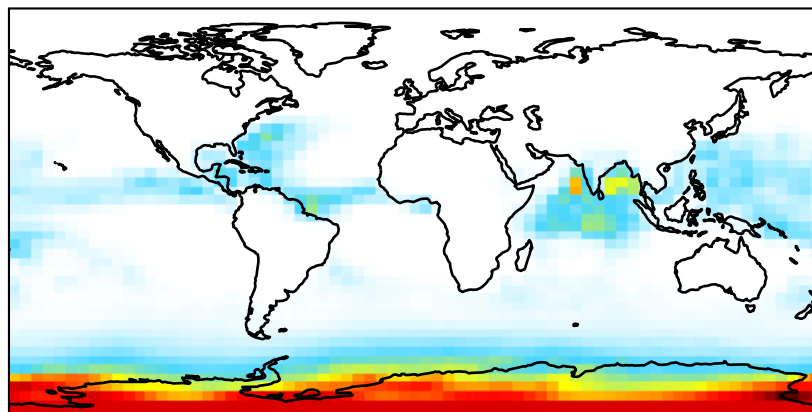


# SpeciesConc\_ICI (Jul2019)

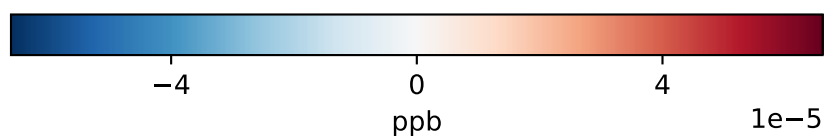
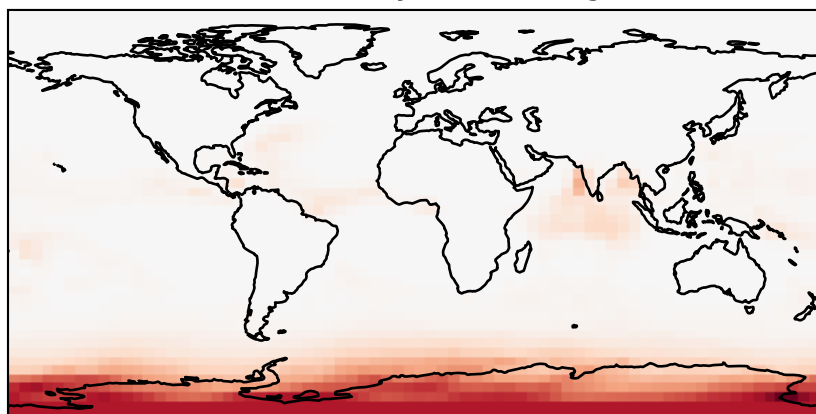
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



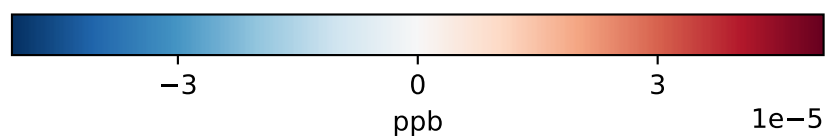
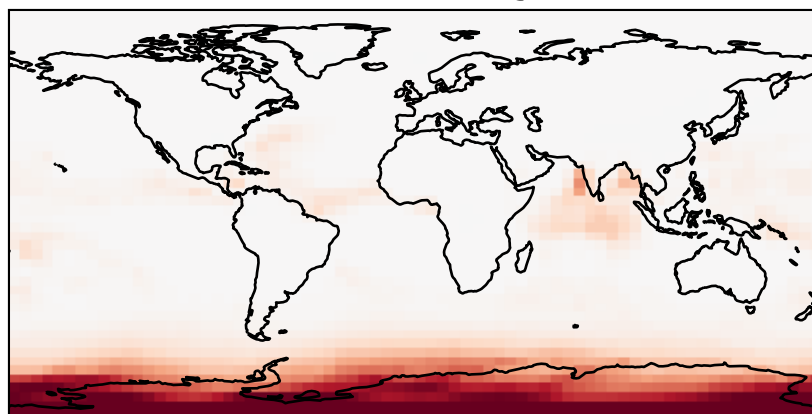
GCC\_13.4.0-rc.3 (Dev)  
4.0x5.0



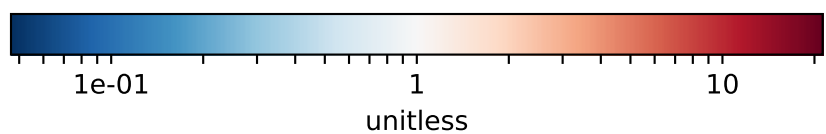
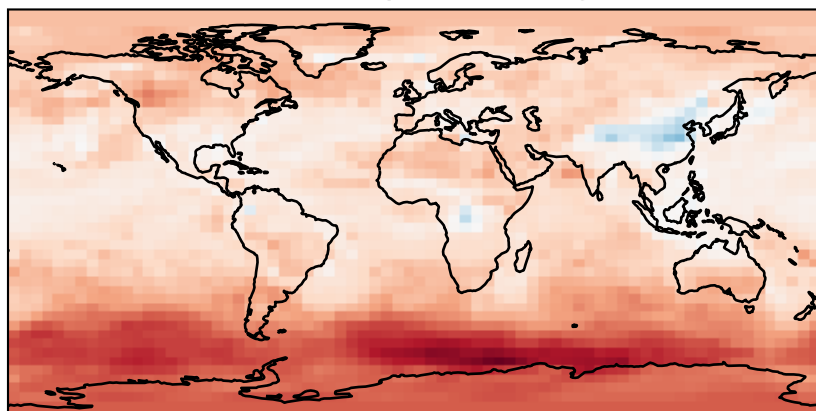
Difference  
Dev - Ref, Dynamic Range



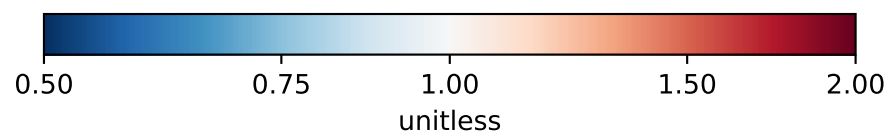
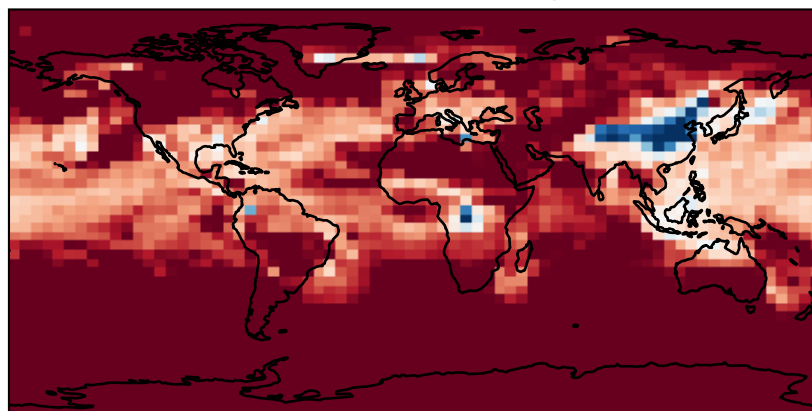
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

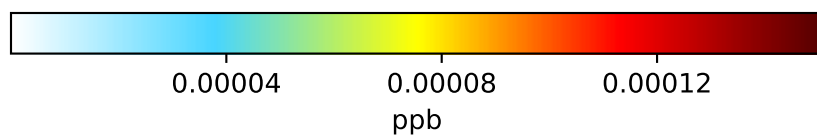
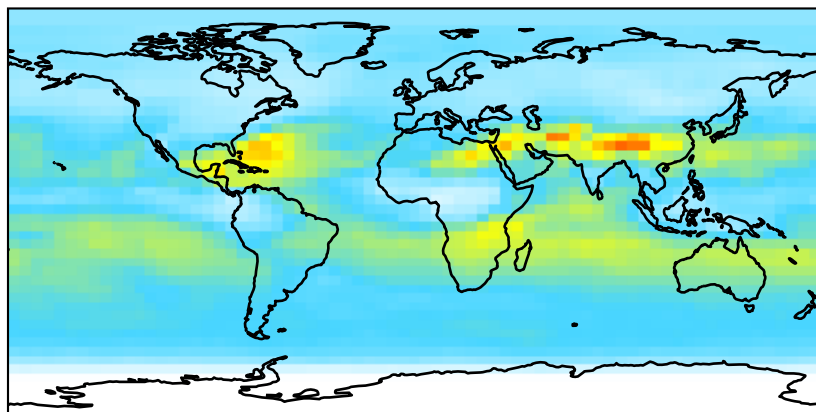


Ratio  
Dev/Ref, Fixed Range

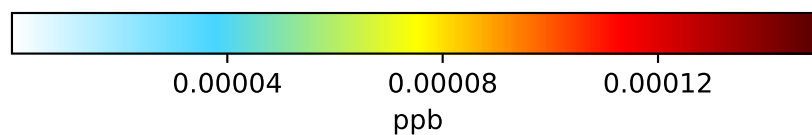
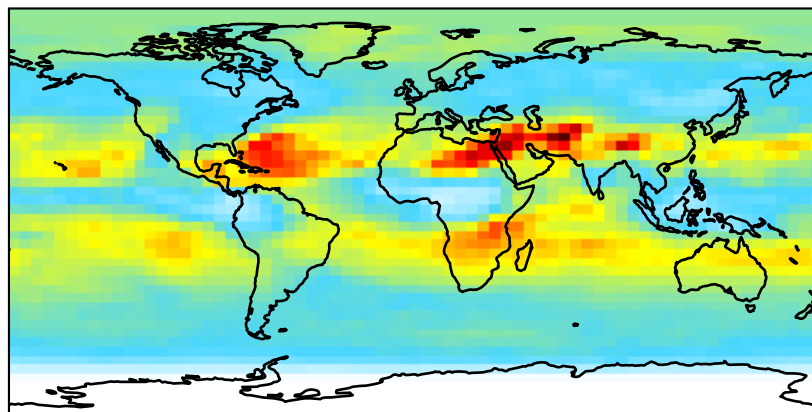


# SpeciesConc\_IO (Jul2019)

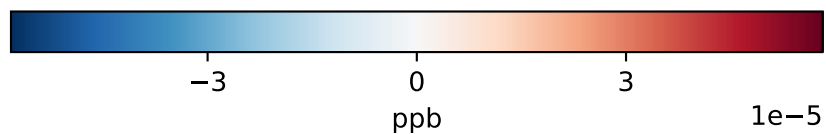
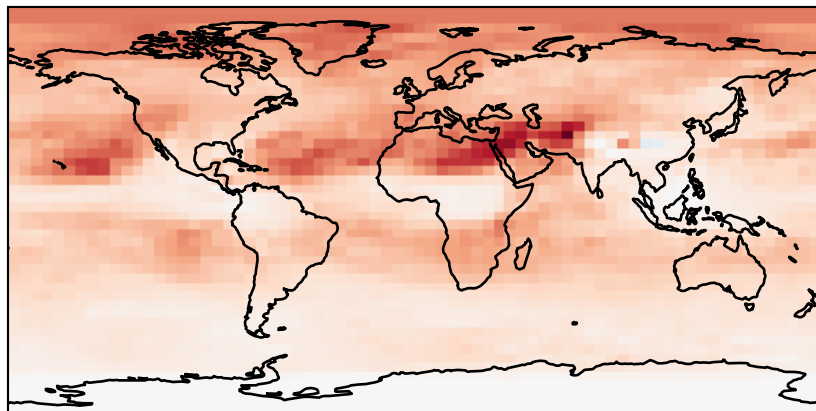
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



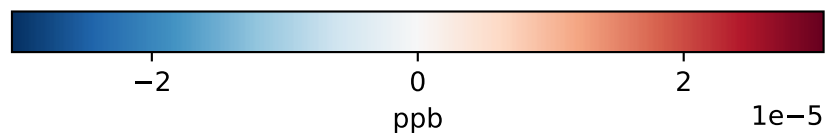
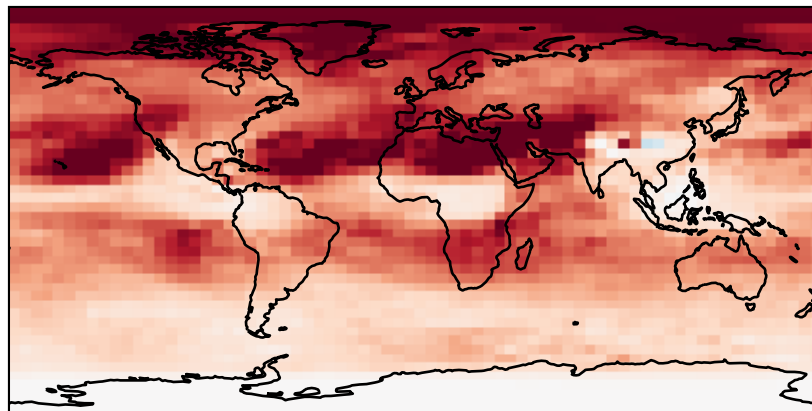
GCC\_13.4.0-rc.3 (Dev)  
4.0x5.0



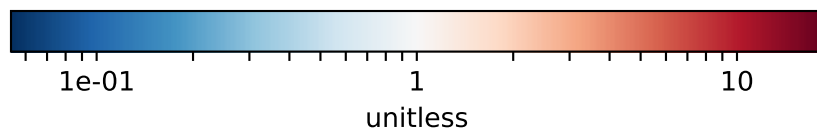
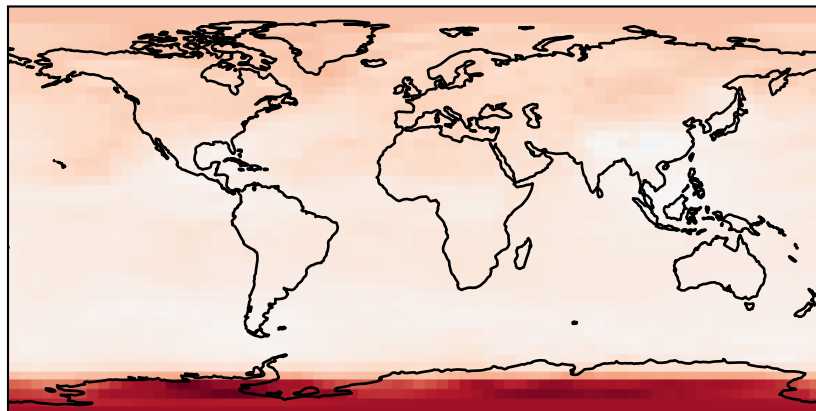
Difference  
Dev - Ref, Dynamic Range



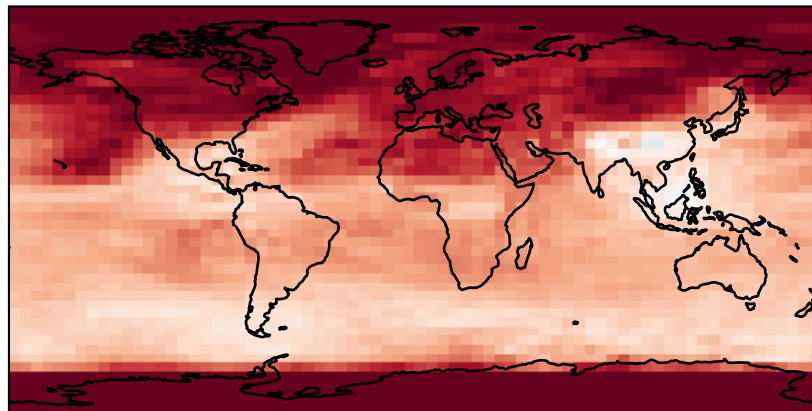
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

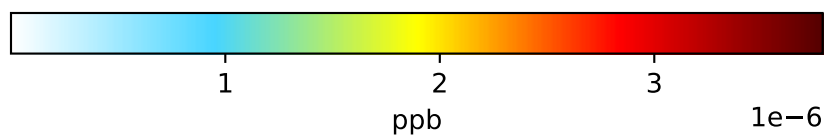
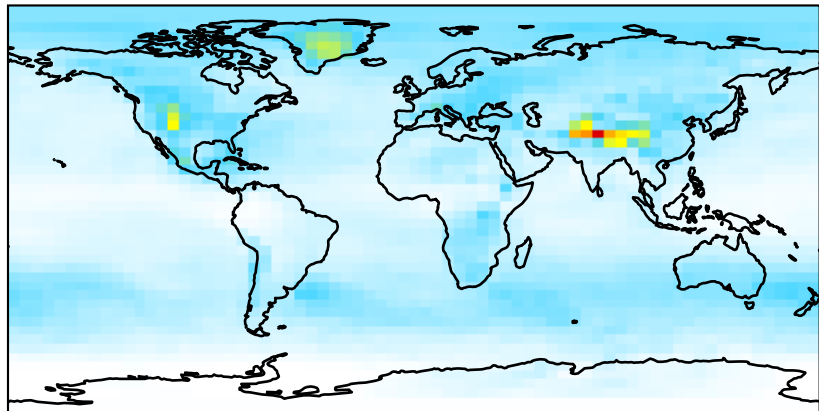


Ratio  
Dev/Ref, Fixed Range

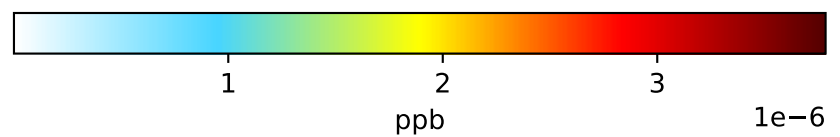
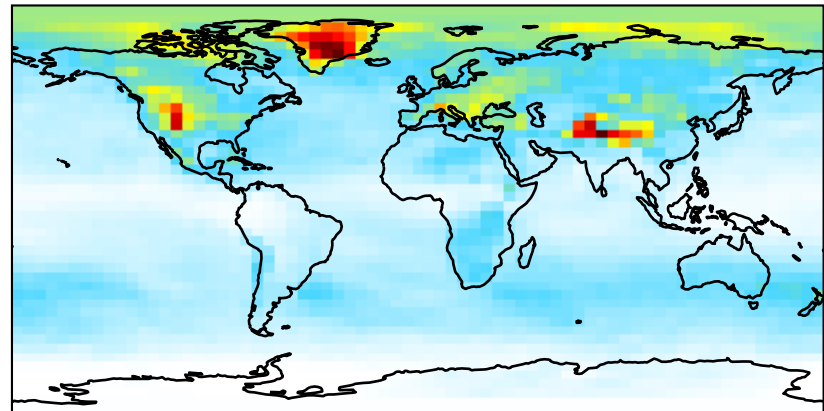


# SpeciesConc\_IONO (Jul2019)

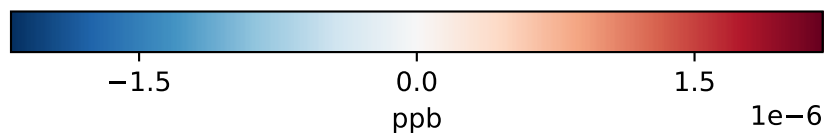
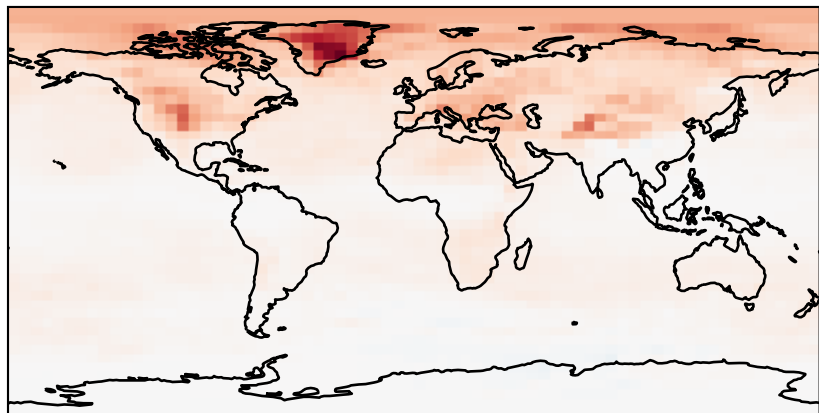
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



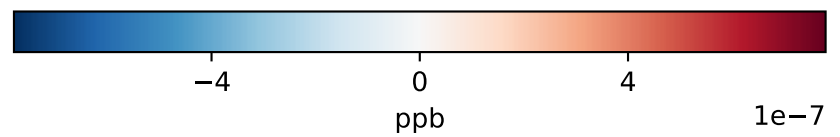
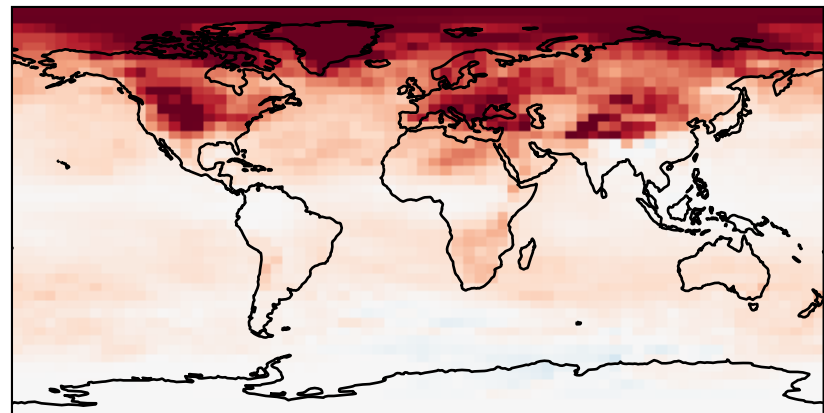
GCC\_13.4.0-rc.3 (Dev)  
4.0x5.0



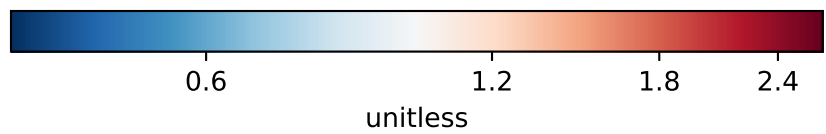
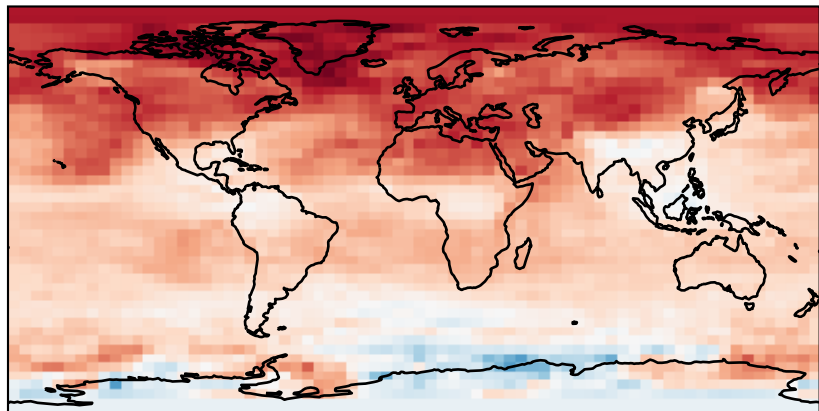
Difference  
Dev - Ref, Dynamic Range



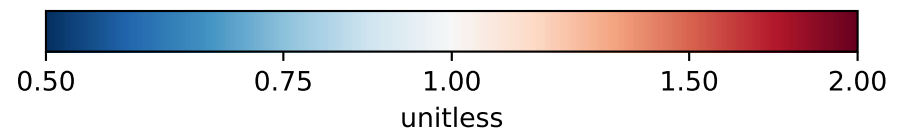
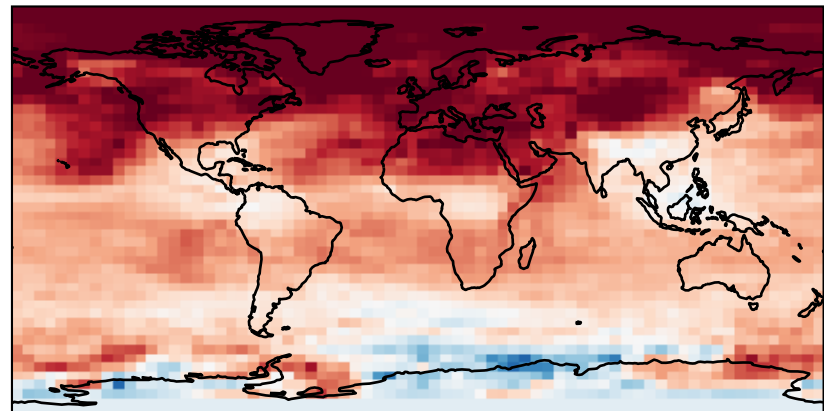
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



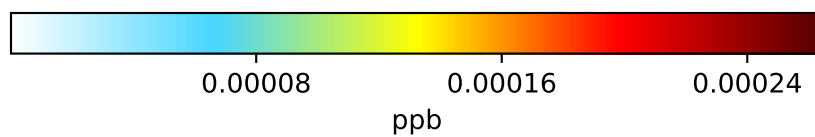
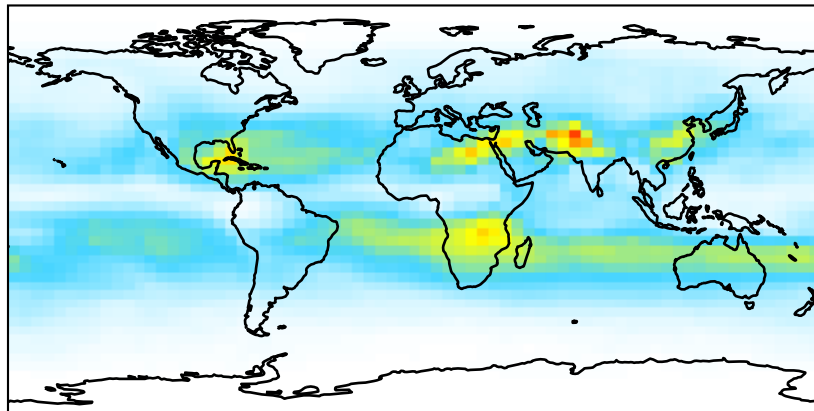
Ratio  
Dev/Ref, Fixed Range



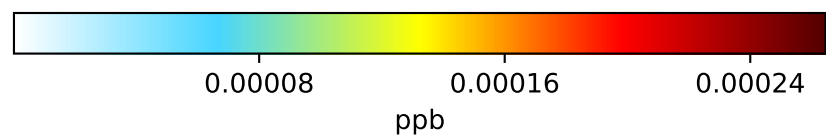
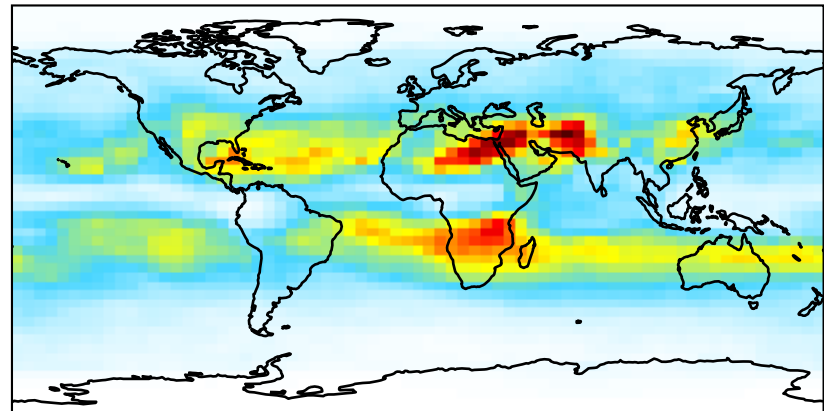


# SpeciesConc\_IONO2 (Jul2019)

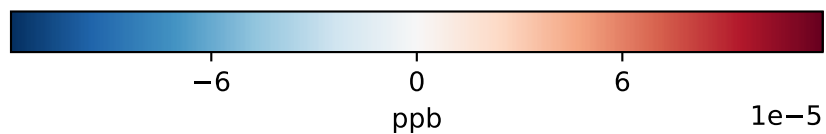
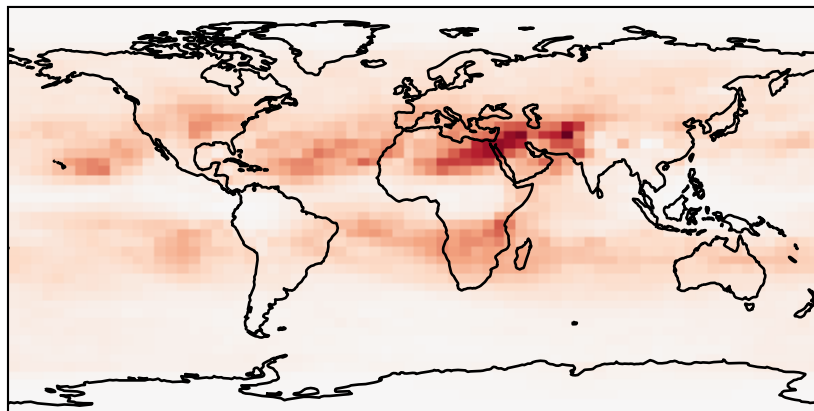
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



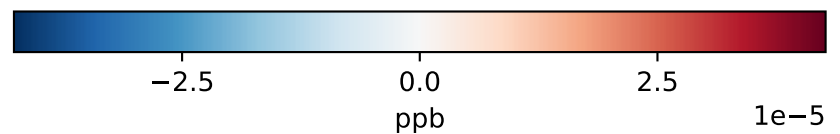
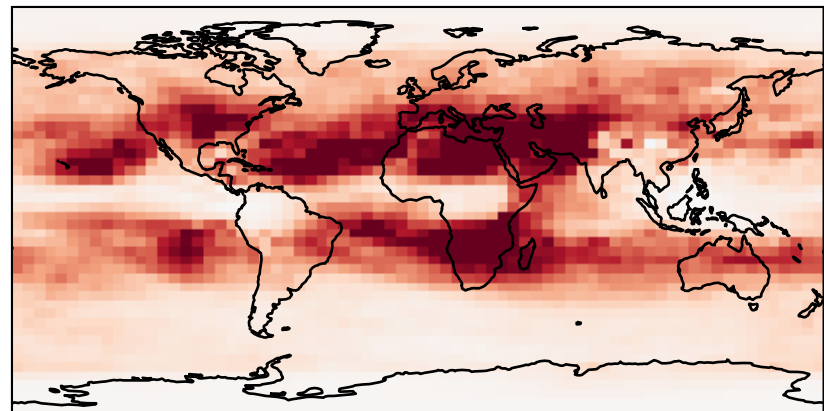
GCC\_13.4.0-rc.3 (Dev)  
4.0x5.0



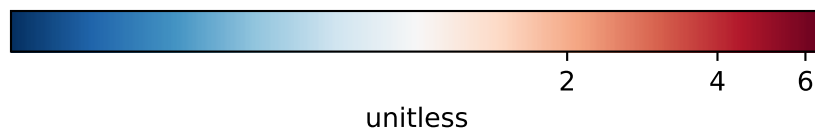
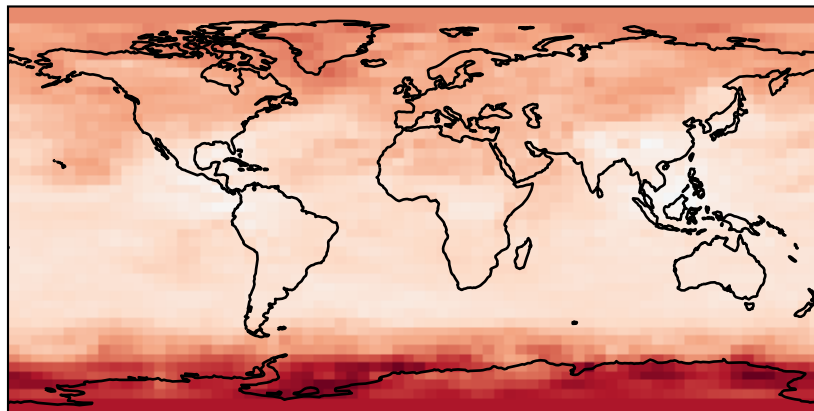
Difference  
Dev - Ref, Dynamic Range



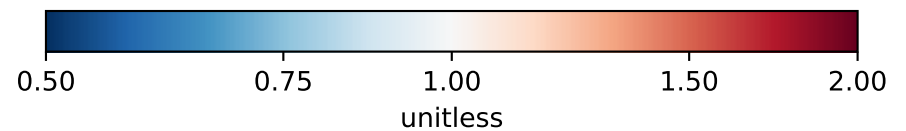
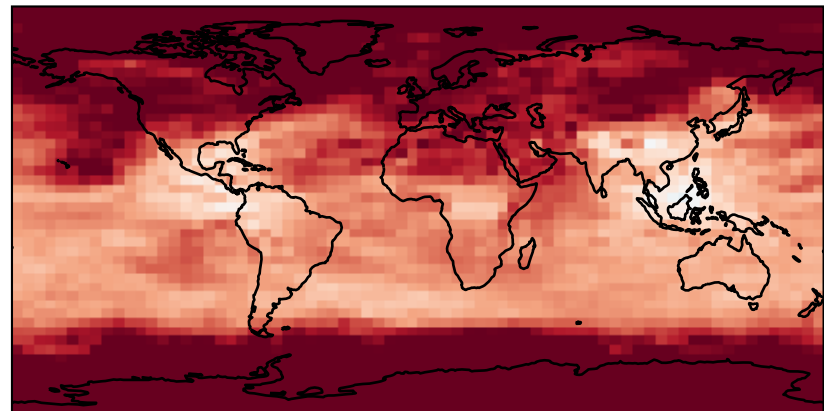
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

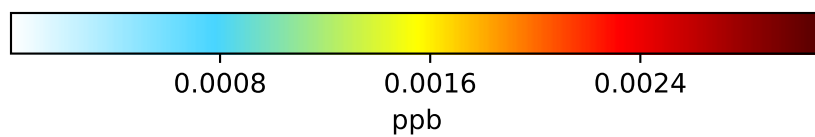
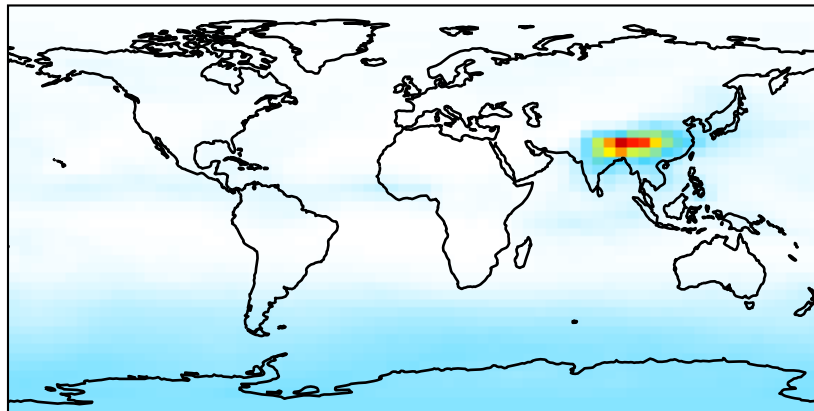


Ratio  
Dev/Ref, Fixed Range

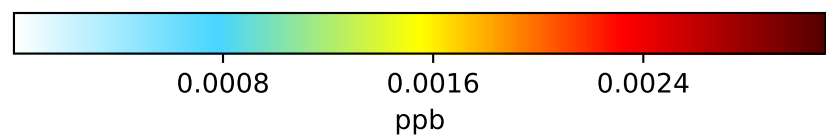
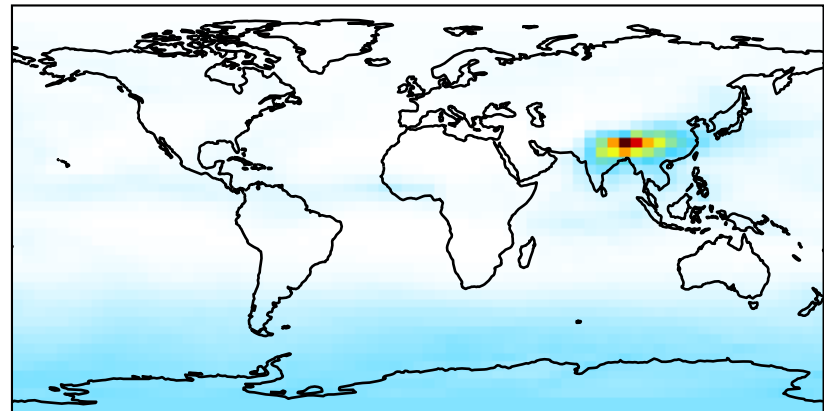


# SpeciesConc\_CH3I (Jul2019)

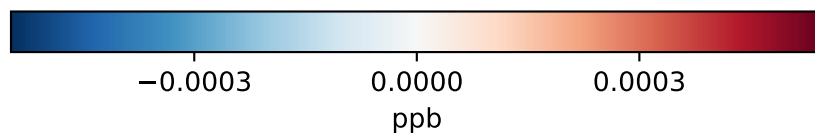
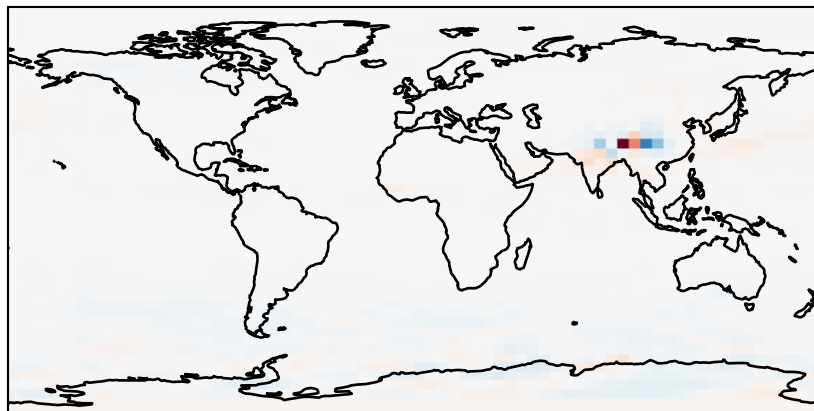
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



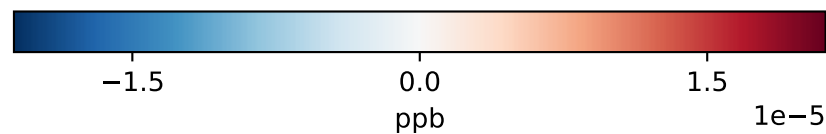
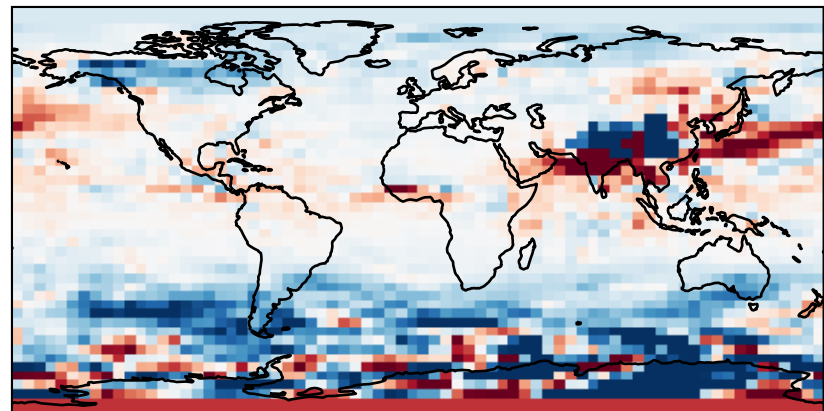
GCC\_13.4.0-rc.3 (Dev)  
4.0x5.0



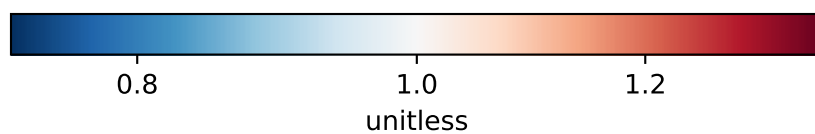
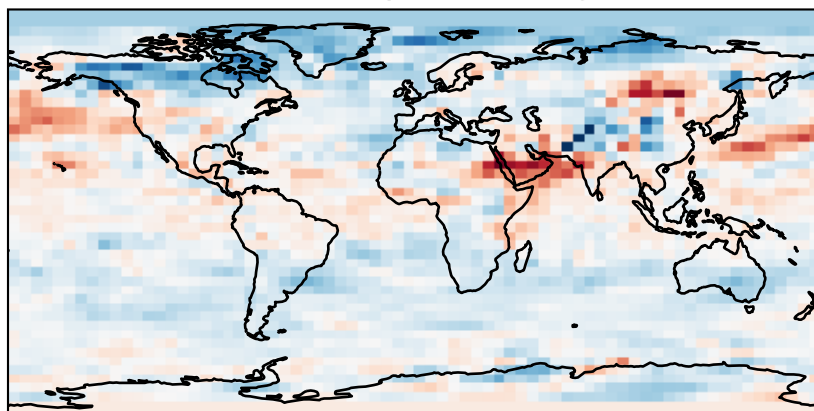
Difference  
Dev - Ref, Dynamic Range



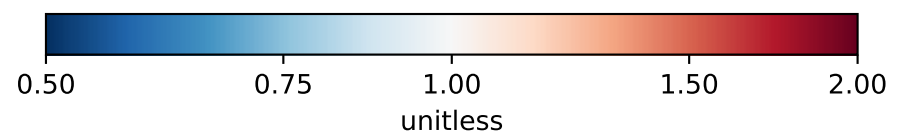
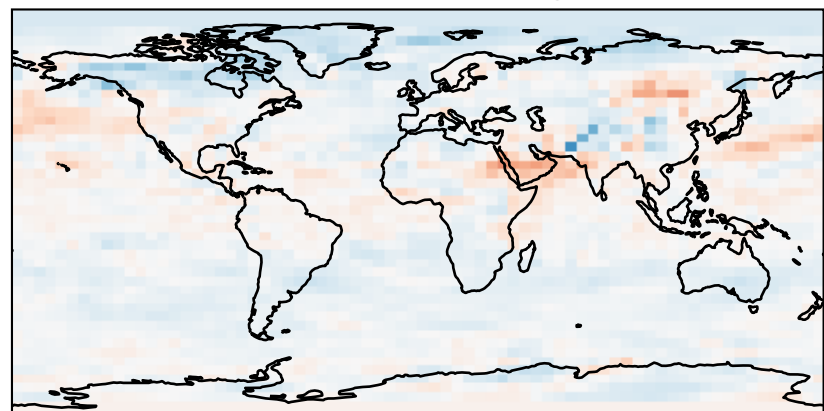
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

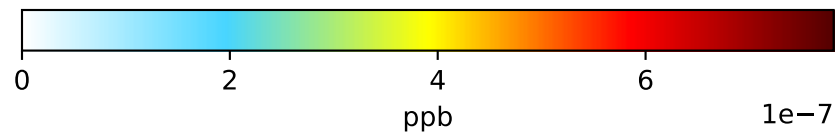
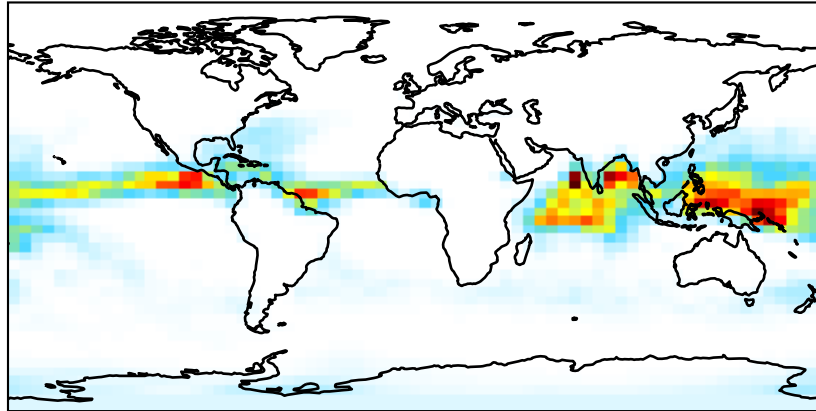


Ratio  
Dev/Ref, Fixed Range

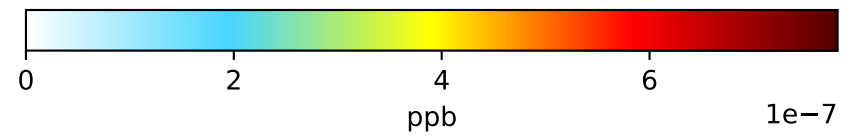
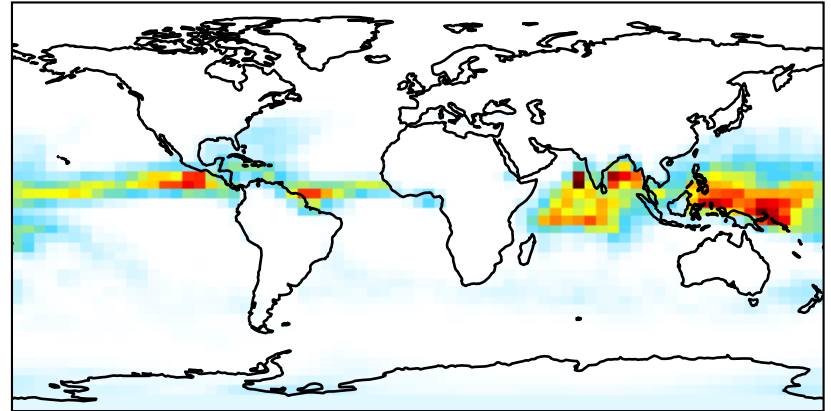


# SpeciesConc\_CH2I2 (Jul2019)

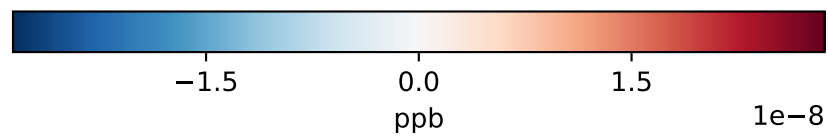
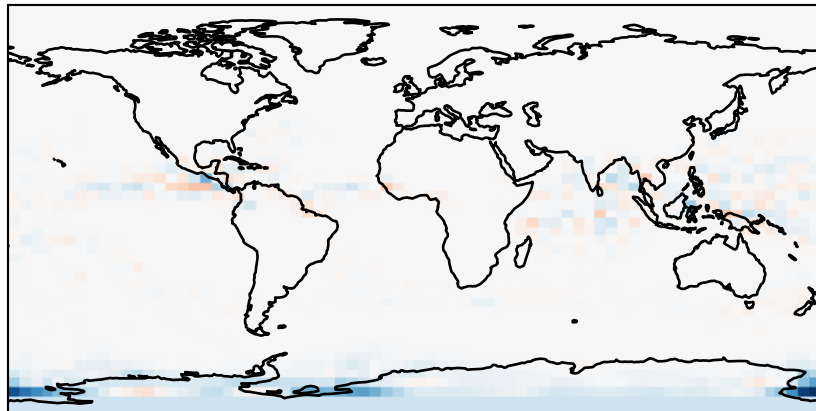
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



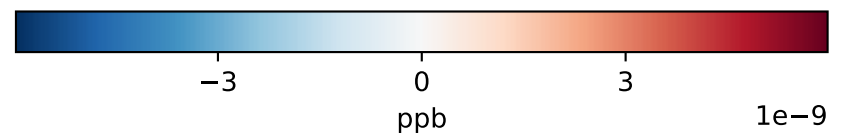
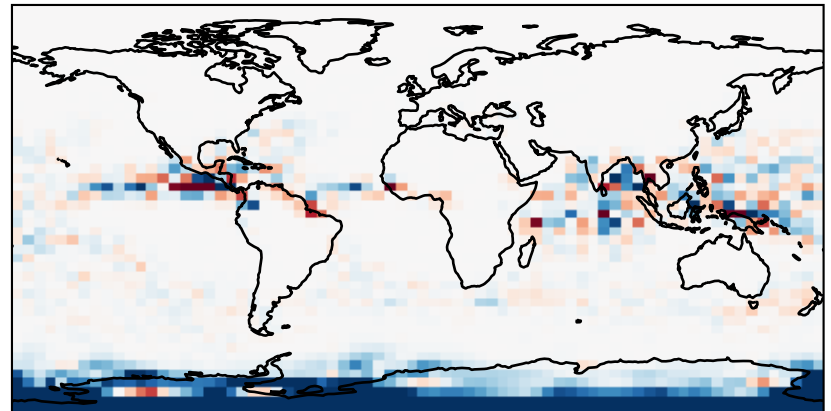
GCC\_13.4.0-rc.3 (Dev)  
4.0x5.0



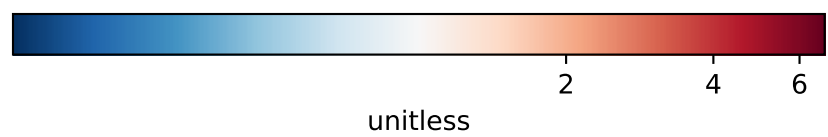
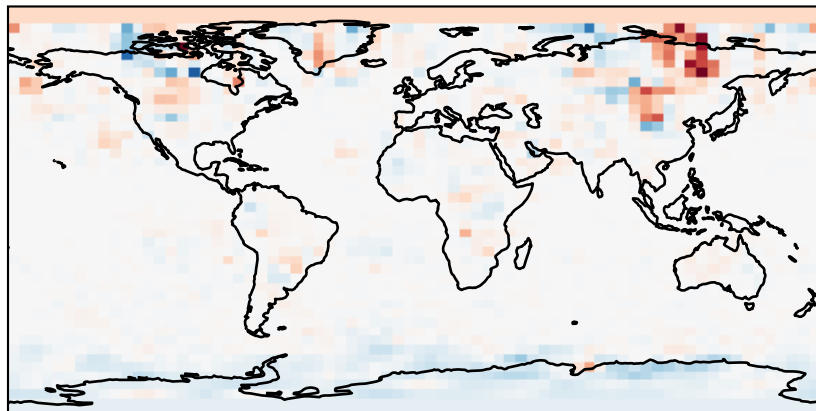
Difference  
Dev - Ref, Dynamic Range



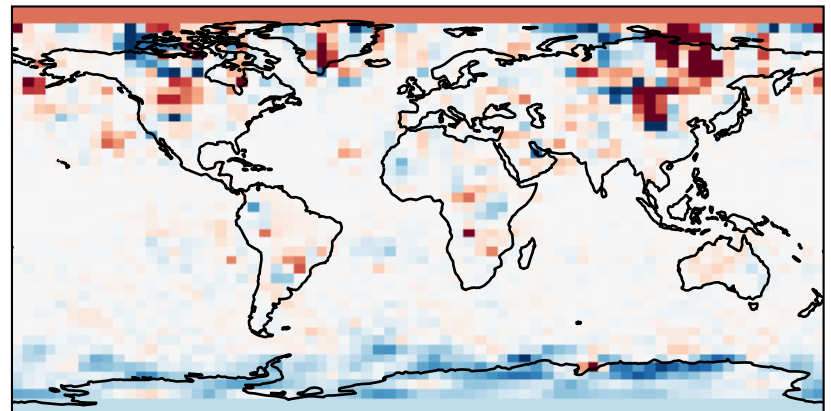
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

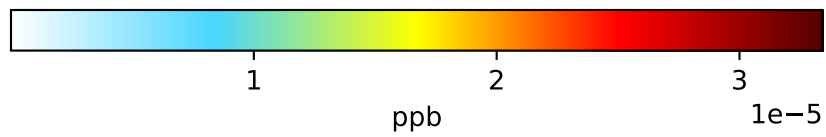
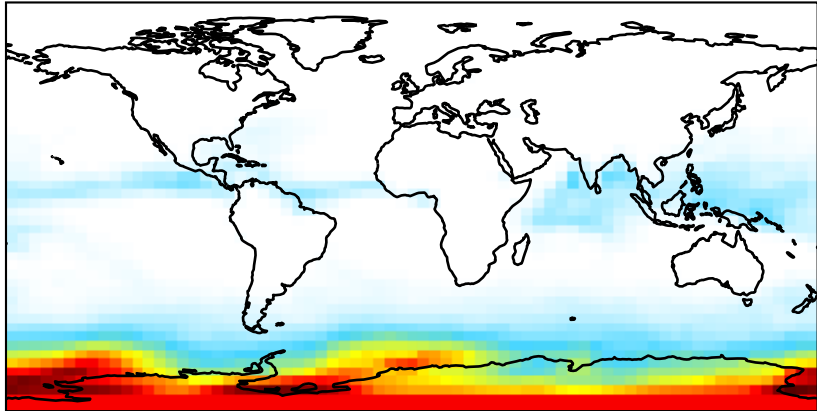


Ratio  
Dev/Ref, Fixed Range

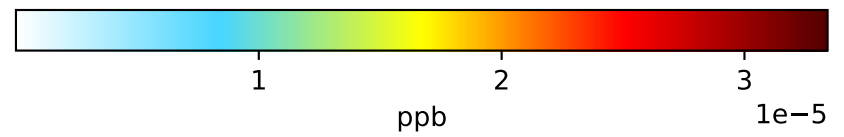
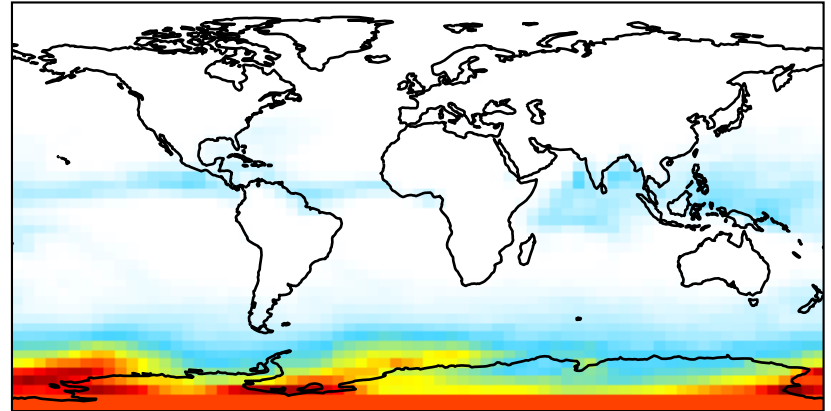


# SpeciesConc\_CH2ICl (Jul2019)

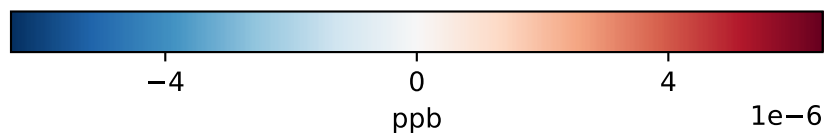
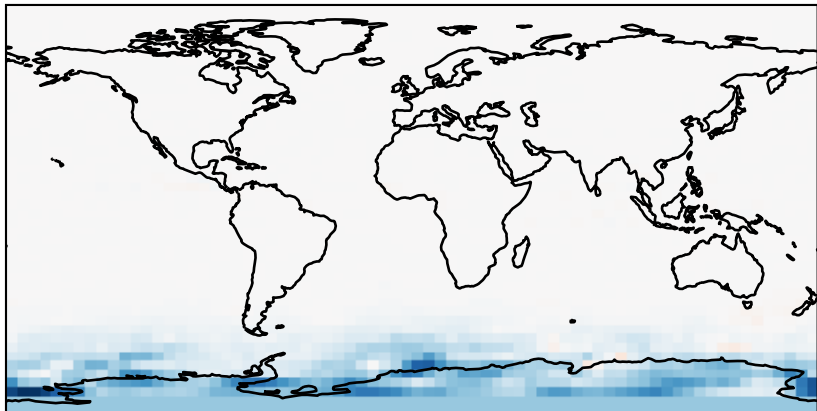
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



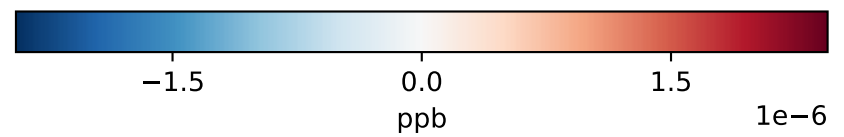
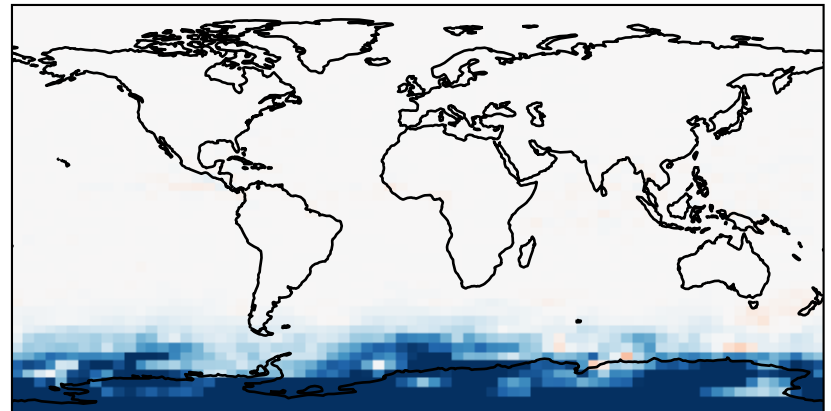
GCC\_13.4.0-rc.3 (Dev)  
4.0x5.0



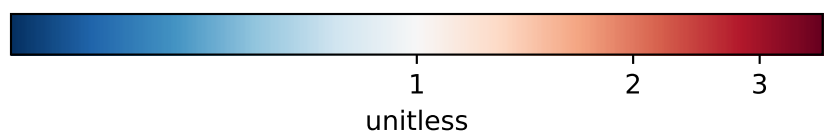
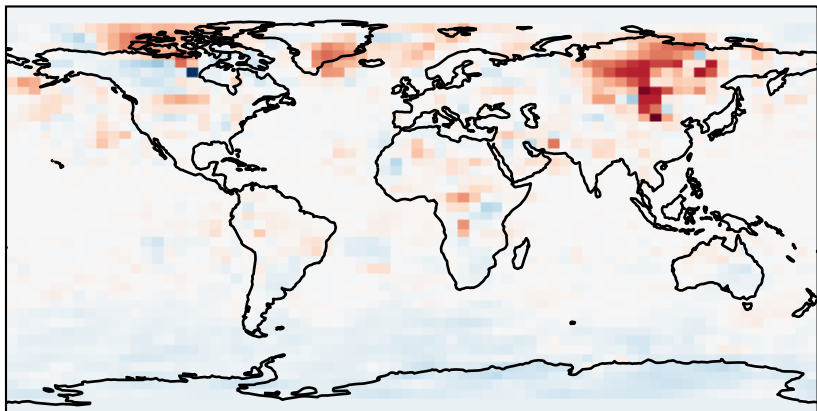
Difference  
Dev - Ref, Dynamic Range



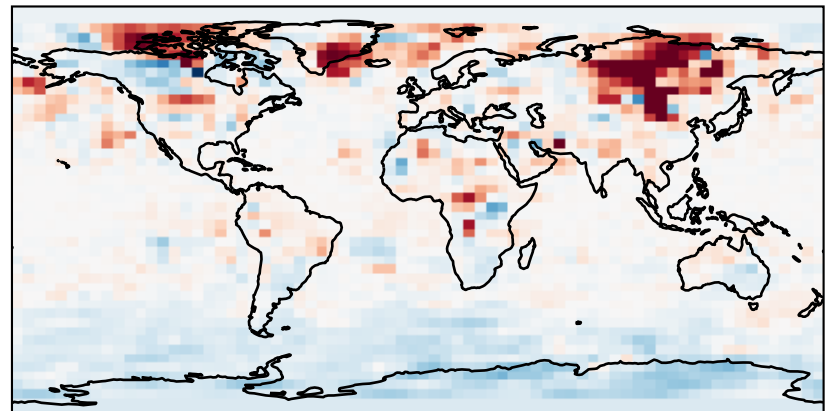
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



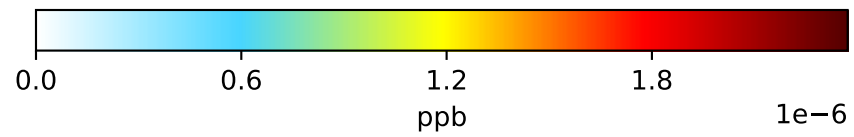
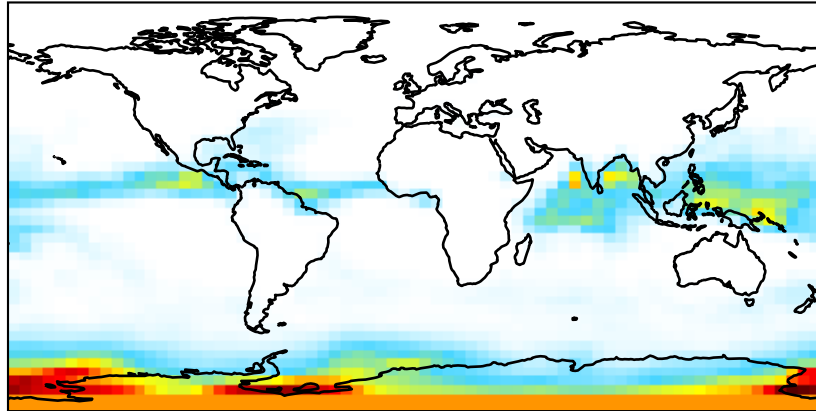
Ratio  
Dev/Ref, Fixed Range



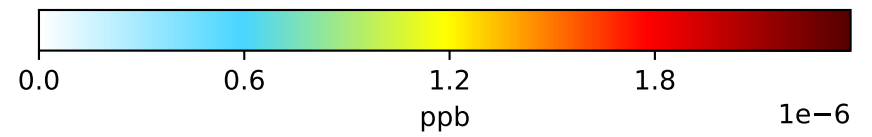
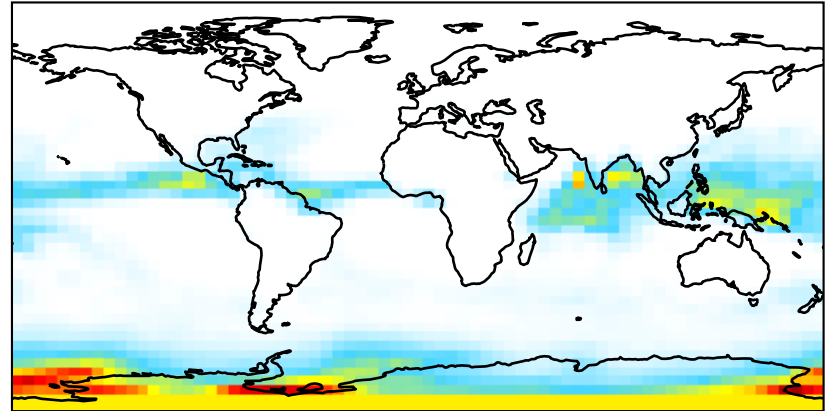


# SpeciesConc\_CH2IBr (Jul2019)

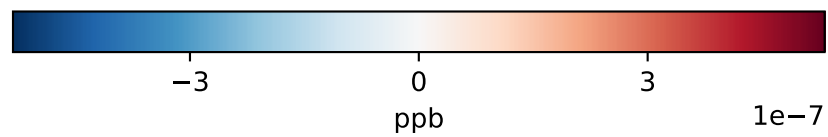
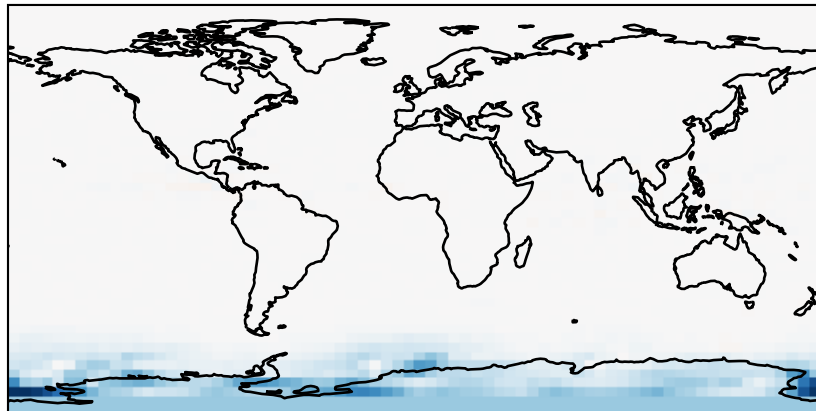
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



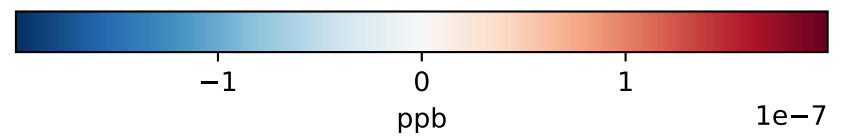
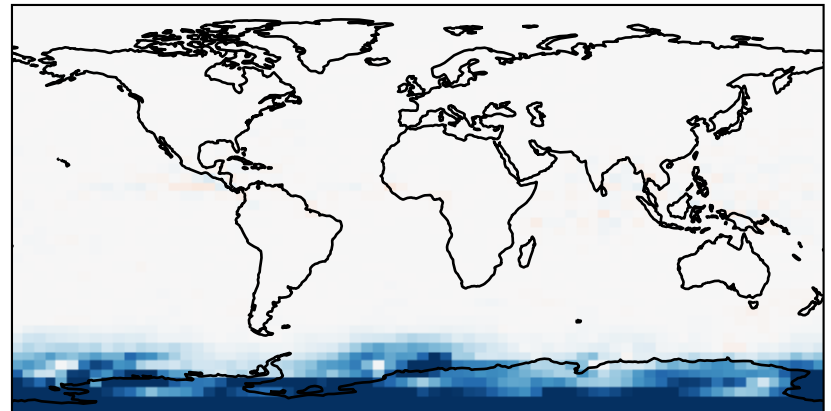
GCC\_13.4.0-rc.3 (Dev)  
4.0x5.0



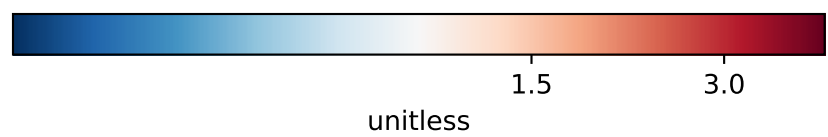
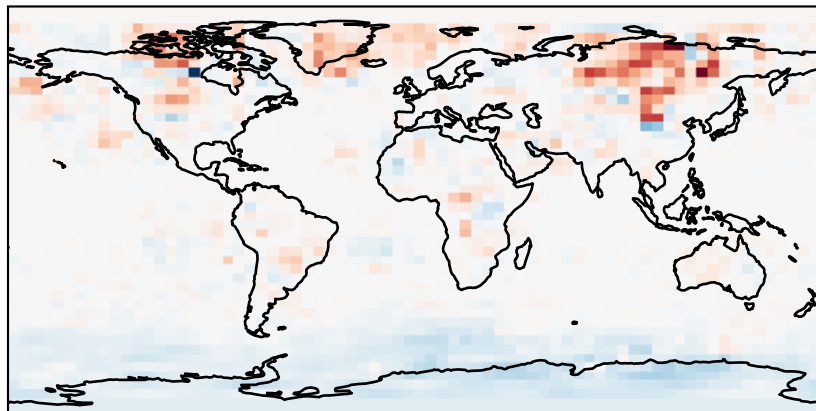
Difference  
Dev - Ref, Dynamic Range



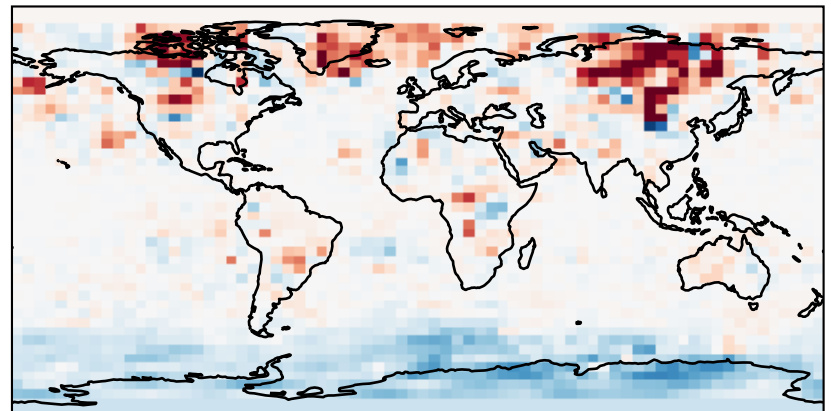
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

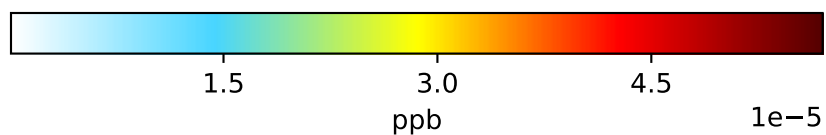
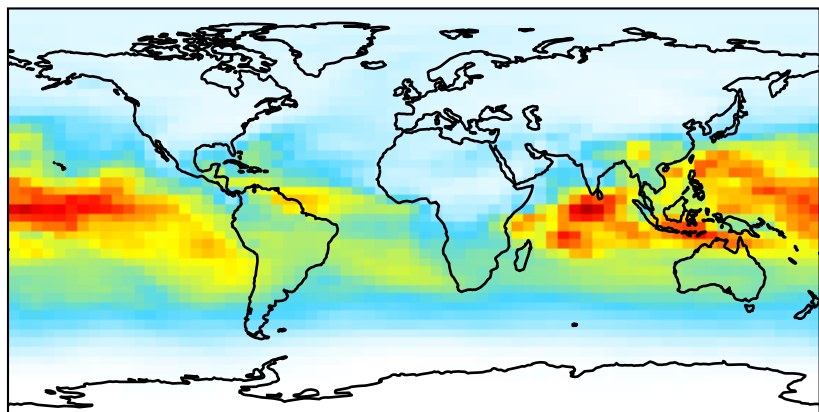


Ratio  
Dev/Ref, Fixed Range

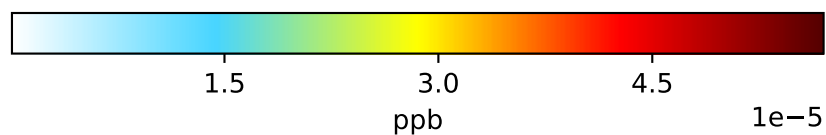
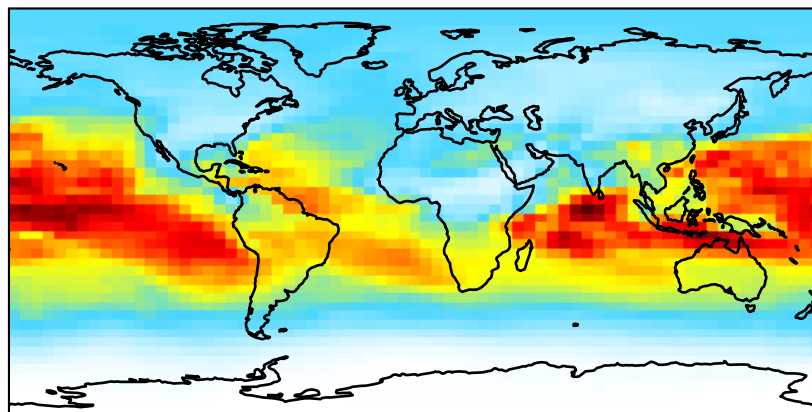


# SpeciesConc\_HI (Jul2019)

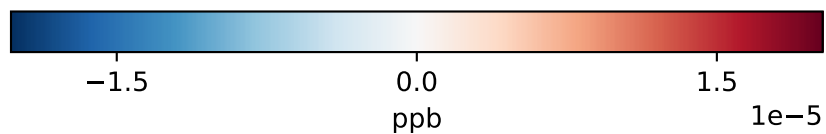
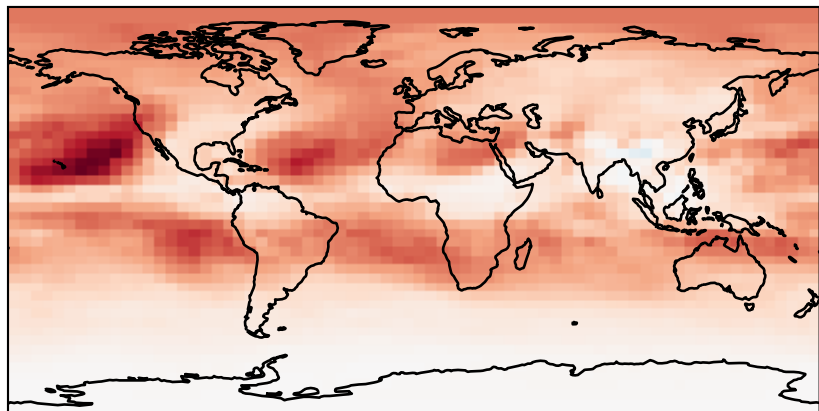
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



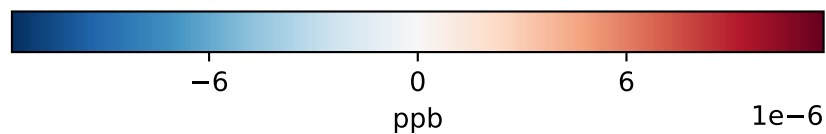
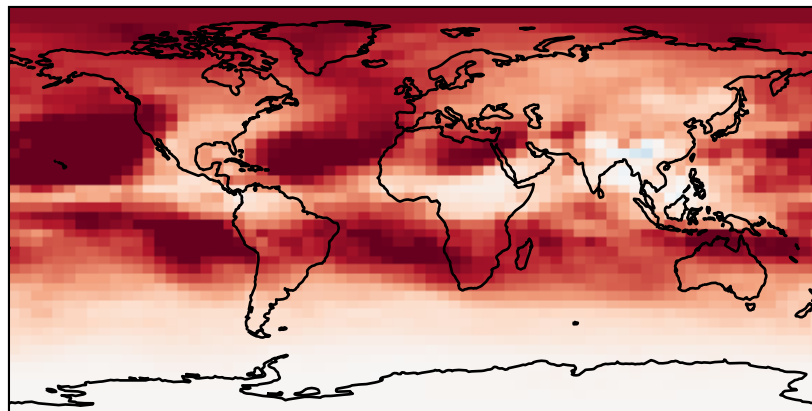
GCC\_13.4.0-rc.3 (Dev)  
4.0x5.0



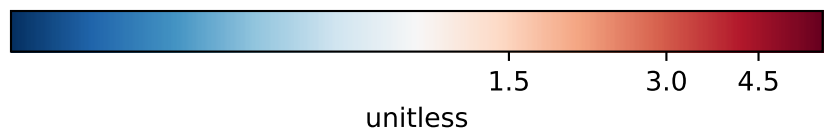
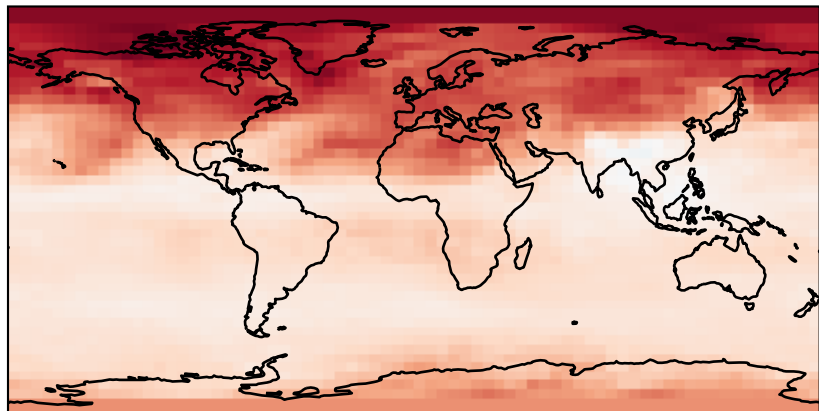
Difference  
Dev - Ref, Dynamic Range



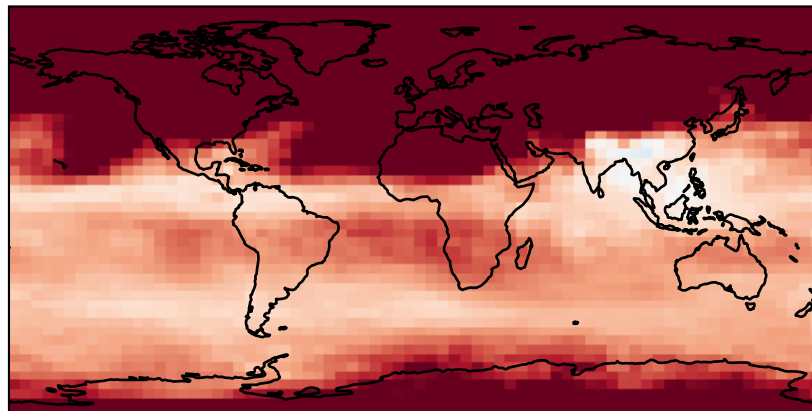
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

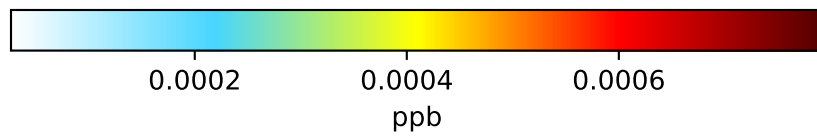
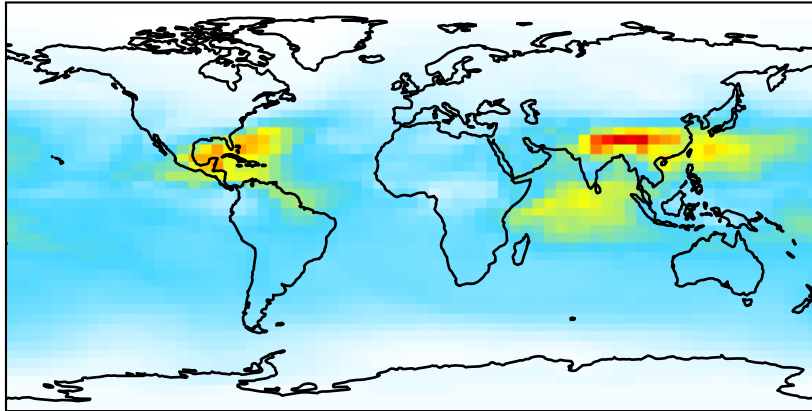


Ratio  
Dev/Ref, Fixed Range

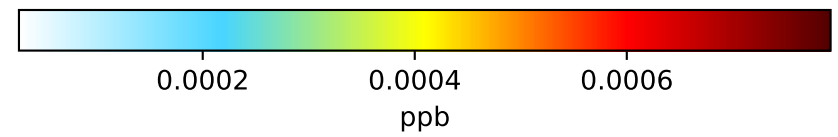
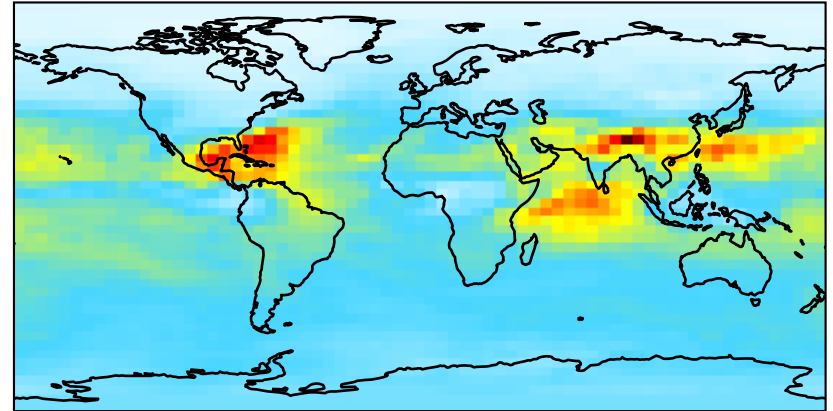


# SpeciesConc\_HOI (Jul2019)

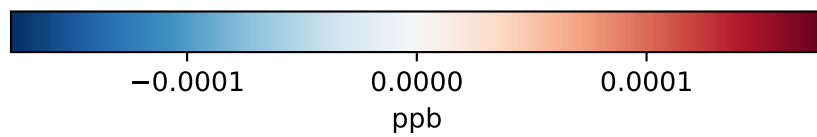
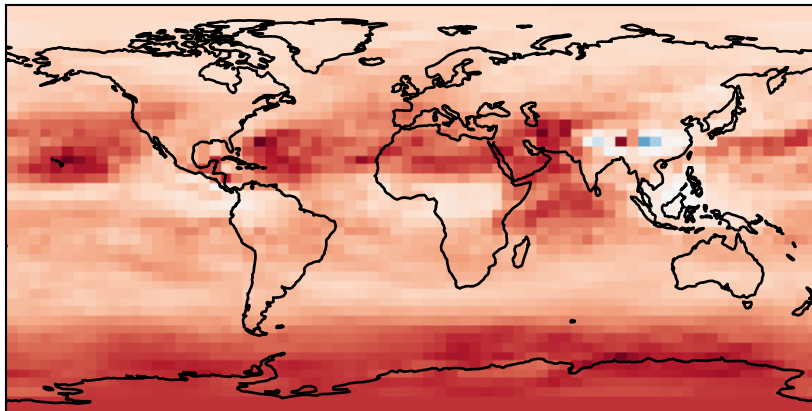
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



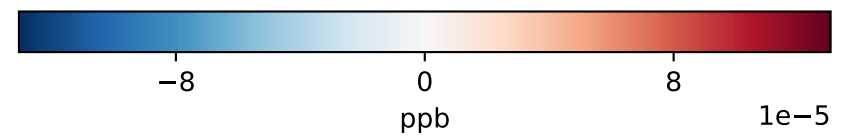
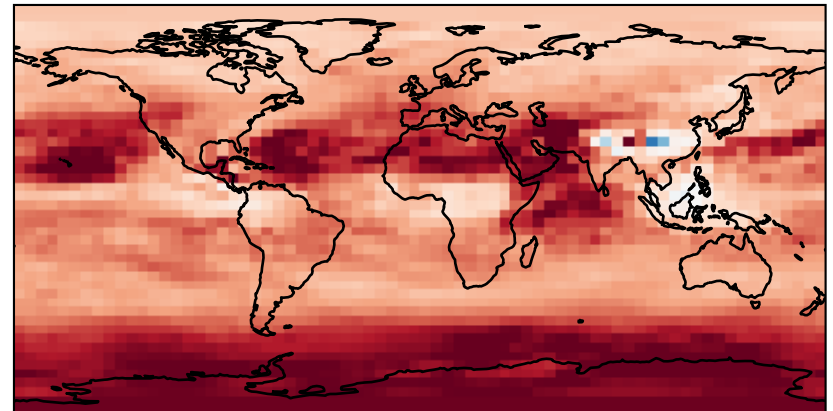
GCC\_13.4.0-rc.3 (Dev)  
4.0x5.0



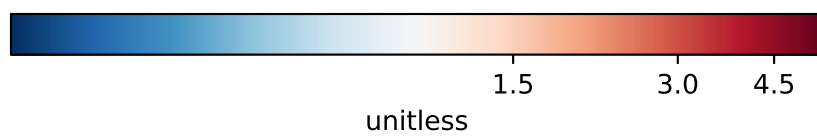
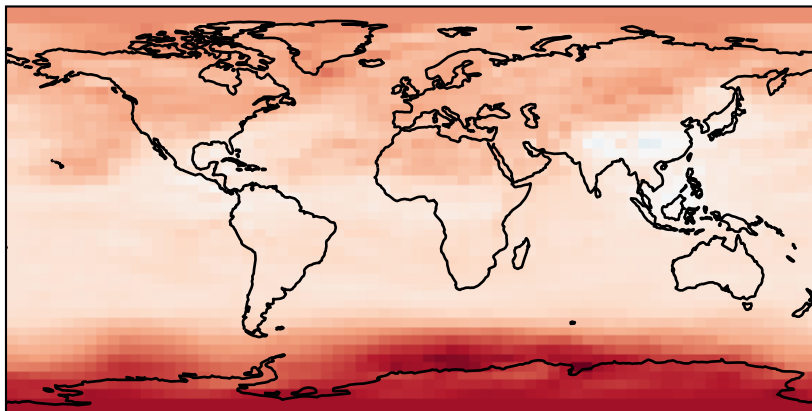
Difference  
Dev - Ref, Dynamic Range



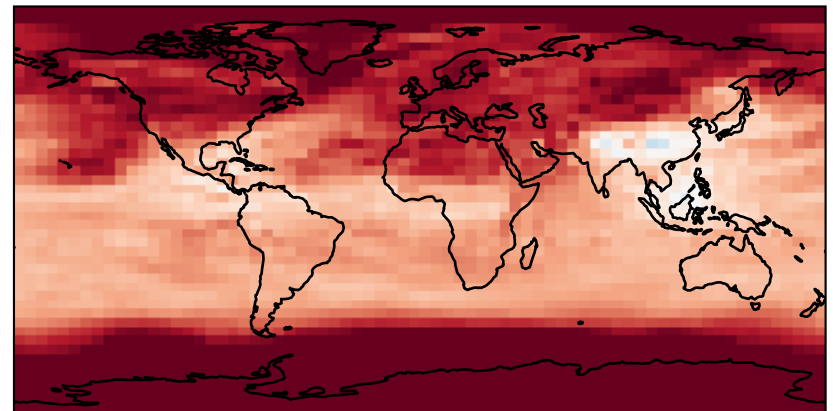
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

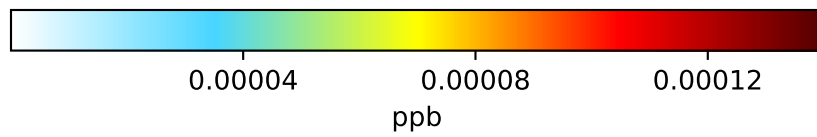
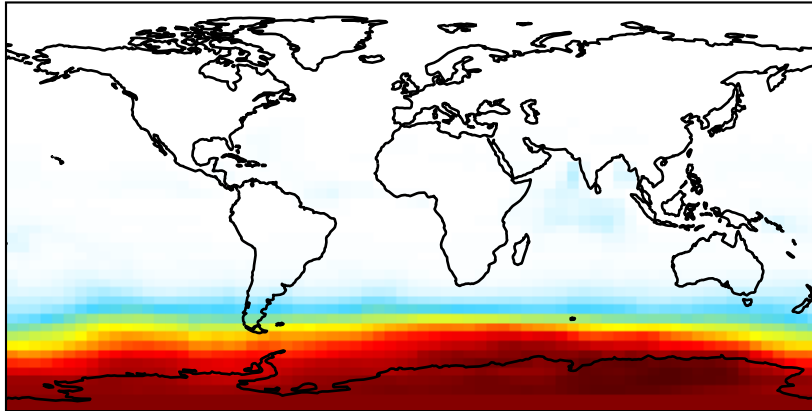


Ratio  
Dev/Ref, Fixed Range

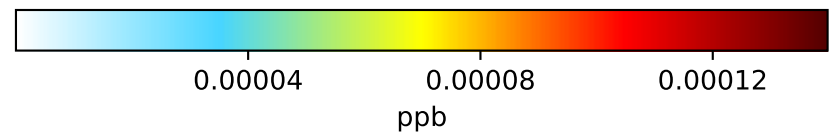
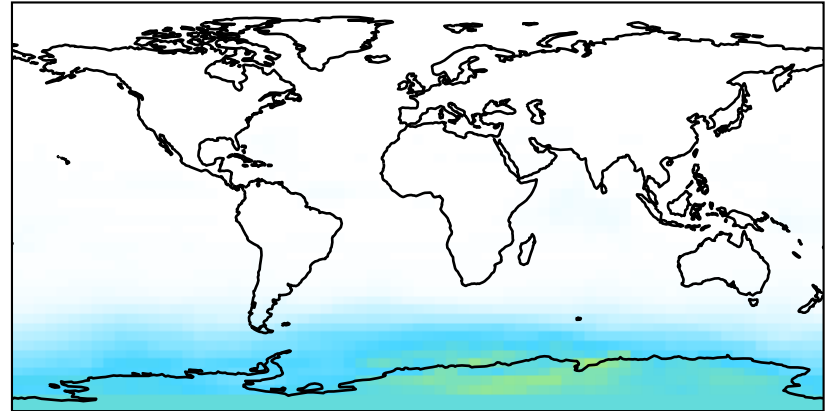


# SpeciesConc\_OIO (Jul2019)

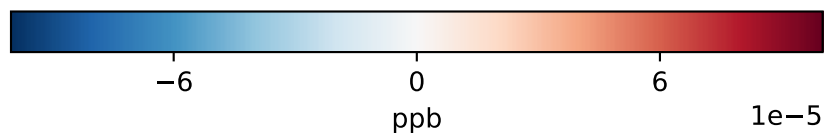
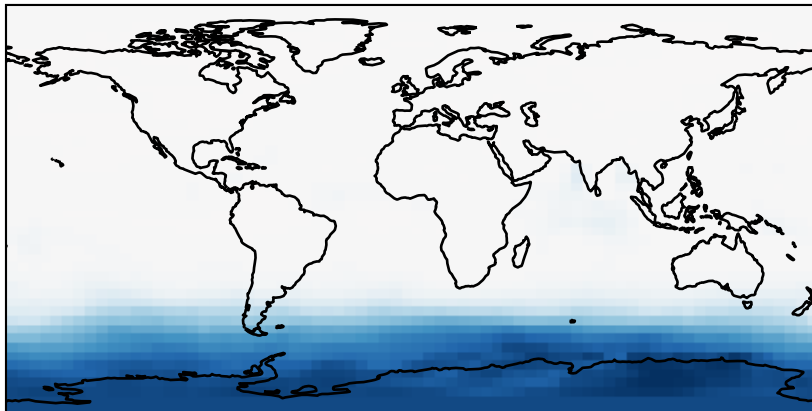
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



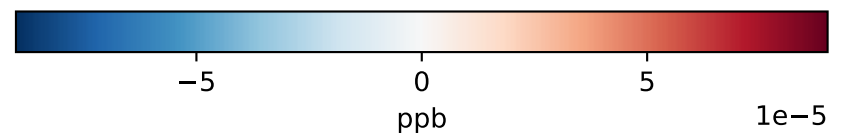
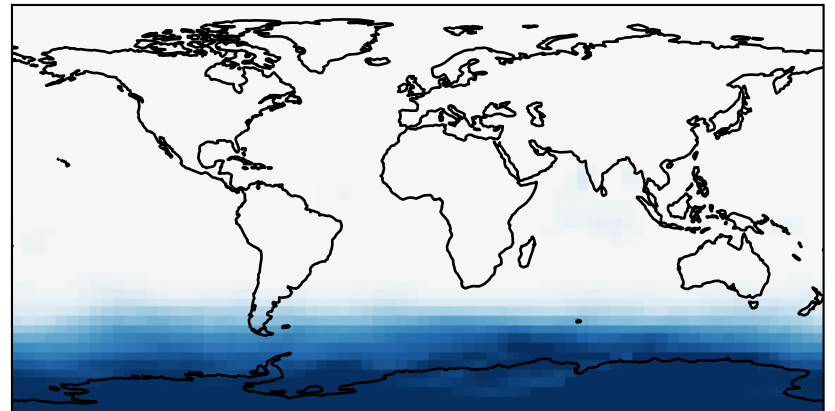
GCC\_13.4.0-rc.3 (Dev)  
4.0x5.0



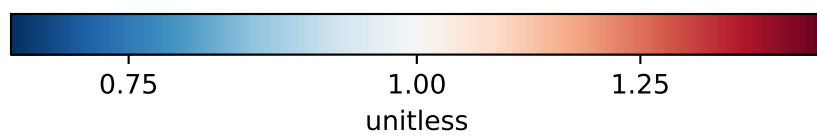
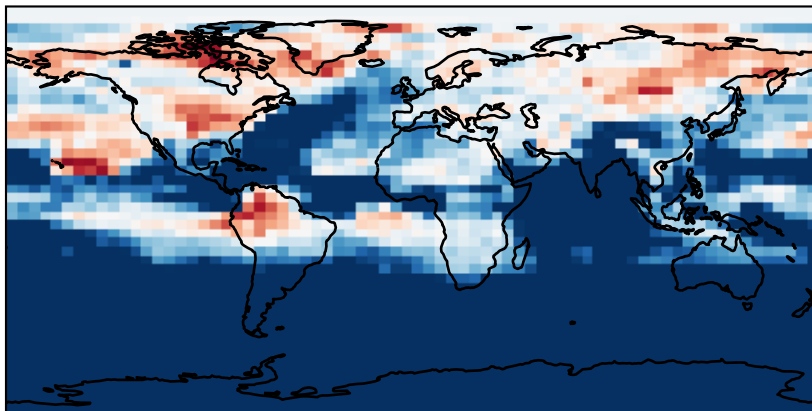
Difference  
Dev - Ref, Dynamic Range



Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

