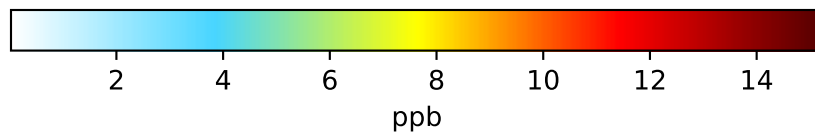
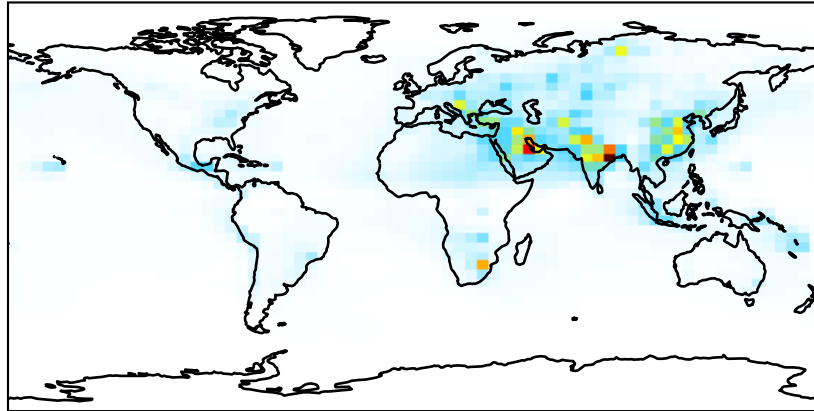
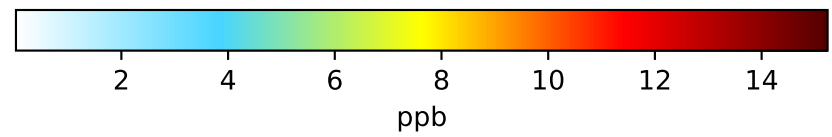
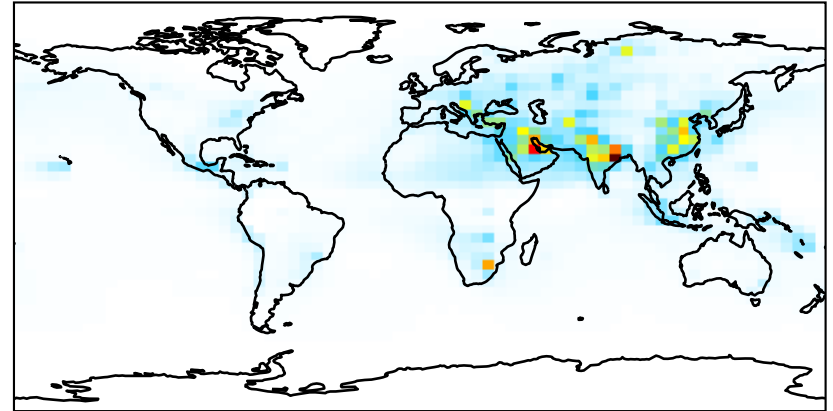


SpeciesConc_SOx (Oct2019)

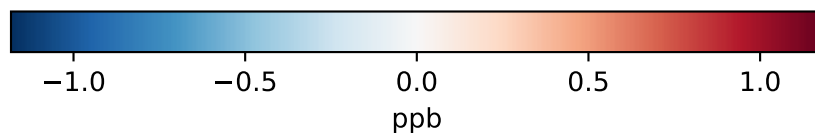
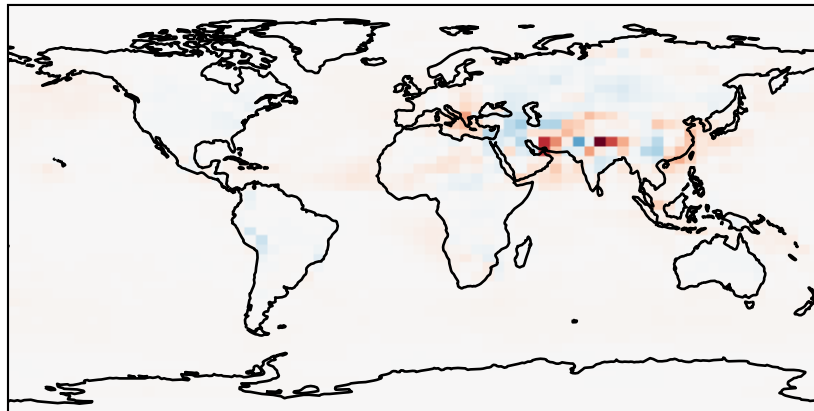
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



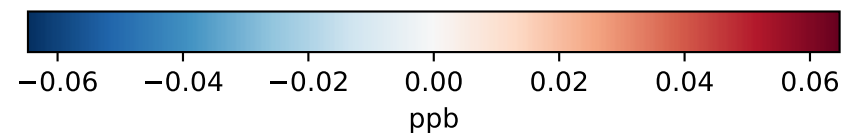
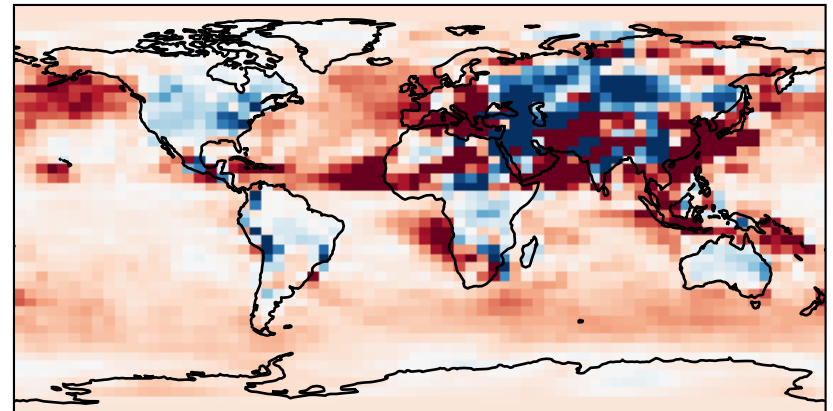
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



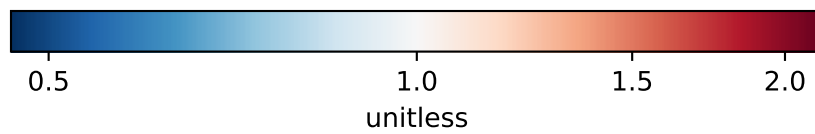
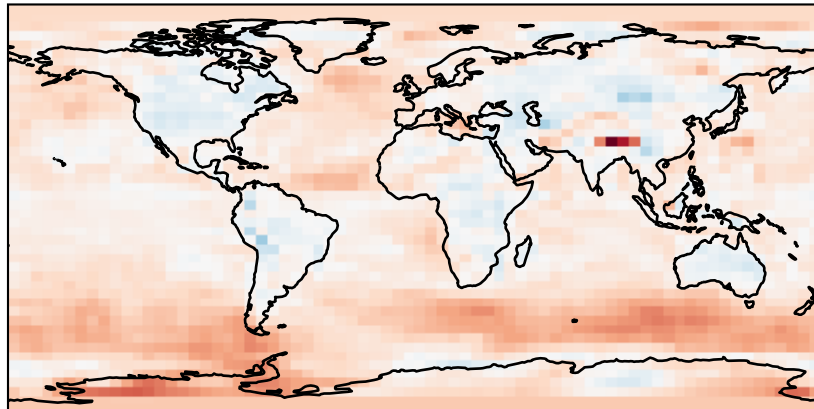
Difference
Dev - Ref, Dynamic Range



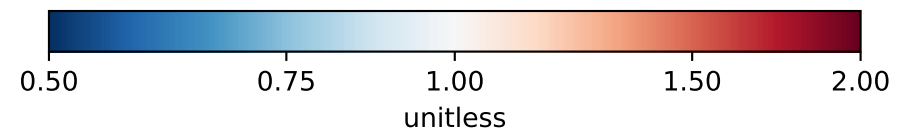
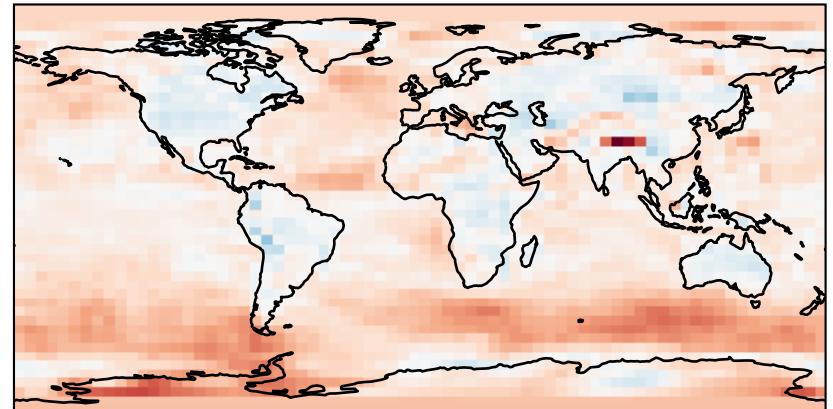
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

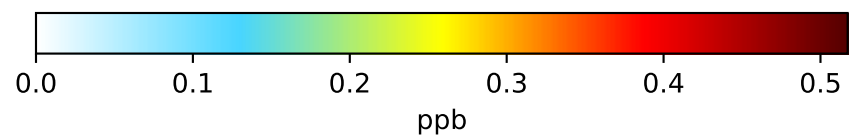
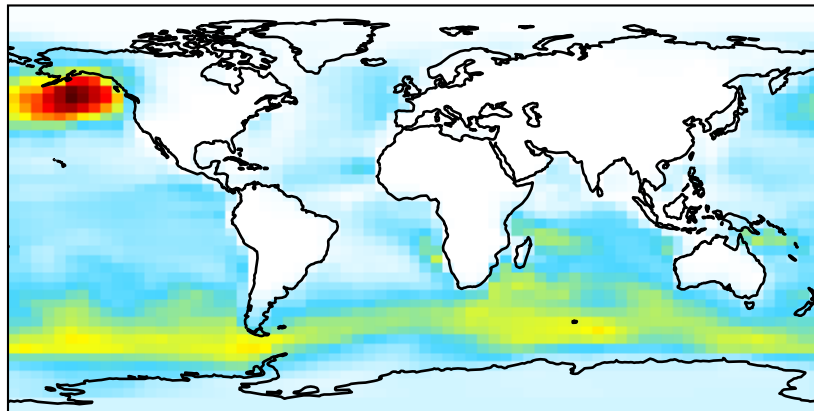


Ratio
Dev/Ref, Fixed Range

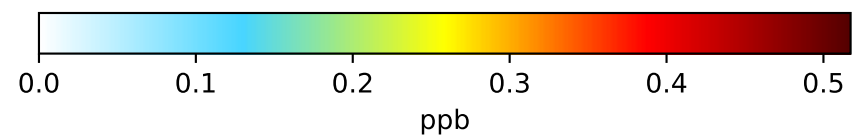
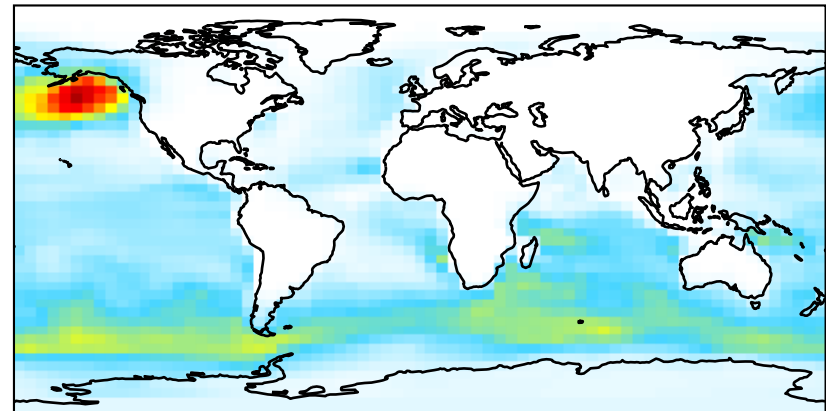


SpeciesConc_DMS (Oct2019)

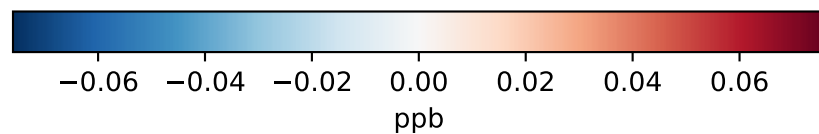
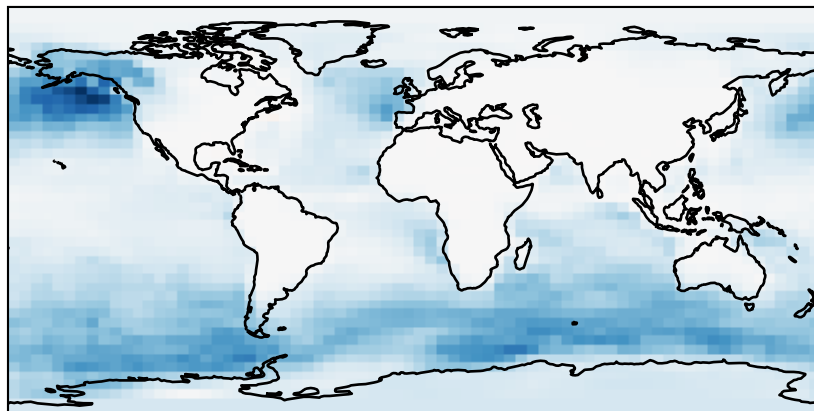
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



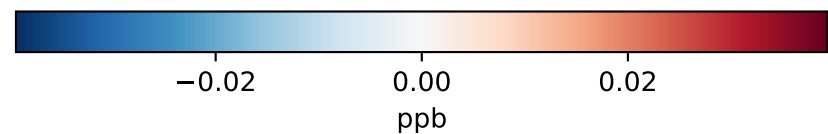
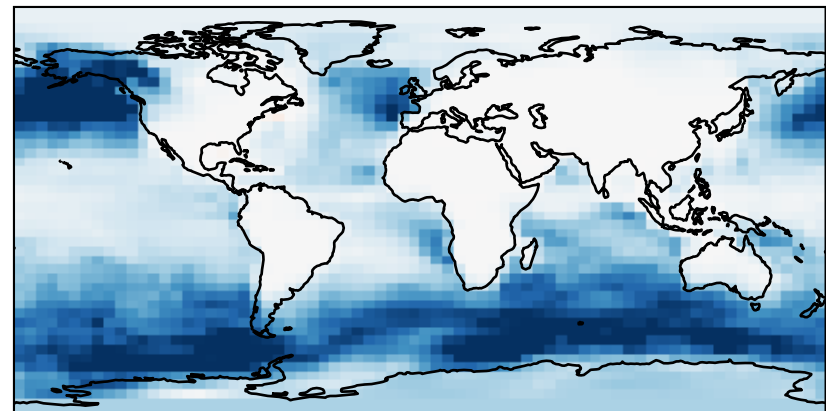
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



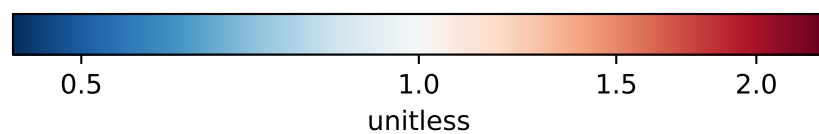
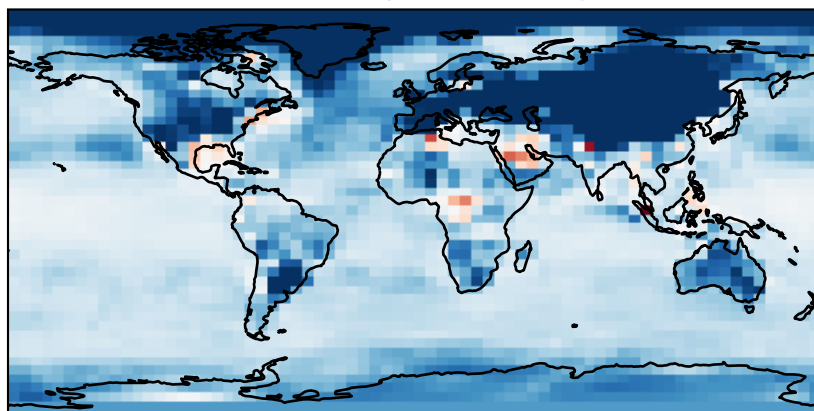
Difference
Dev - Ref, Dynamic Range



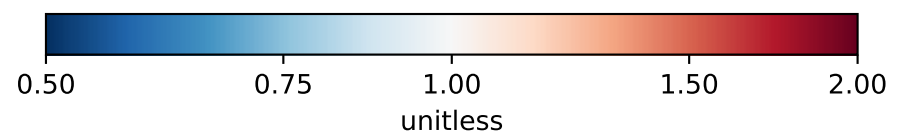
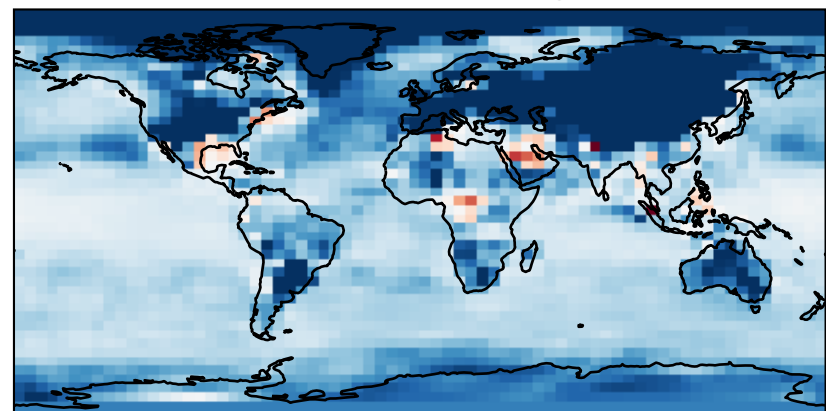
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

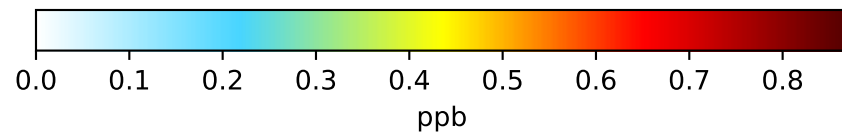


Ratio
Dev/Ref, Fixed Range

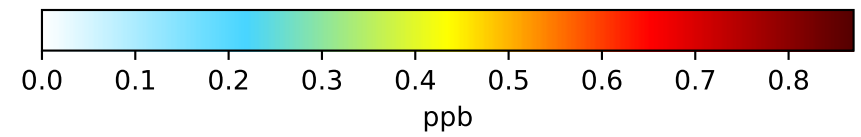
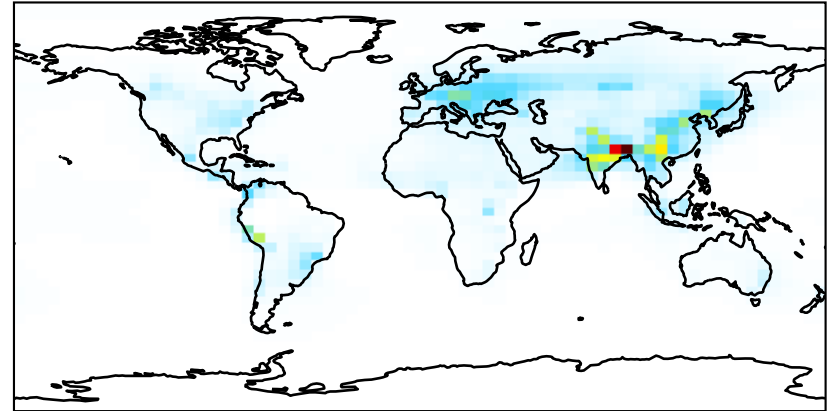


SpeciesConc_HMS (Oct2019)

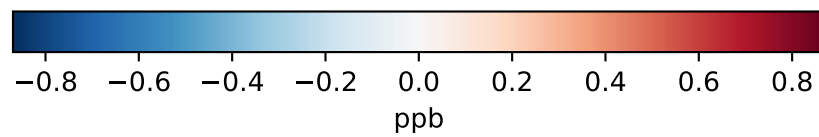
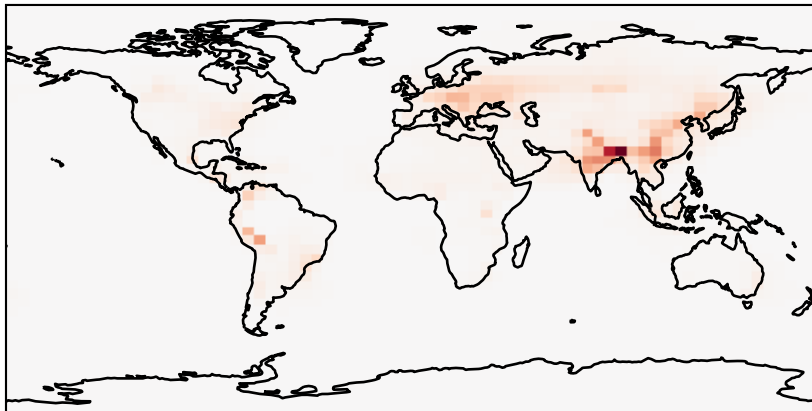
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



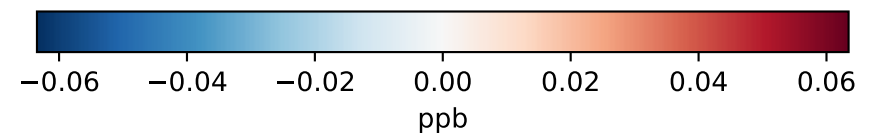
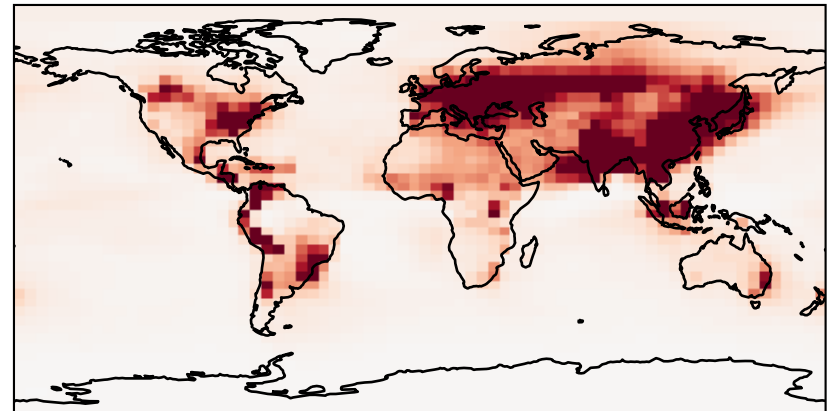
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



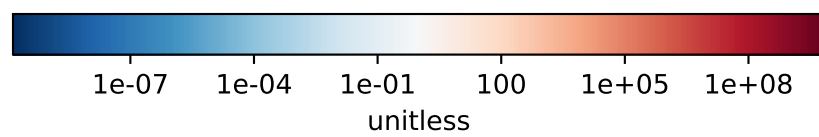
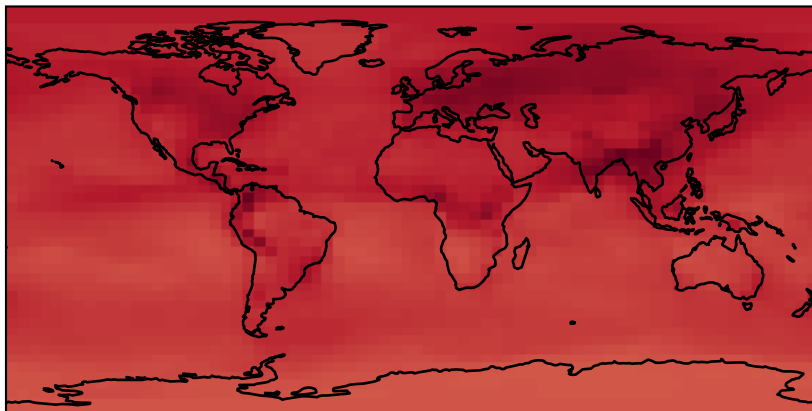
Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

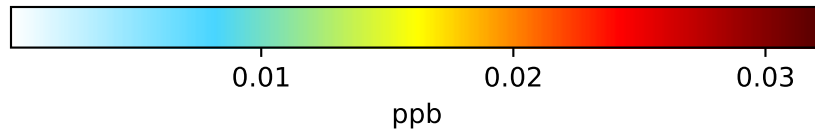
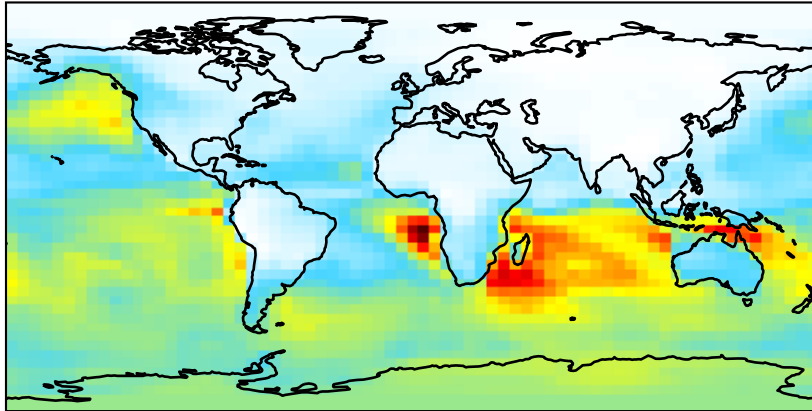


Ratio
Dev/Ref, Fixed Range

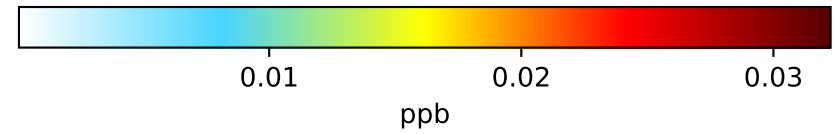
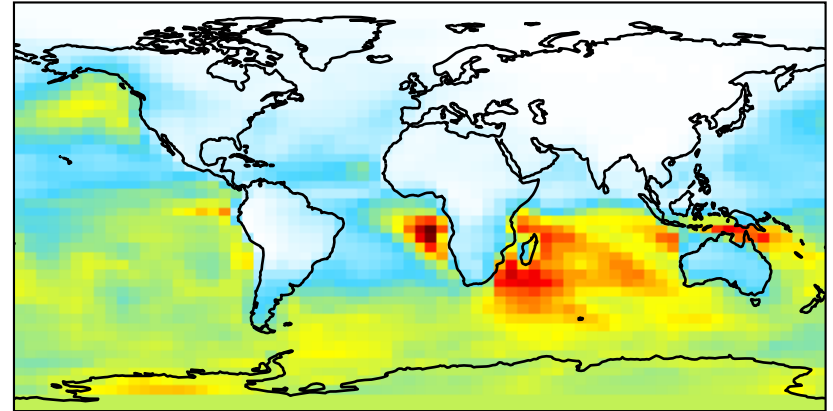


SpeciesConc_MSA (Oct2019)

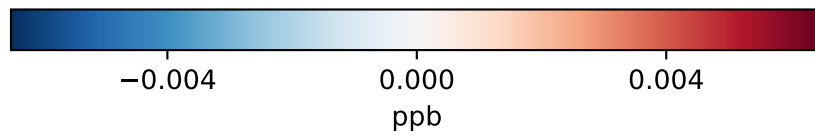
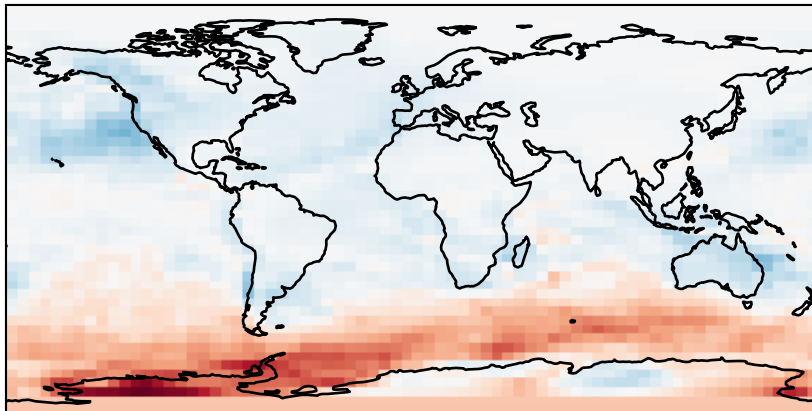
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



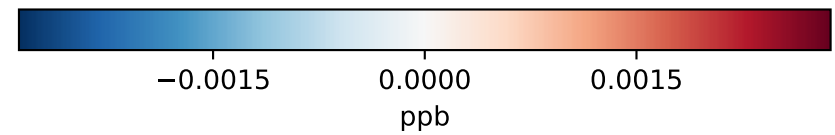
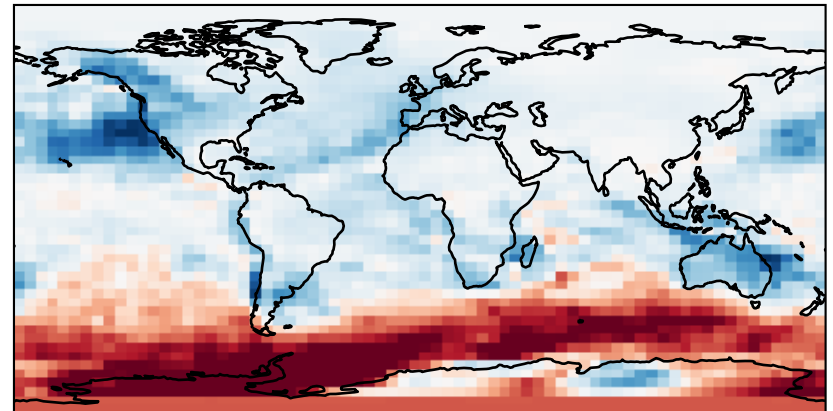
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



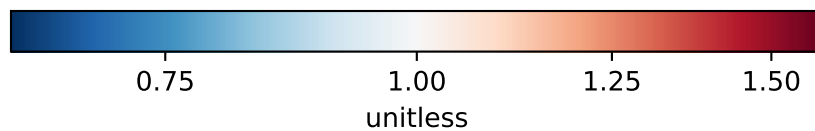
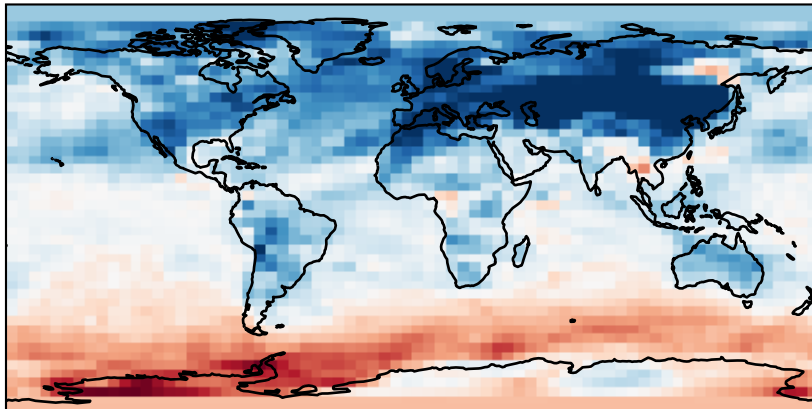
Difference
Dev - Ref, Dynamic Range



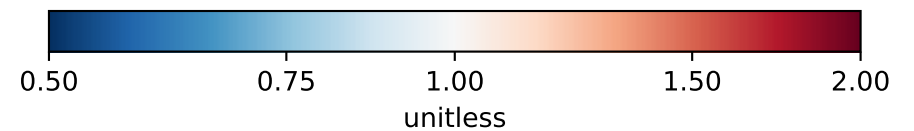
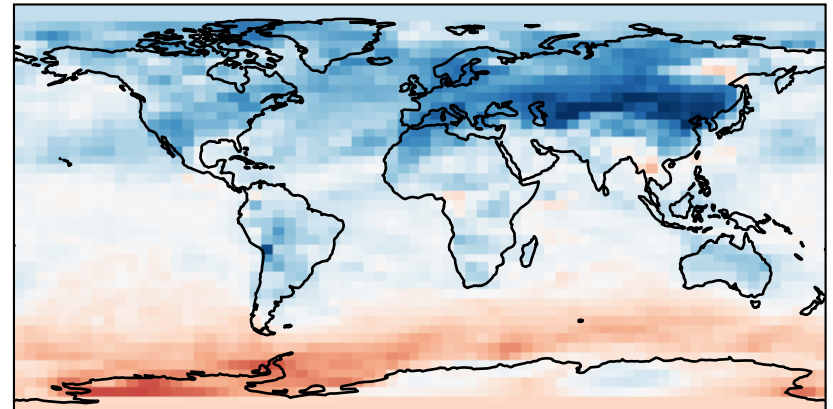
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

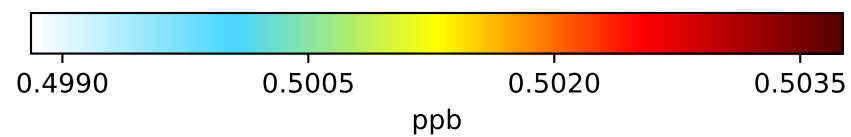
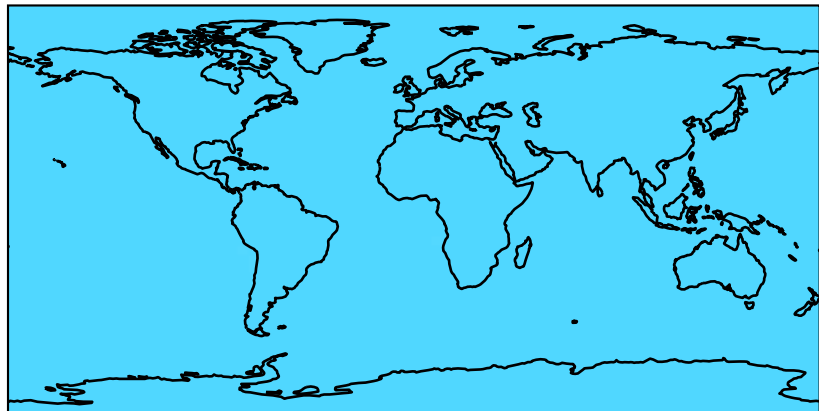


Ratio
Dev/Ref, Fixed Range

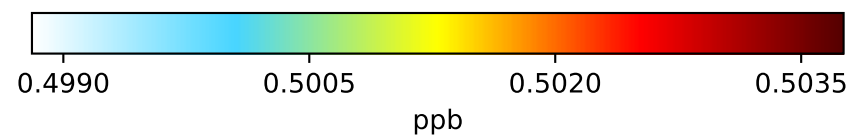
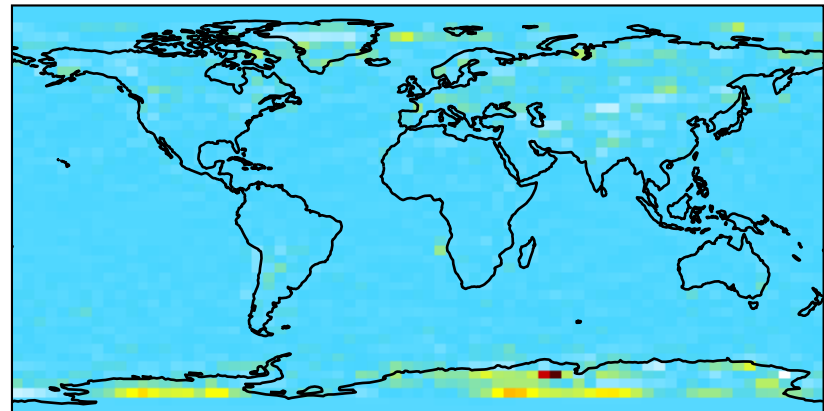


SpeciesConc_OCS (Oct2019)

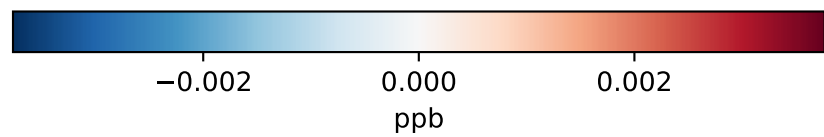
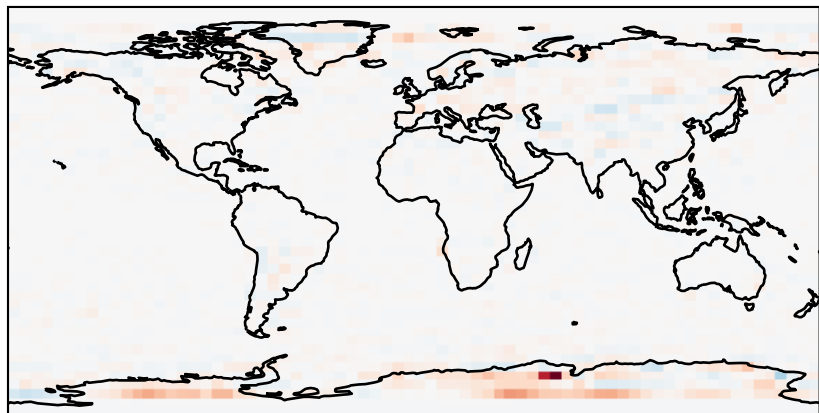
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



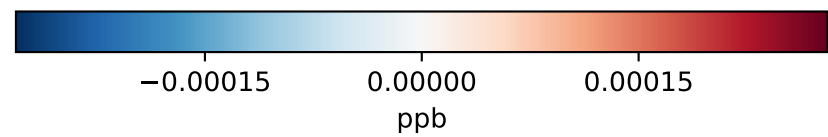
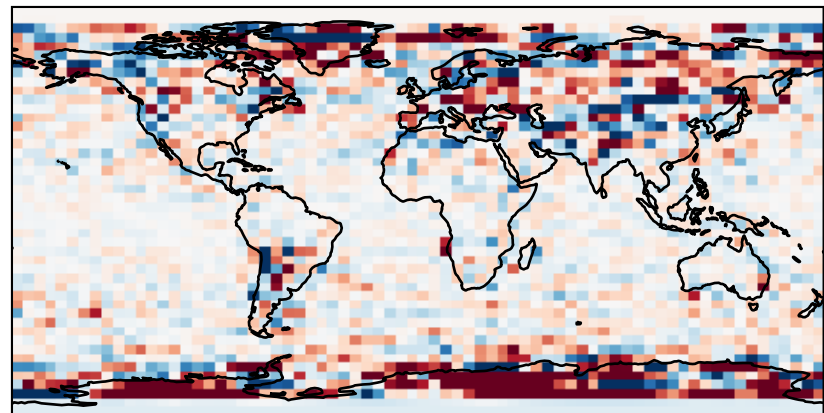
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



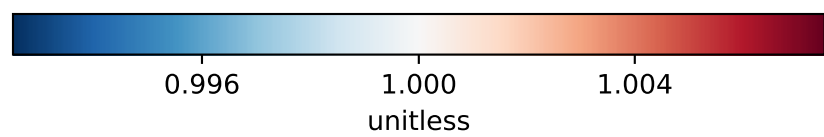
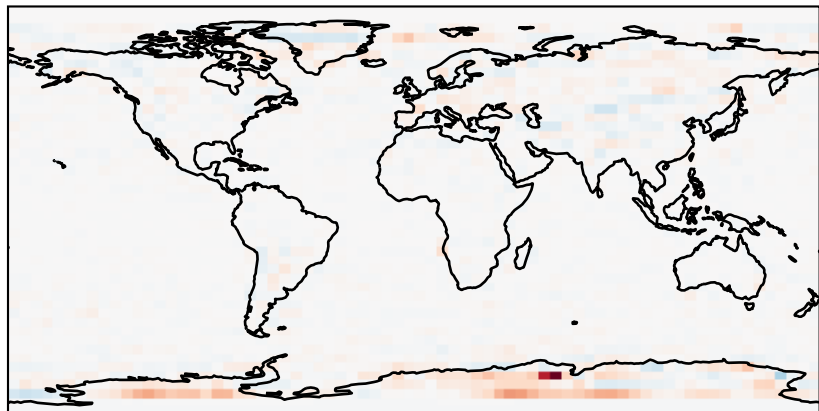
Difference
Dev - Ref, Dynamic Range



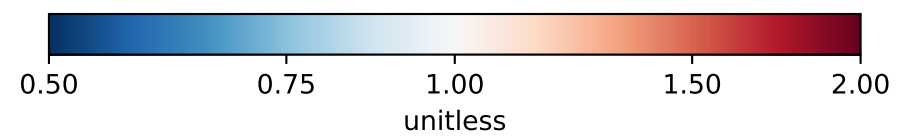
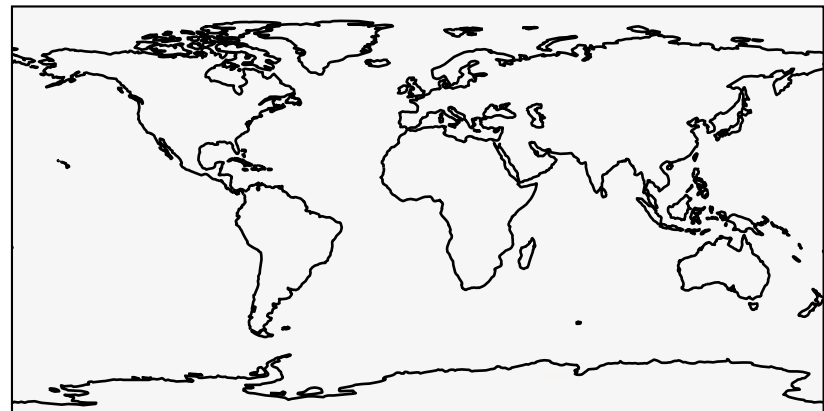
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

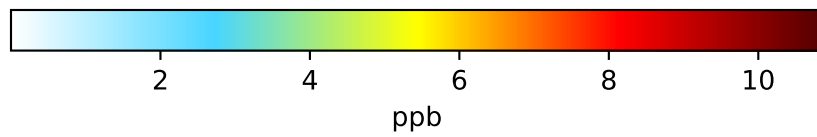
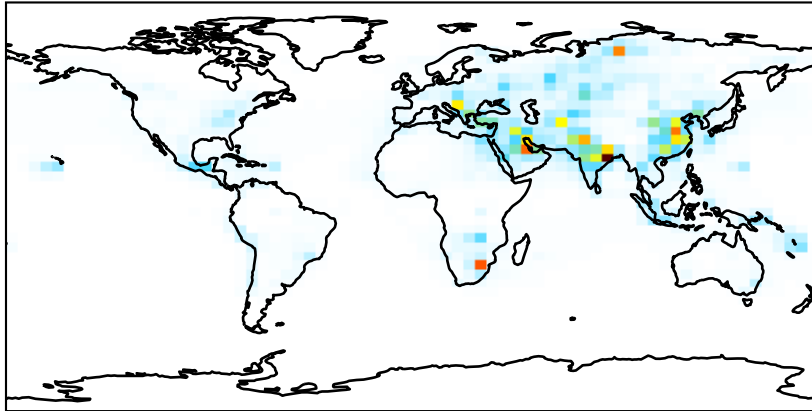


Ratio
Dev/Ref, Fixed Range

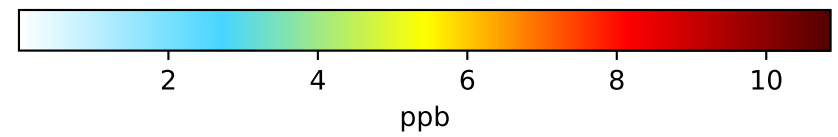
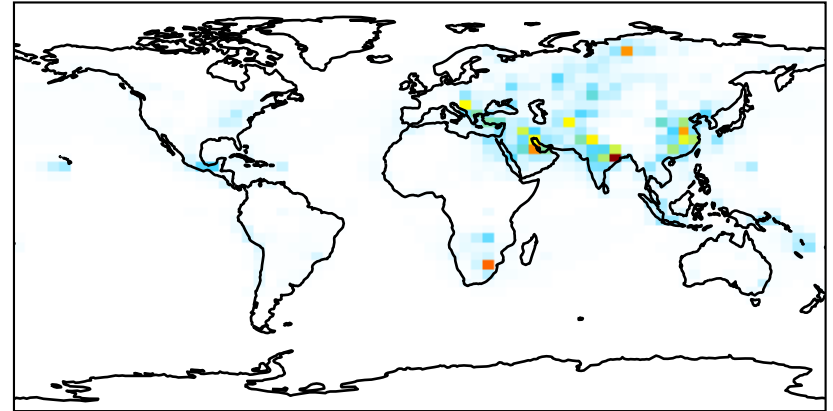


SpeciesConc_SO2 (Oct2019)

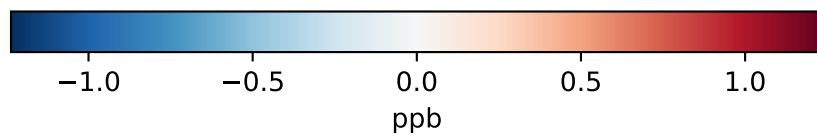
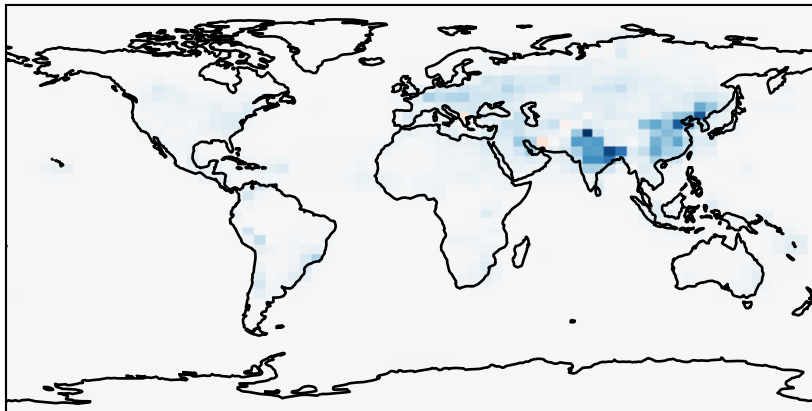
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



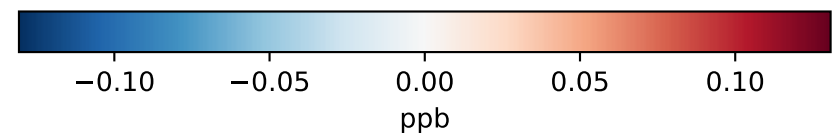
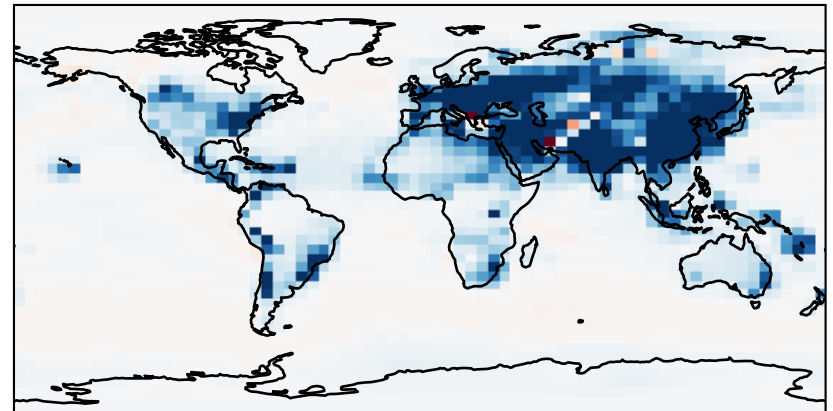
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



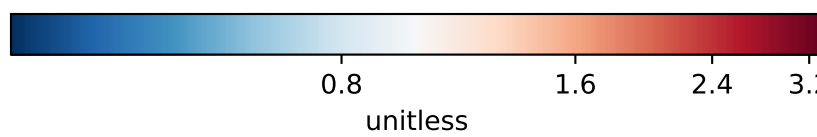
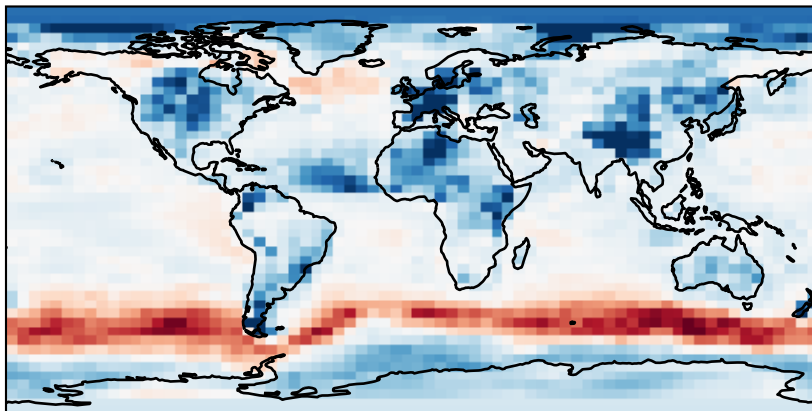
Difference
Dev - Ref, Dynamic Range



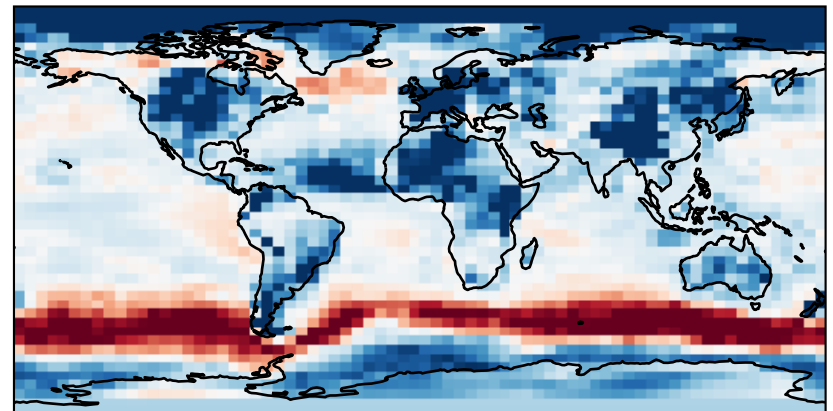
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

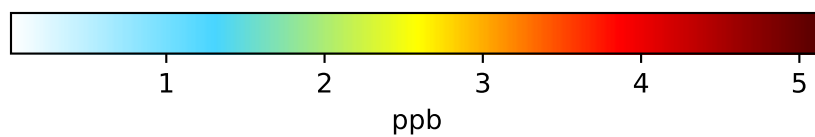
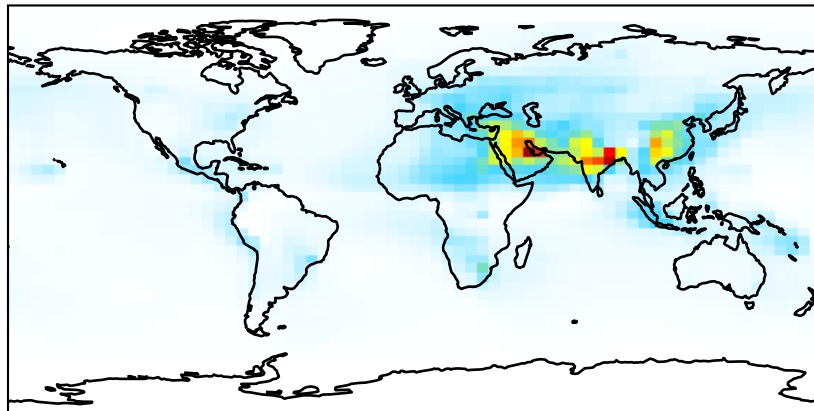


Ratio
Dev/Ref, Fixed Range

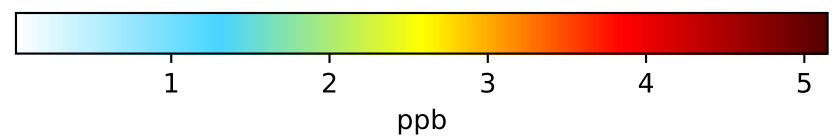
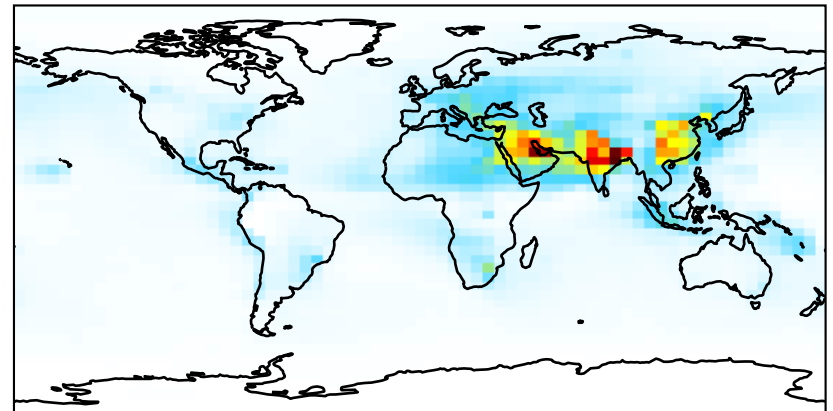


SpeciesConc_S04 (Oct2019)

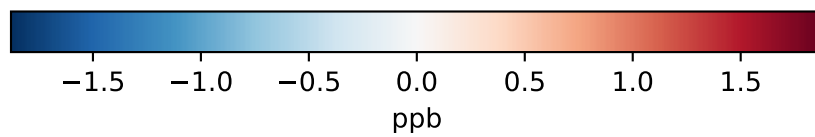
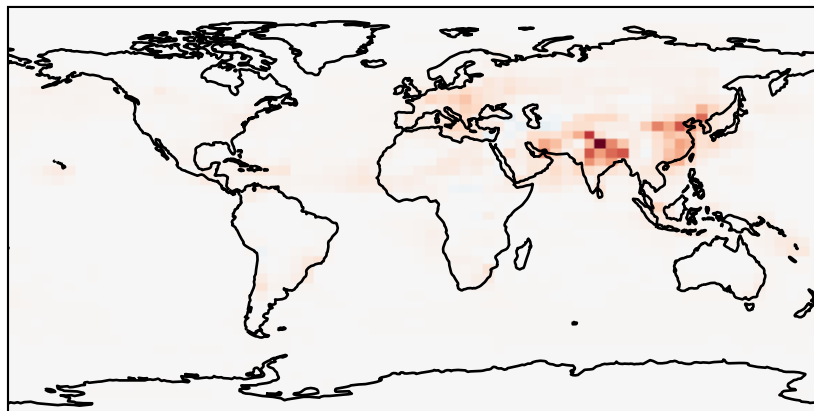
GCC_13.3.0-rc.0 (Ref)
4.0x5.0



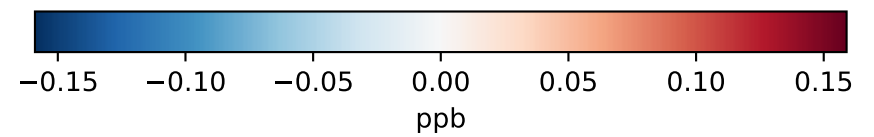
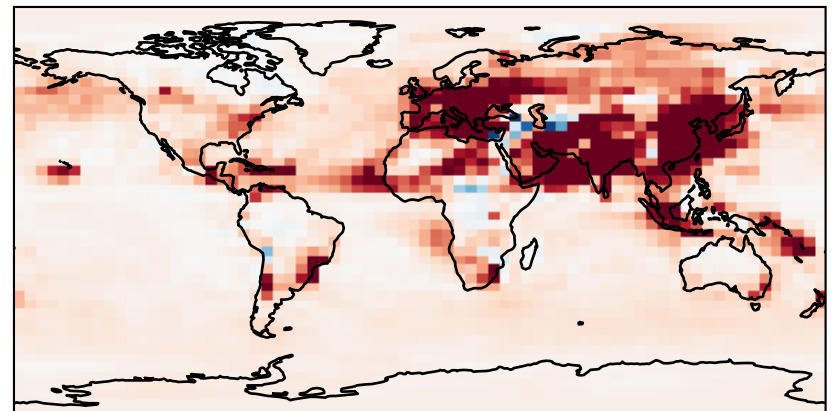
GCC_13.4.0-rc.3 (Dev)
4.0x5.0



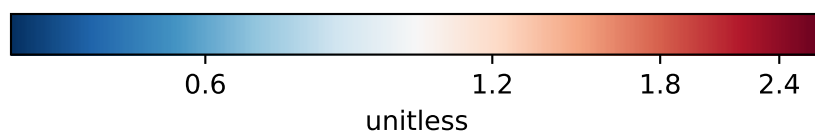
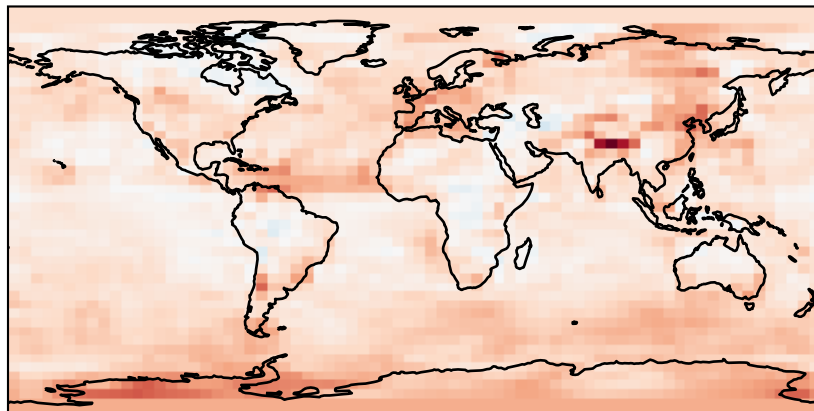
Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range



Ratio
Dev/Ref, Fixed Range

