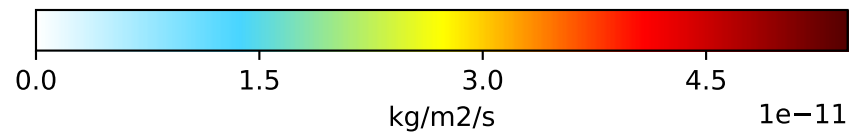
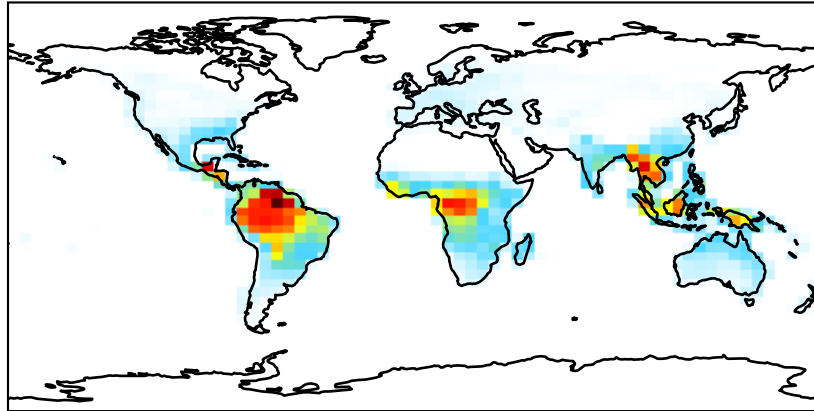
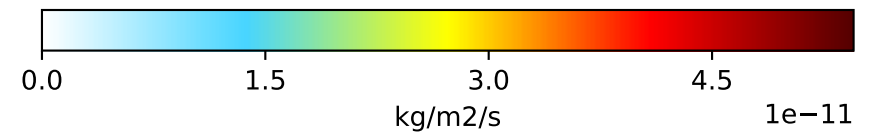
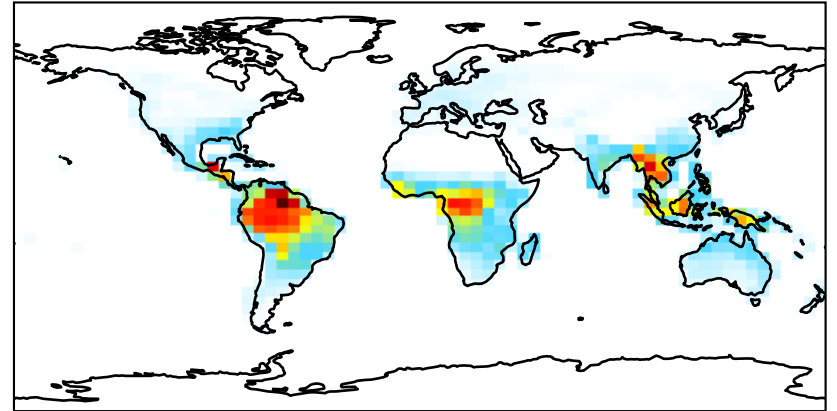


# EmisACET\_Biogenic (Apr2019)

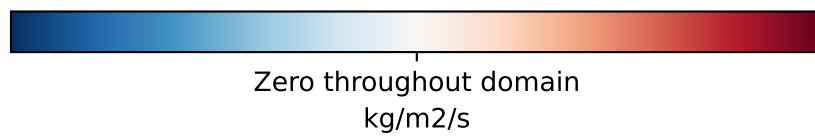
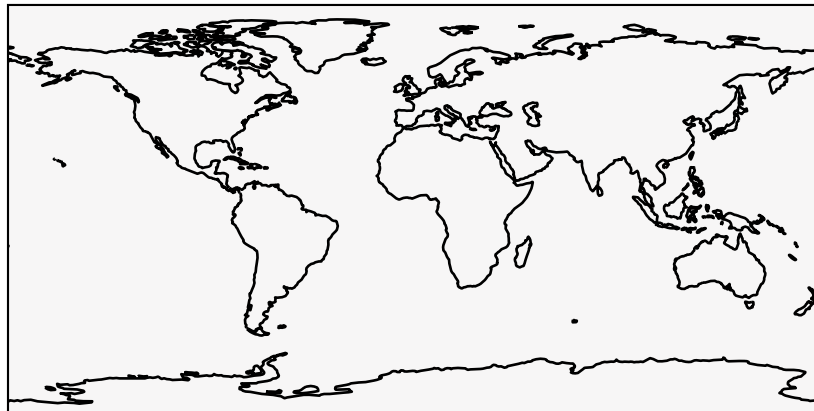
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



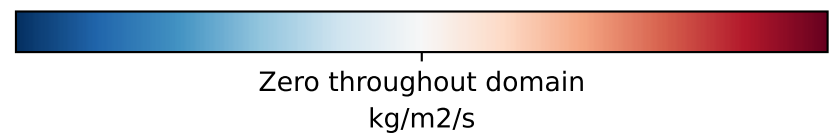
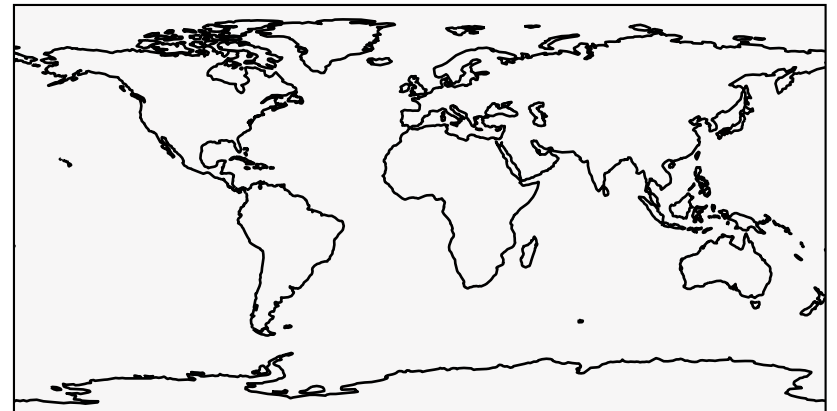
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



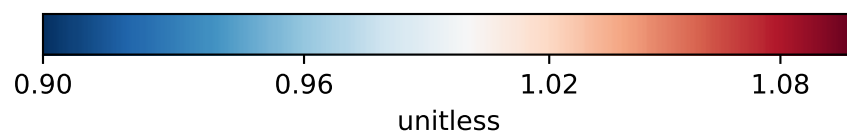
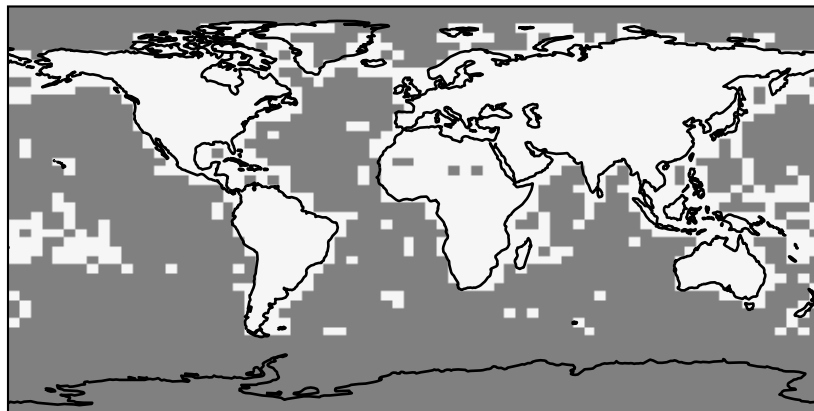
Difference  
Dev - Ref, Dynamic Range



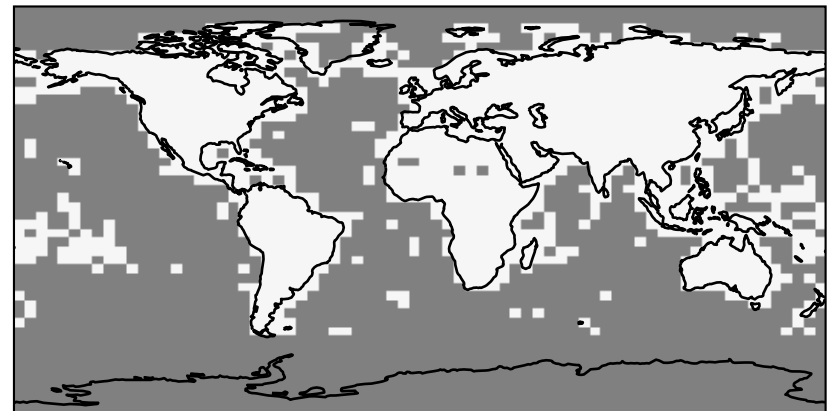
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

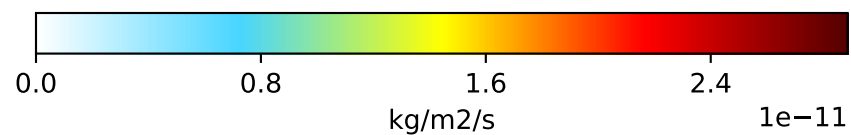
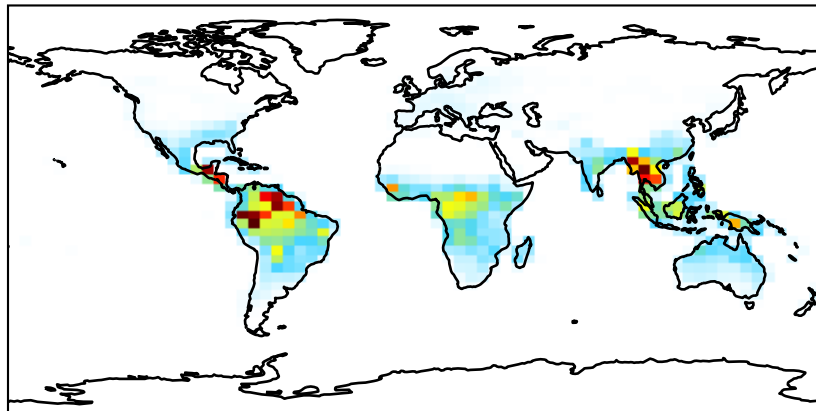


Ratio  
Dev/Ref, Fixed Range

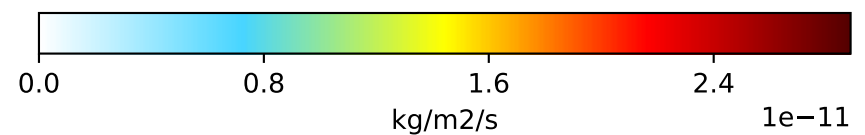
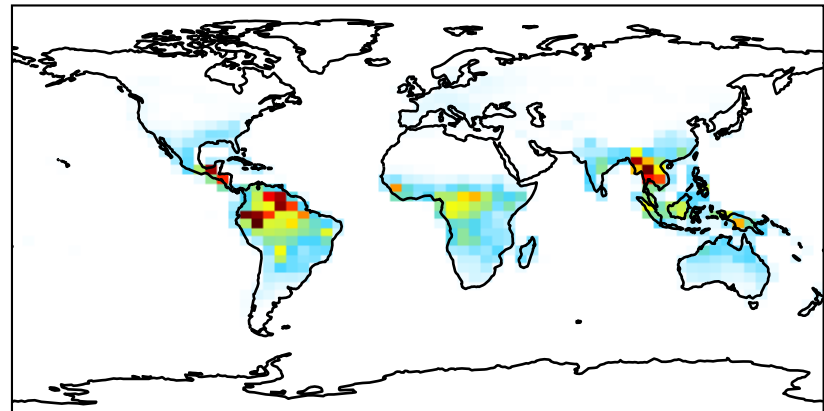


# EmisALD2\_Biogenic (Apr2019)

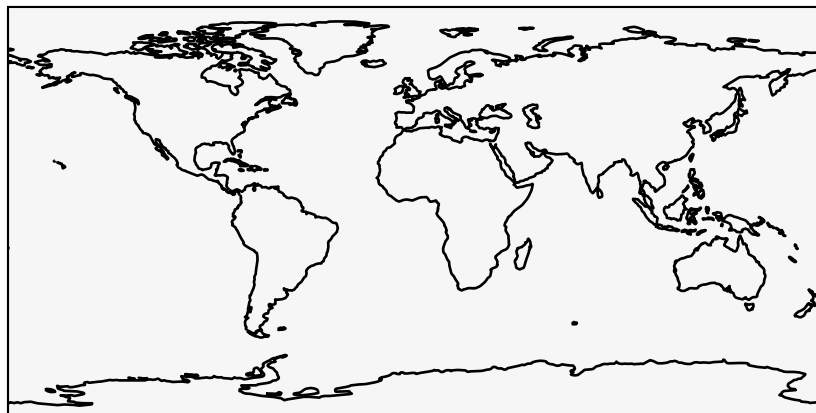
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



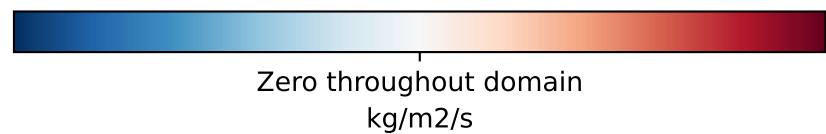
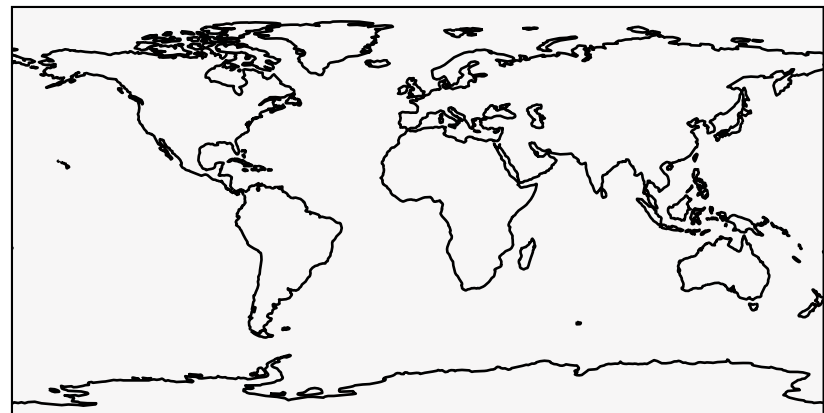
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



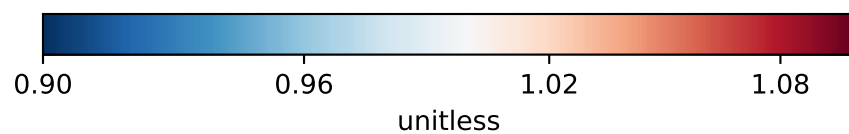
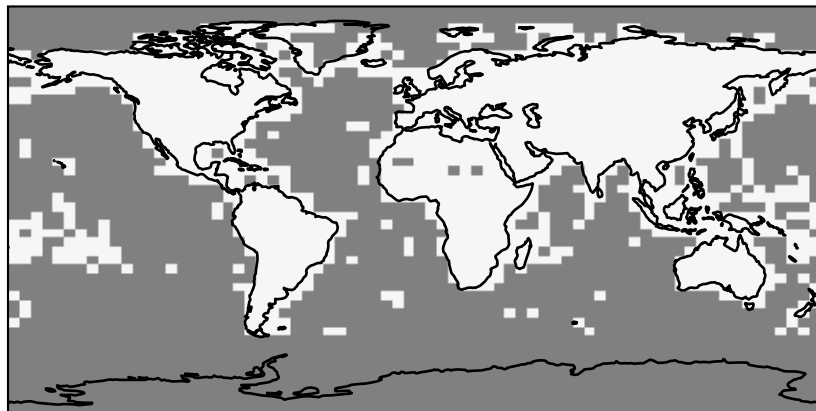
Difference  
Dev - Ref, Dynamic Range



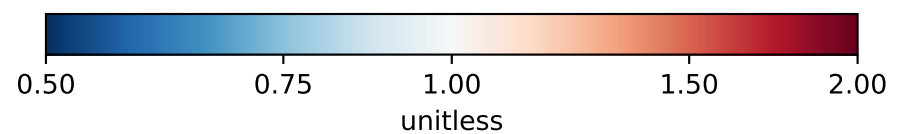
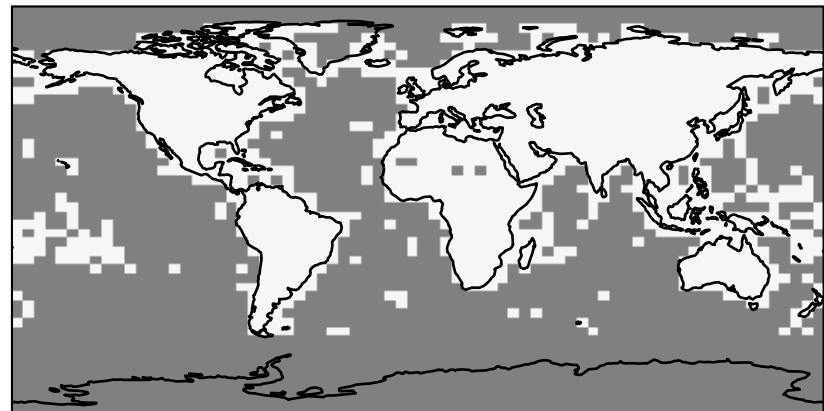
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

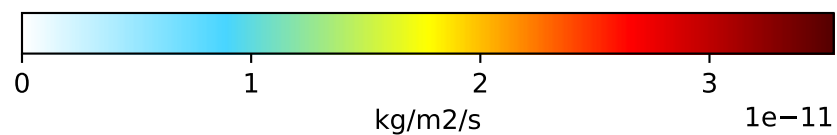
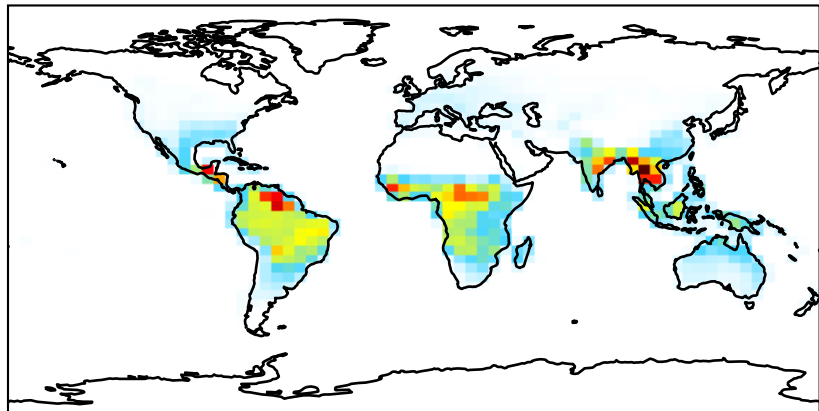


Ratio  
Dev/Ref, Fixed Range

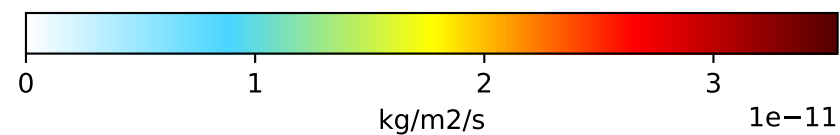
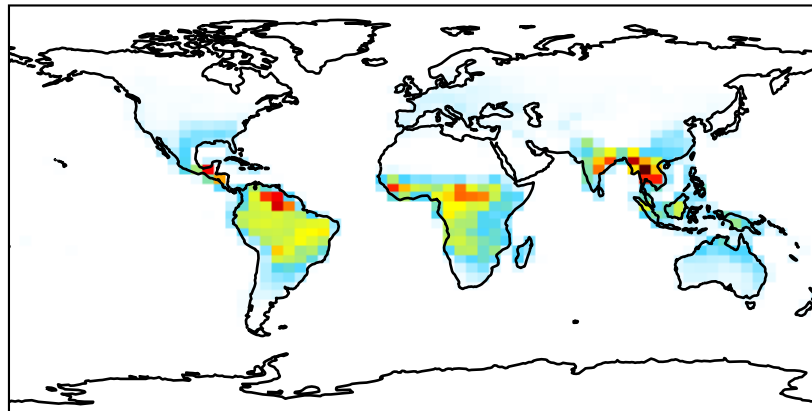


# EmisC2H4\_Biogenic (Apr2019)

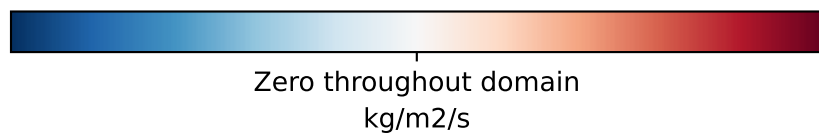
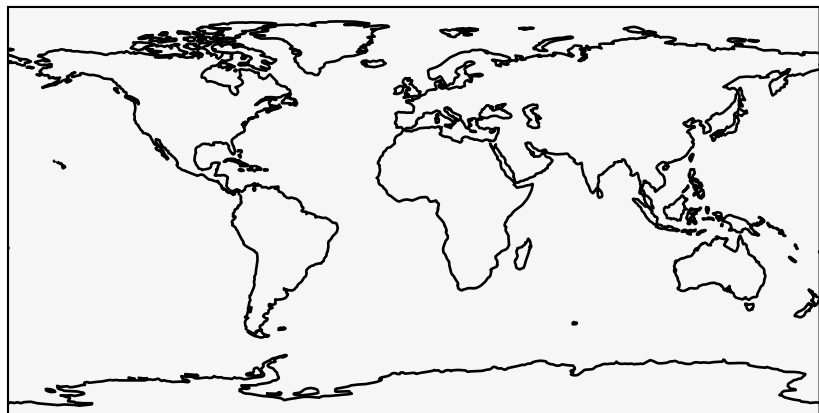
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



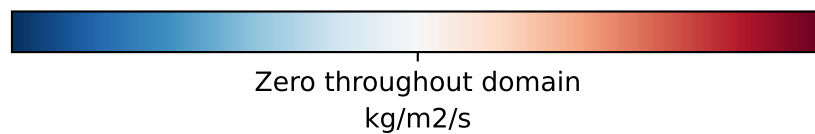
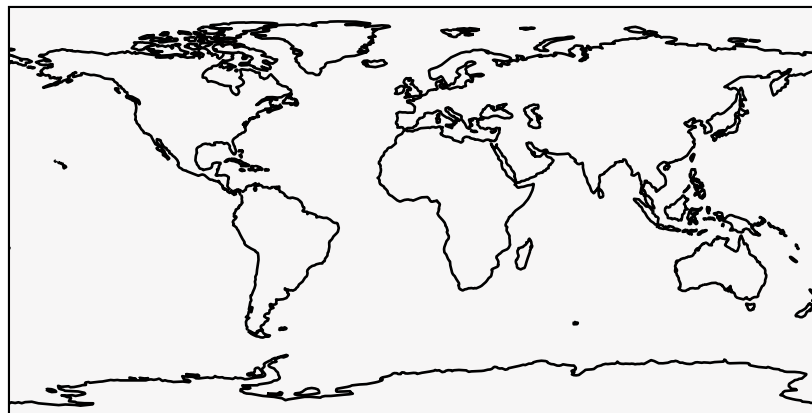
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



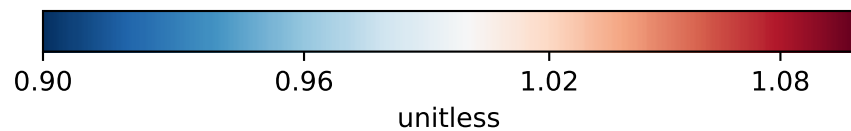
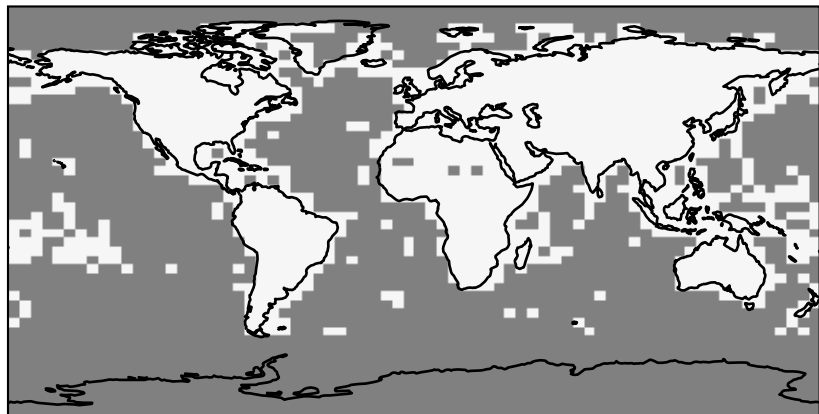
Difference  
Dev - Ref, Dynamic Range



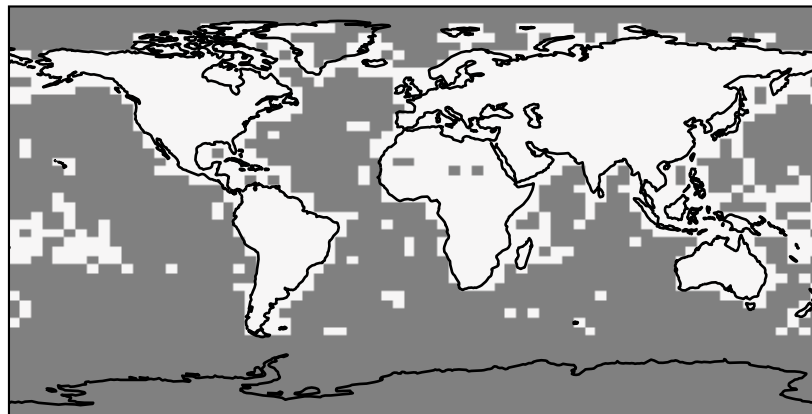
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

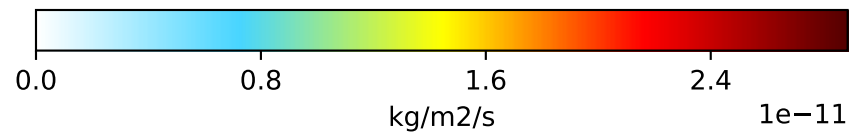
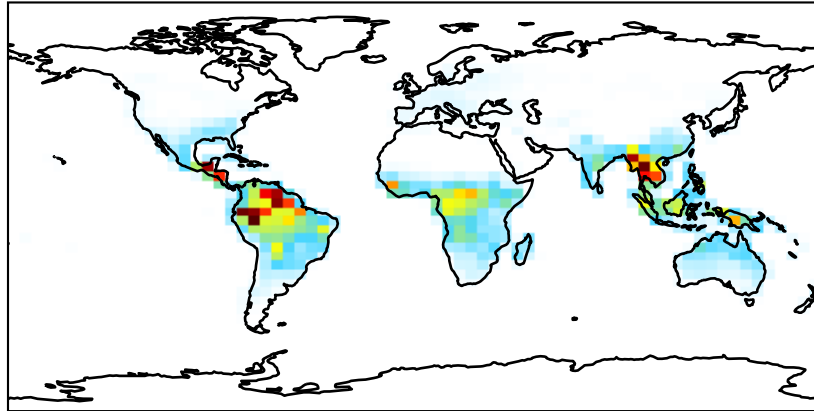


Ratio  
Dev/Ref, Fixed Range

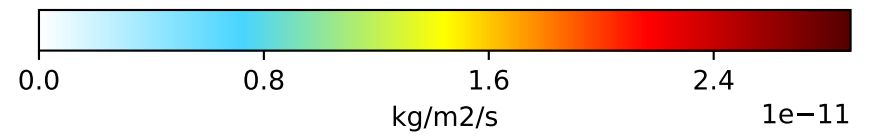
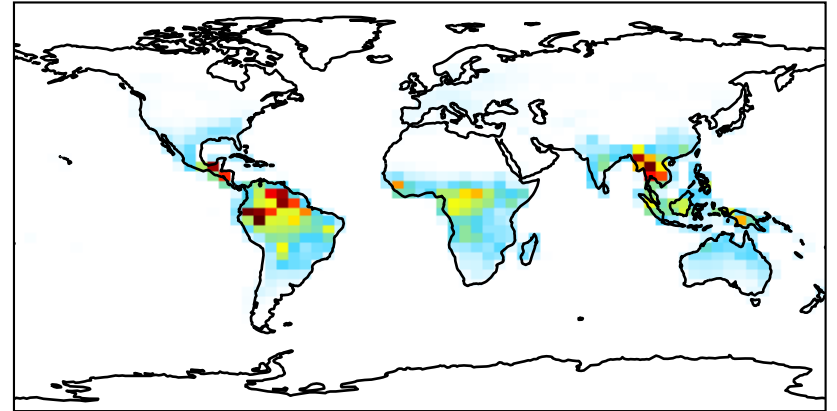


# EmisEOH\_Biogenic (Apr2019)

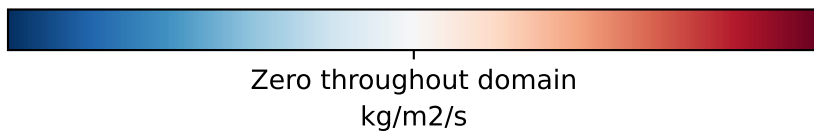
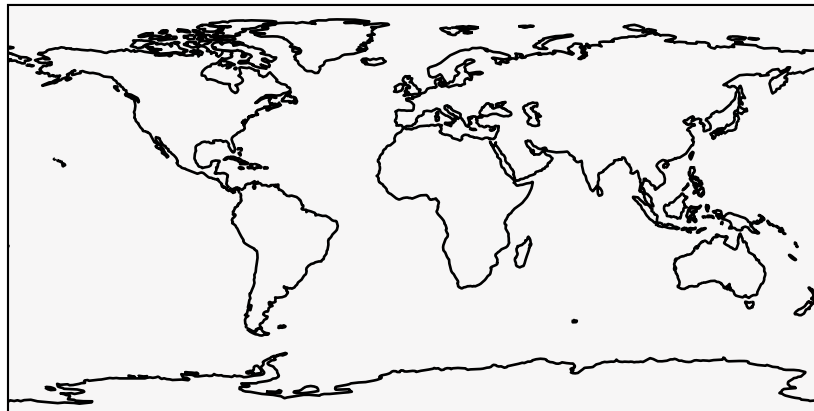
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



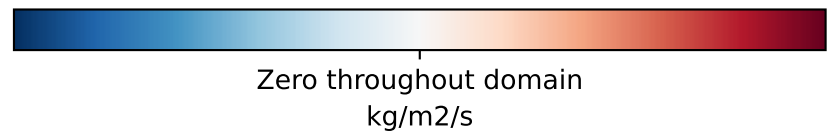
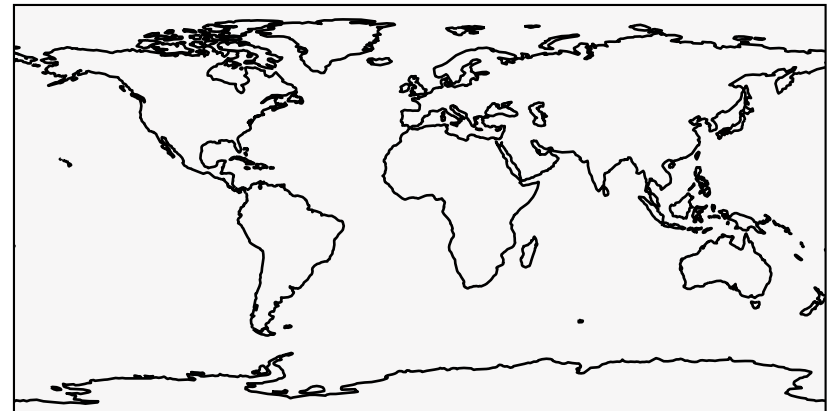
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



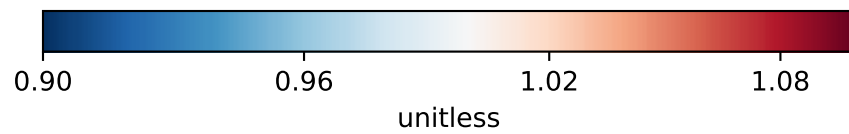
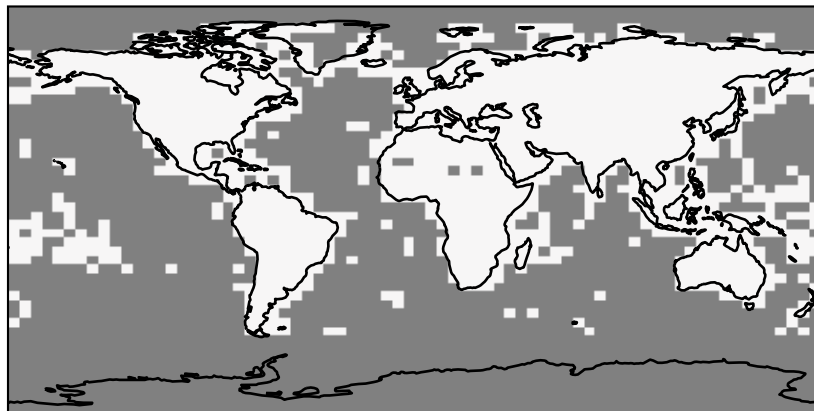
Difference  
Dev - Ref, Dynamic Range



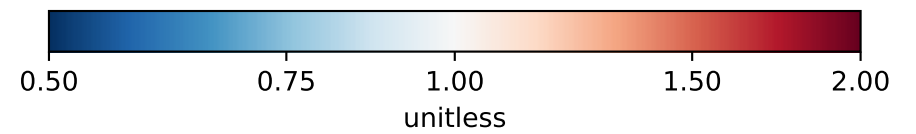
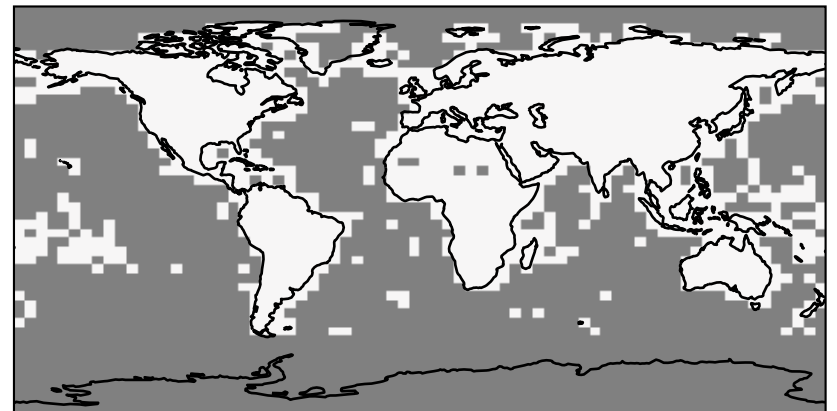
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

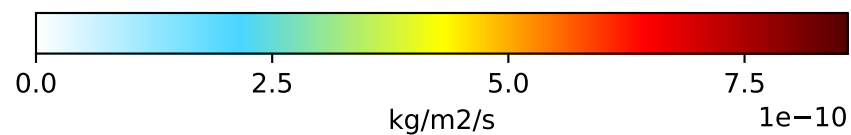
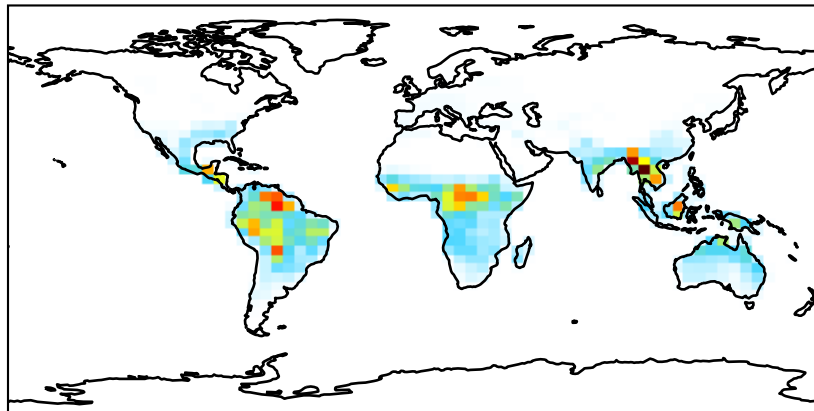


Ratio  
Dev/Ref, Fixed Range

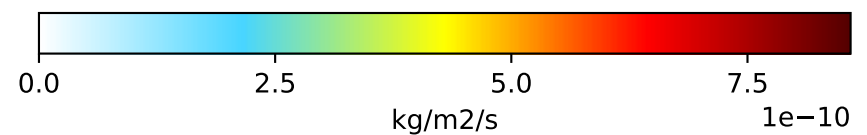
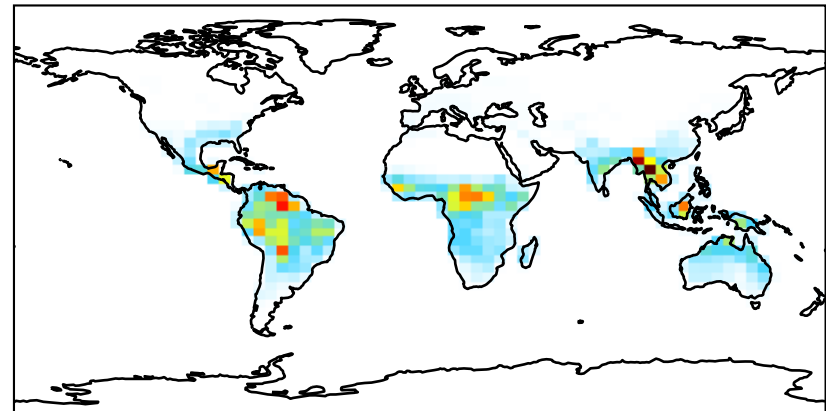


# EmisISOP\_Biogenic (Apr2019)

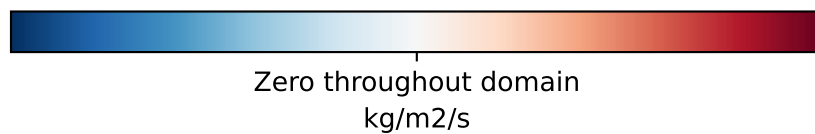
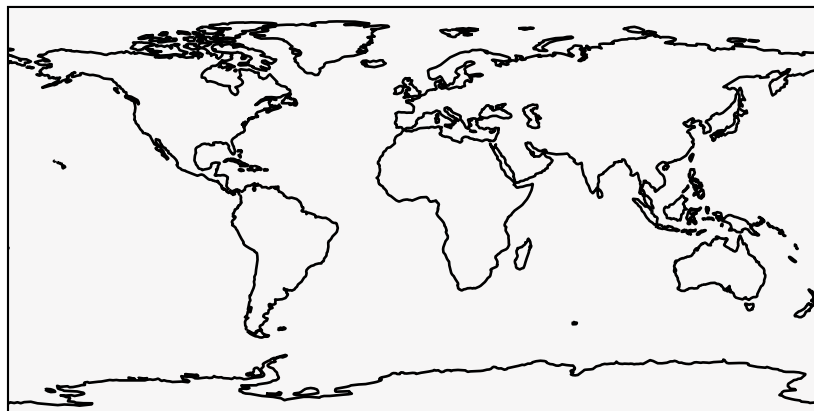
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



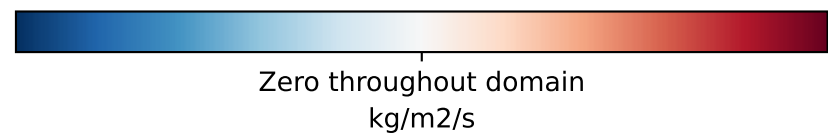
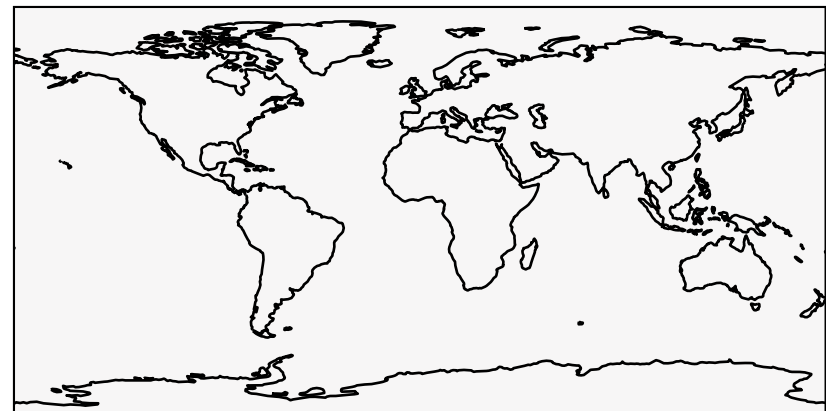
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



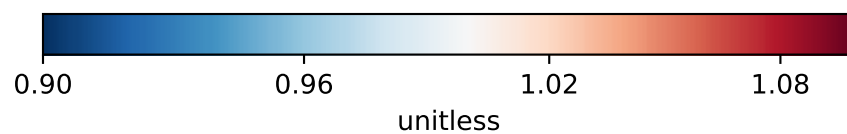
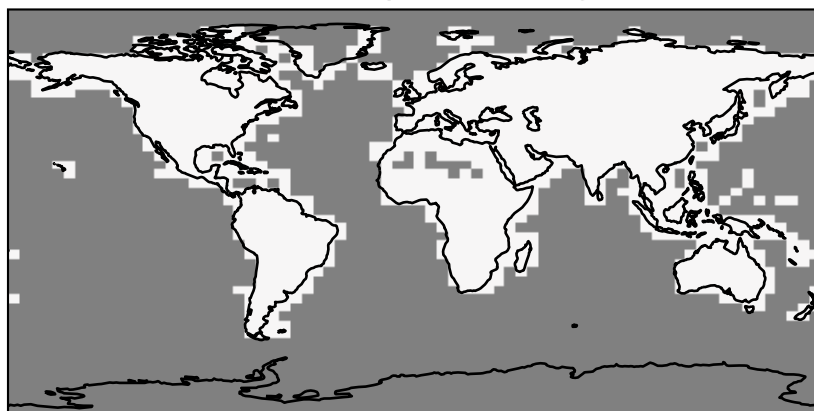
Difference  
Dev - Ref, Dynamic Range



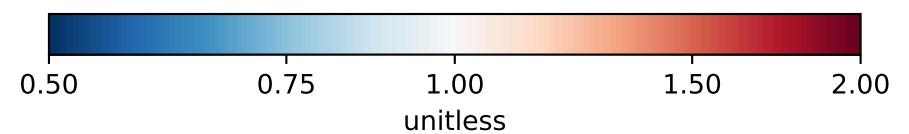
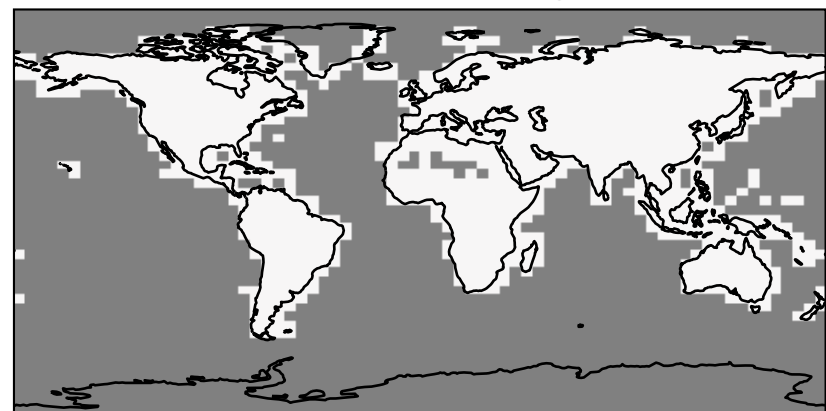
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



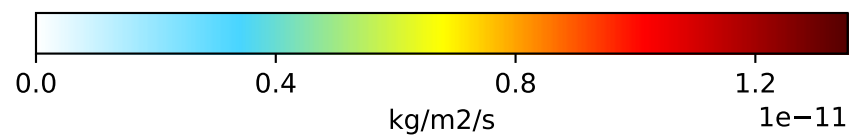
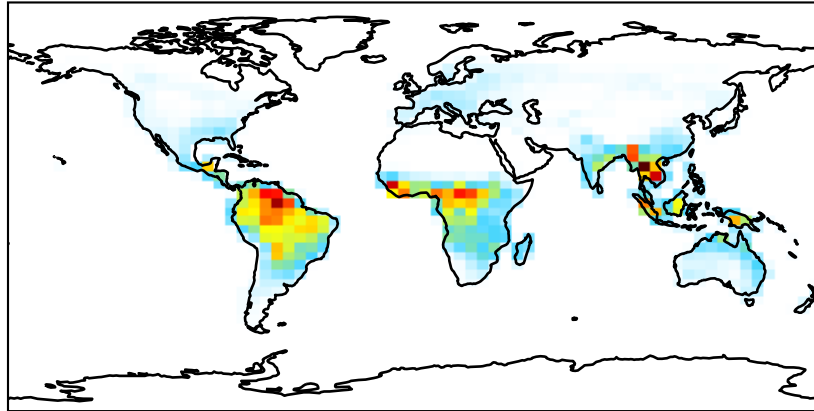
Ratio  
Dev/Ref, Fixed Range



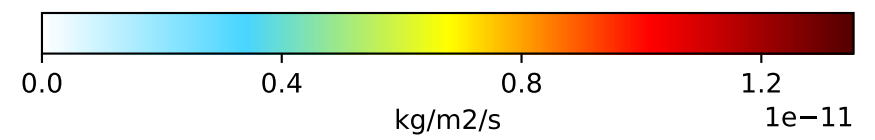
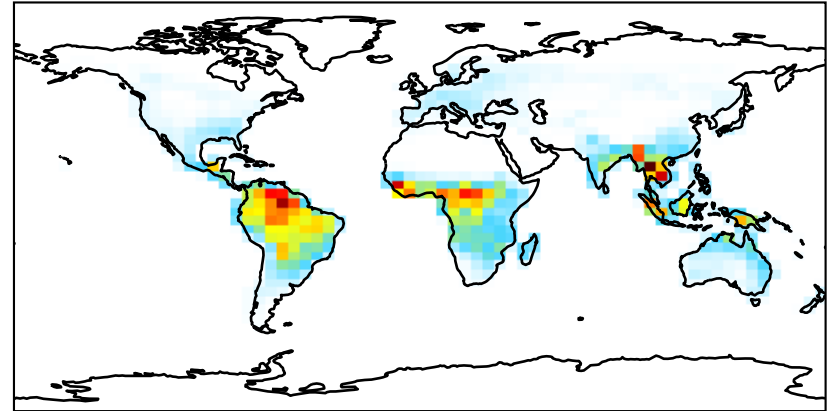


# EmisLIMO\_Biogenic (Apr2019)

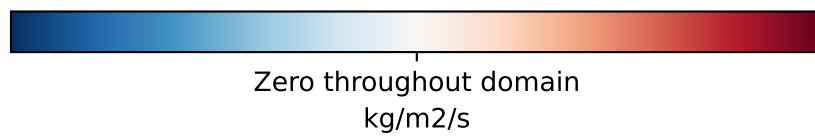
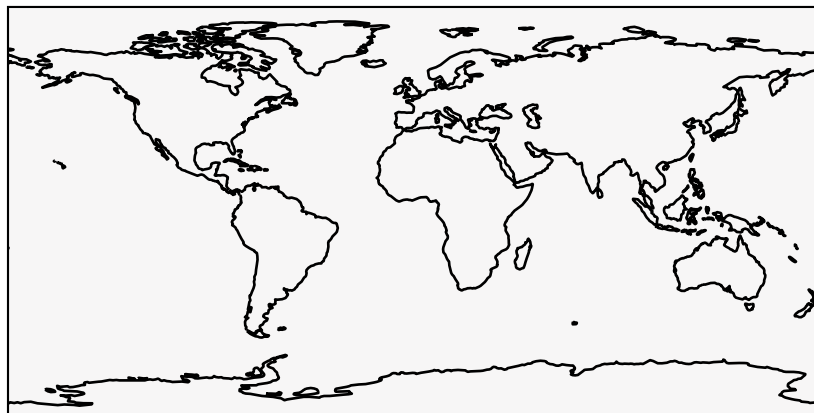
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



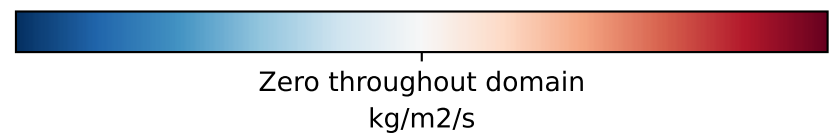
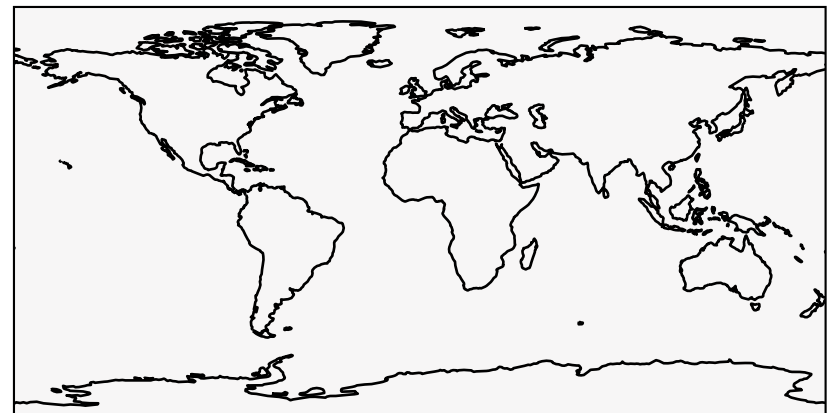
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



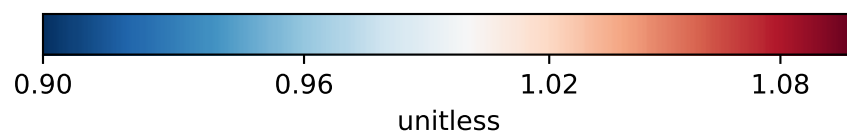
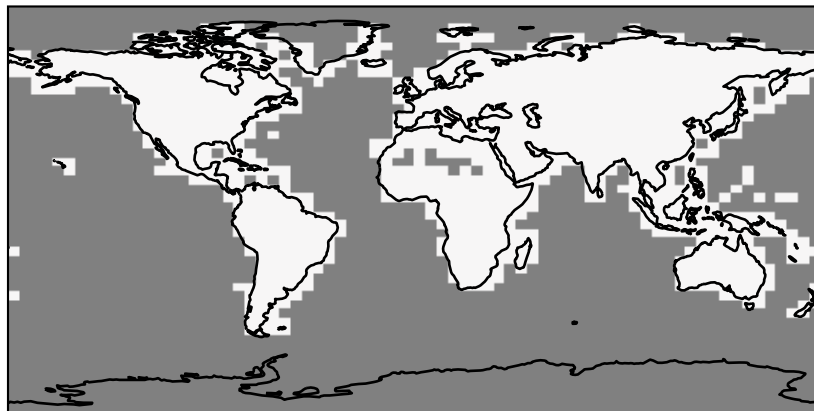
Difference  
Dev - Ref, Dynamic Range



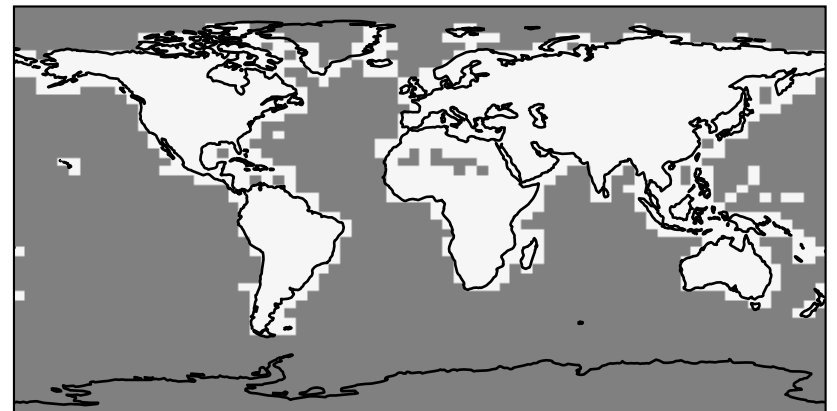
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

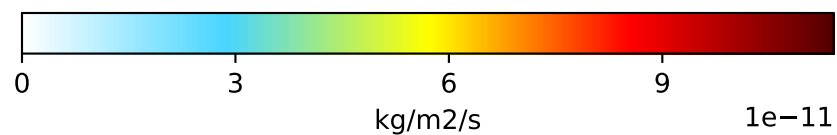
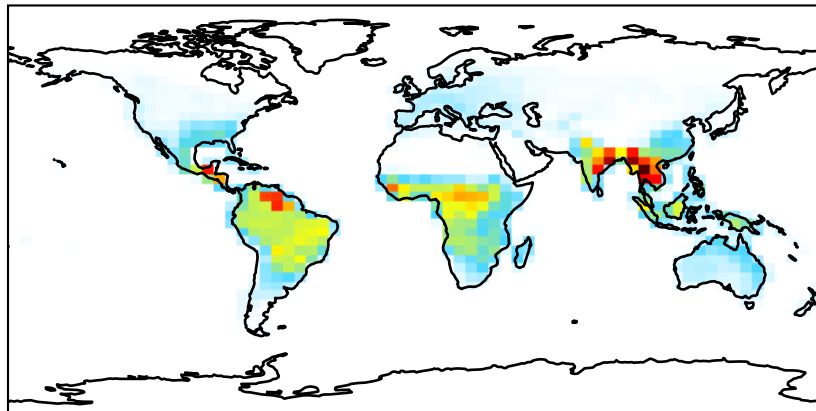


Ratio  
Dev/Ref, Fixed Range

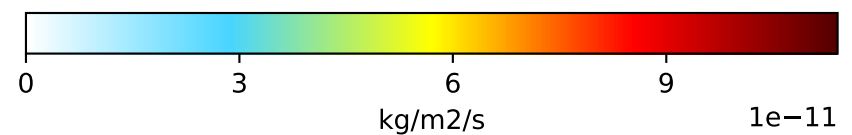
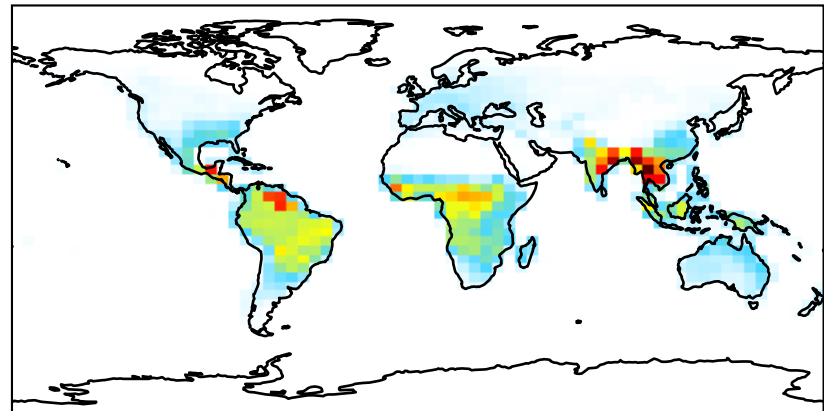


# EmisMOH\_Biogenic (Apr2019)

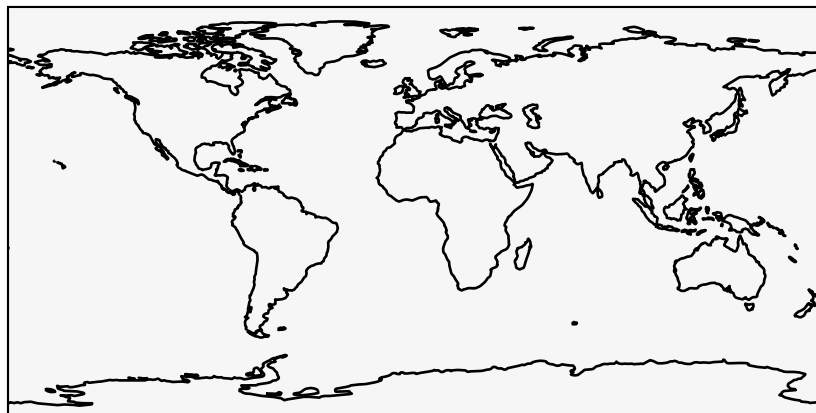
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



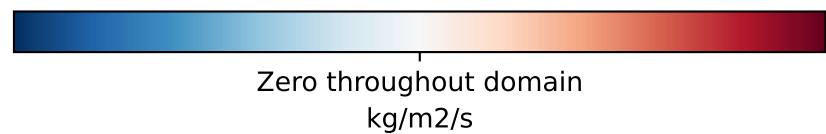
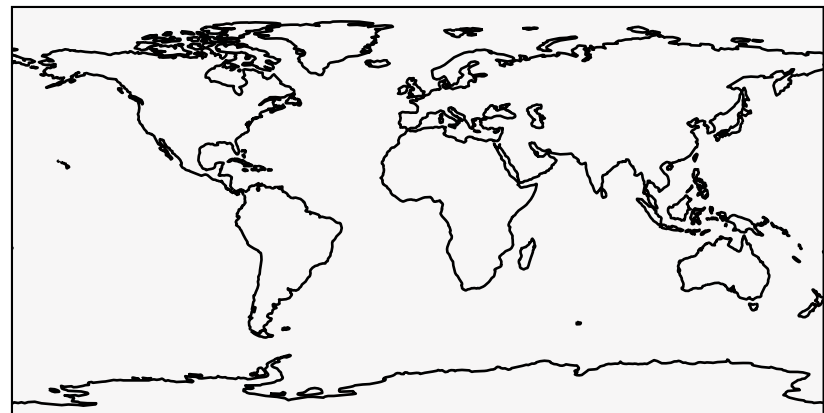
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



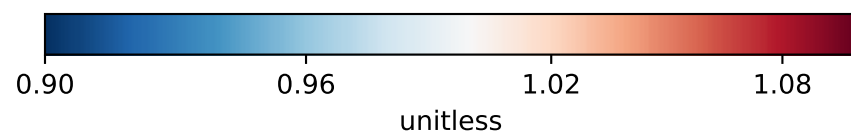
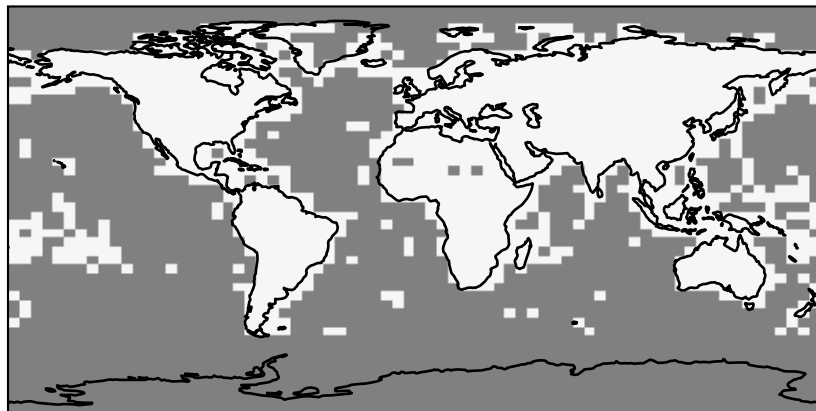
Difference  
Dev - Ref, Dynamic Range



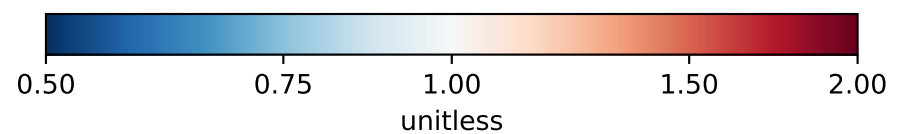
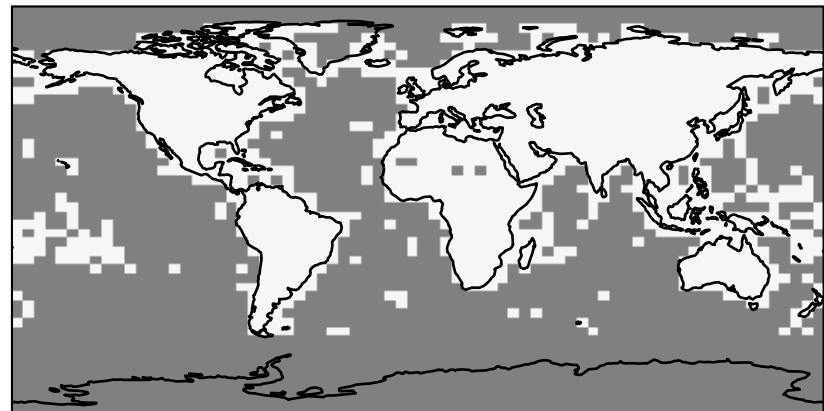
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

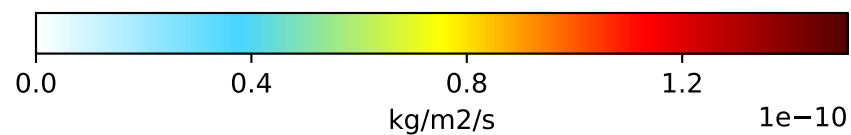
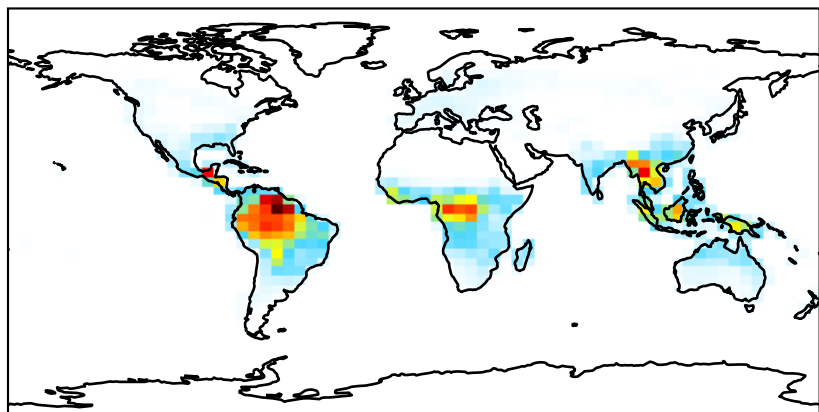


Ratio  
Dev/Ref, Fixed Range

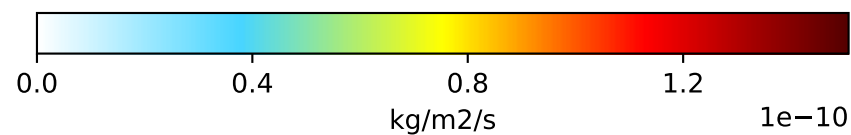
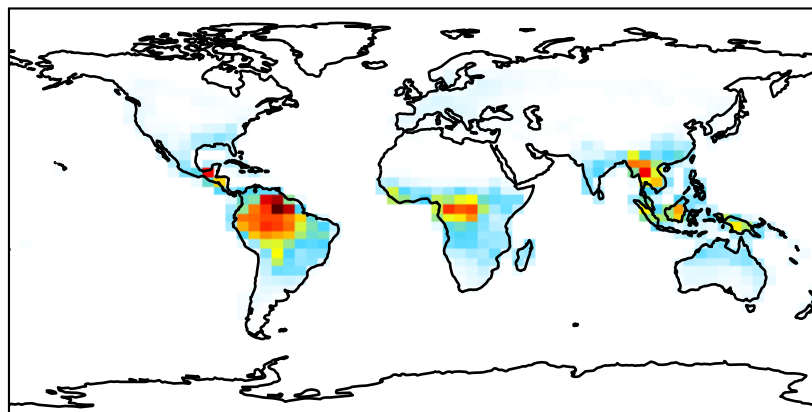


# EmisMTPA\_Biogenic (Apr2019)

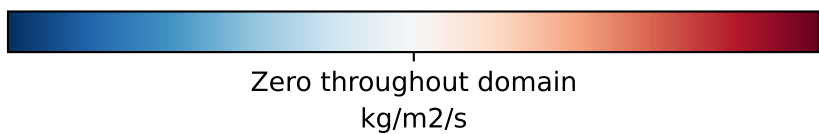
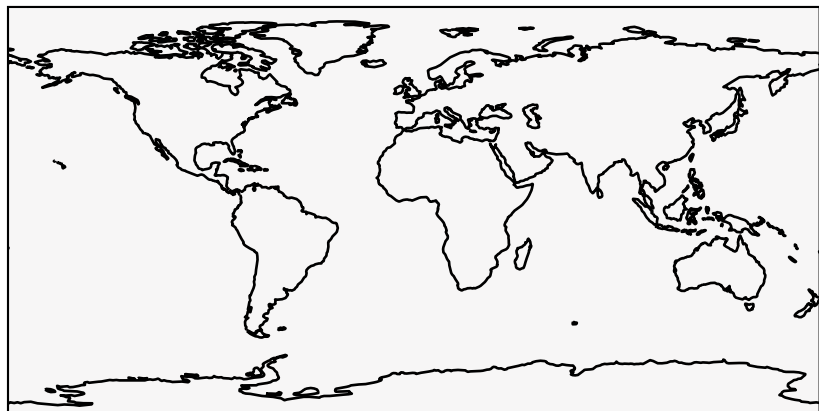
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



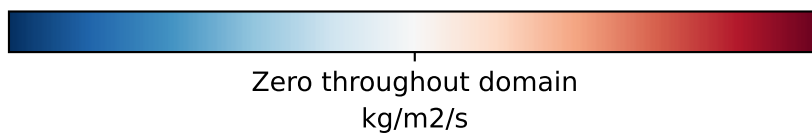
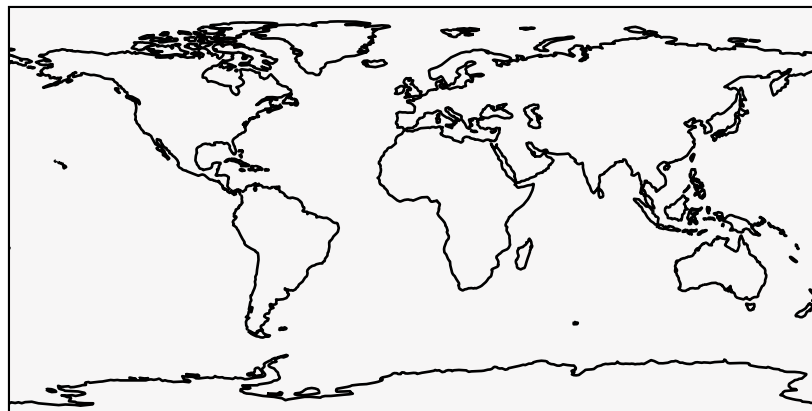
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



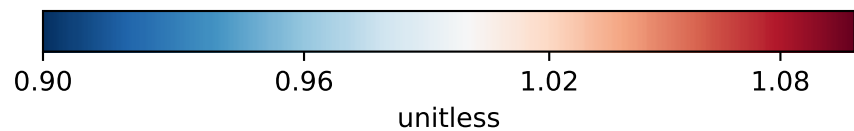
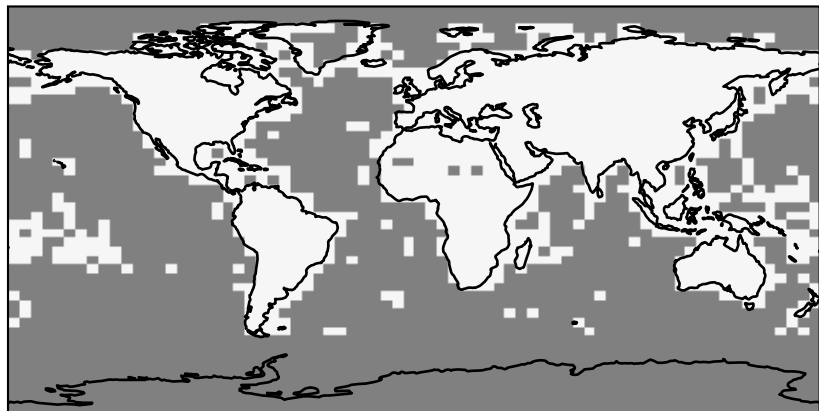
Difference  
Dev - Ref, Dynamic Range



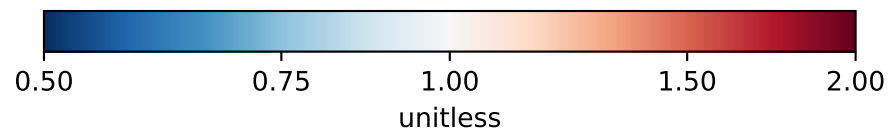
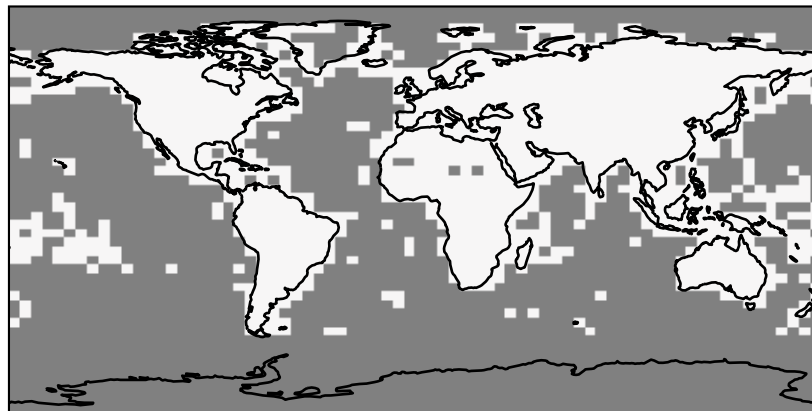
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



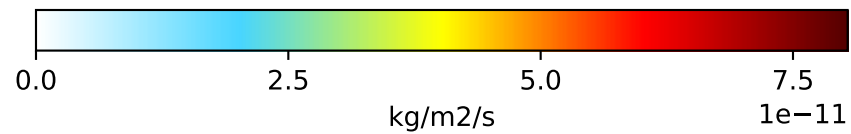
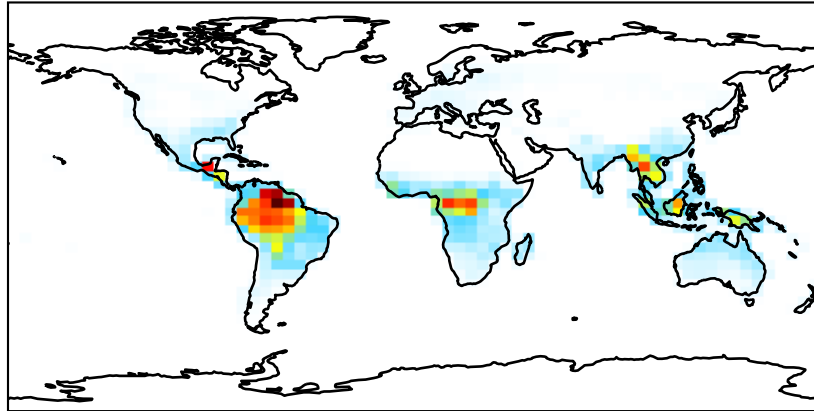
Ratio  
Dev/Ref, Fixed Range



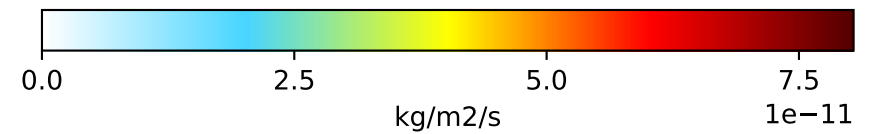
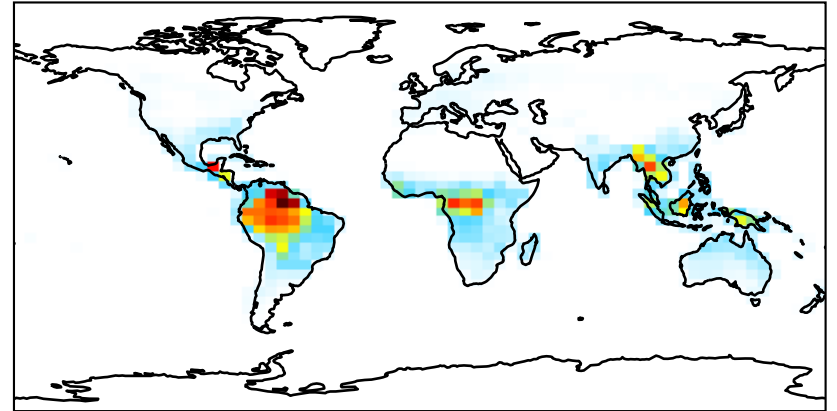


# EmisMTPO\_Biogenic (Apr2019)

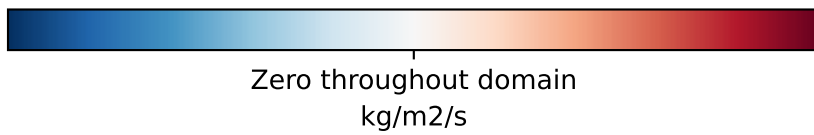
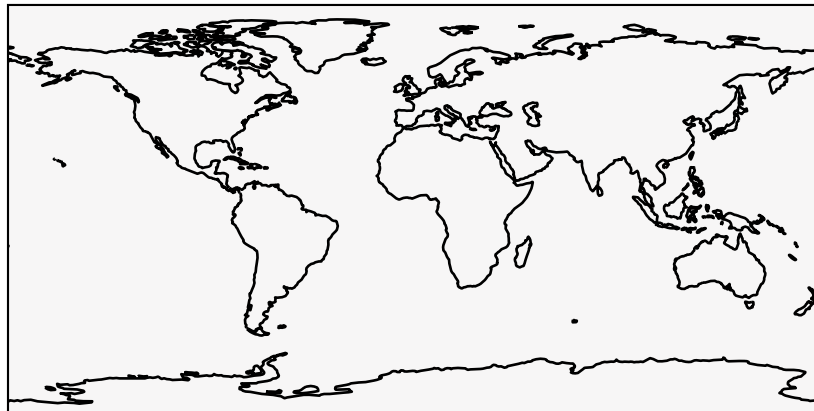
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



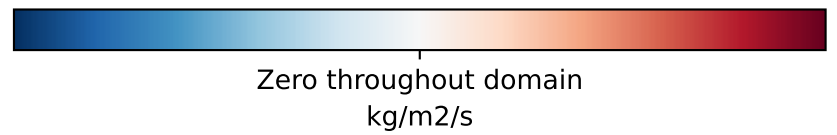
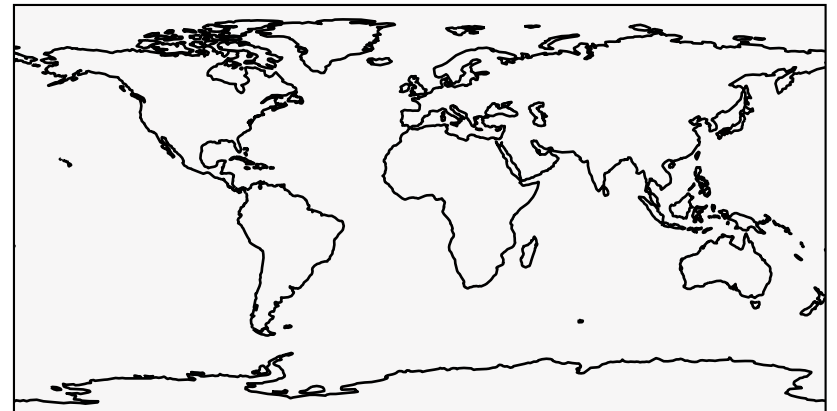
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



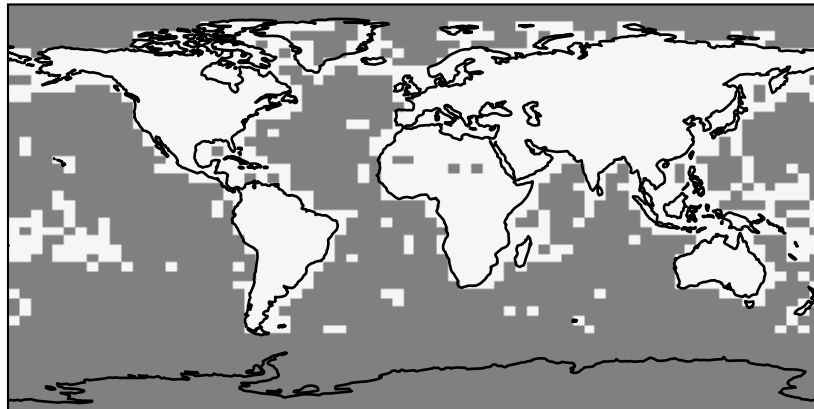
Difference  
Dev - Ref, Dynamic Range



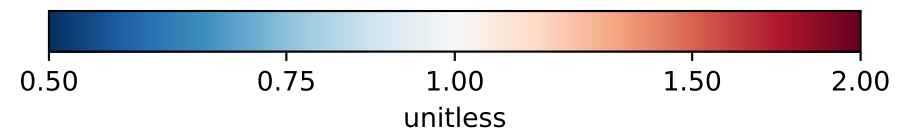
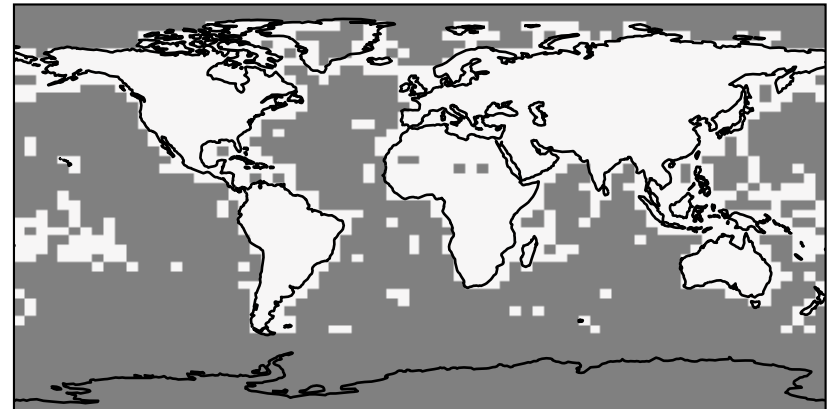
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

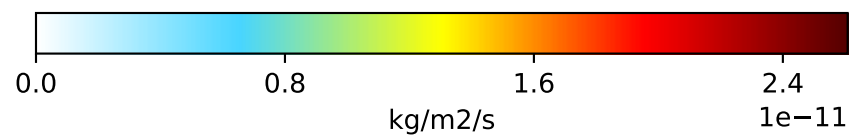
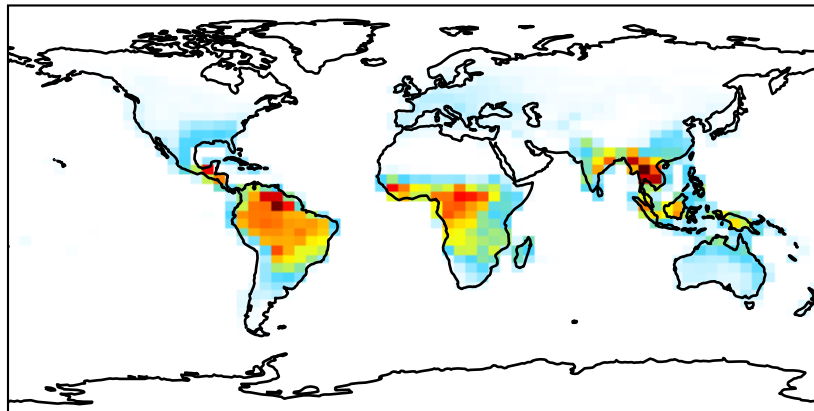


Ratio  
Dev/Ref, Fixed Range

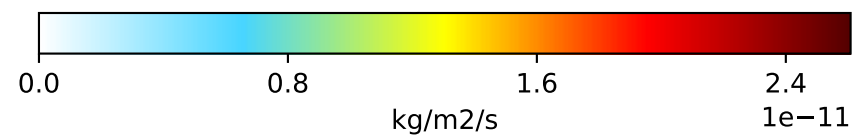
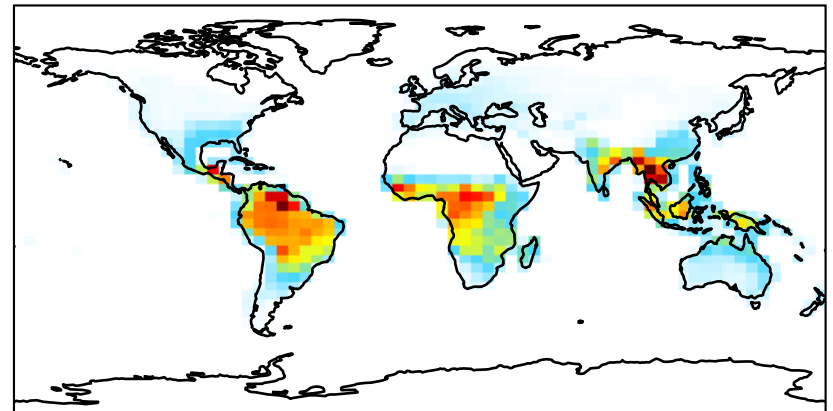


# EmisPRPE\_Biogenic (Apr2019)

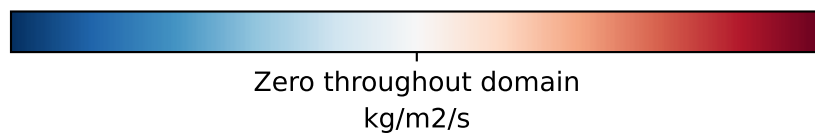
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



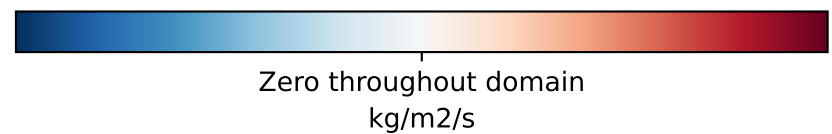
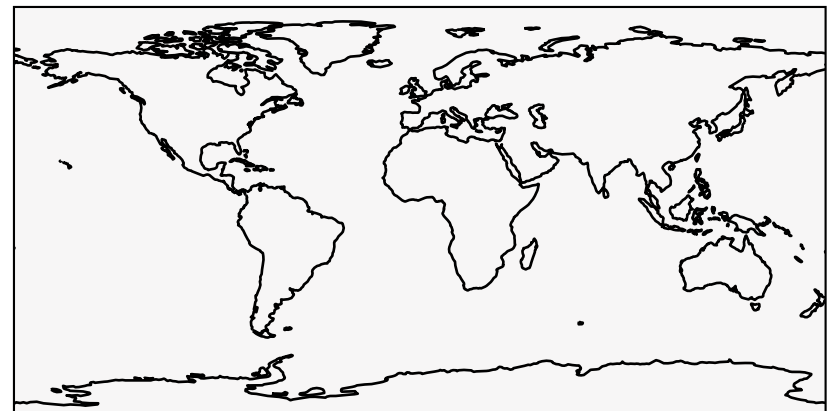
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



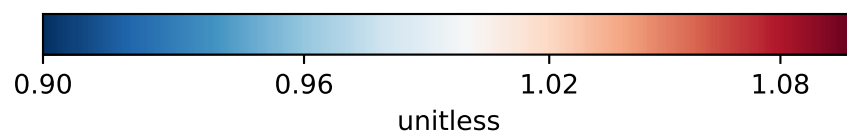
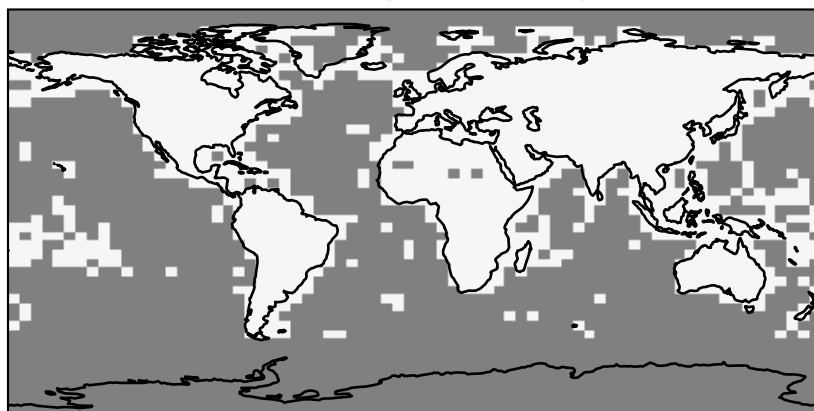
Difference  
Dev - Ref, Dynamic Range



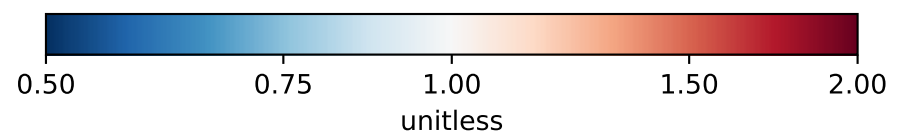
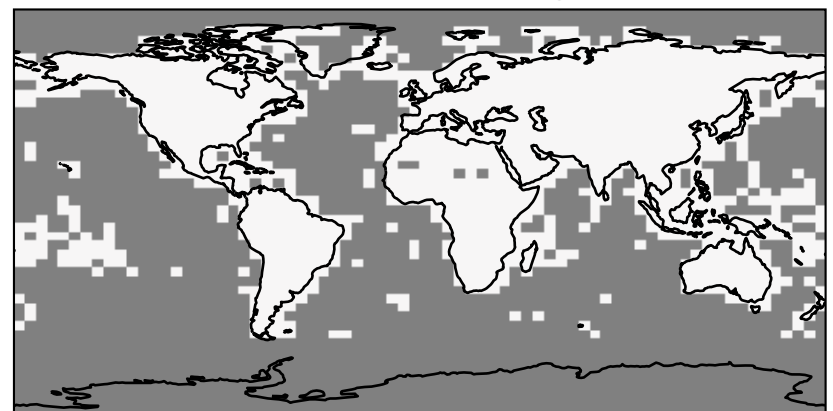
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

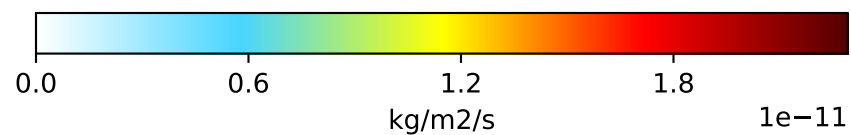
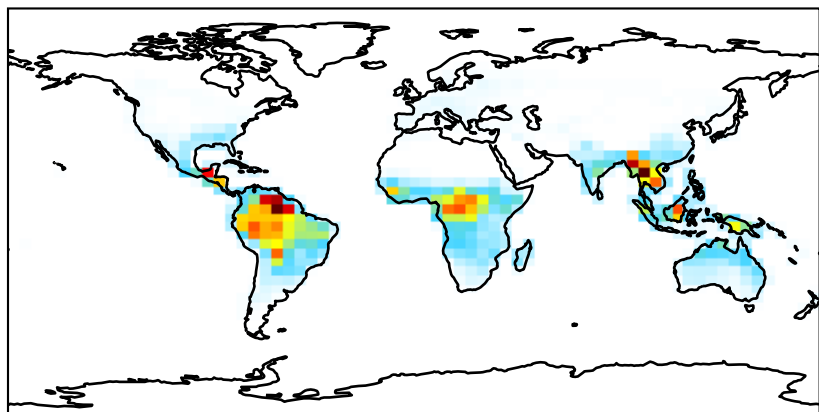


Ratio  
Dev/Ref, Fixed Range

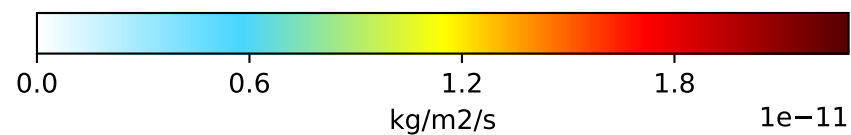
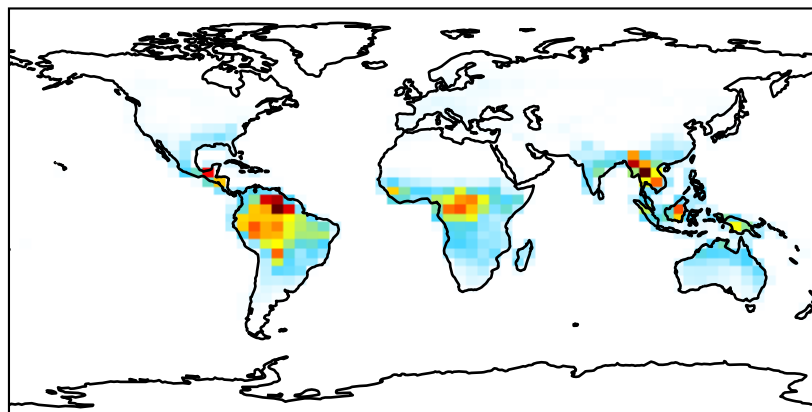


# EmisSOAP\_Biogenic (Apr2019)

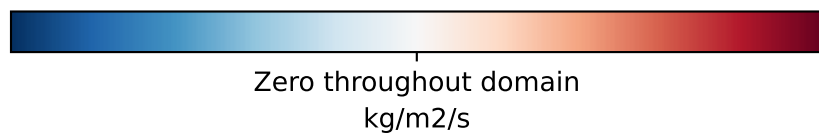
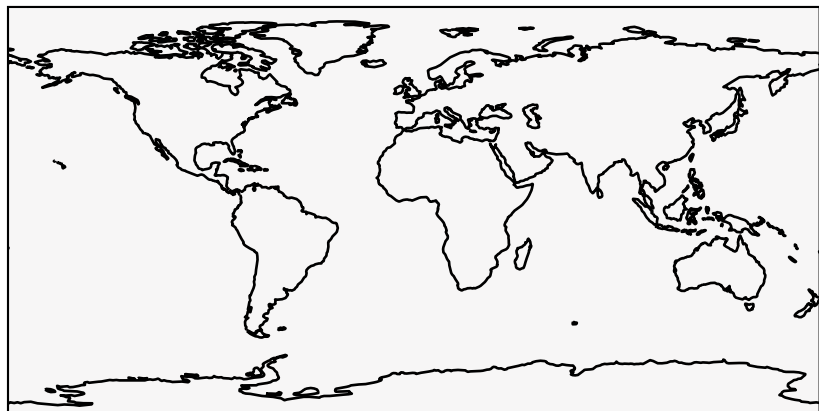
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



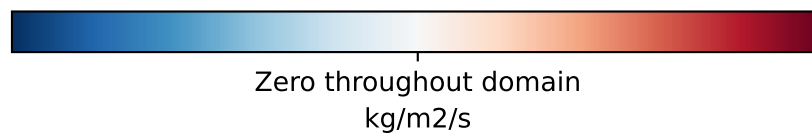
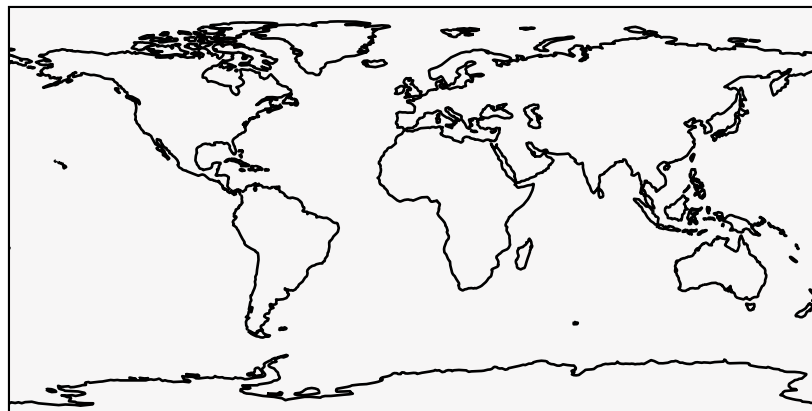
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



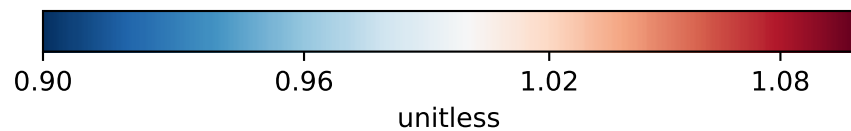
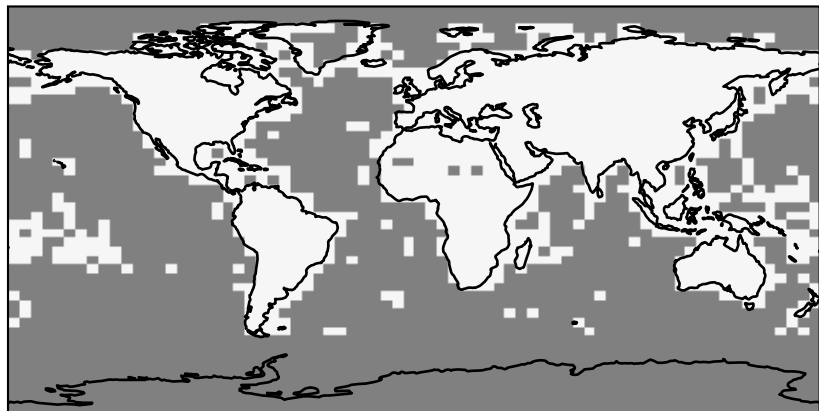
Difference  
Dev - Ref, Dynamic Range



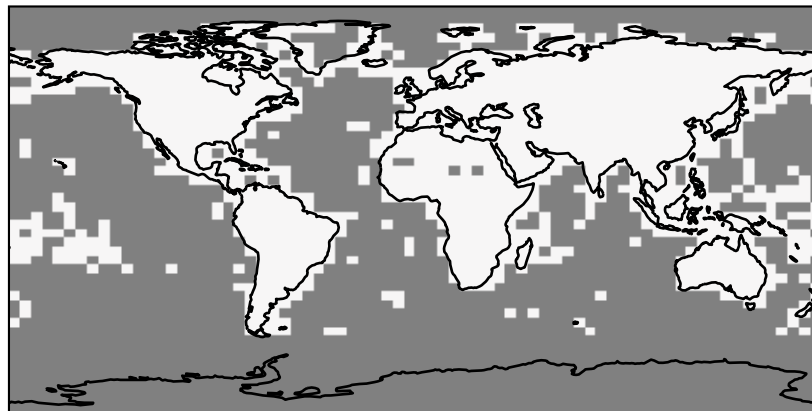
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

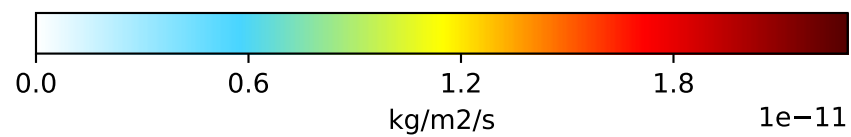
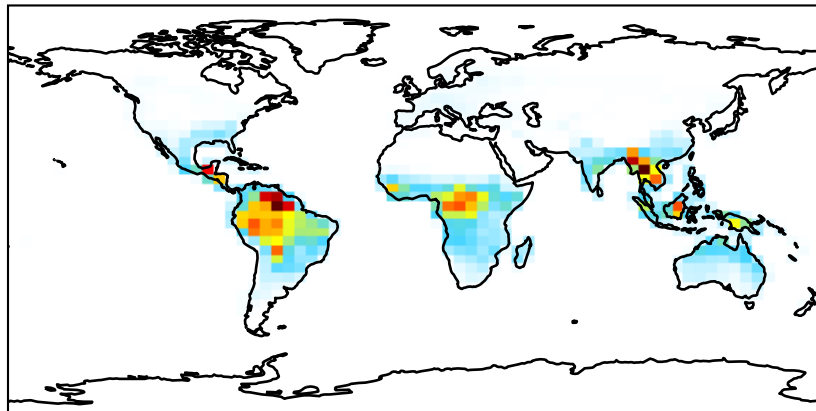


Ratio  
Dev/Ref, Fixed Range

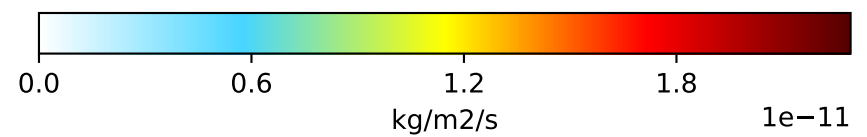
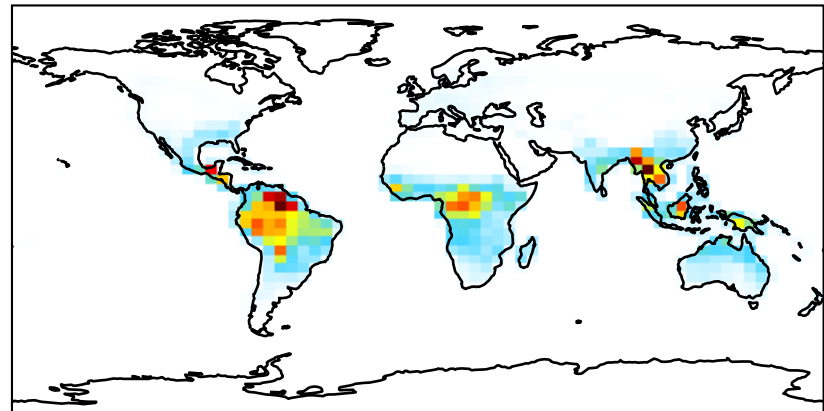


# EmisSOAS\_Biogenic (Apr2019)

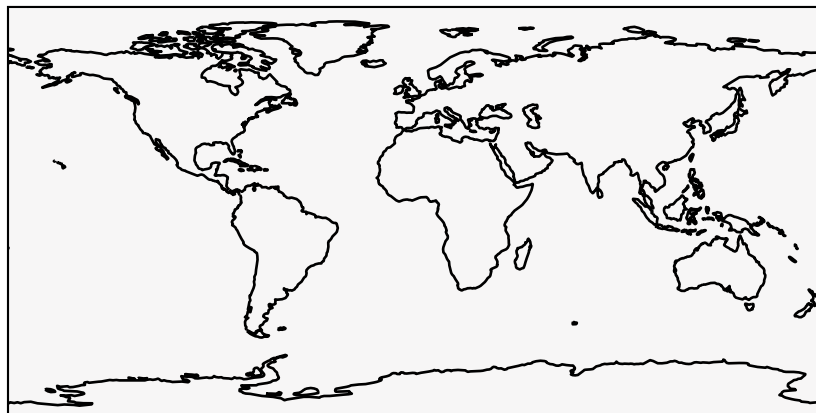
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



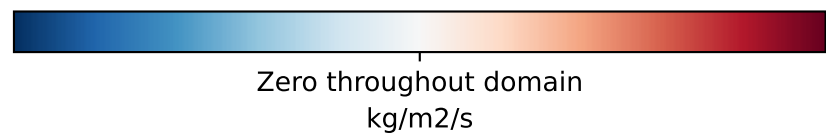
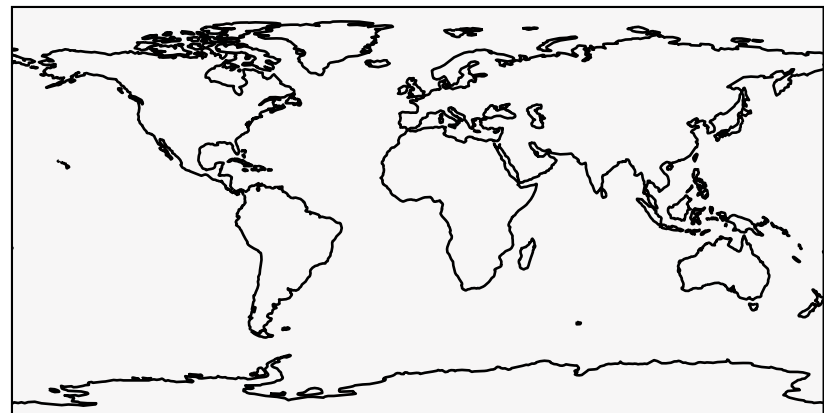
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



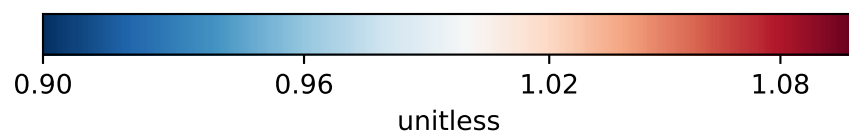
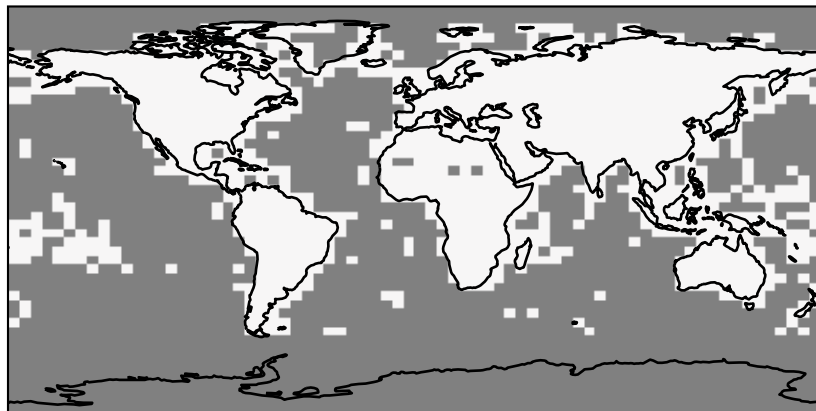
Difference  
Dev - Ref, Dynamic Range



Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

