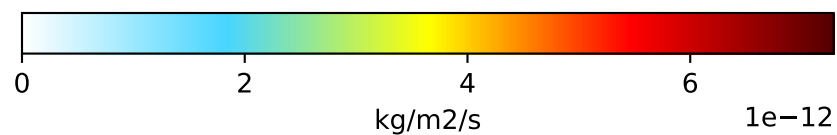
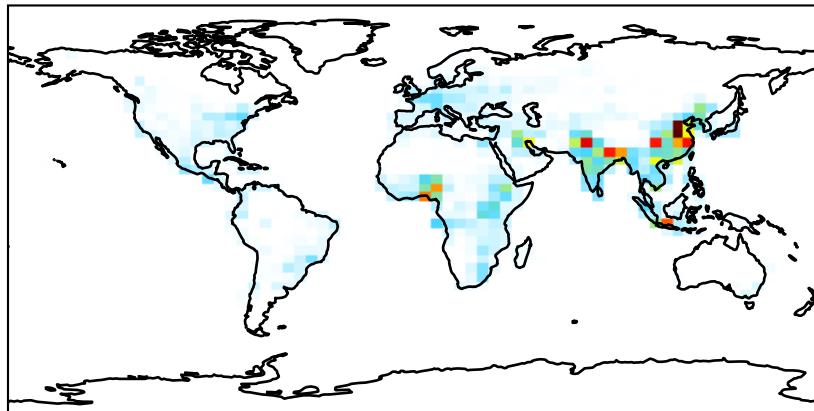
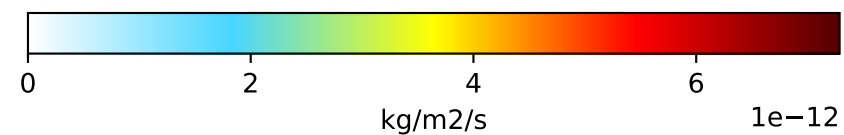
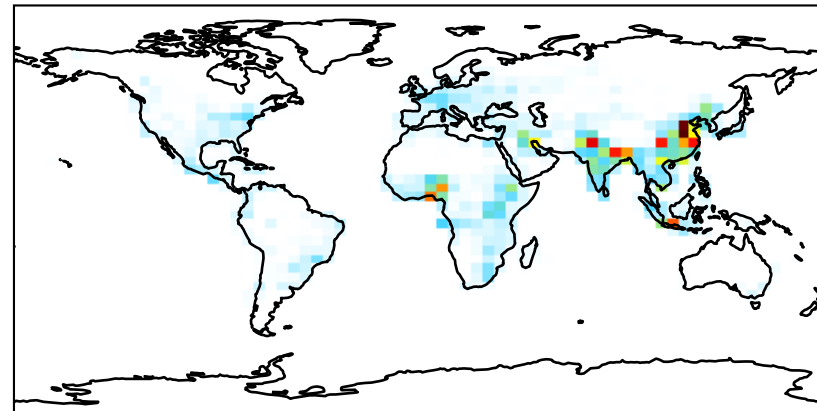


# EmisEOH\_Anthro (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



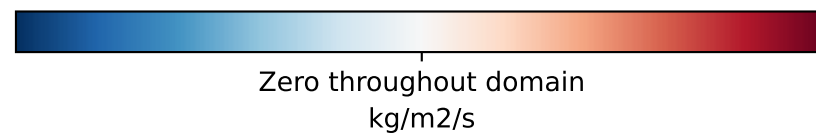
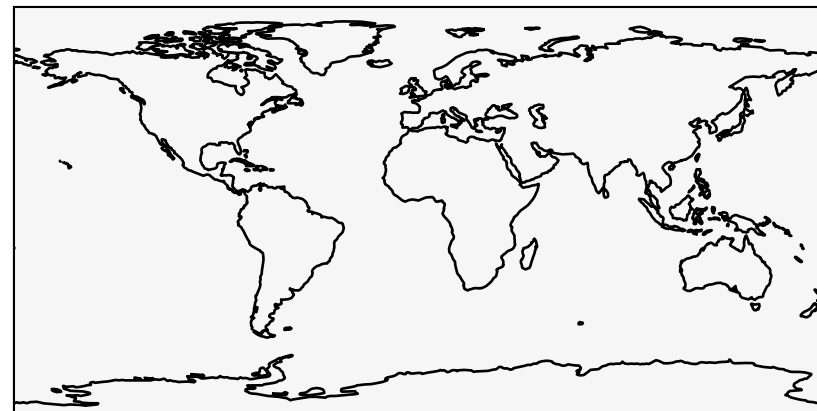
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



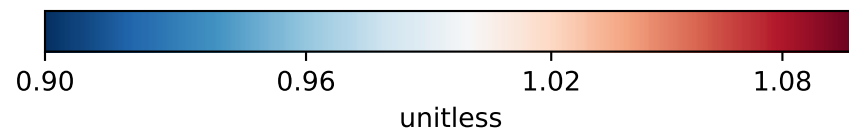
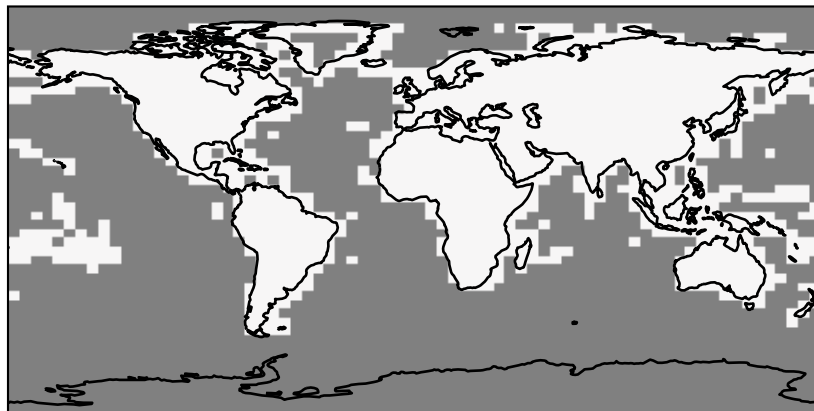
Difference  
Dev - Ref, Dynamic Range



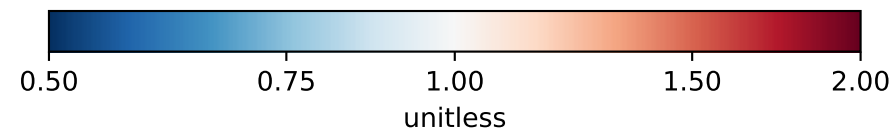
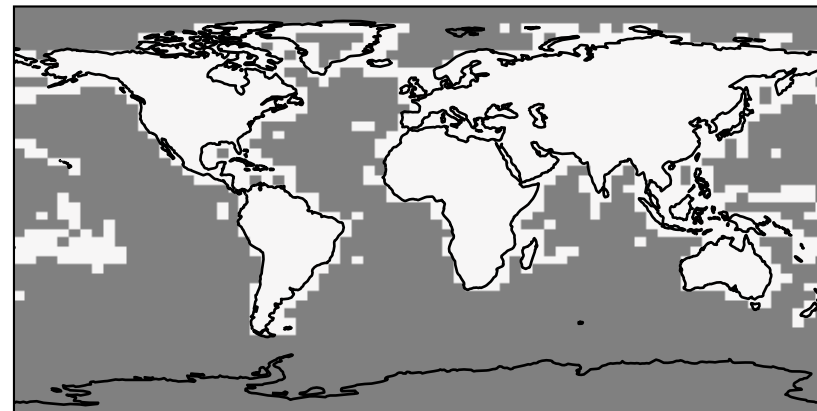
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

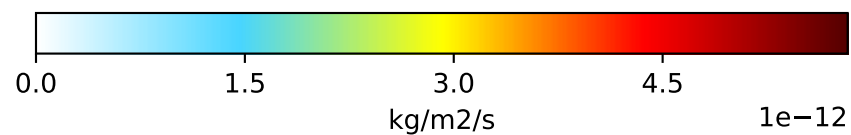
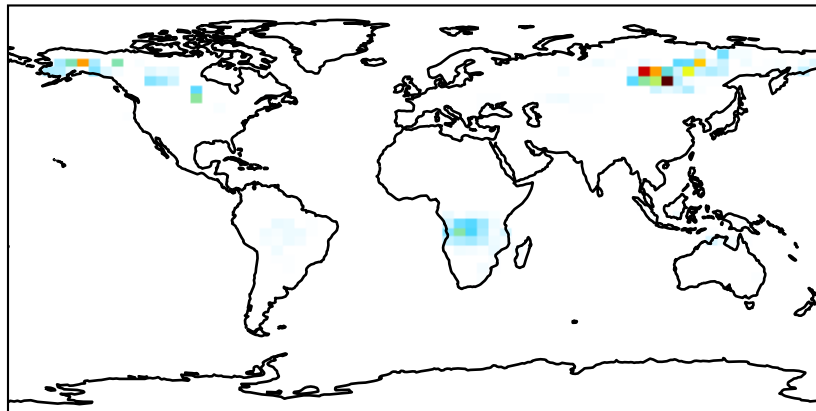


Ratio  
Dev/Ref, Fixed Range

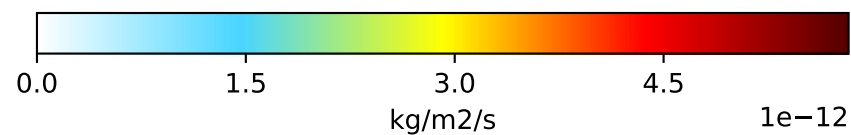
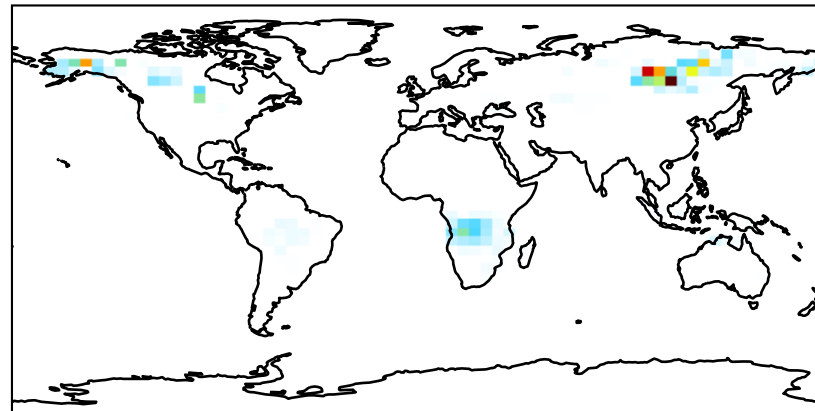


# EmisEOH\_BioBurn (Jul2019)

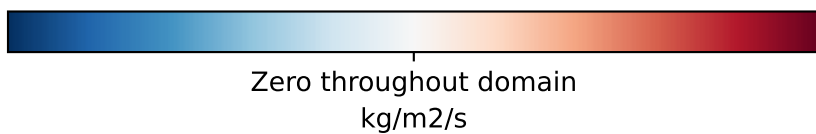
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



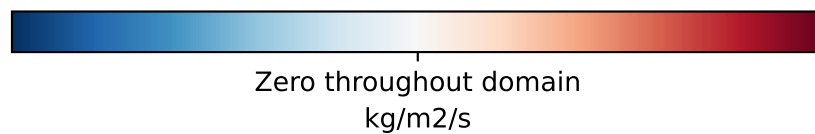
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



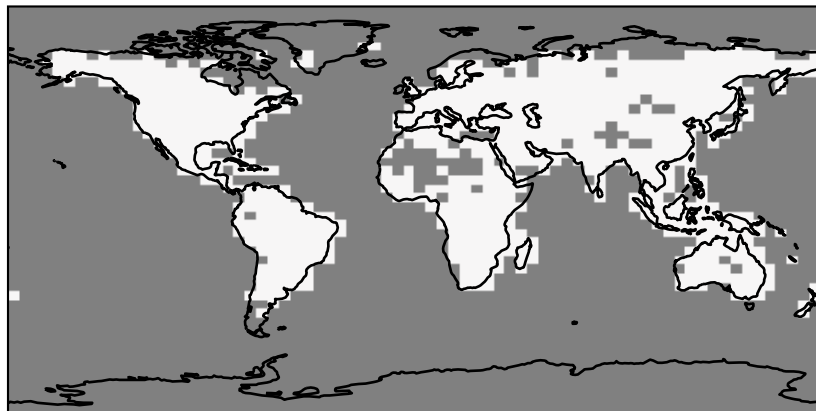
Difference  
Dev - Ref, Dynamic Range



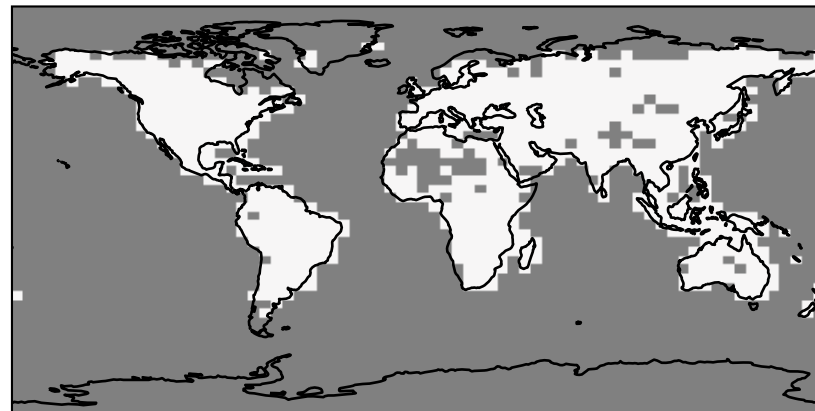
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

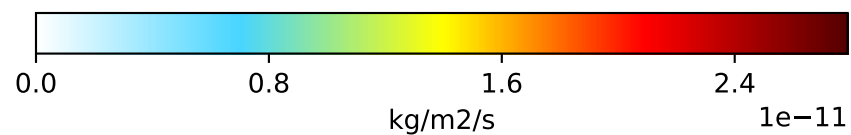
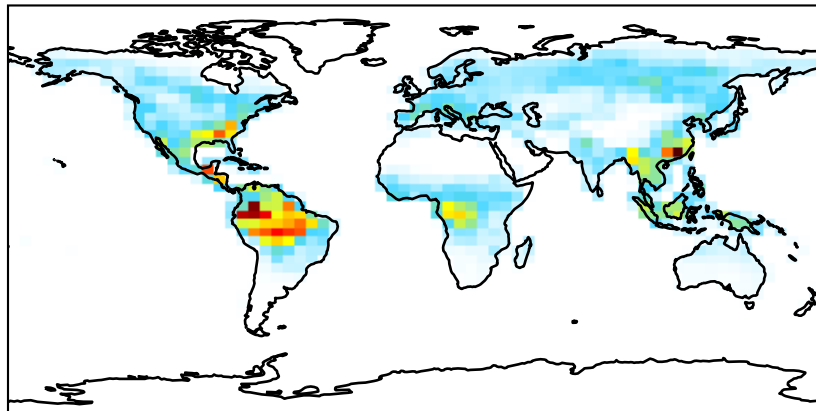


Ratio  
Dev/Ref, Fixed Range

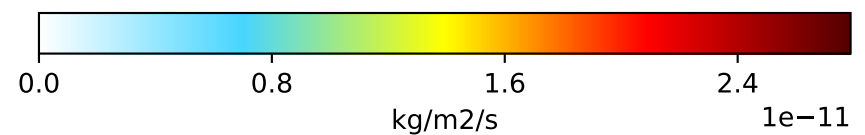
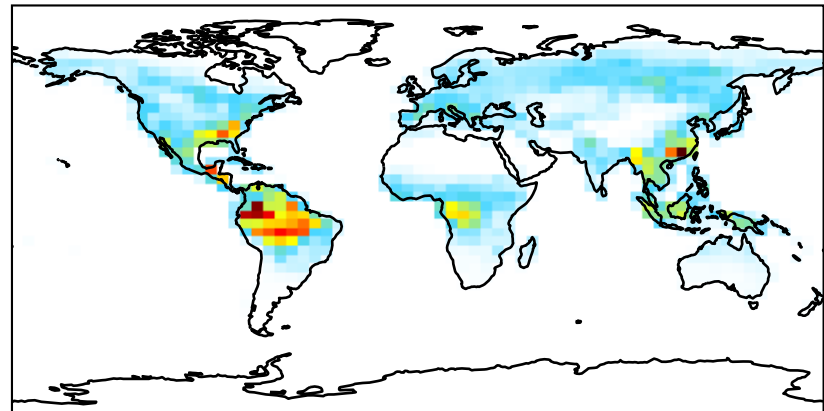


# EmisEOH\_Biogenic (Jul2019)

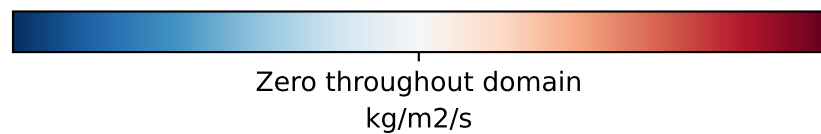
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



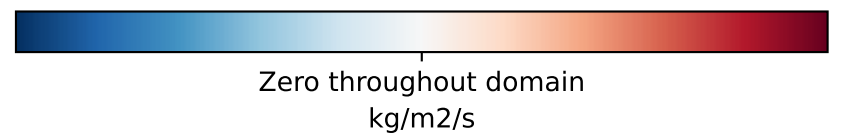
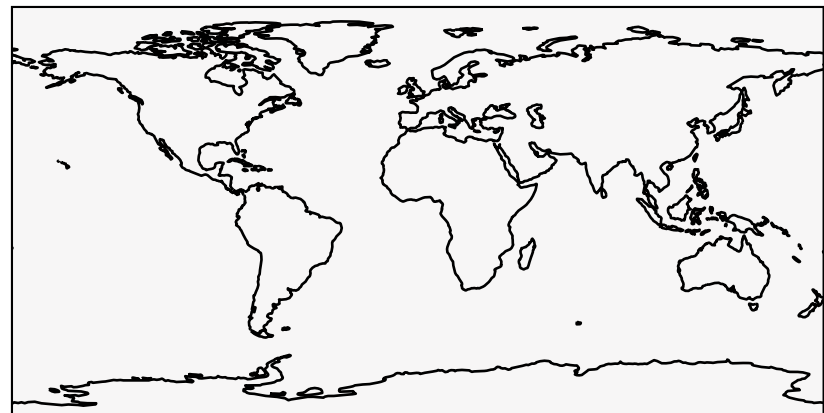
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



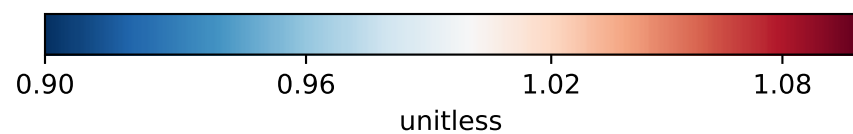
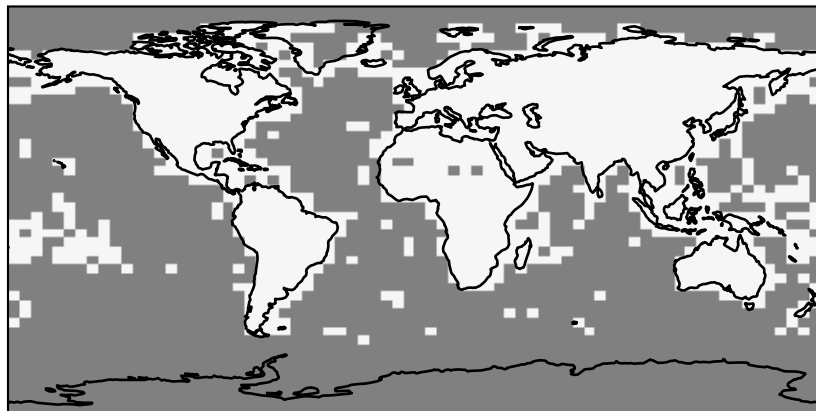
Difference  
Dev - Ref, Dynamic Range



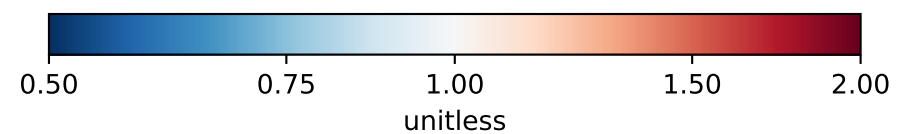
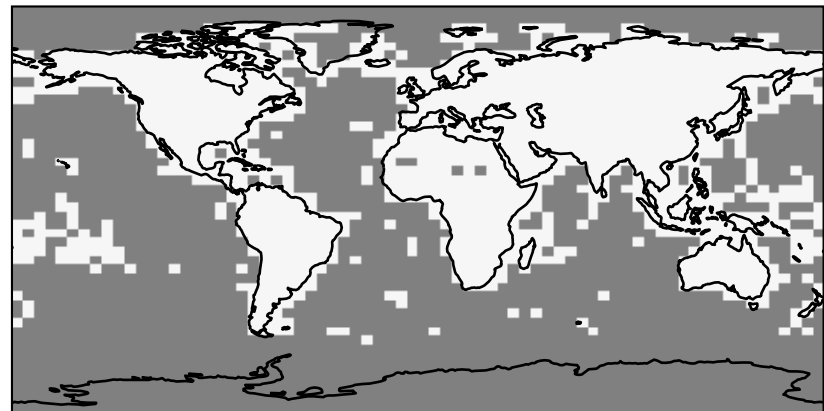
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

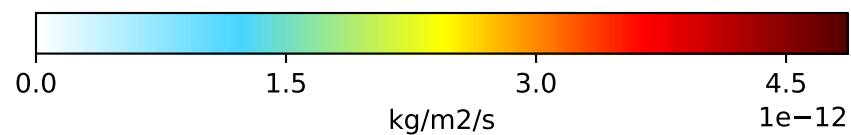
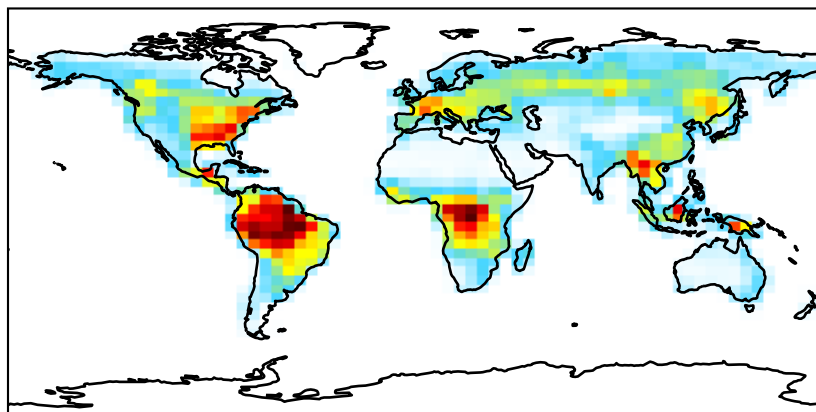


Ratio  
Dev/Ref, Fixed Range

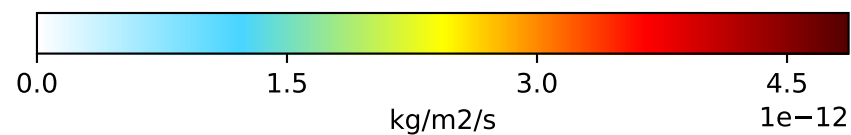
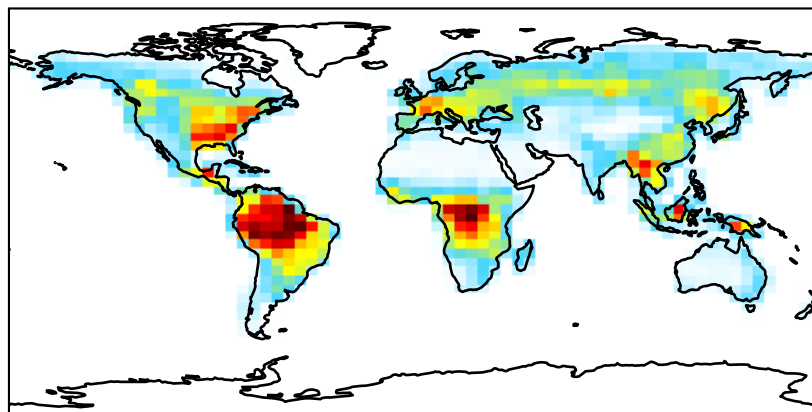


# EmisEOH\_PlantDecay (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



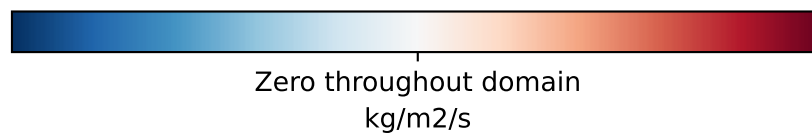
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



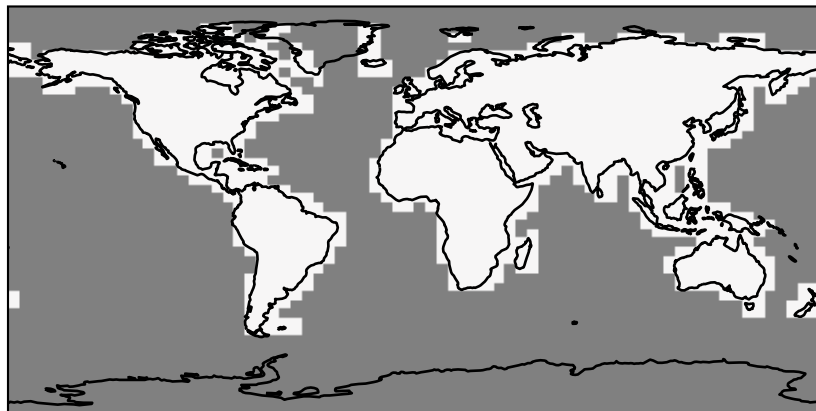
Difference  
Dev - Ref, Dynamic Range



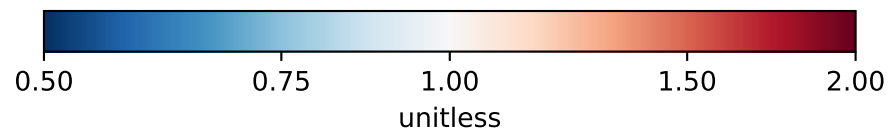
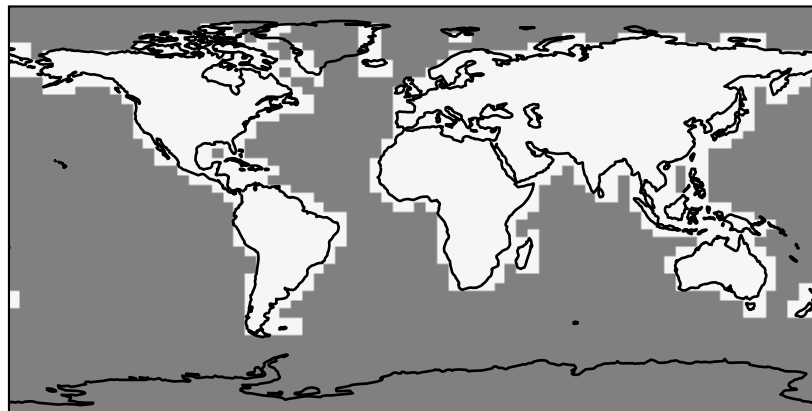
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



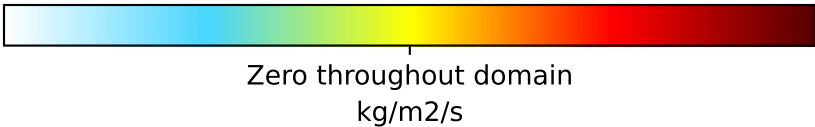
Ratio  
Dev/Ref, Fixed Range



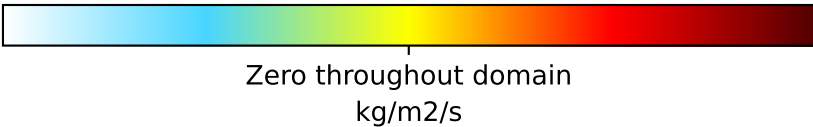


# EmisEOH\_Ship (Jul2019)

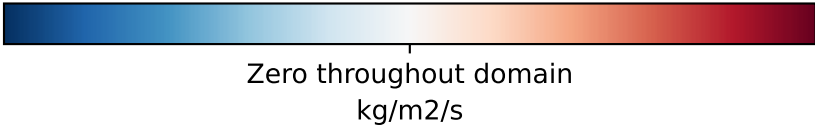
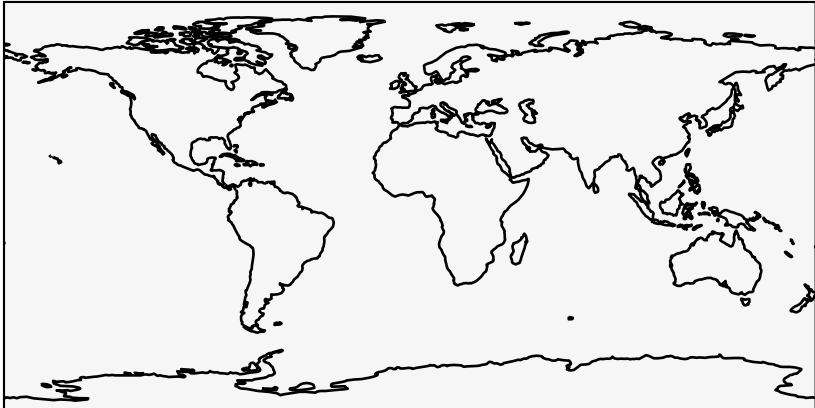
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



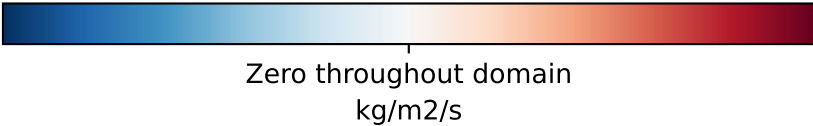
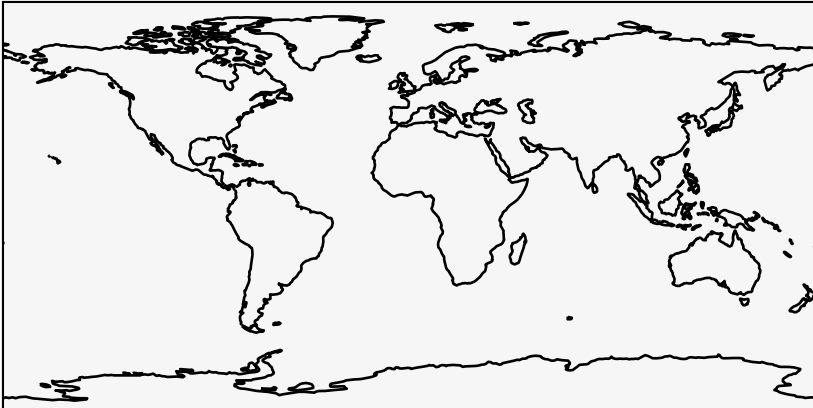
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



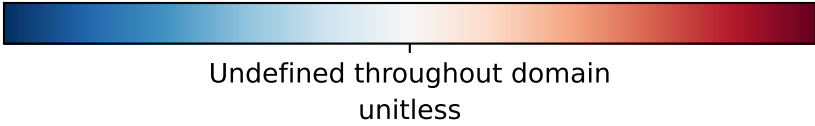
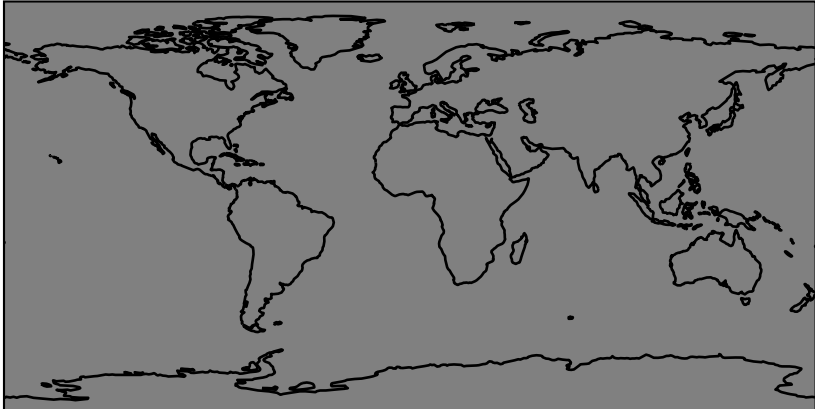
Difference  
Dev - Ref, Dynamic Range



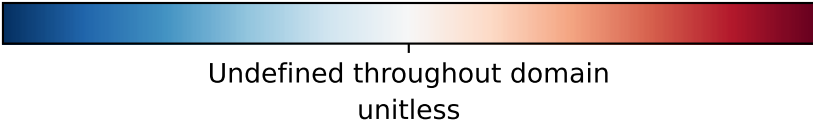
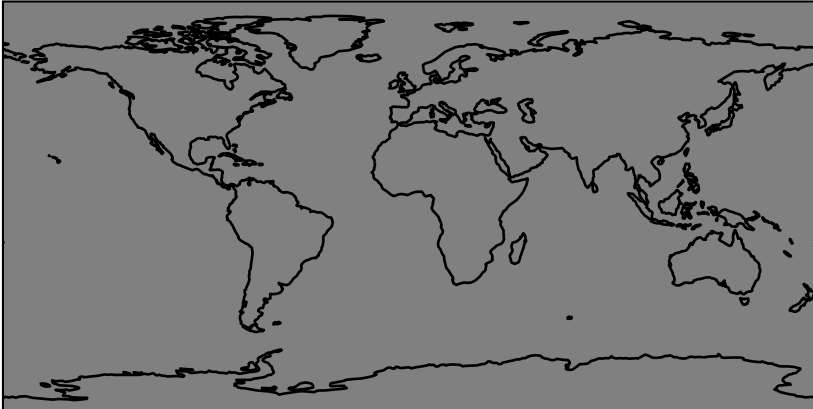
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

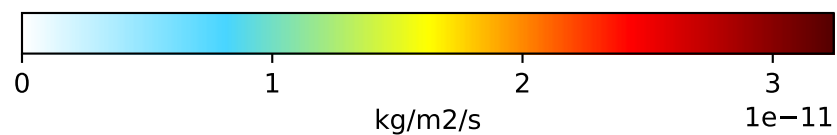
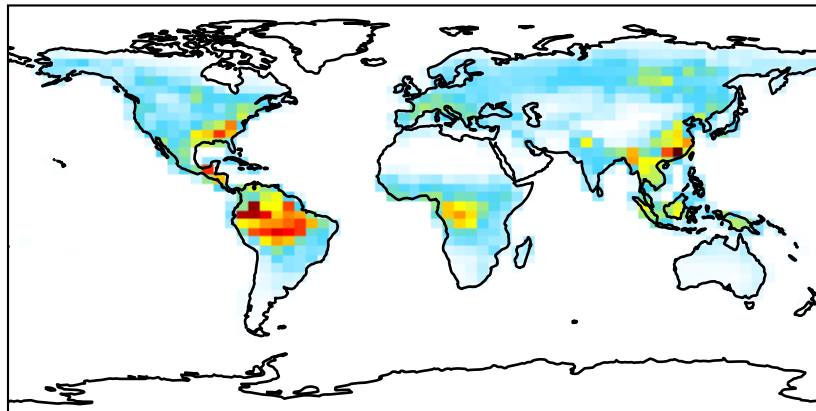


Ratio  
Dev/Ref, Fixed Range

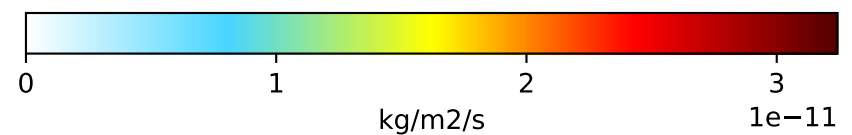
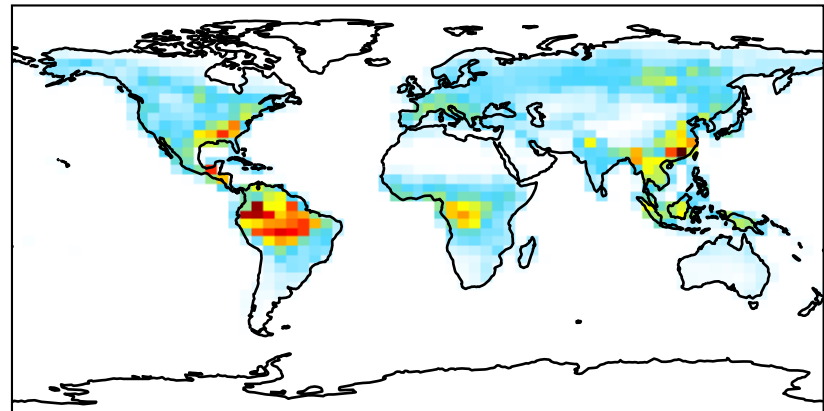


# EmisEOH\_Total (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



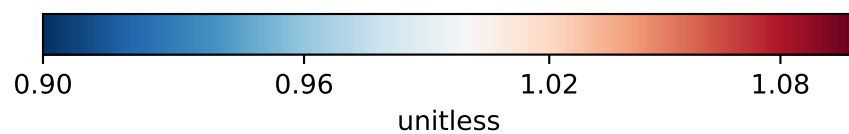
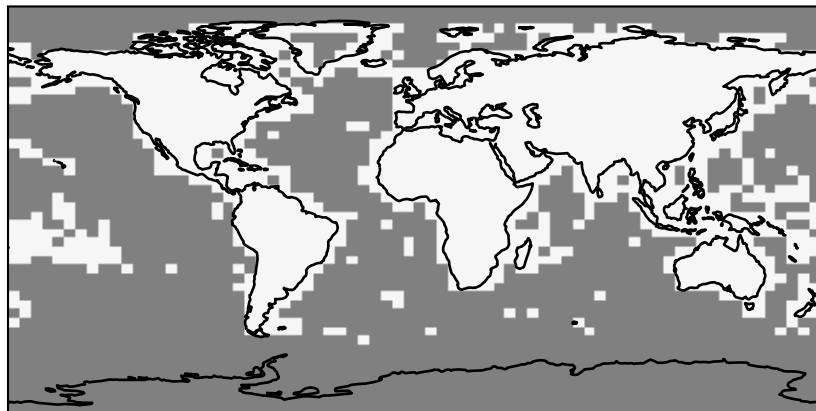
Difference  
Dev - Ref, Dynamic Range



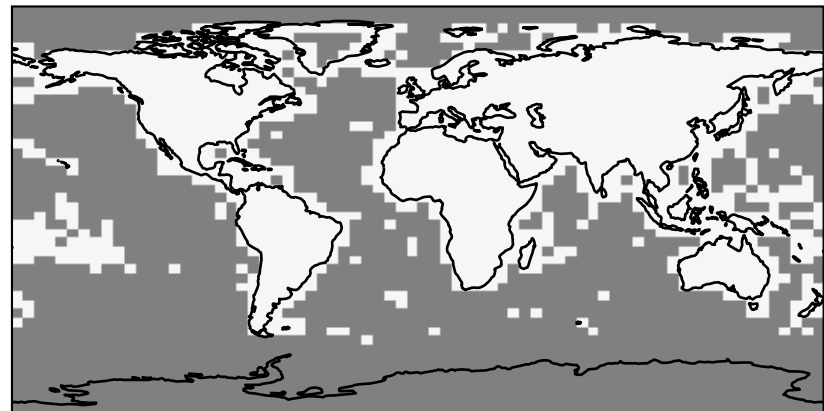
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

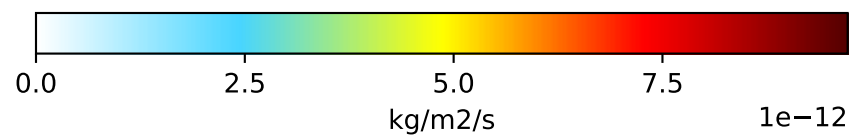
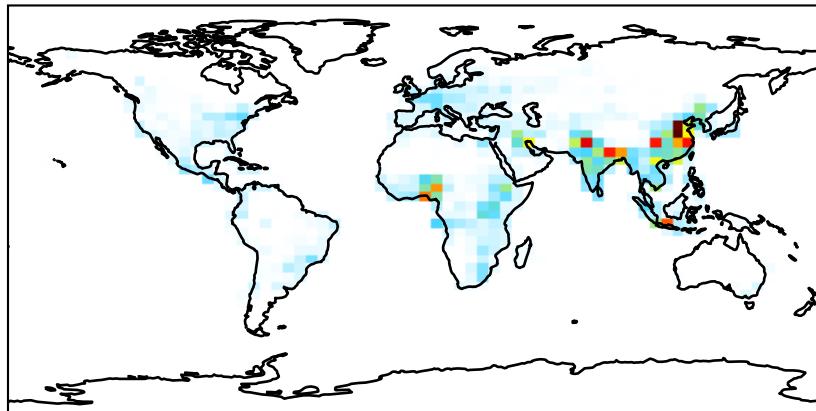


Ratio  
Dev/Ref, Fixed Range

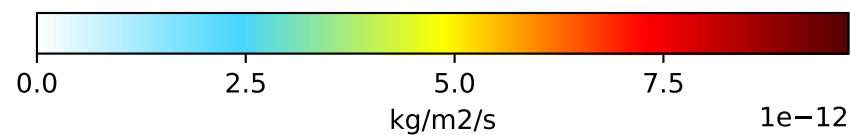
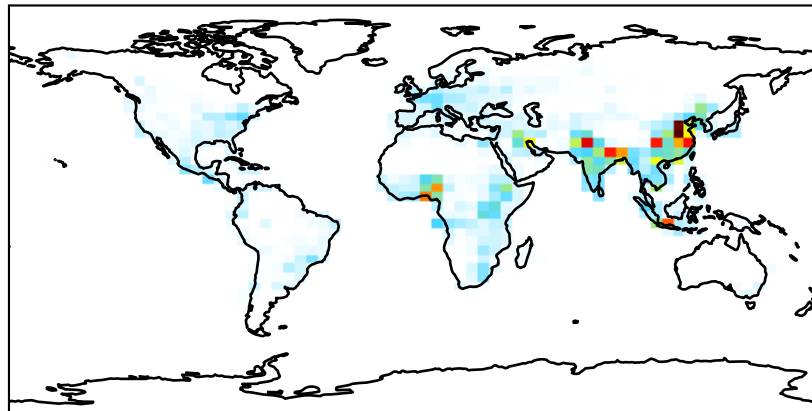


# EmisMOH\_Anthro (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



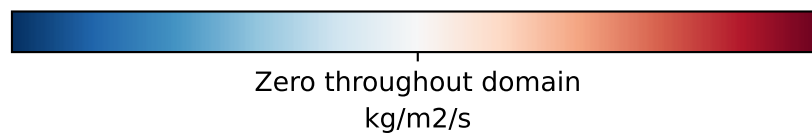
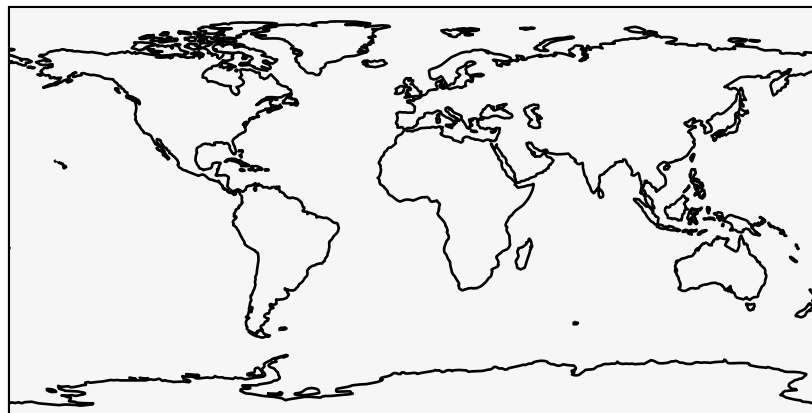
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



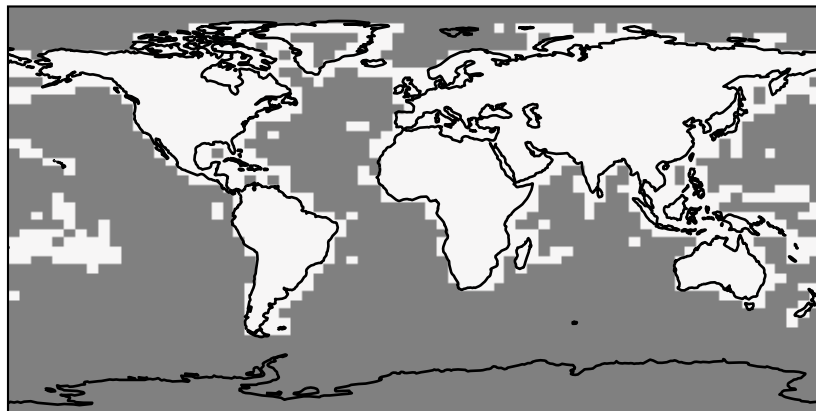
Difference  
Dev - Ref, Dynamic Range



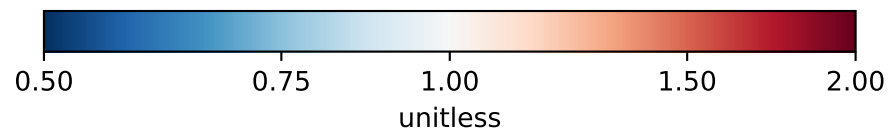
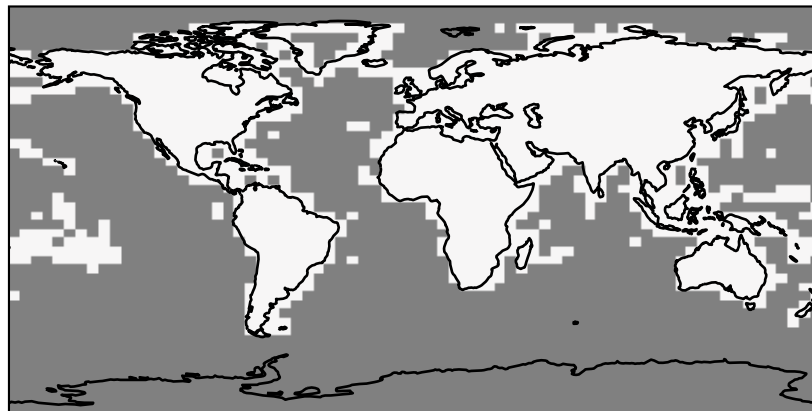
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

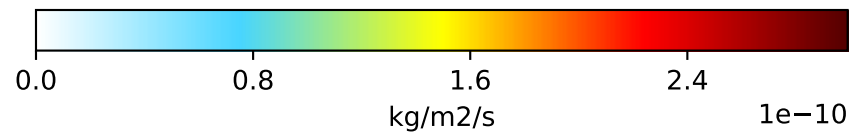
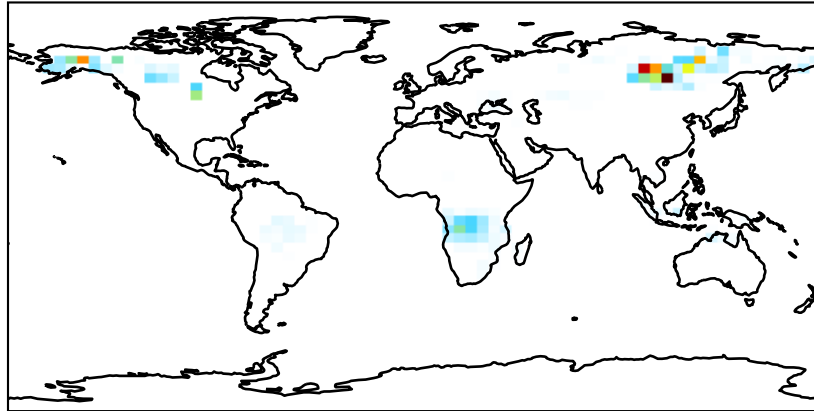


Ratio  
Dev/Ref, Fixed Range

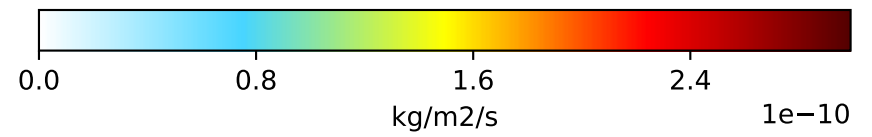
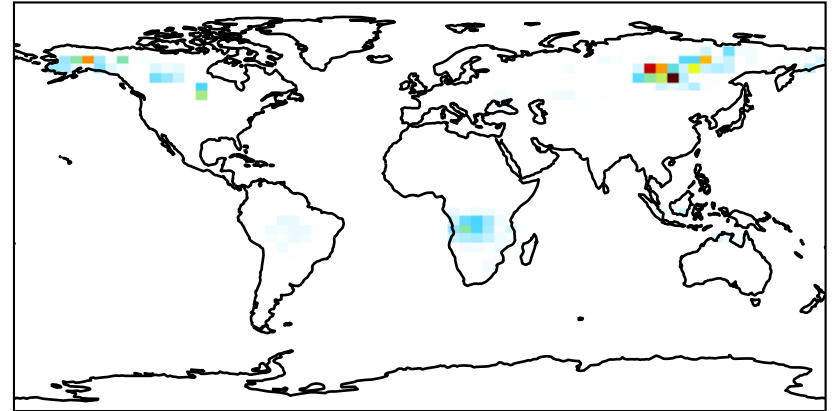


# EmisMOH\_BioBurn (Jul2019)

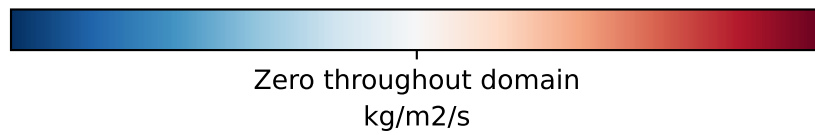
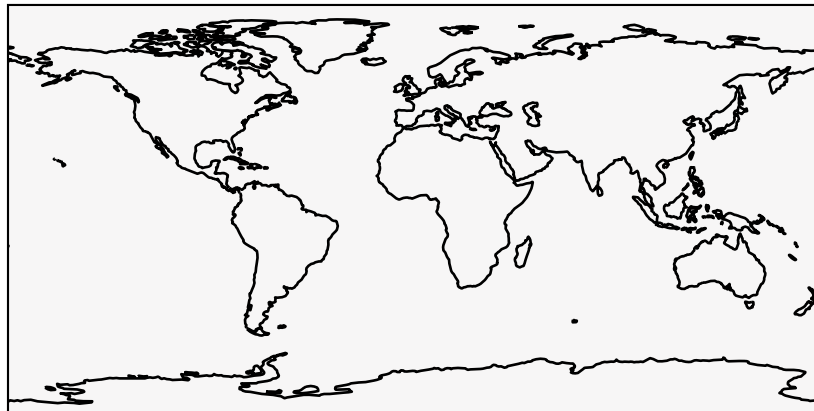
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



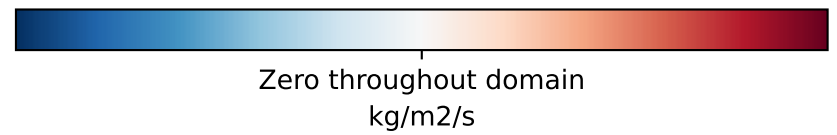
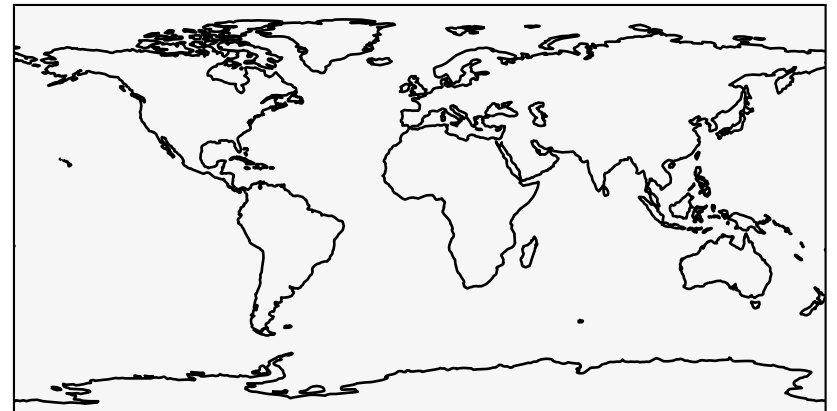
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



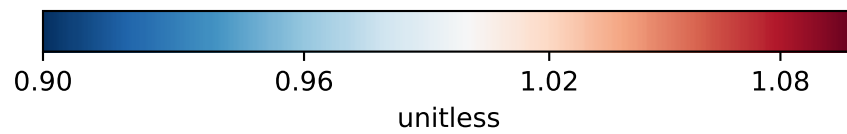
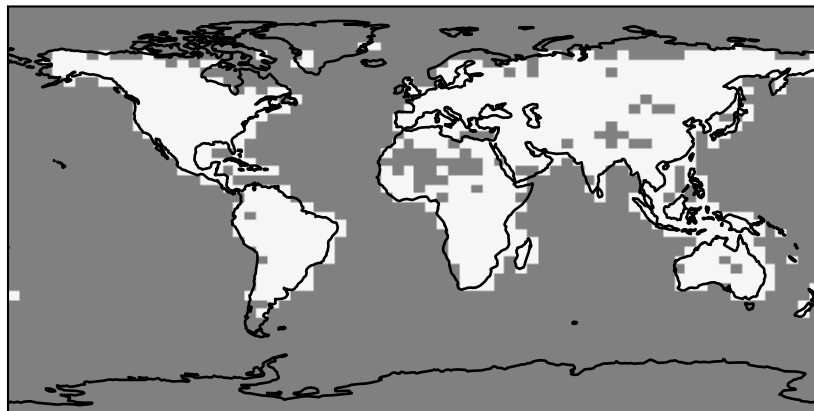
Difference  
Dev - Ref, Dynamic Range



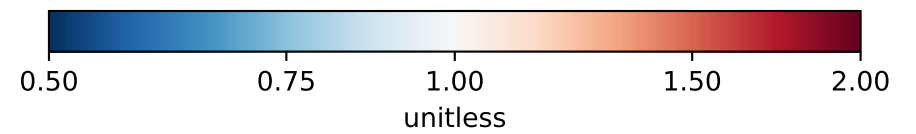
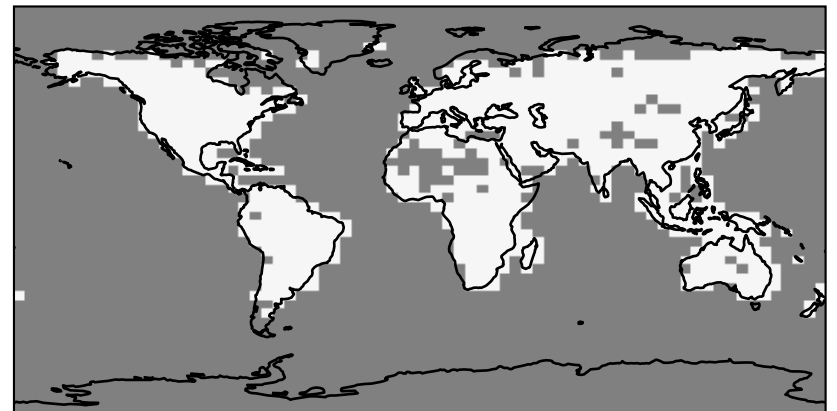
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

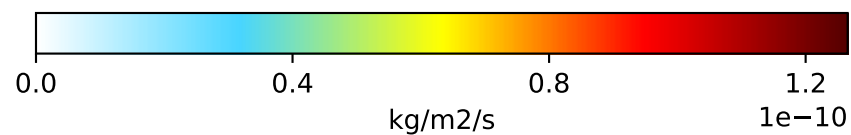
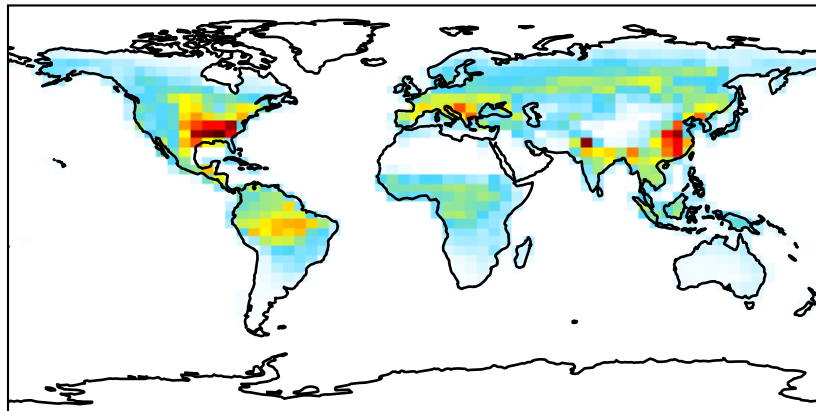


Ratio  
Dev/Ref, Fixed Range

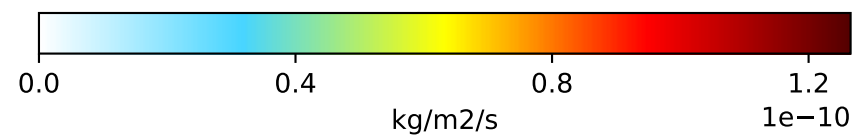
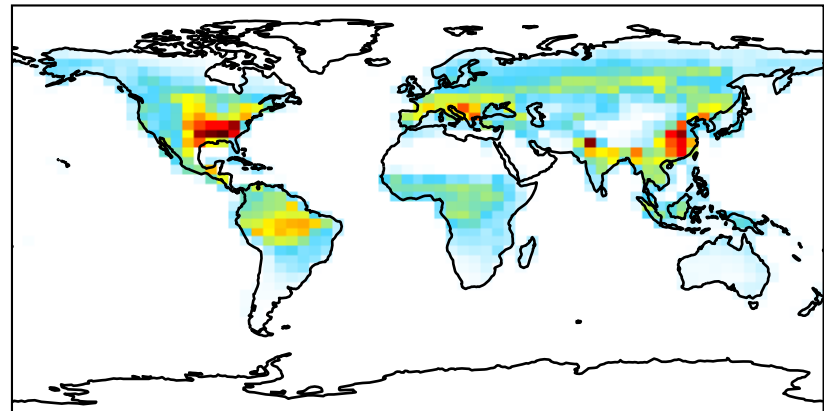


# EmisMOH\_Biogenic (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



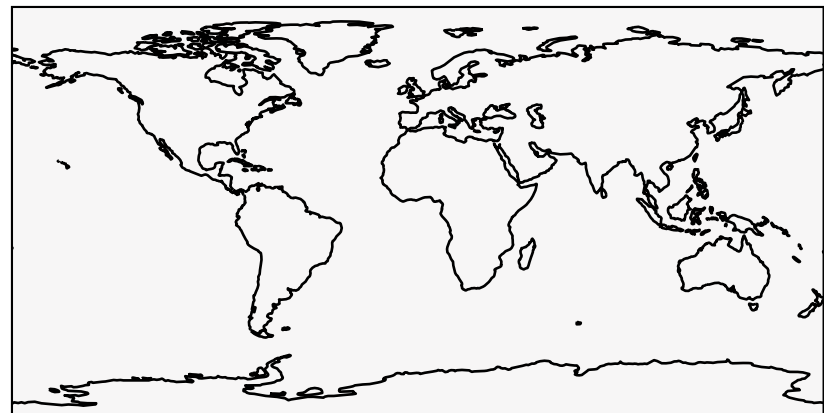
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



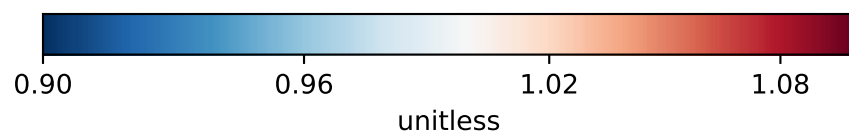
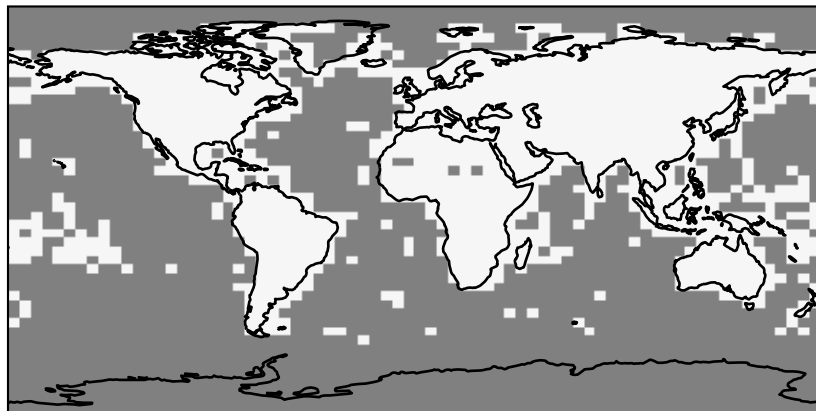
Difference  
Dev - Ref, Dynamic Range



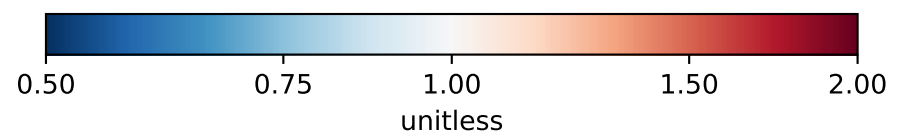
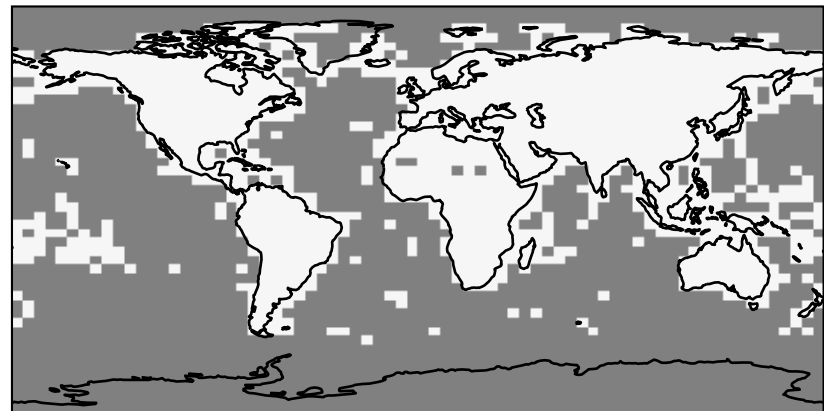
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



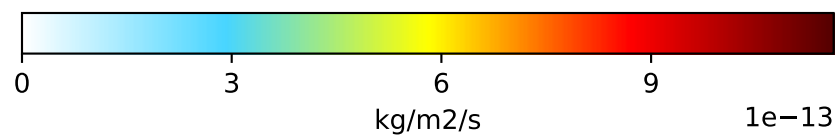
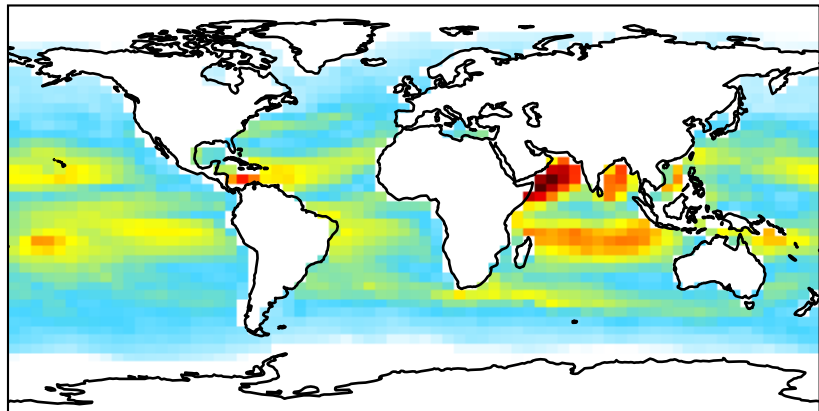
Ratio  
Dev/Ref, Fixed Range



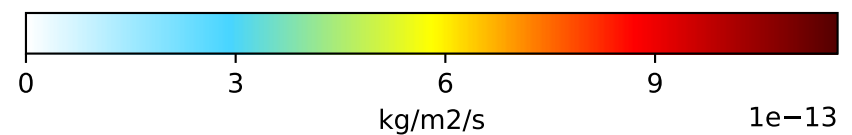
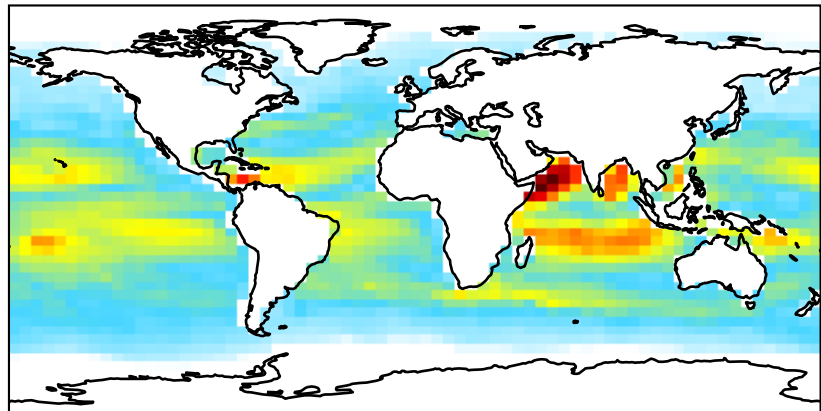


# EmisMOH\_Ocean (Jul2019)

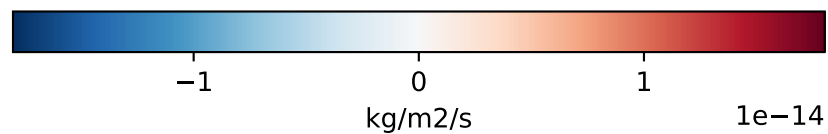
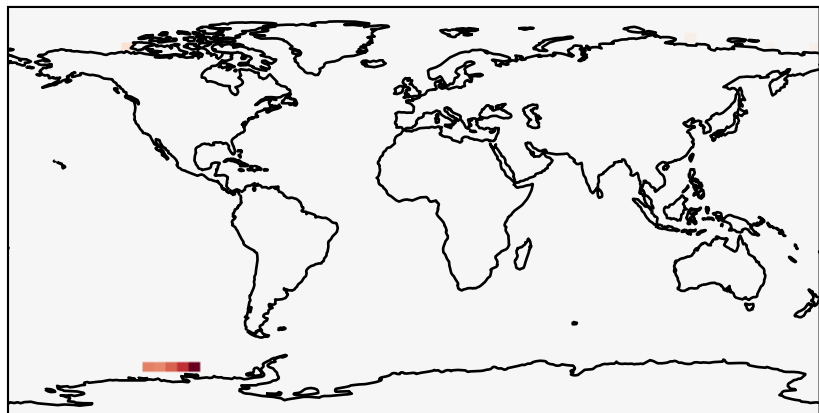
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



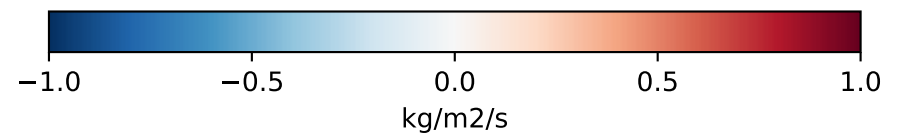
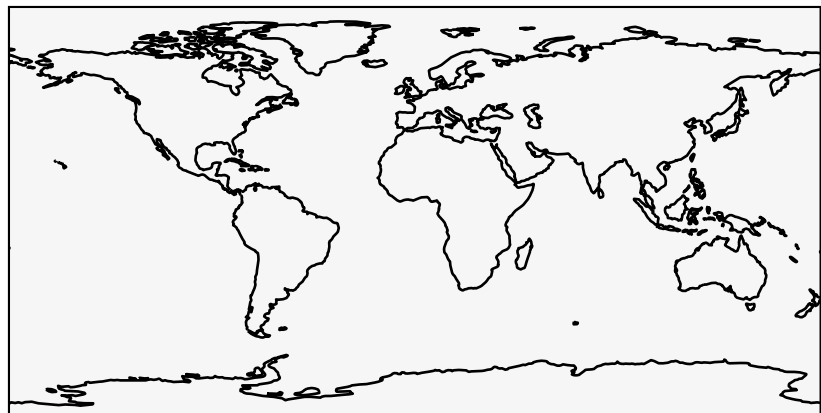
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



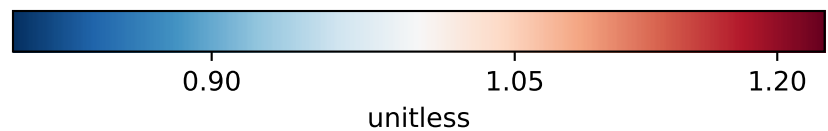
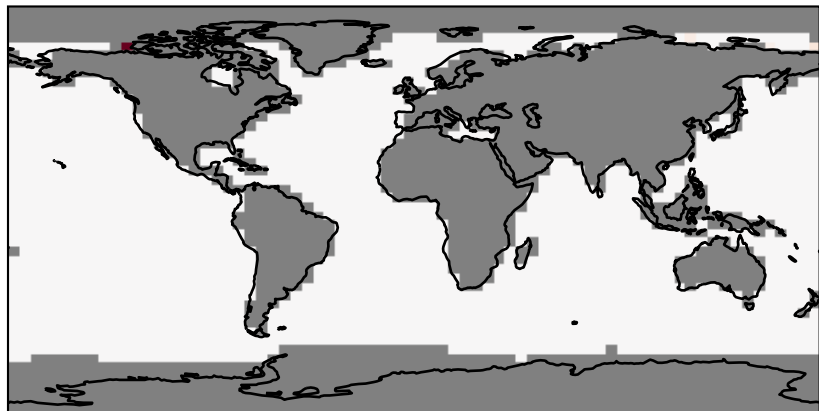
Difference  
Dev - Ref, Dynamic Range



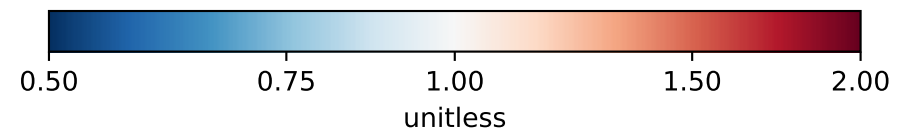
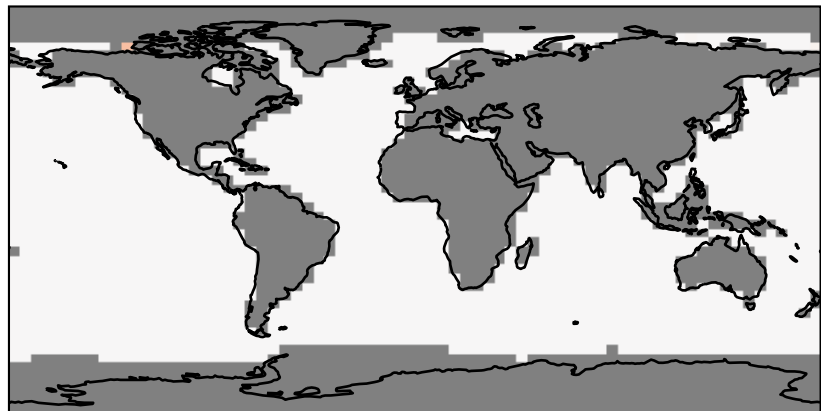
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

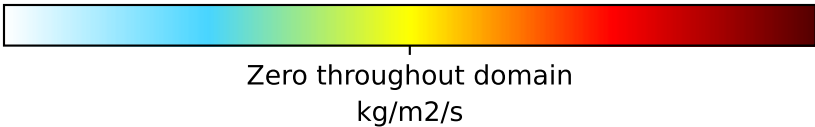
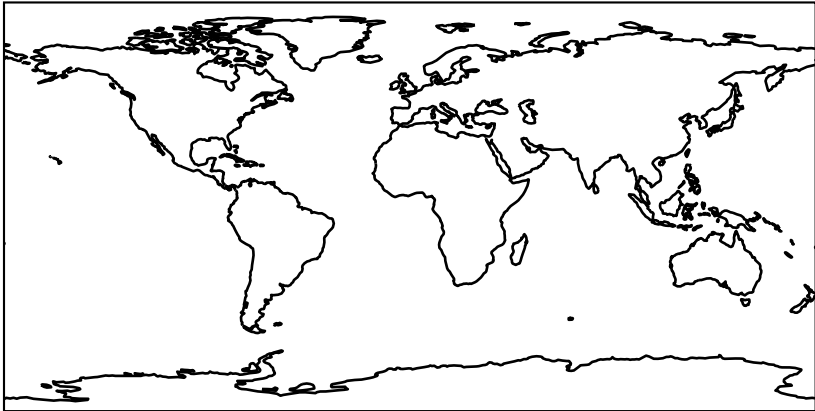


Ratio  
Dev/Ref, Fixed Range

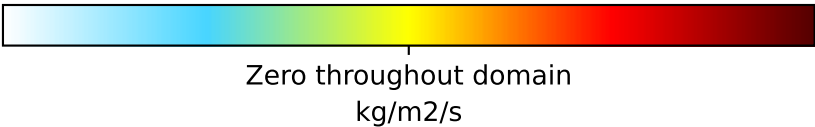
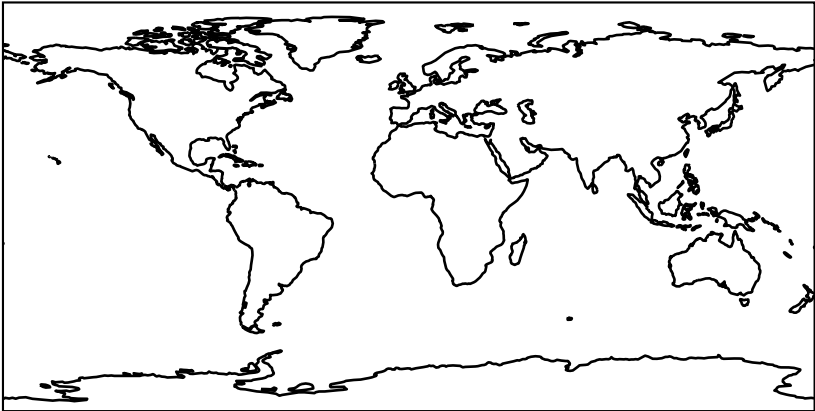


# EmisMOH\_Ship (Jul2019)

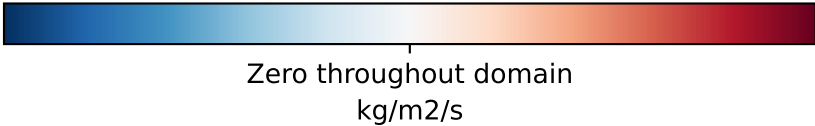
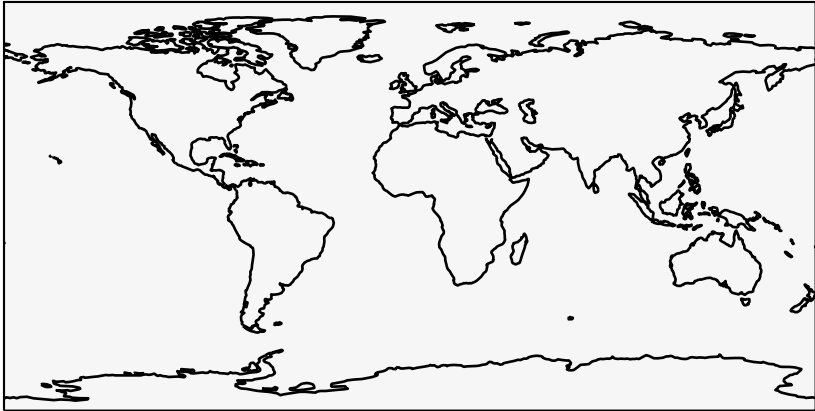
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



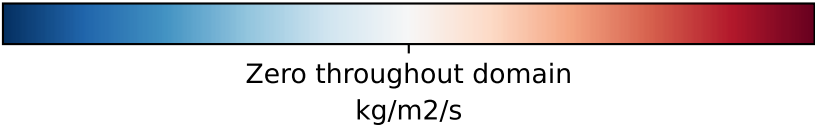
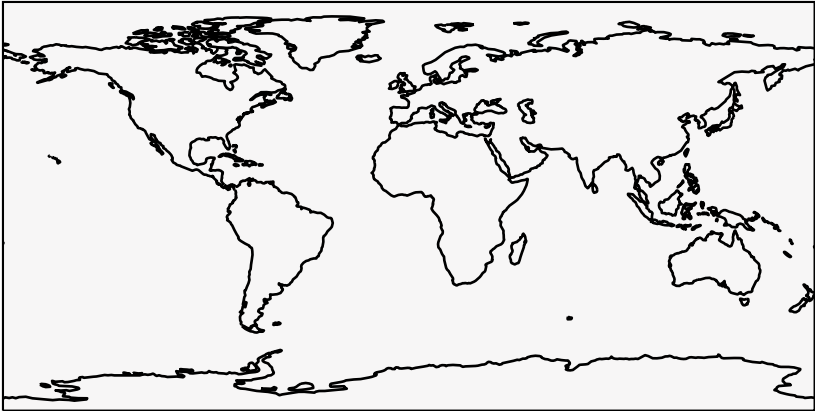
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



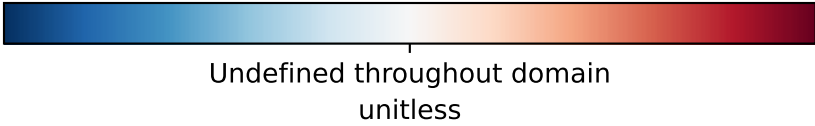
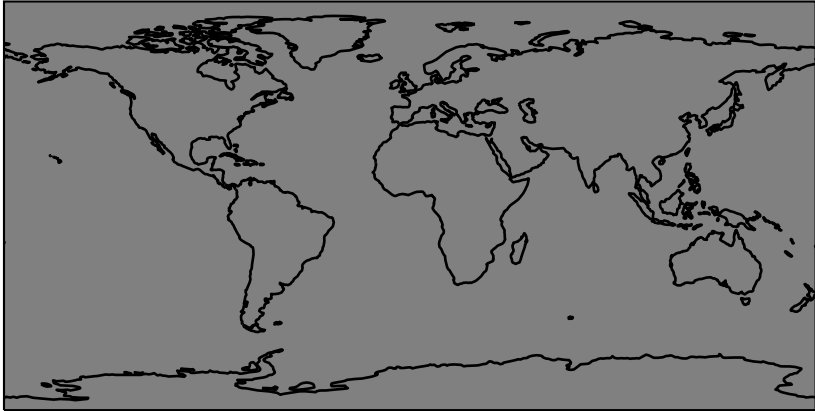
Difference  
Dev - Ref, Dynamic Range



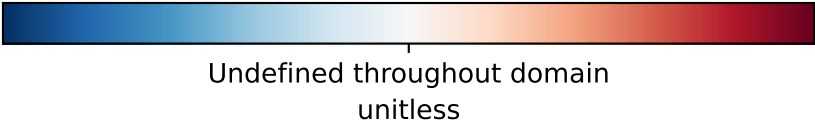
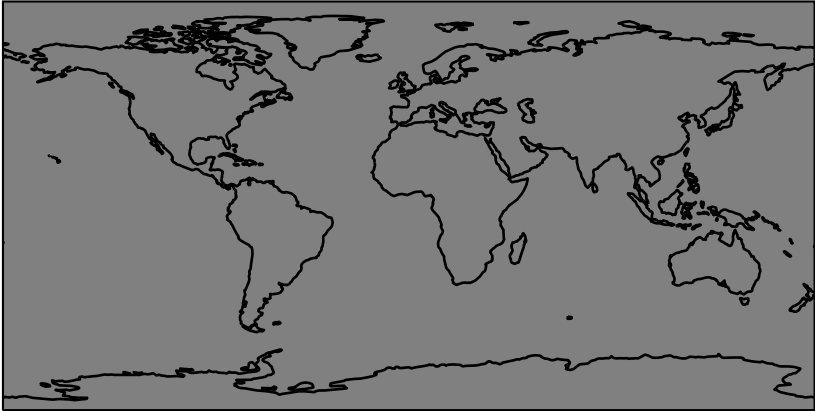
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

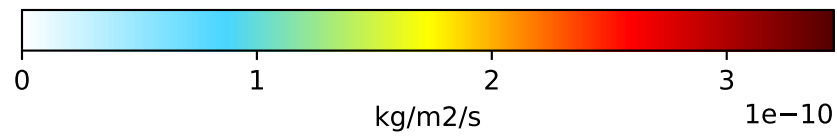
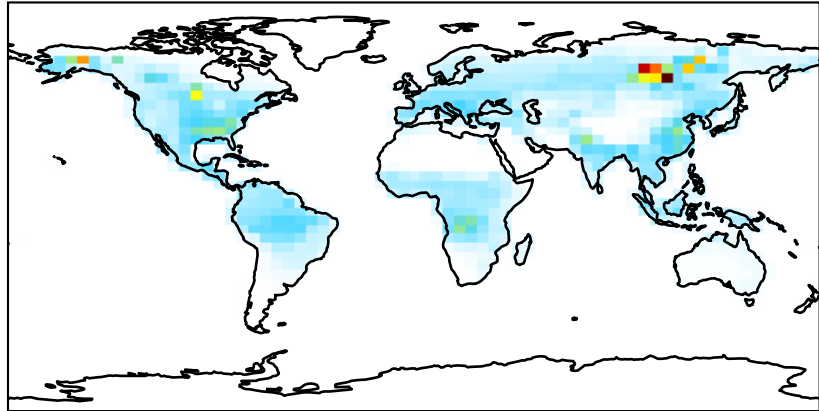


Ratio  
Dev/Ref, Fixed Range

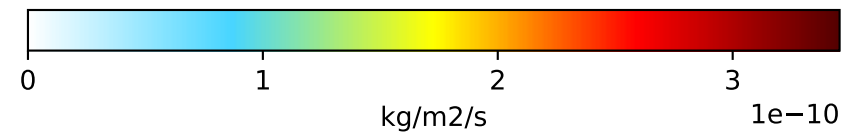
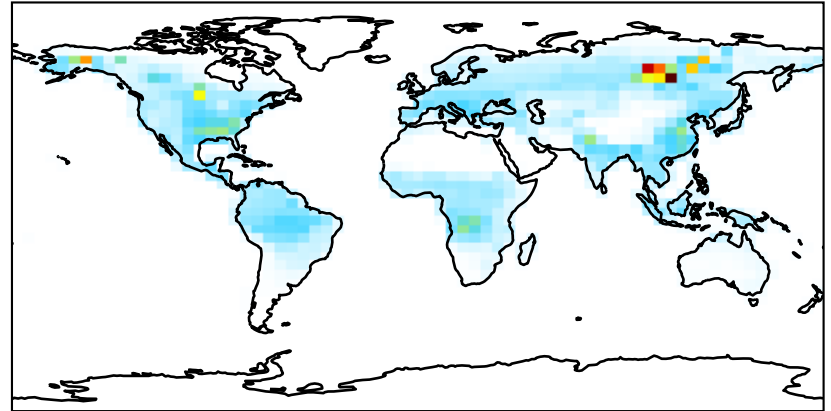


# EmisMOH\_Total (Jul2019)

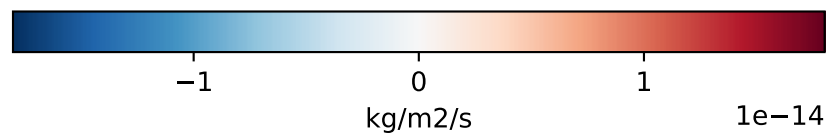
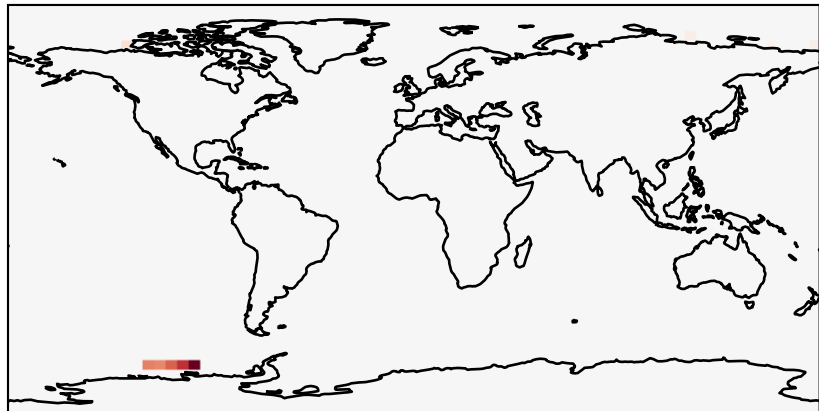
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



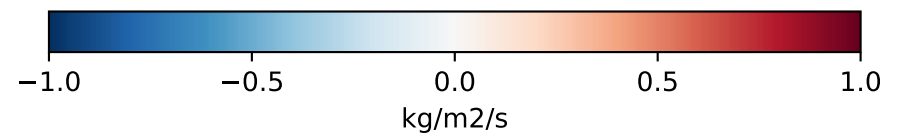
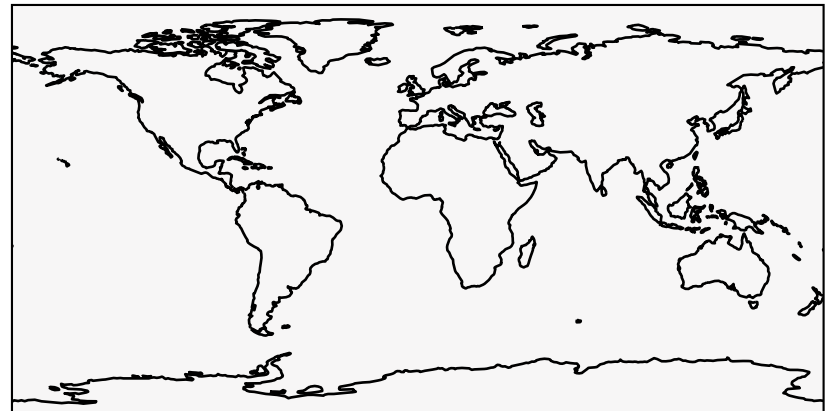
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



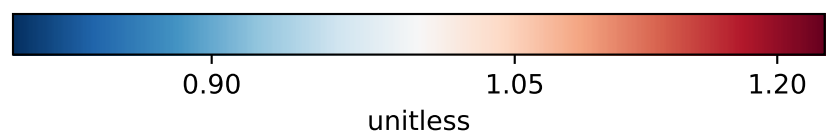
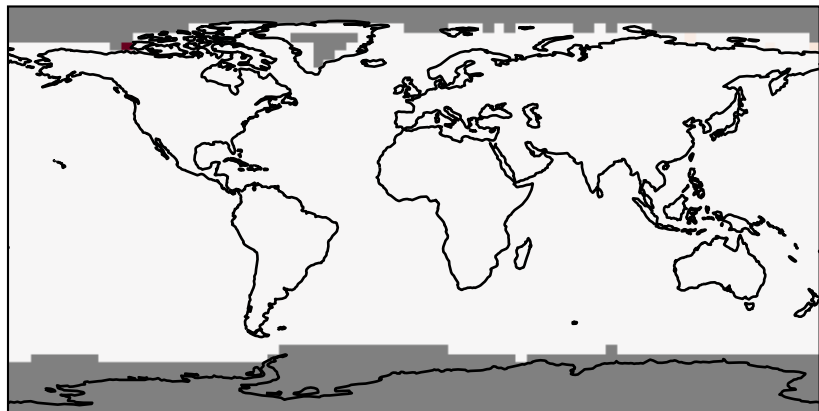
Difference  
Dev - Ref, Dynamic Range



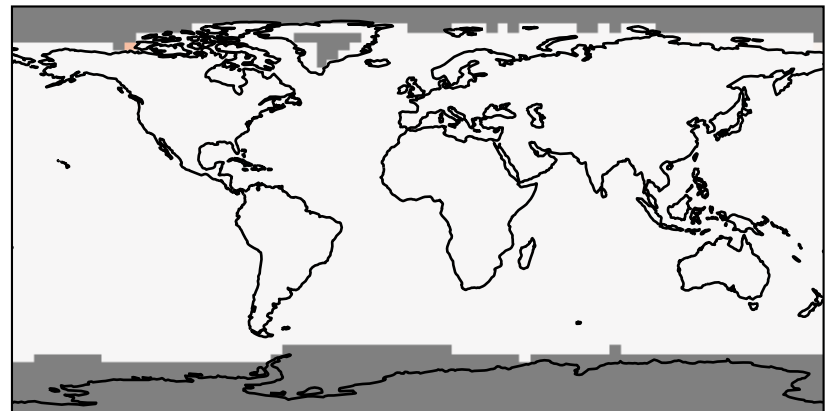
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

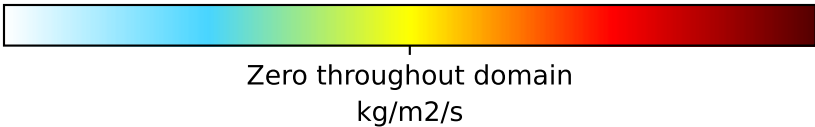
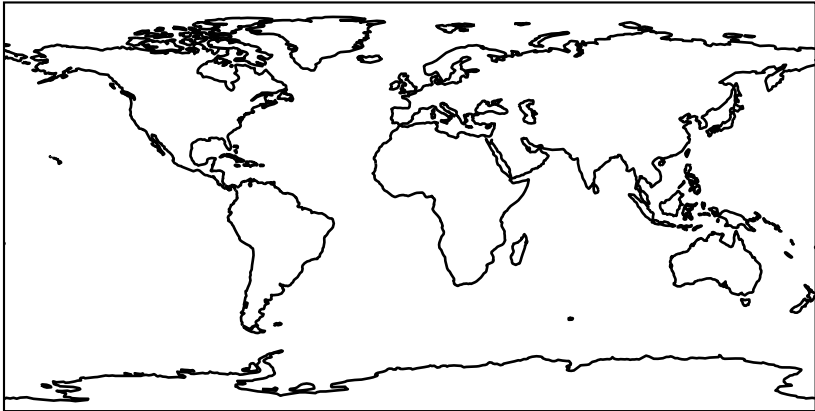


Ratio  
Dev/Ref, Fixed Range

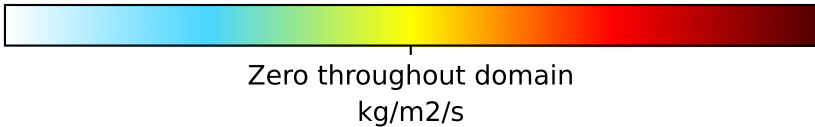
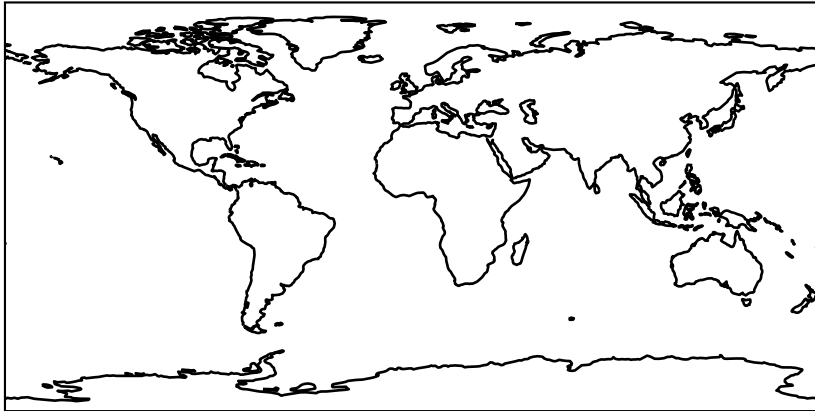


# EmisISOP\_Anthro (Jul2019)

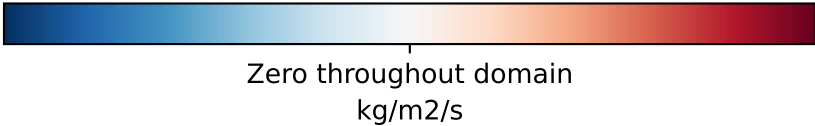
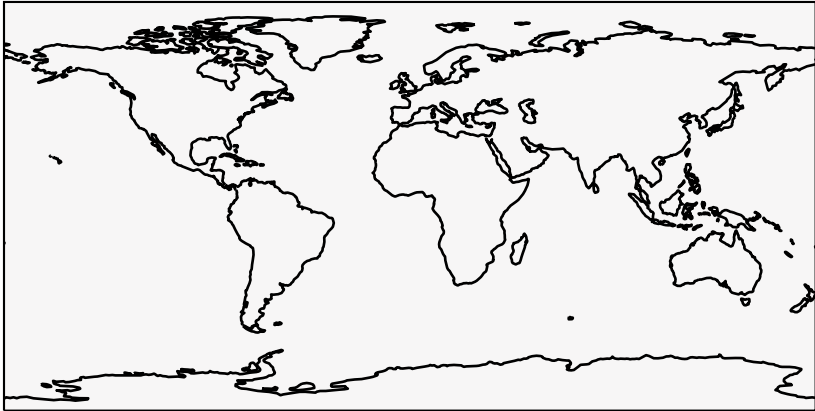
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



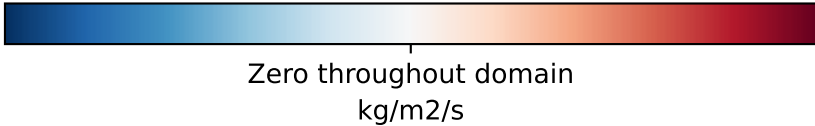
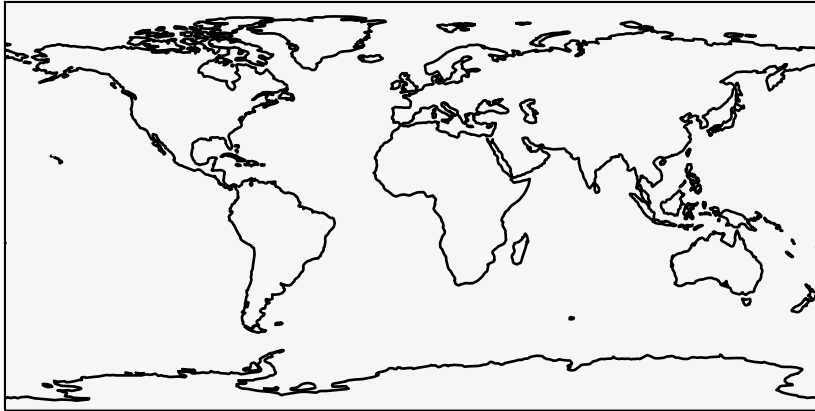
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



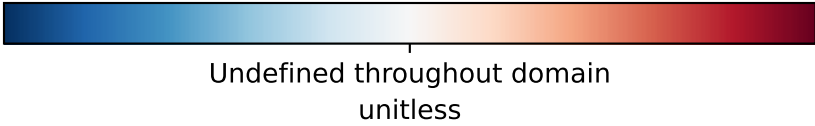
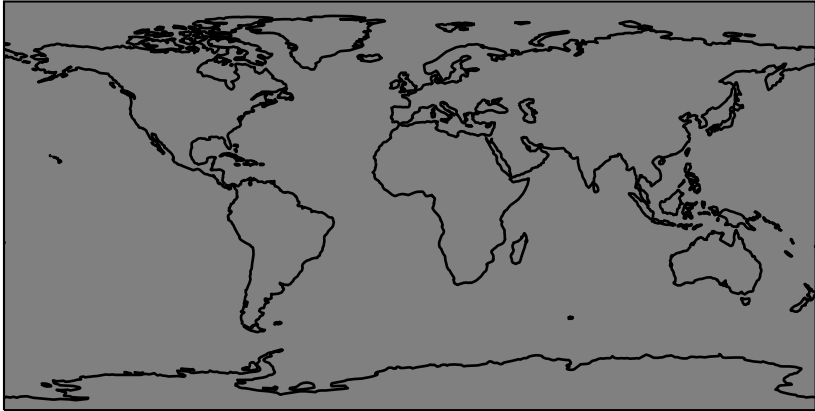
Difference  
Dev - Ref, Dynamic Range



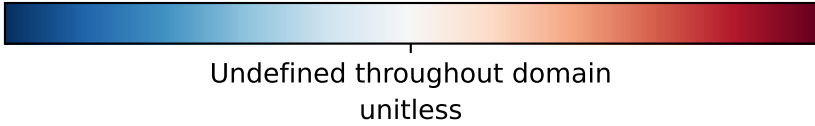
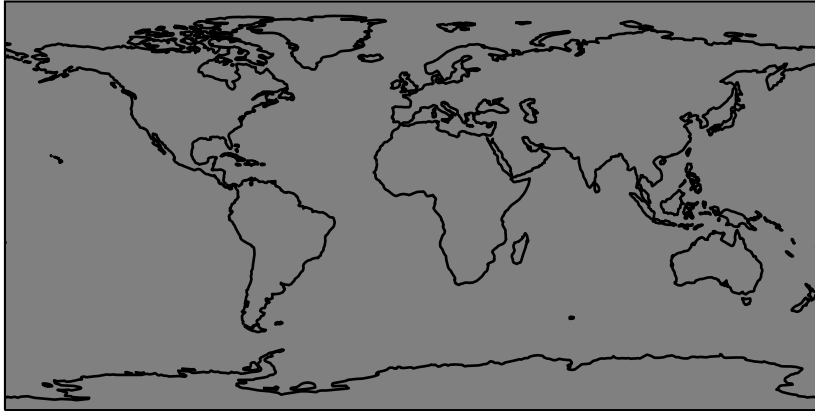
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

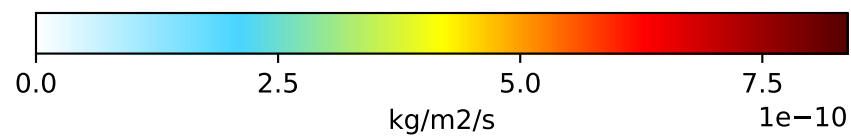
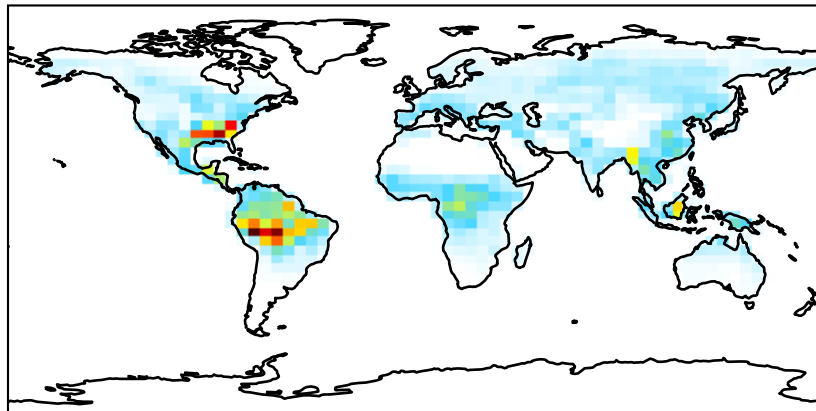


Ratio  
Dev/Ref, Fixed Range

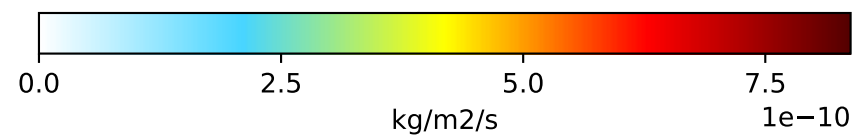
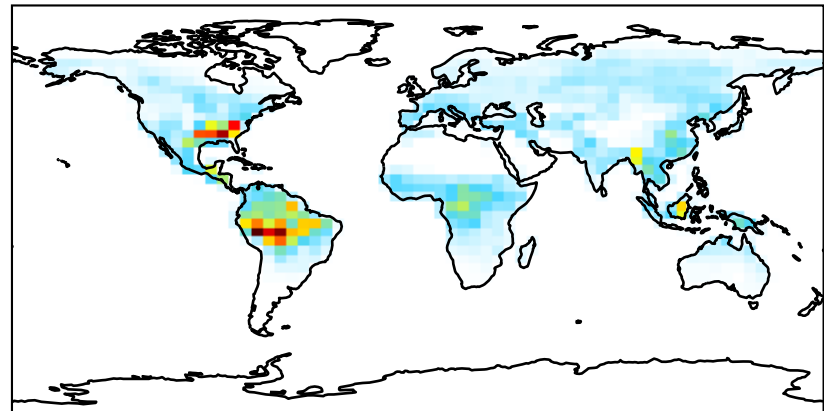


# EmisSOP\_Biogenic (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



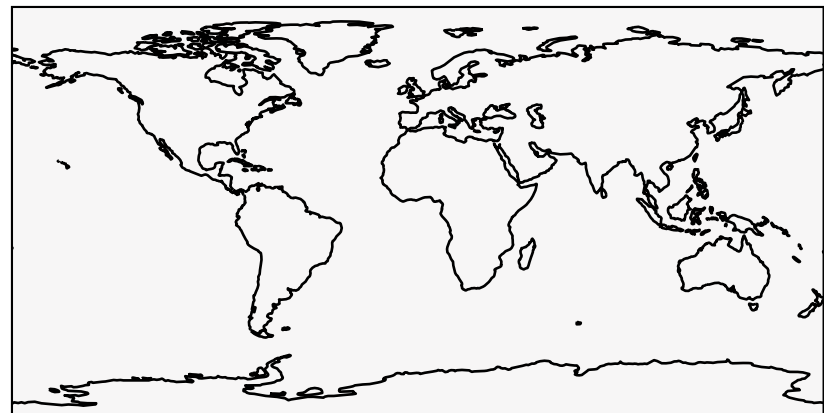
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



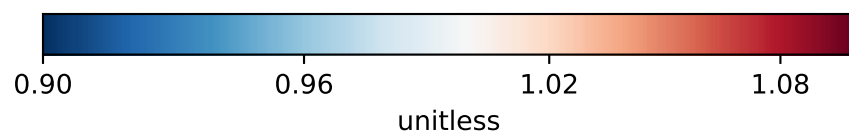
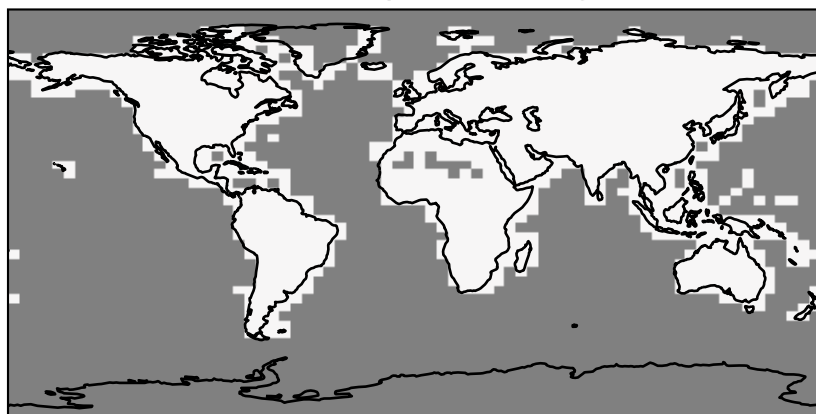
Difference  
Dev - Ref, Dynamic Range



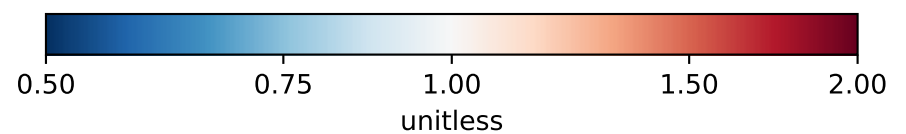
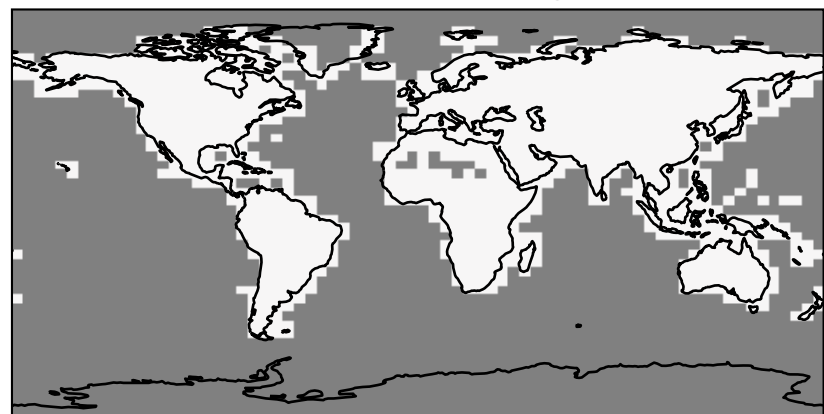
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



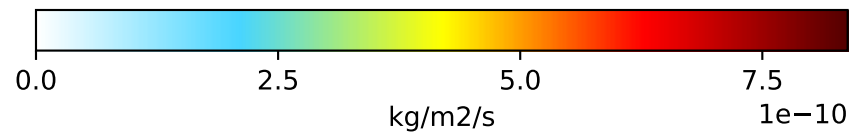
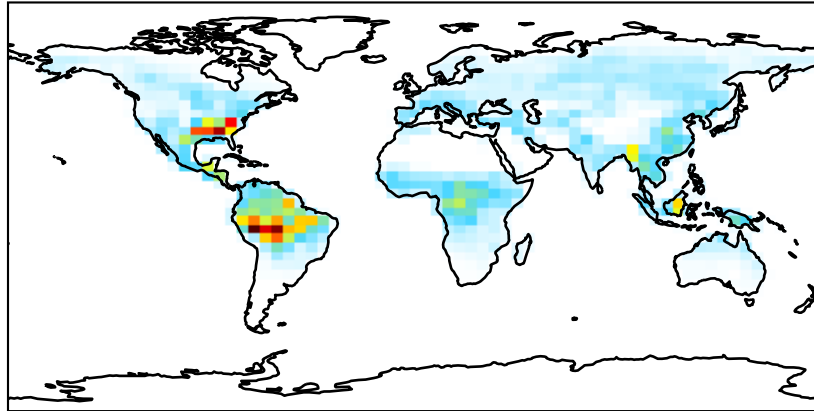
Ratio  
Dev/Ref, Fixed Range



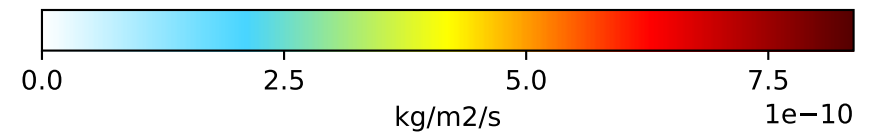
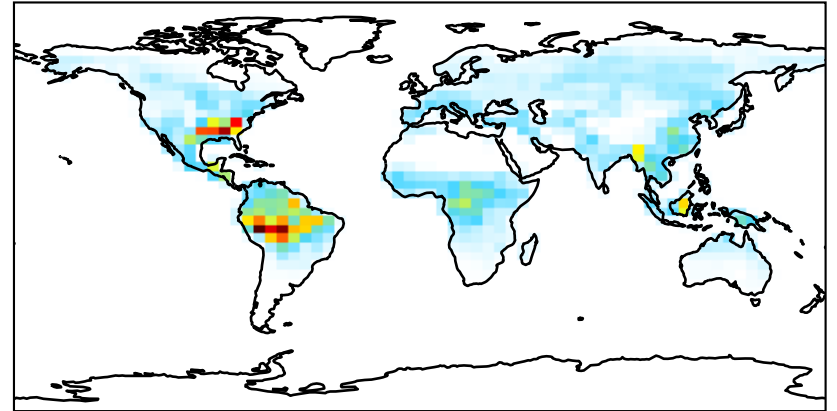


# EmisISOP\_Total (Jul2019)

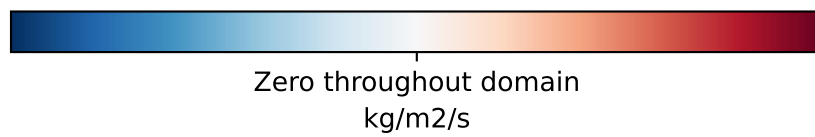
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



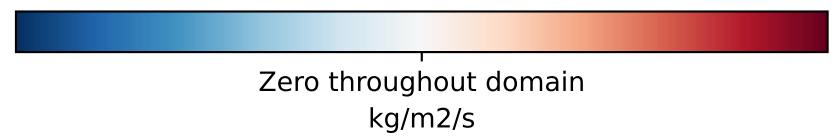
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



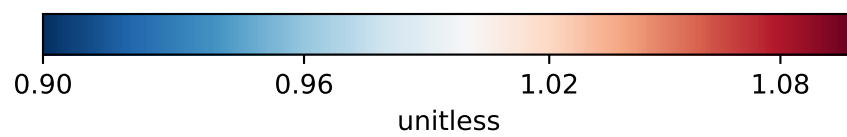
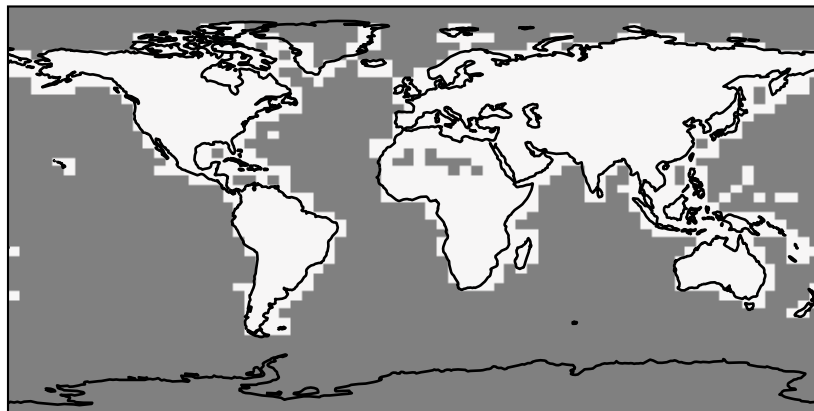
Difference  
Dev - Ref, Dynamic Range



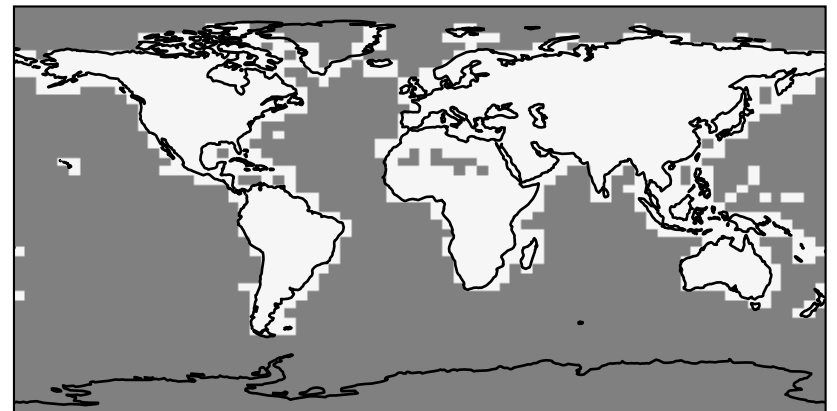
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

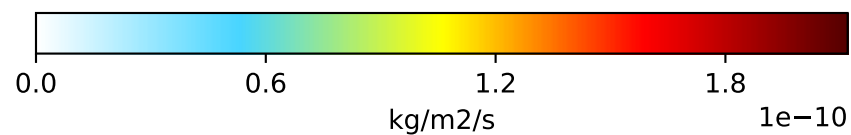


Ratio  
Dev/Ref, Fixed Range

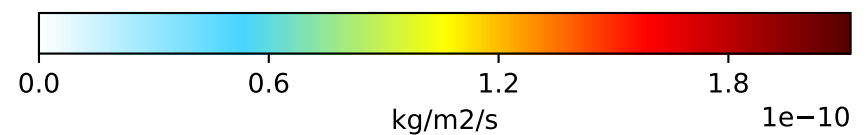
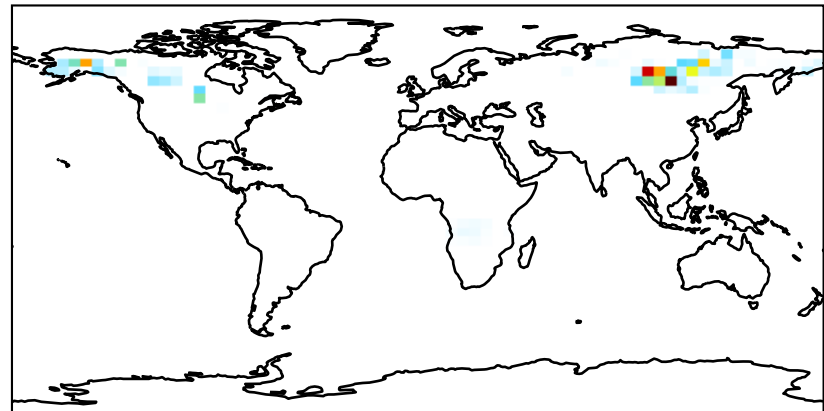


# EmisMTPA\_BioBurn (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



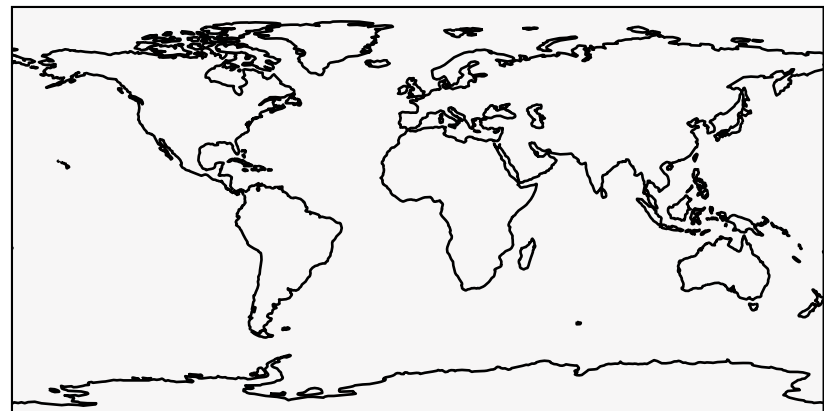
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



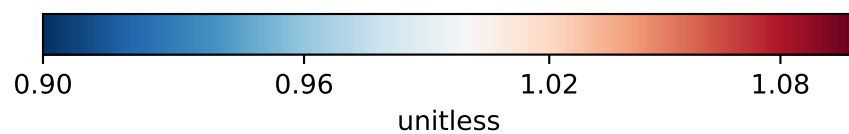
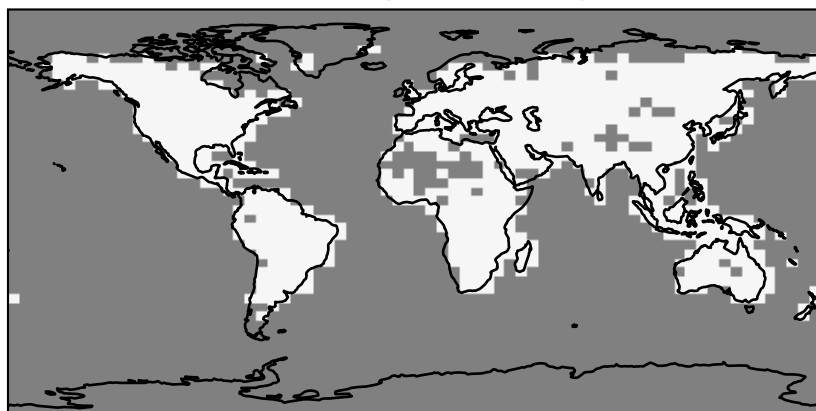
Difference  
Dev - Ref, Dynamic Range



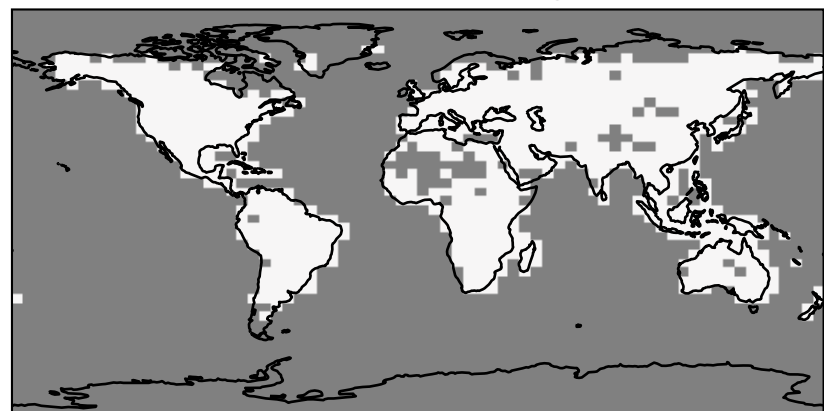
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

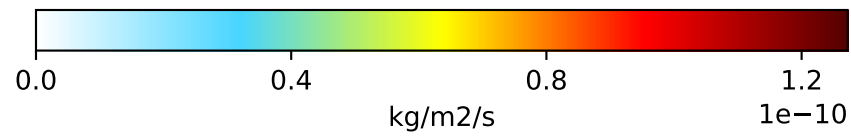
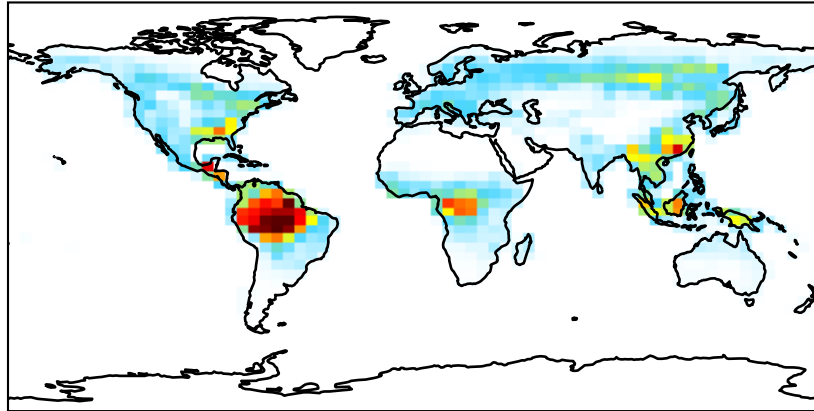


Ratio  
Dev/Ref, Fixed Range

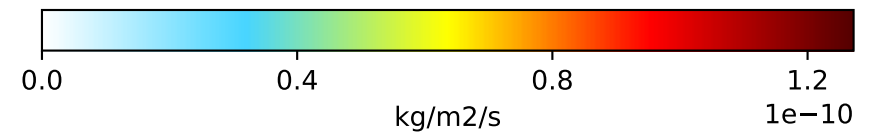
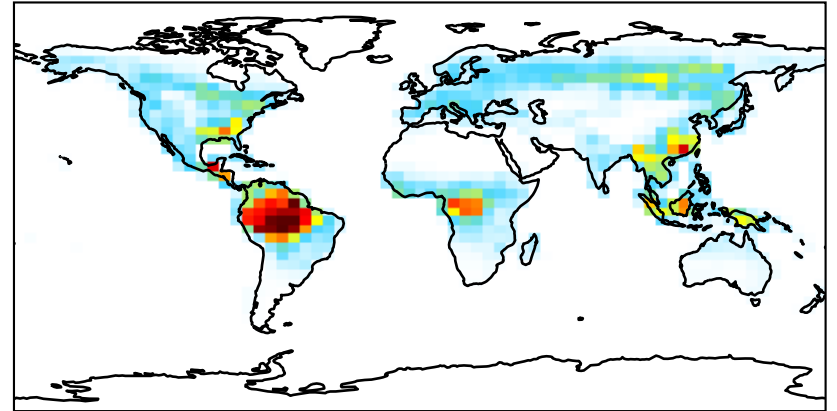


# EmisMTPA\_Biogenic (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



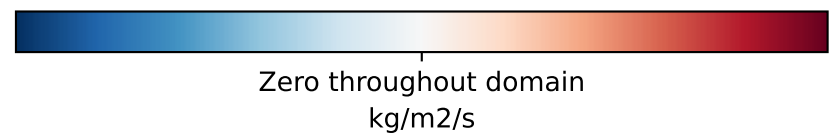
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



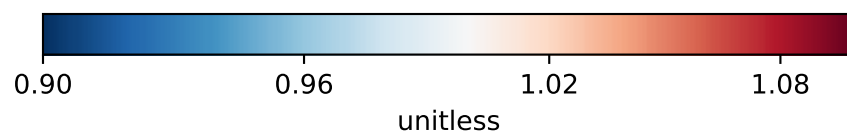
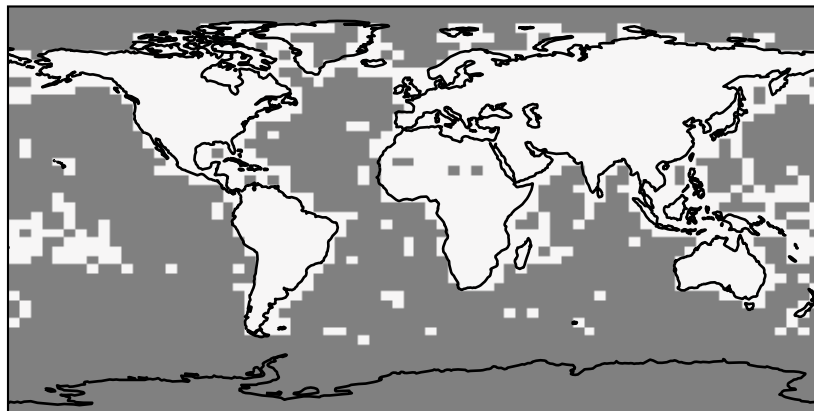
Difference  
Dev - Ref, Dynamic Range



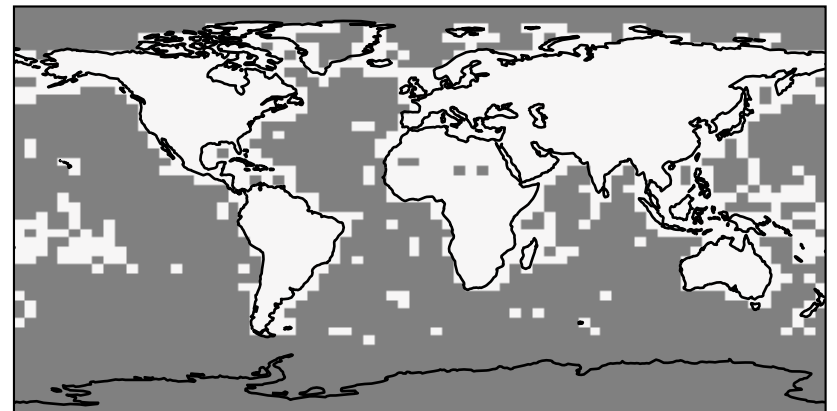
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

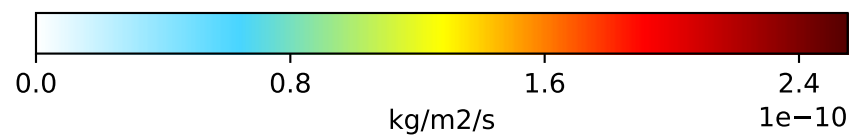
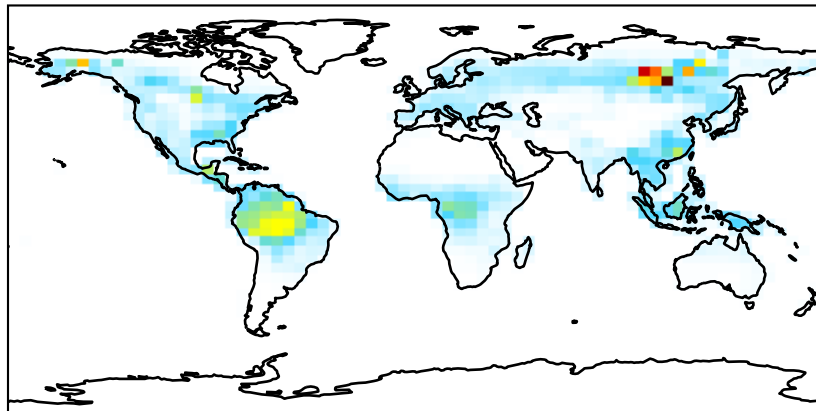


Ratio  
Dev/Ref, Fixed Range

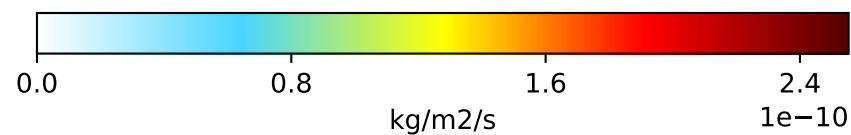
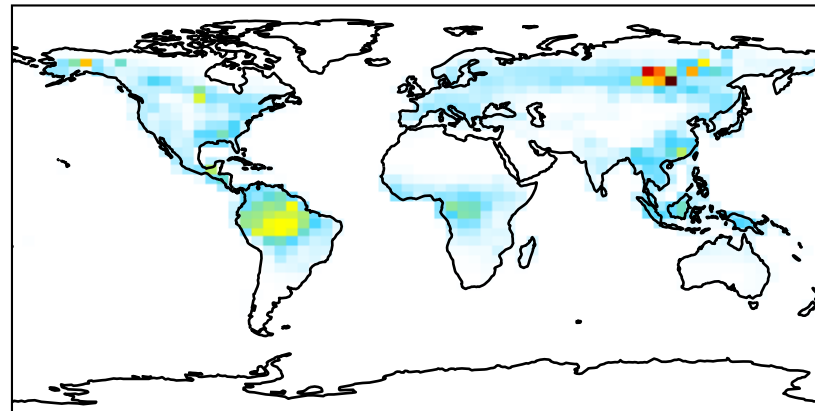


# EmisMTPA\_Total (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



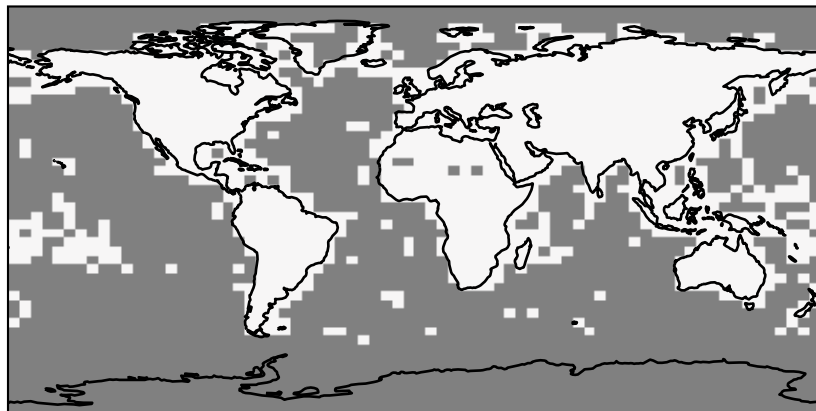
Difference  
Dev - Ref, Dynamic Range



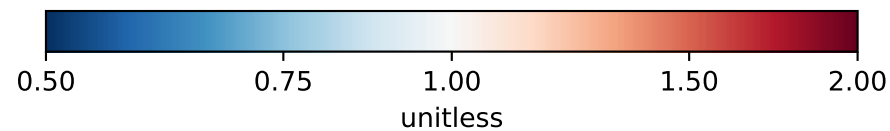
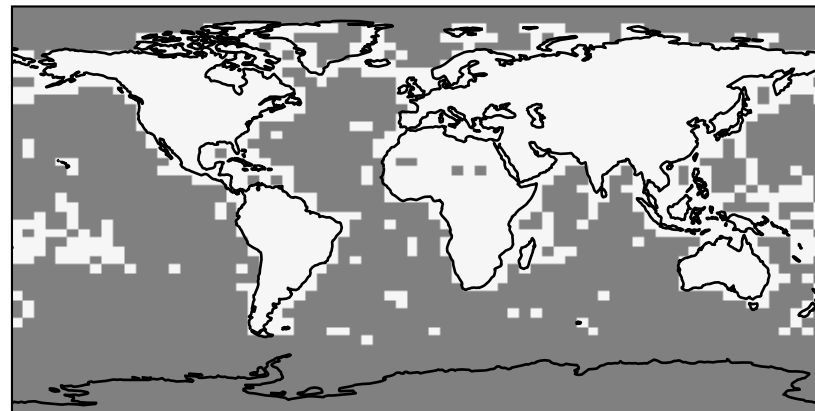
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

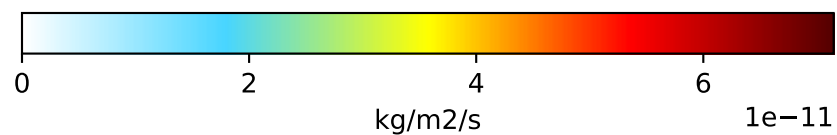
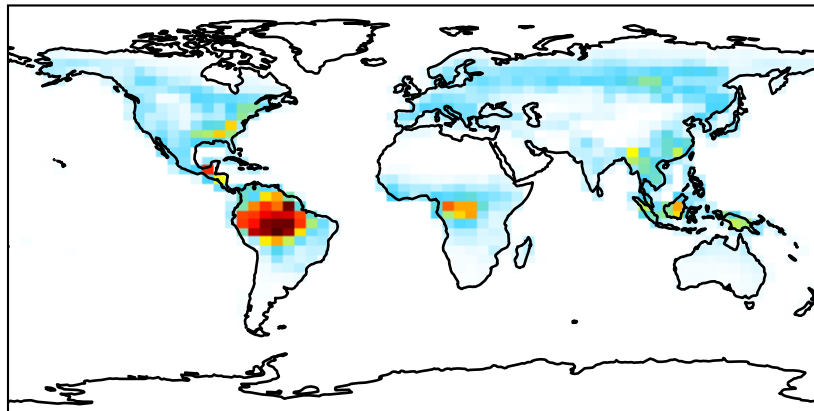


Ratio  
Dev/Ref, Fixed Range

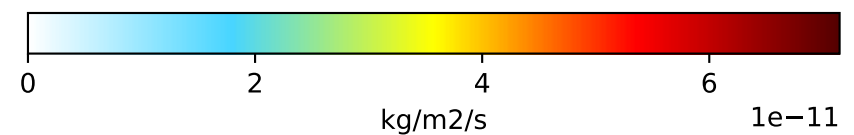
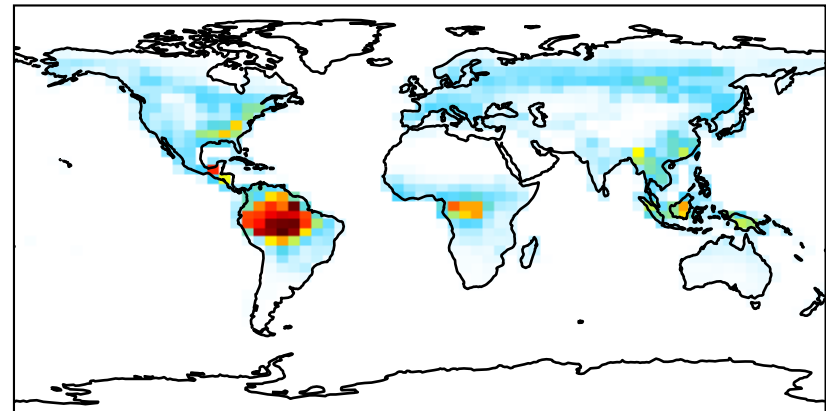


# EmisMTPO\_Biogenic (Jul2019)

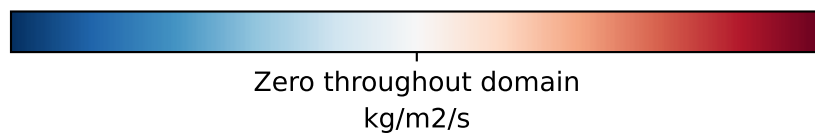
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



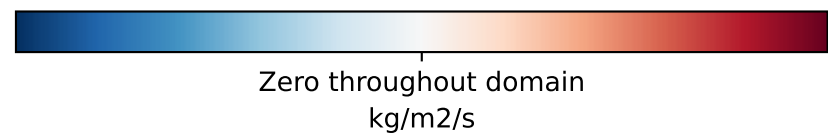
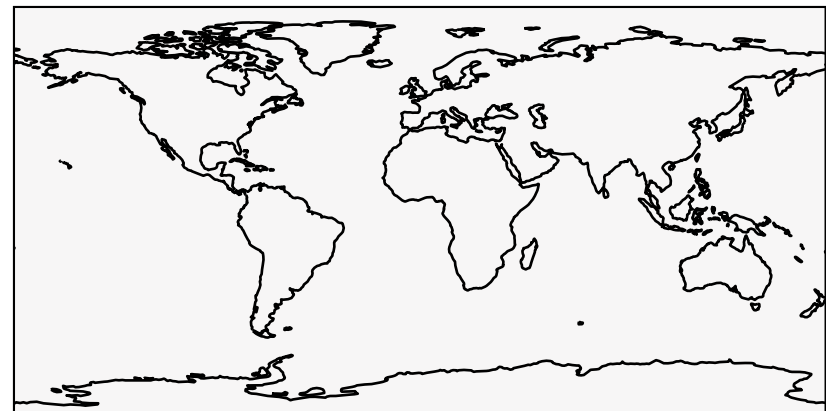
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



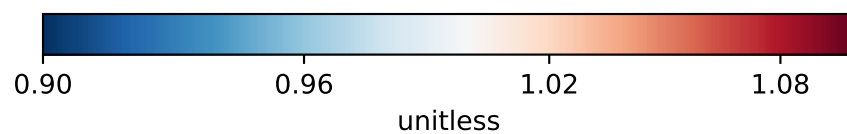
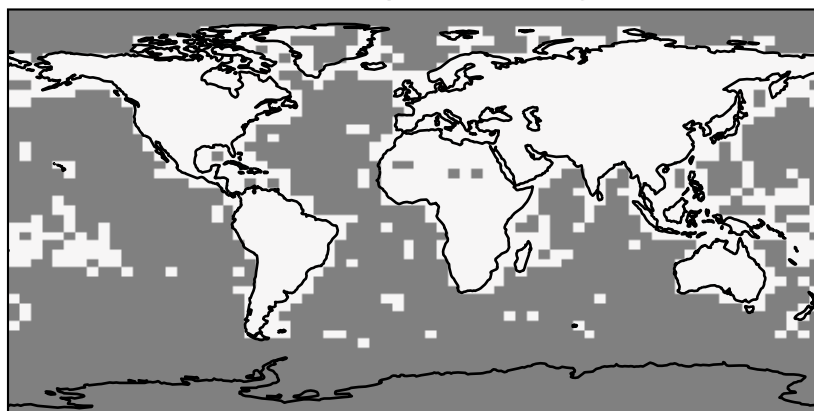
Difference  
Dev - Ref, Dynamic Range



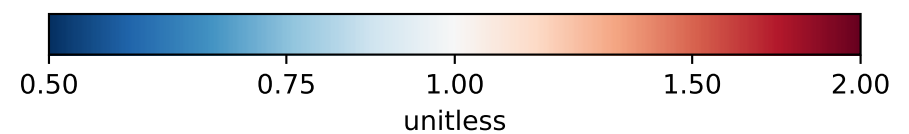
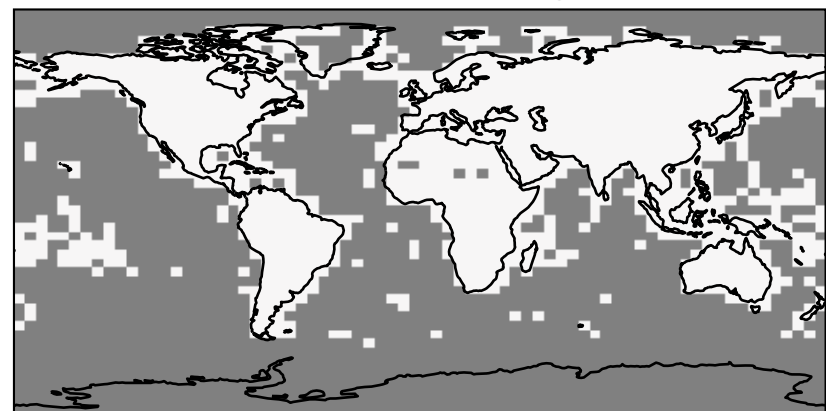
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



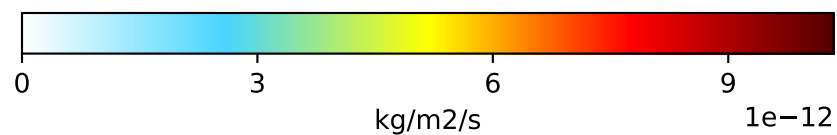
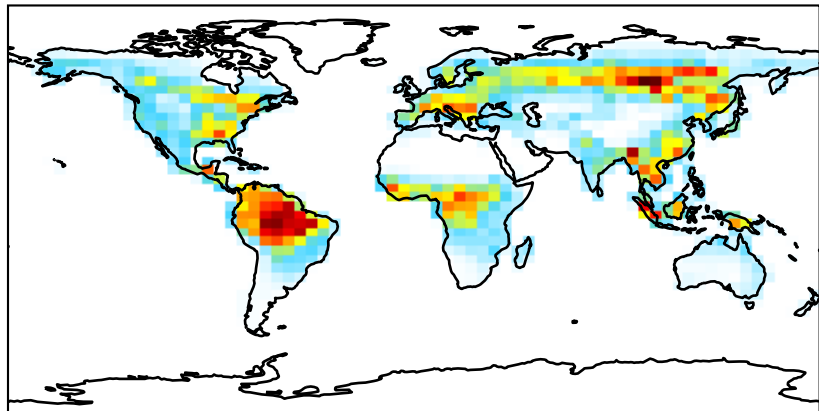
Ratio  
Dev/Ref, Fixed Range



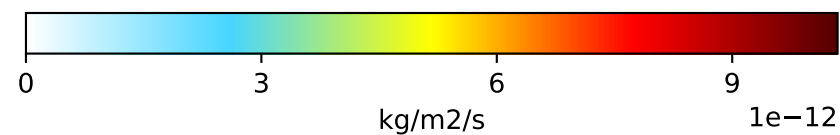
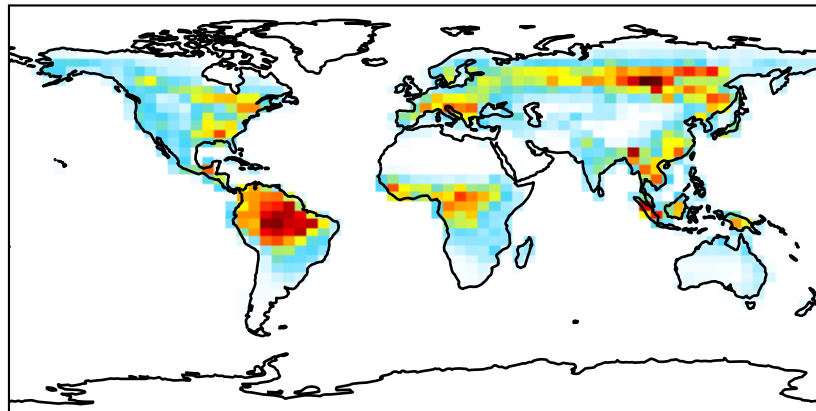


# EmisLIMO\_Biogenic (Jul2019)

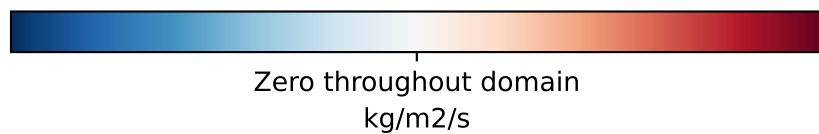
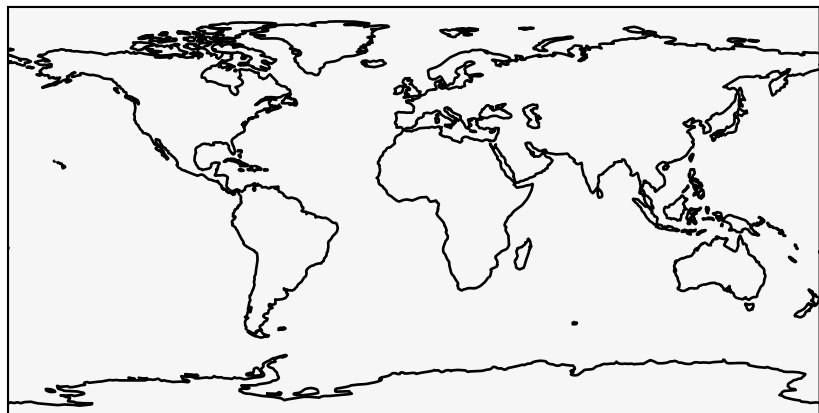
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



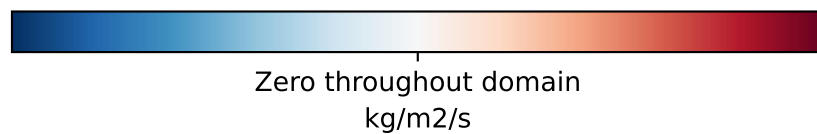
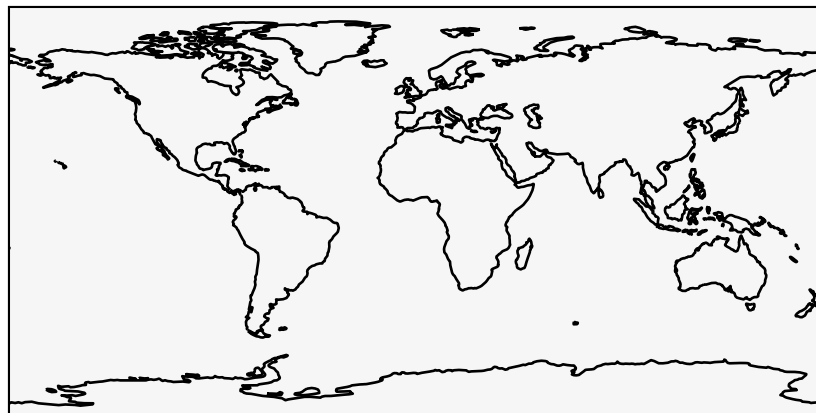
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



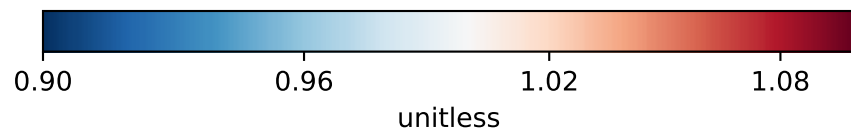
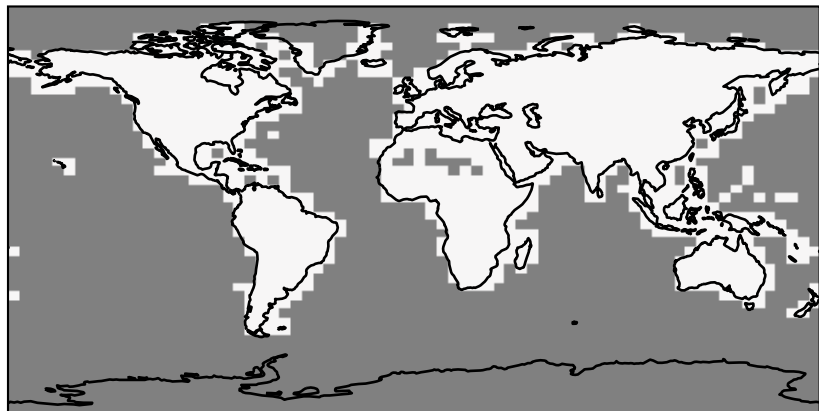
Difference  
Dev - Ref, Dynamic Range



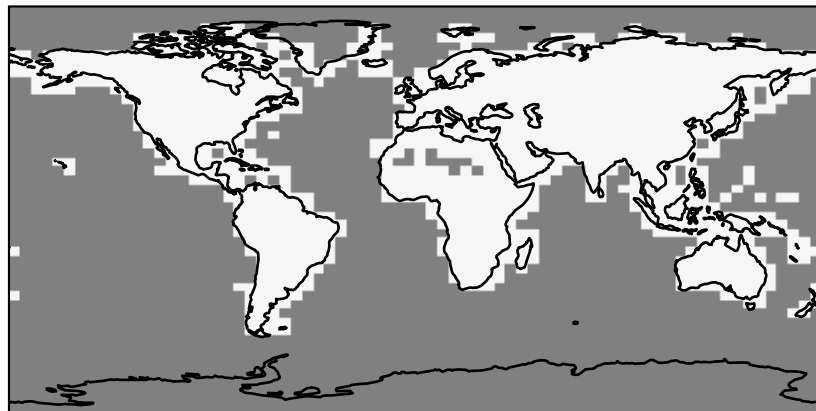
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

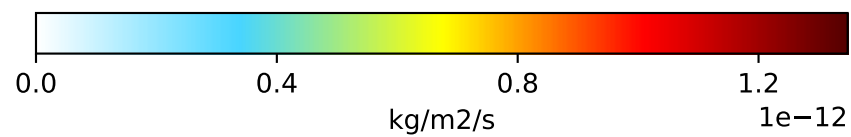
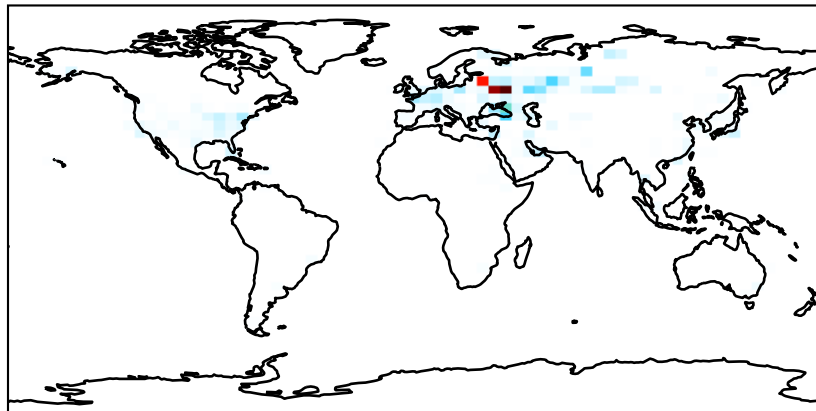


Ratio  
Dev/Ref, Fixed Range

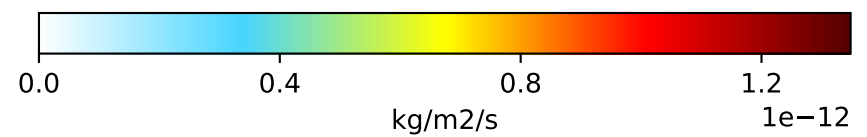


# EmisALK4\_Aircraft (Jul2019)

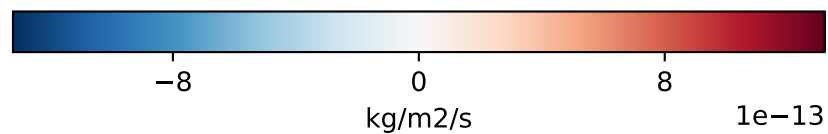
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



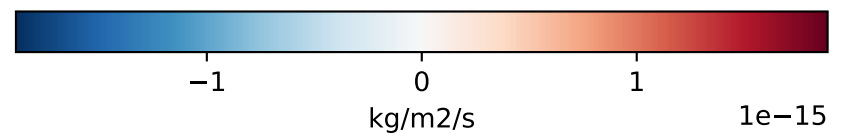
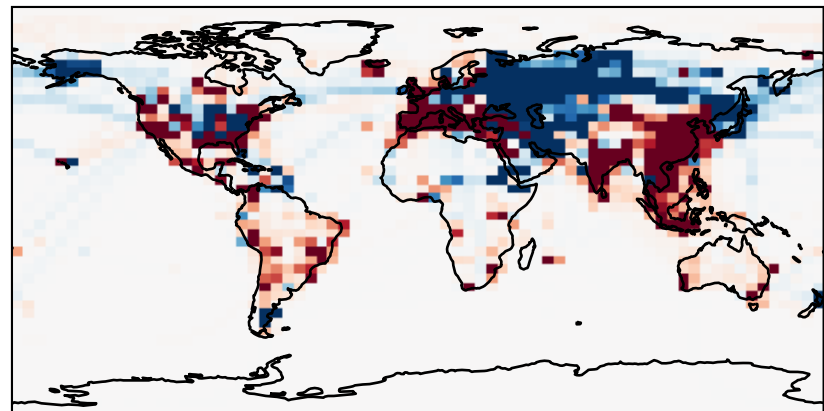
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



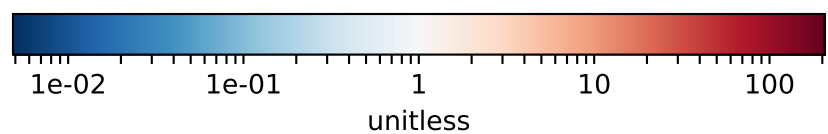
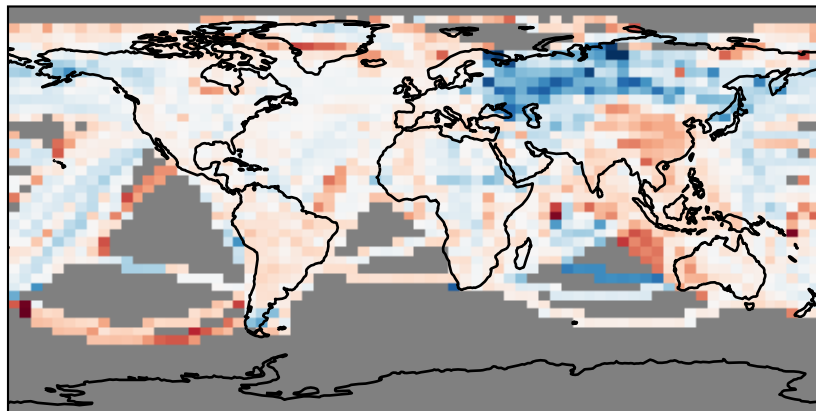
Difference  
Dev - Ref, Dynamic Range



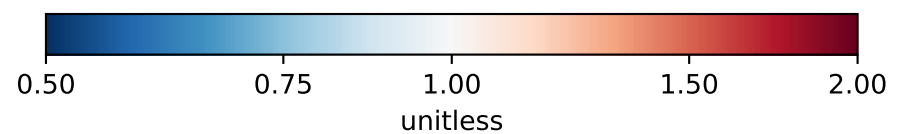
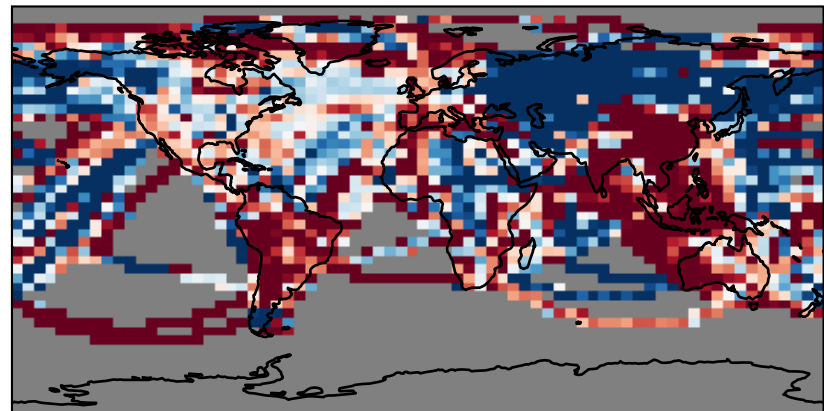
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

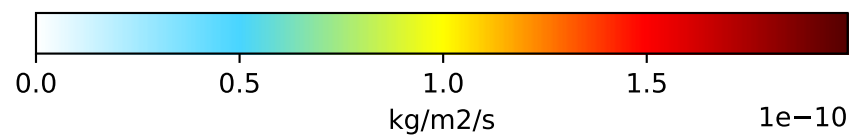
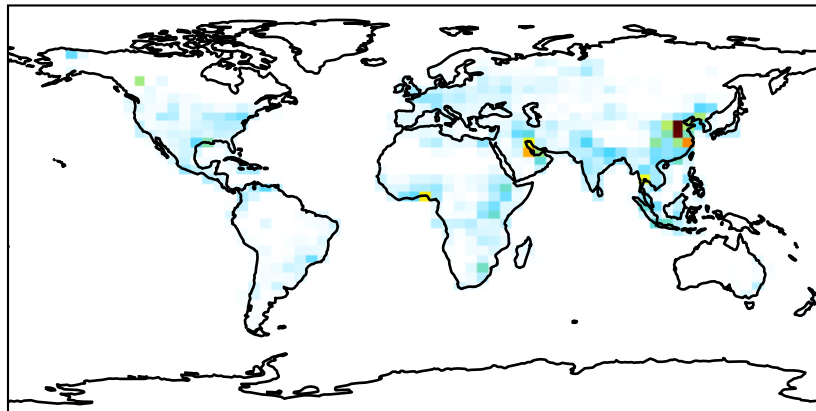


Ratio  
Dev/Ref, Fixed Range

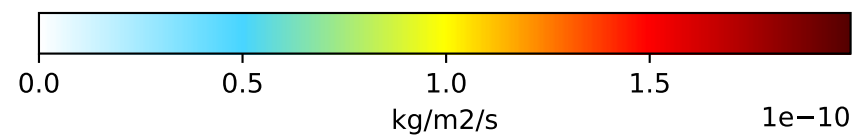
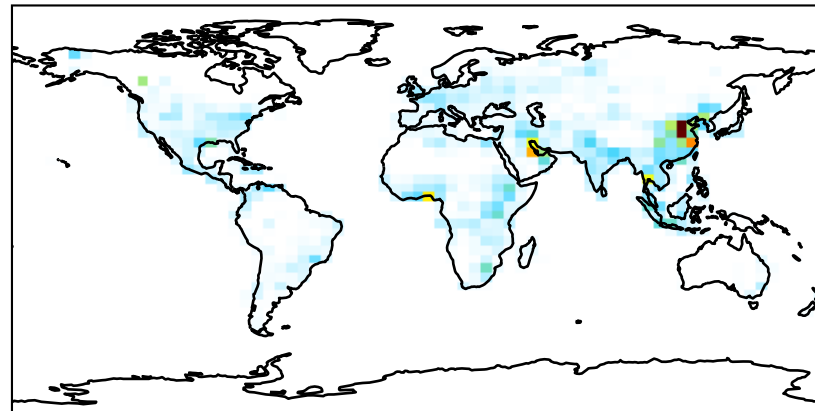


# EmisALK4\_Anthro (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



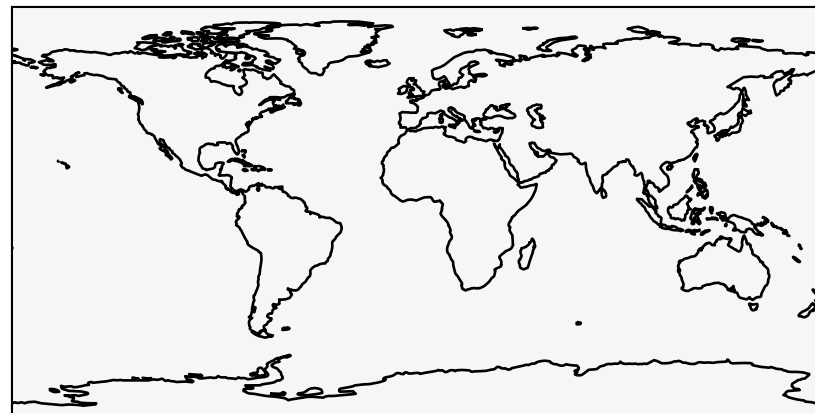
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



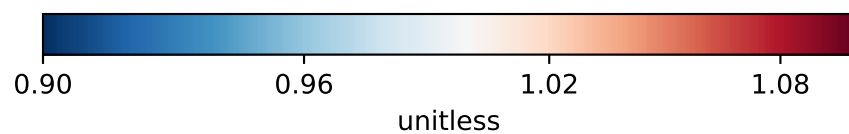
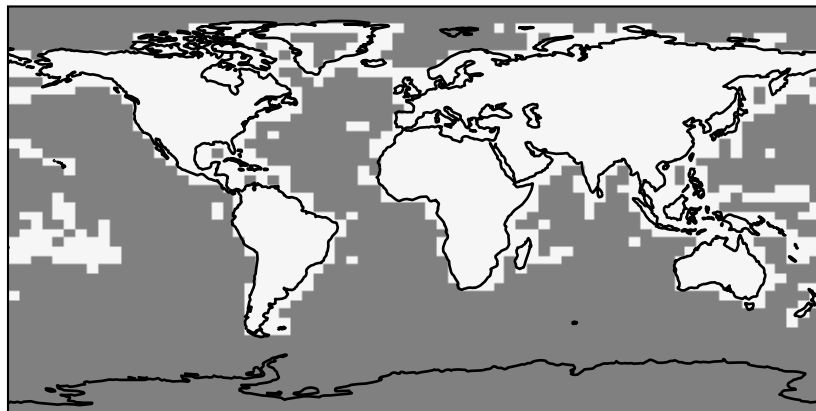
Difference  
Dev - Ref, Dynamic Range



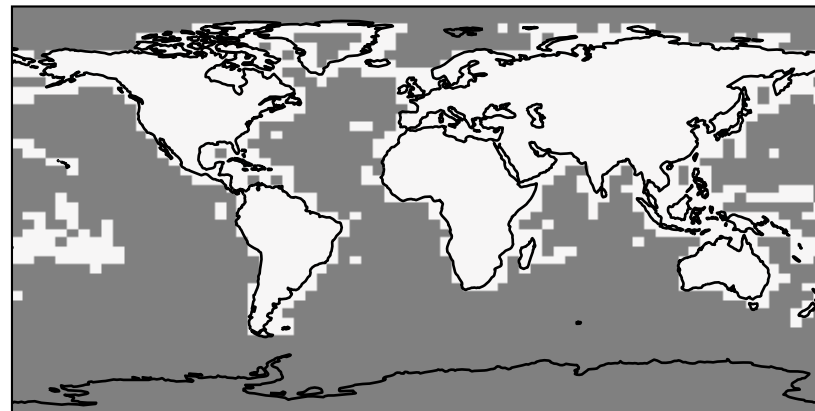
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

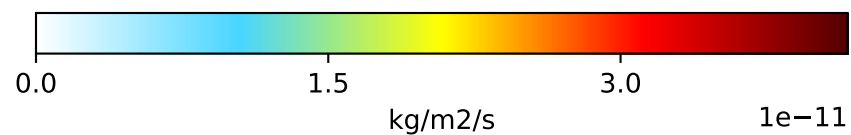
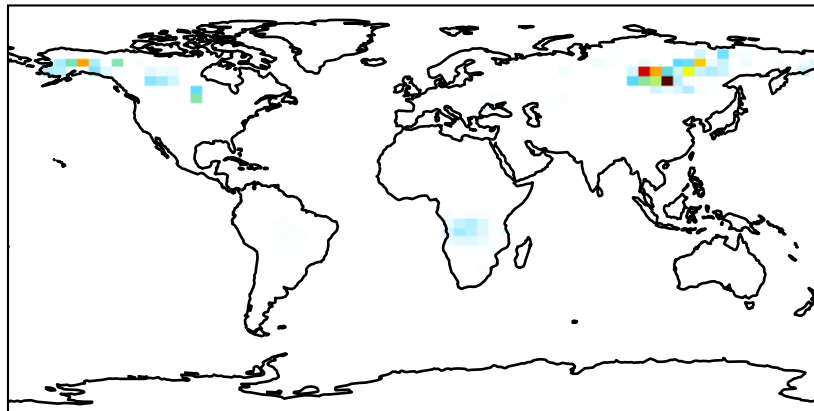


Ratio  
Dev/Ref, Fixed Range

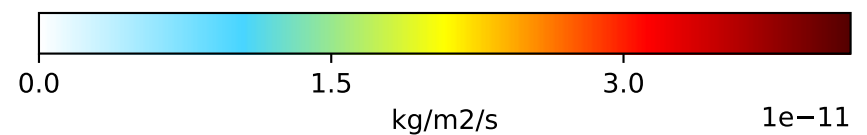
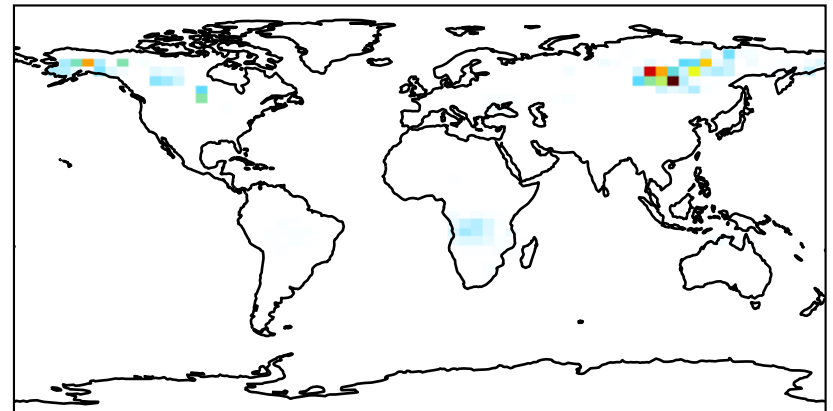


# EmisALK4\_BioBurn (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



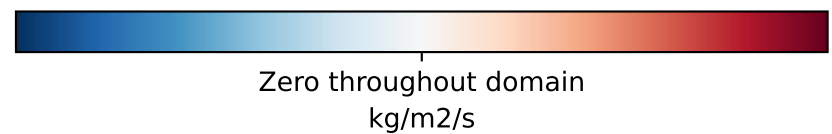
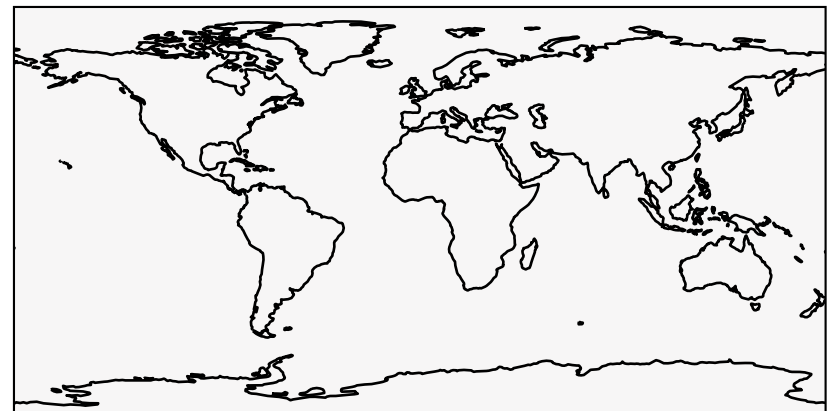
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



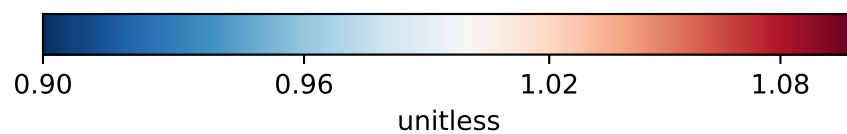
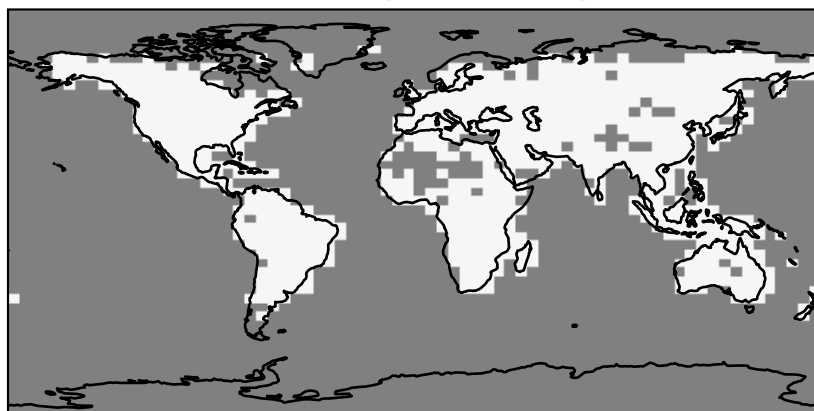
Difference  
Dev - Ref, Dynamic Range



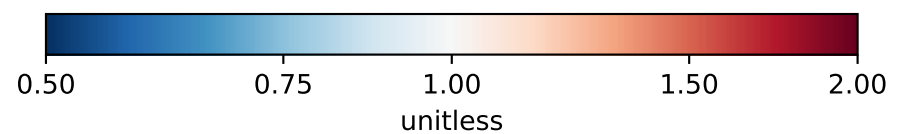
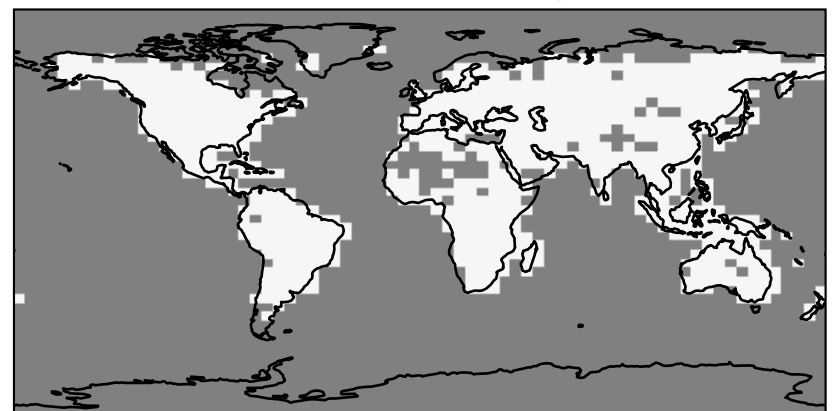
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

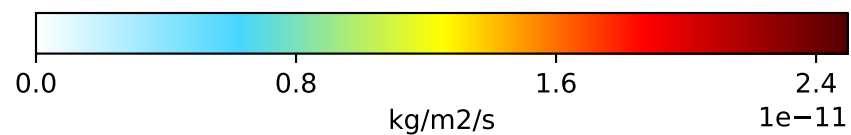


Ratio  
Dev/Ref, Fixed Range

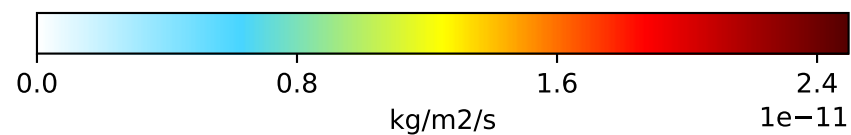
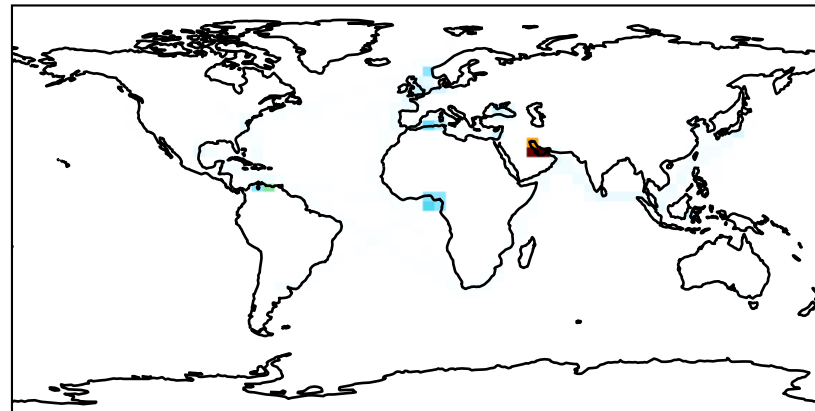


# EmisALK4\_Ship (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



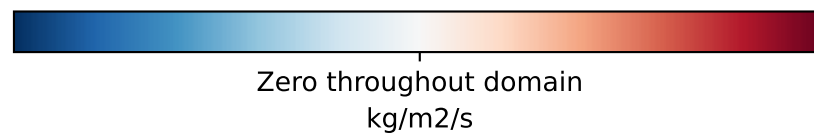
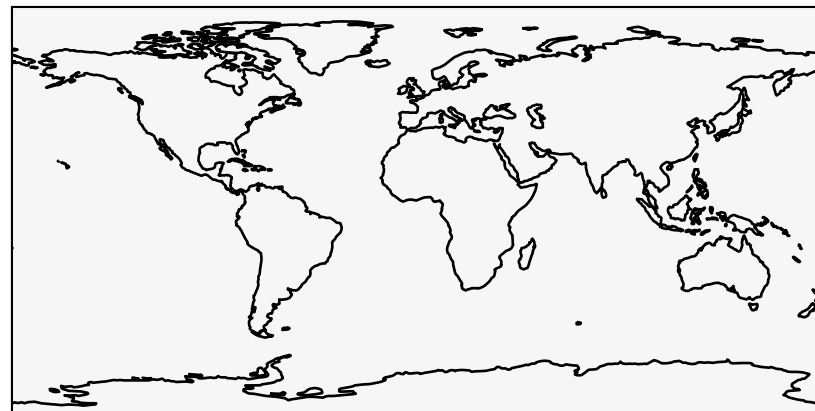
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



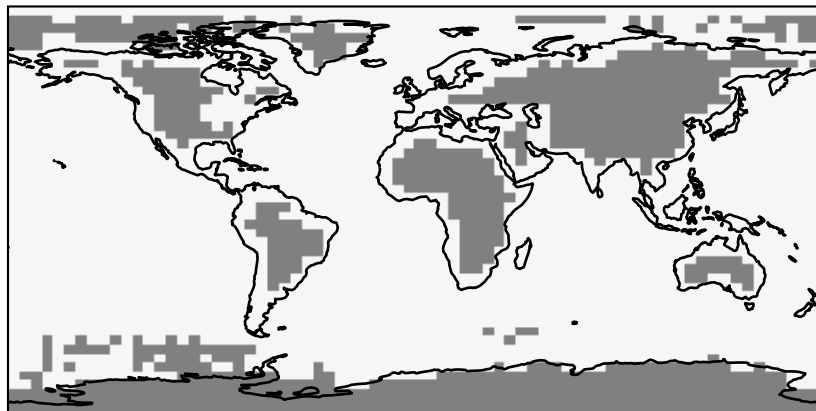
Difference  
Dev - Ref, Dynamic Range



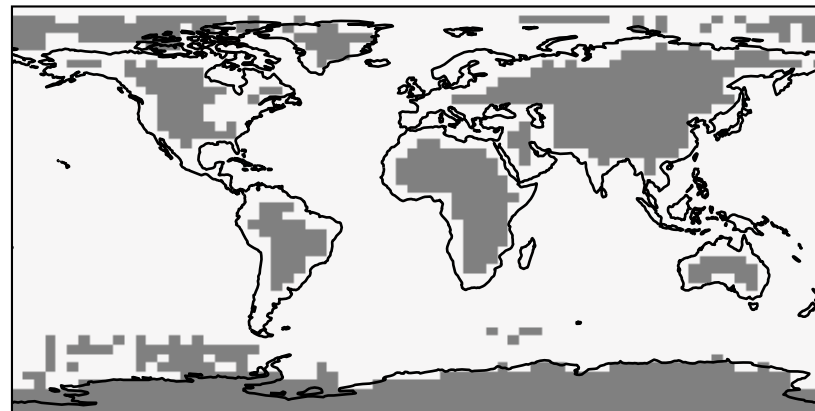
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



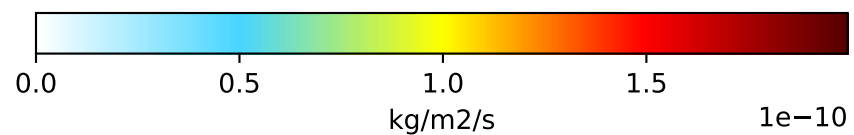
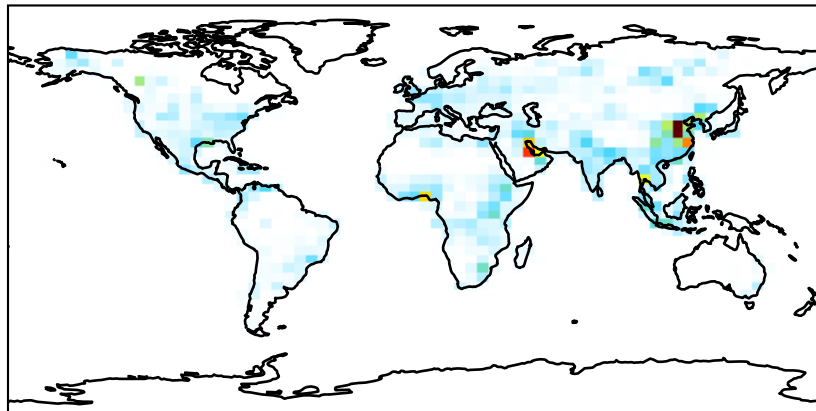
Ratio  
Dev/Ref, Fixed Range



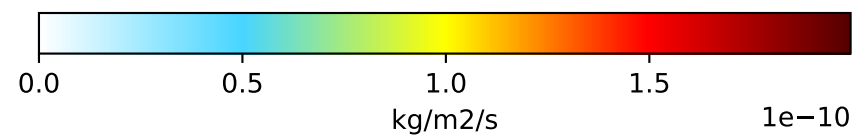
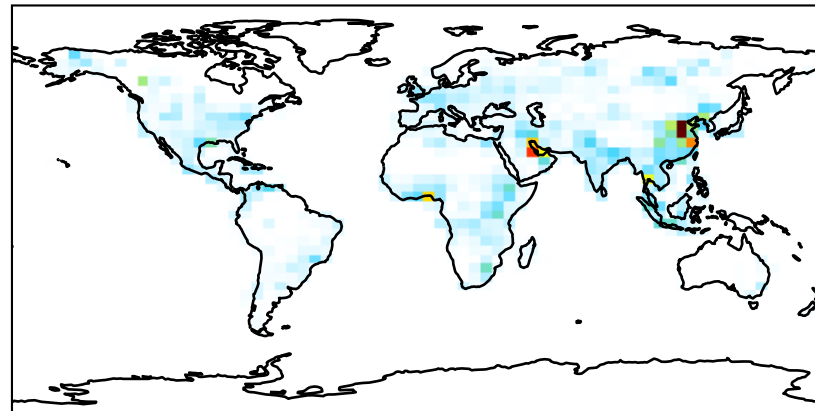


# EmisALK4\_Total (Jul2019)

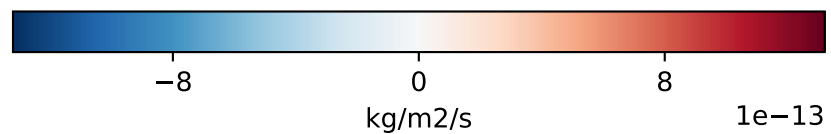
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



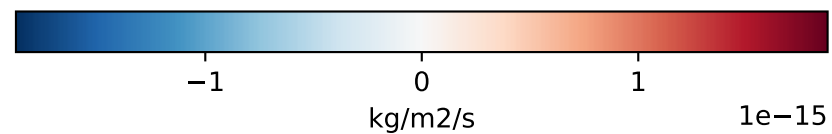
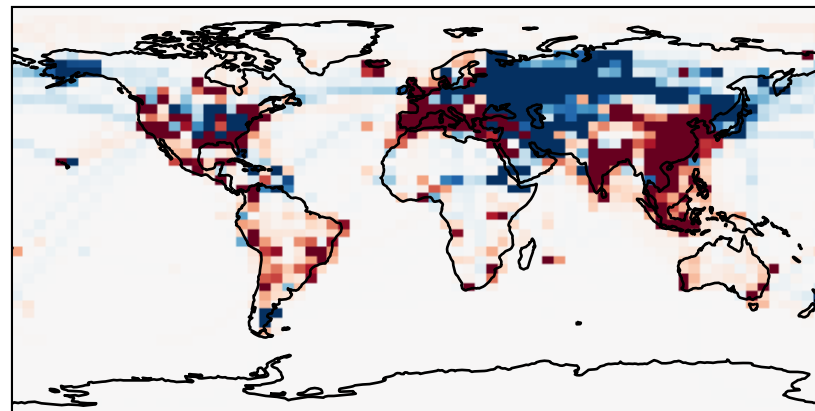
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



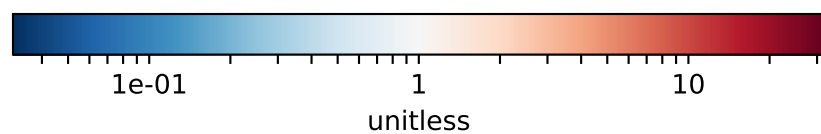
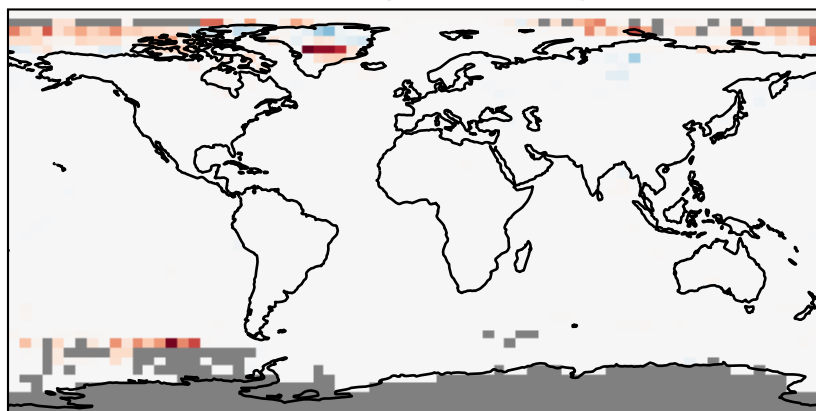
Difference  
Dev - Ref, Dynamic Range



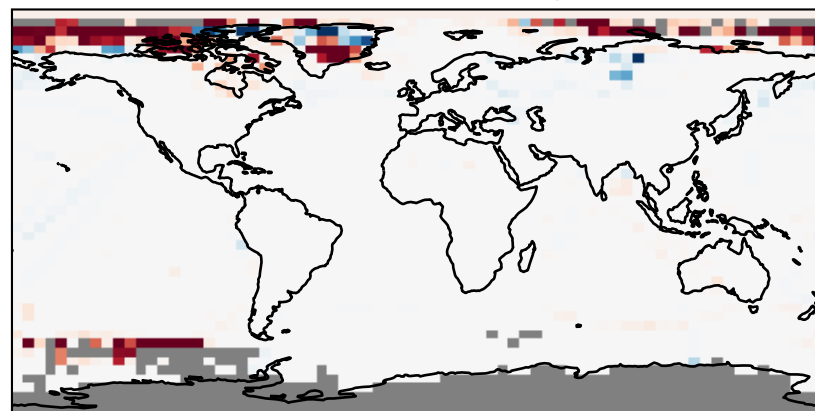
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

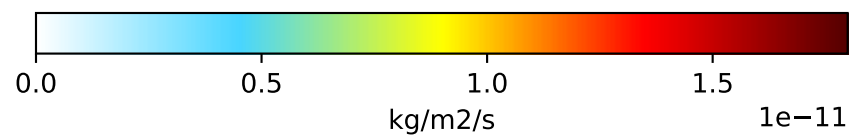
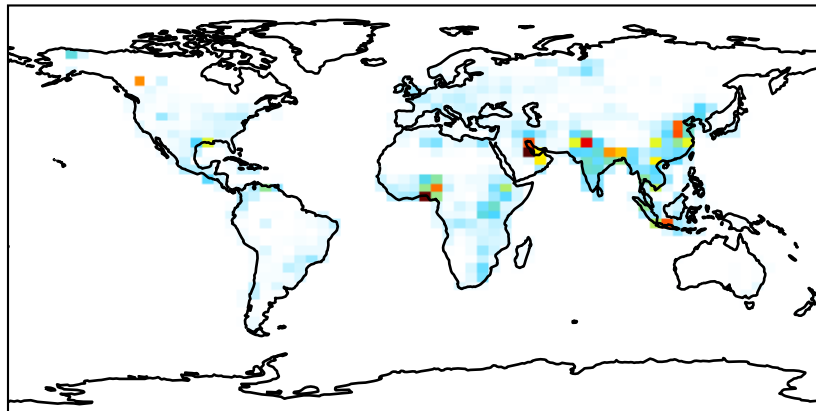


Ratio  
Dev/Ref, Fixed Range

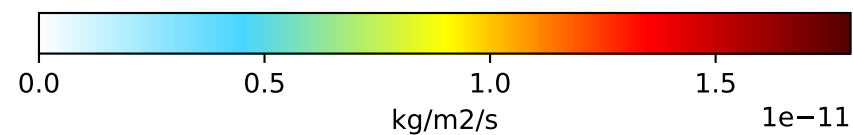
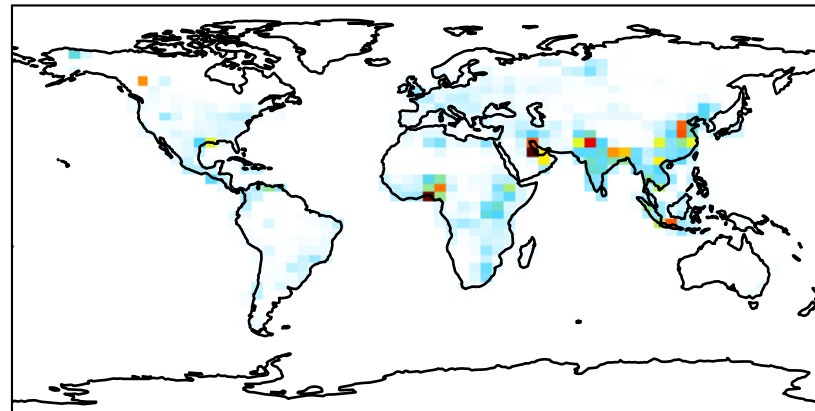


# EmisBENZ\_Anthro (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



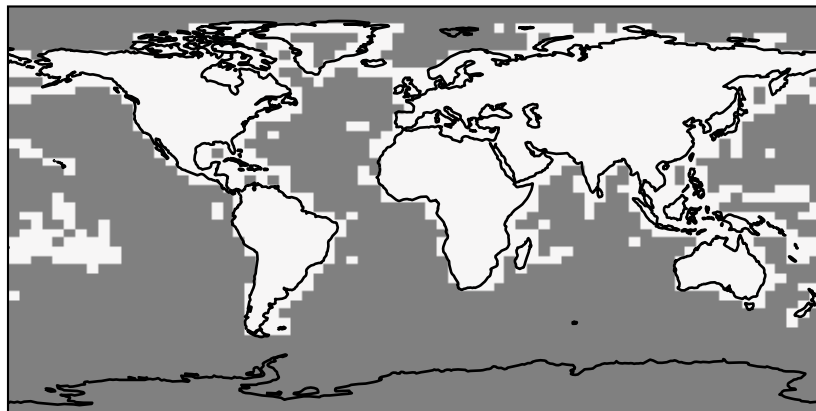
Difference  
Dev - Ref, Dynamic Range



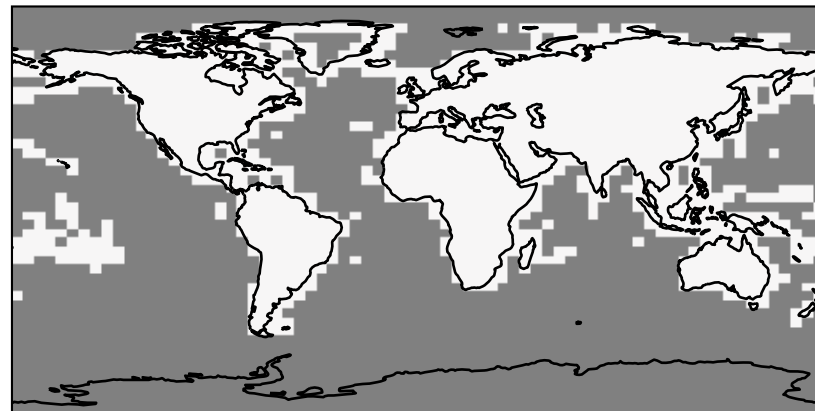
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

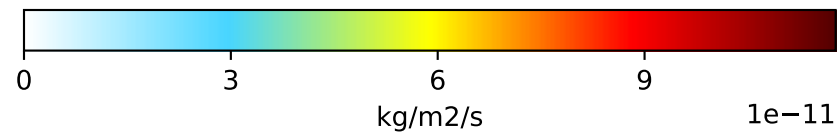
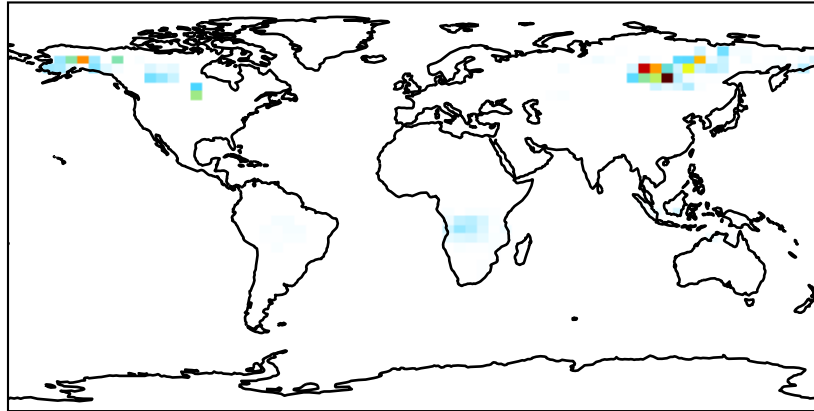


Ratio  
Dev/Ref, Fixed Range

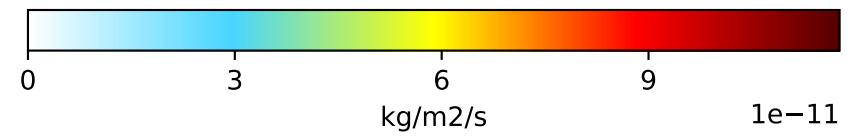
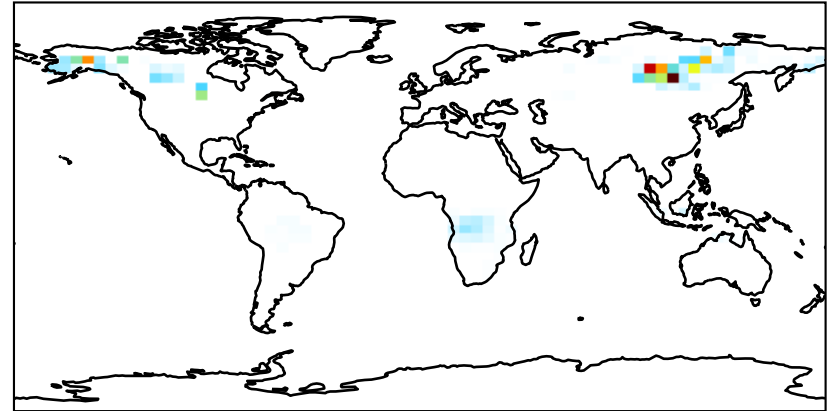


# EmisBENZ\_BioBurn (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



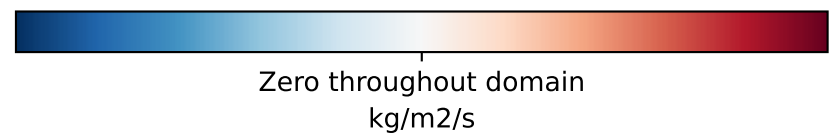
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



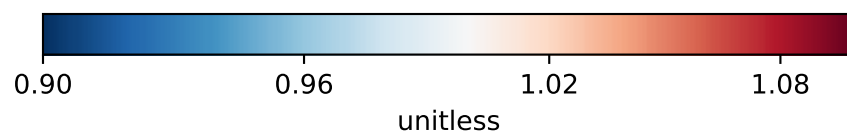
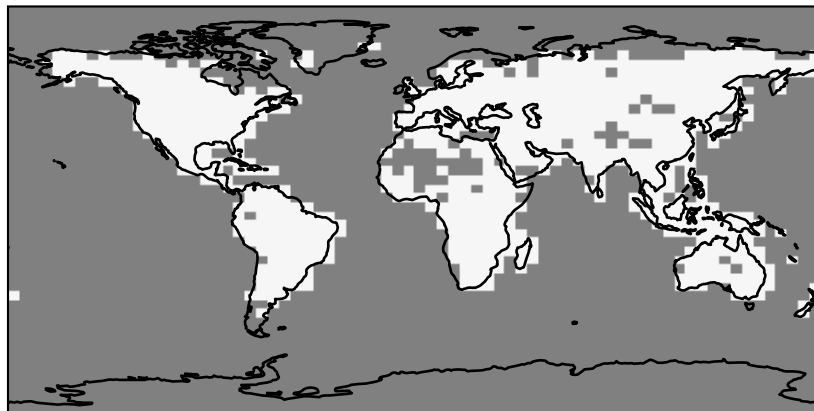
Difference  
Dev - Ref, Dynamic Range



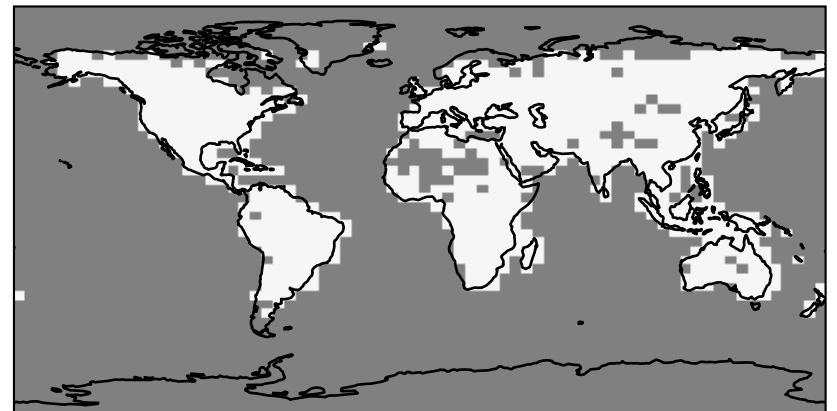
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

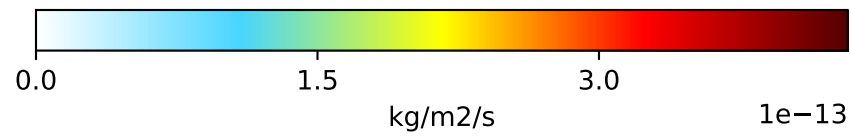
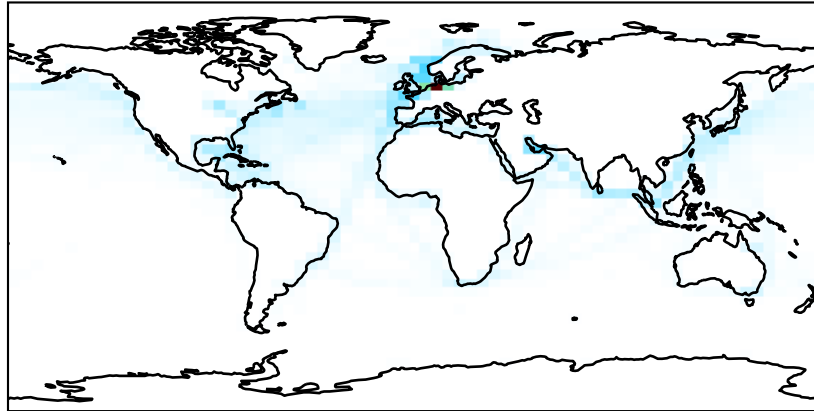


Ratio  
Dev/Ref, Fixed Range

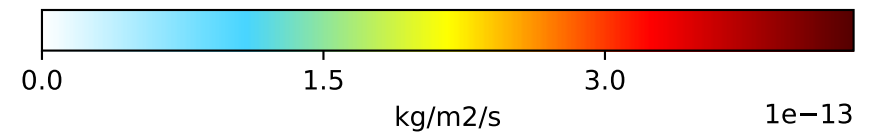
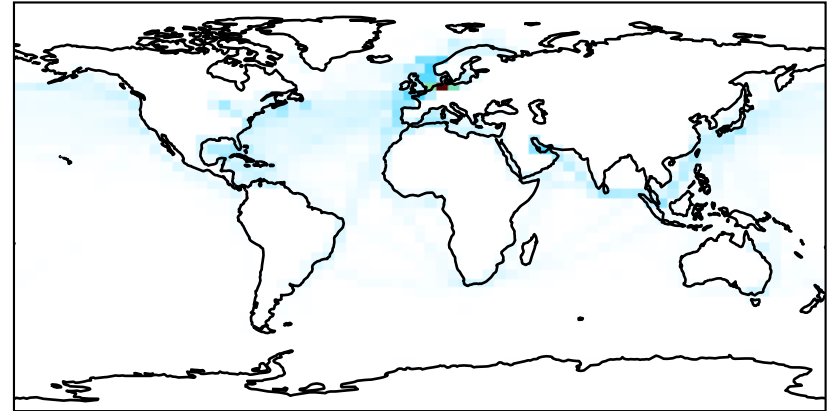


# EmisBENZ\_Ship (Jul2019)

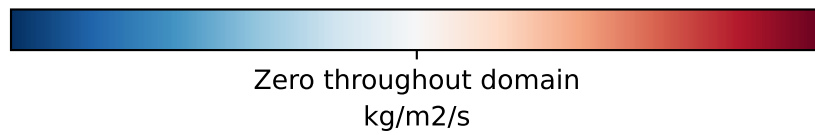
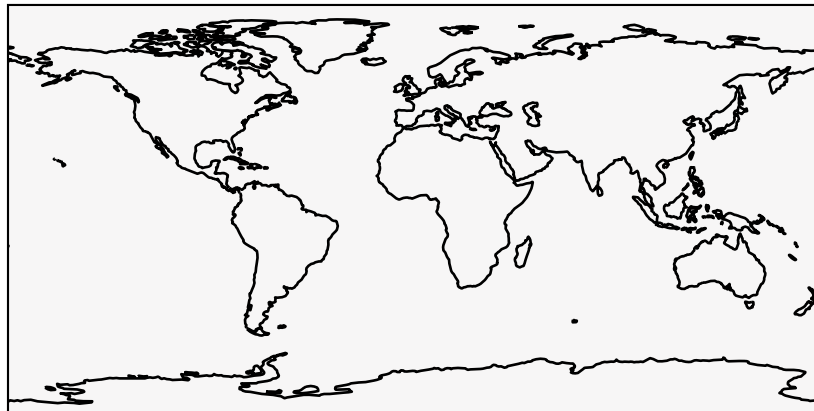
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



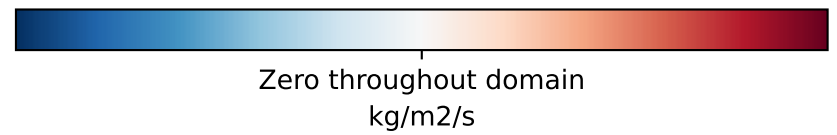
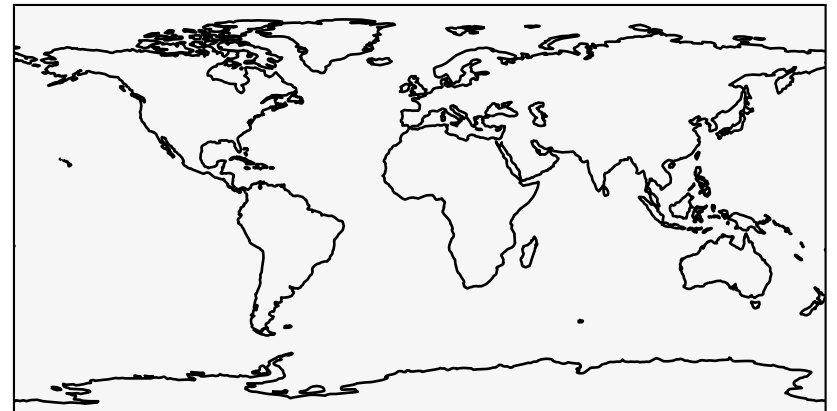
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



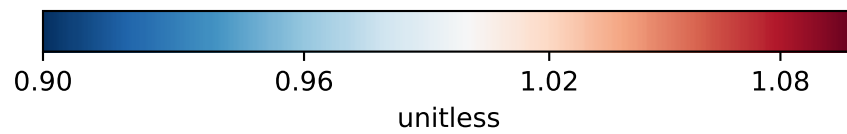
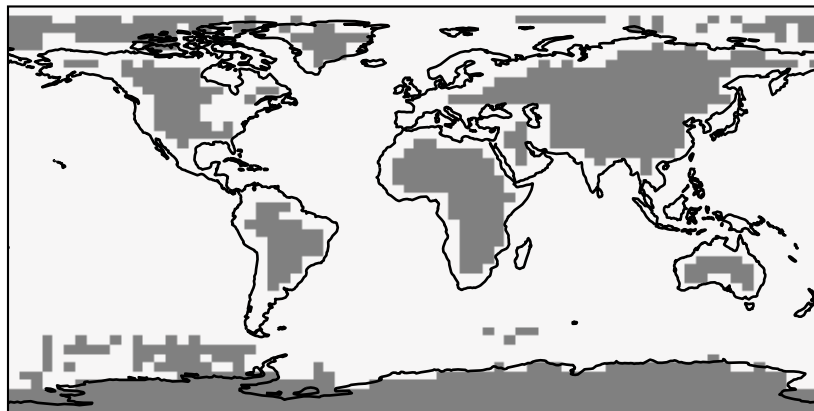
Difference  
Dev - Ref, Dynamic Range



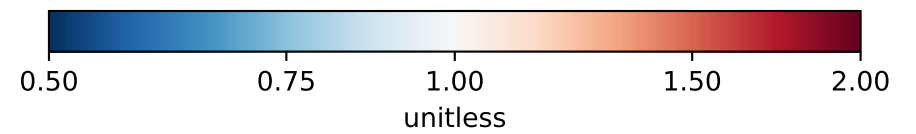
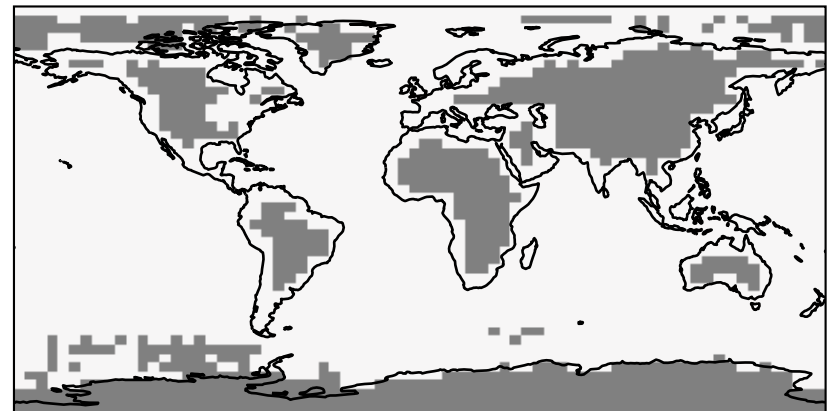
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

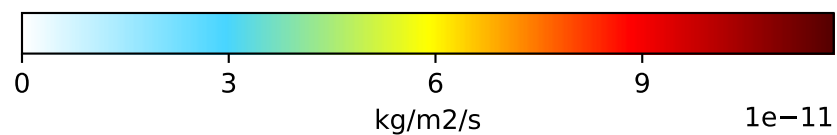
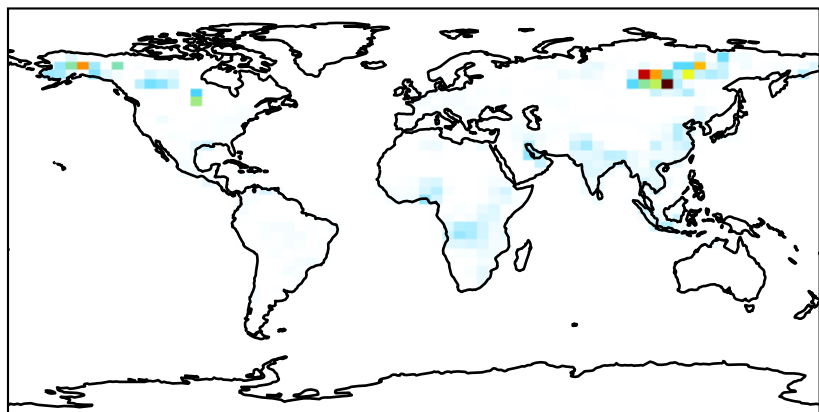


Ratio  
Dev/Ref, Fixed Range

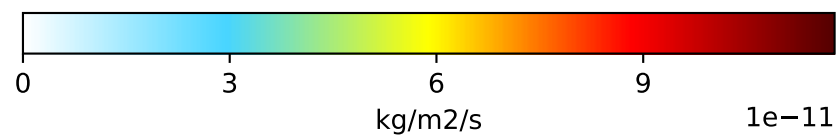
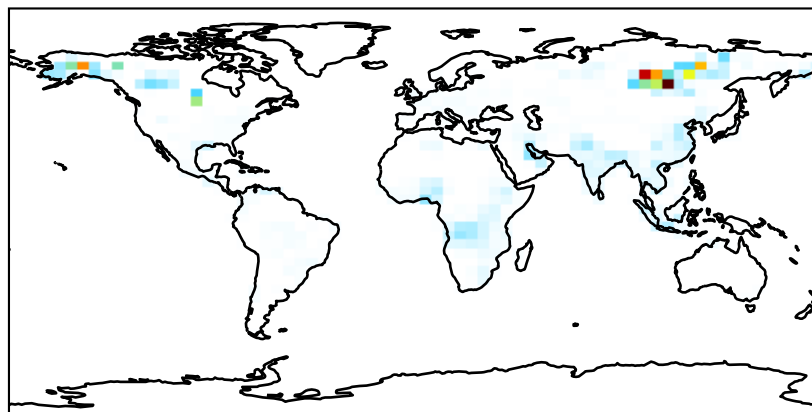


# EmisBENZ\_Total (Jul2019)

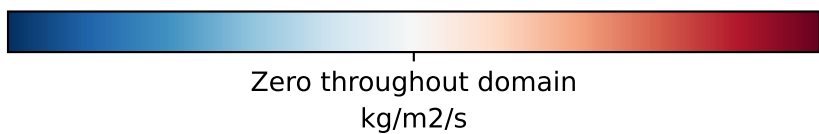
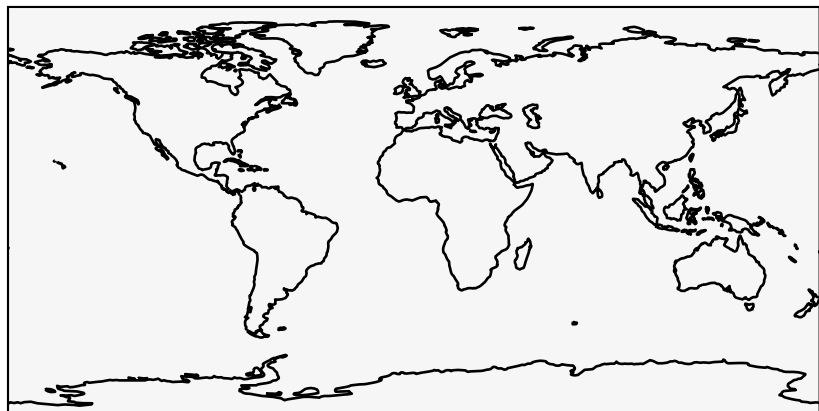
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



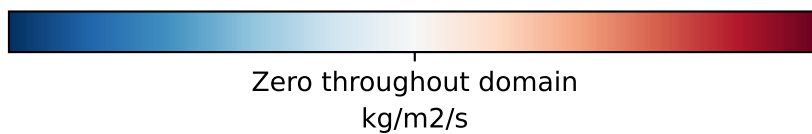
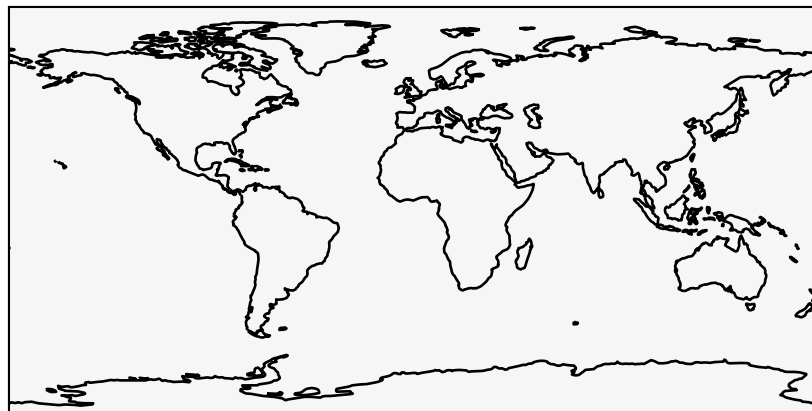
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



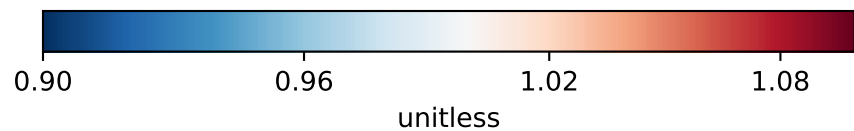
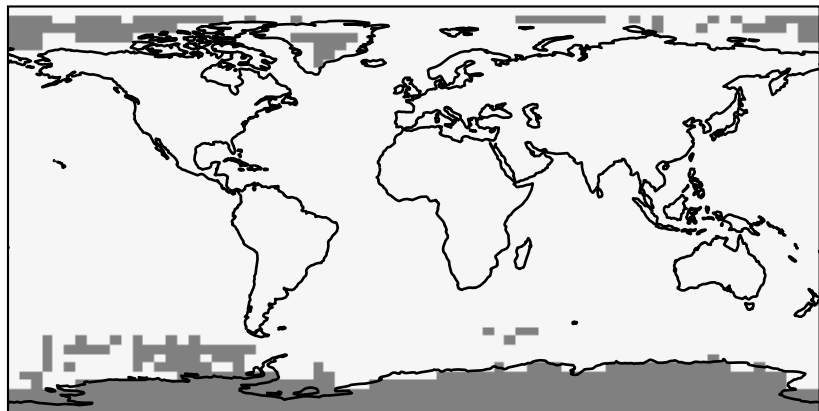
Difference  
Dev - Ref, Dynamic Range



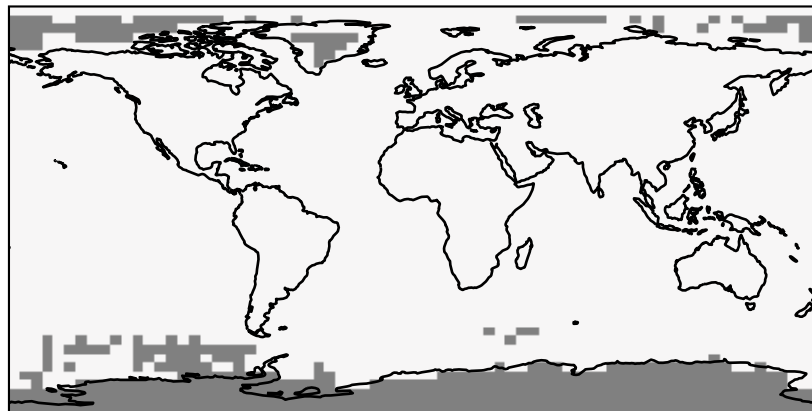
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



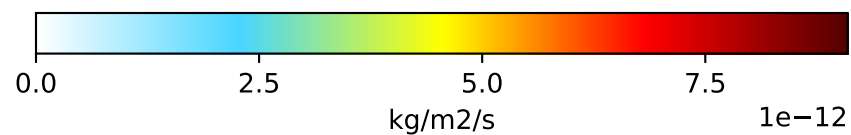
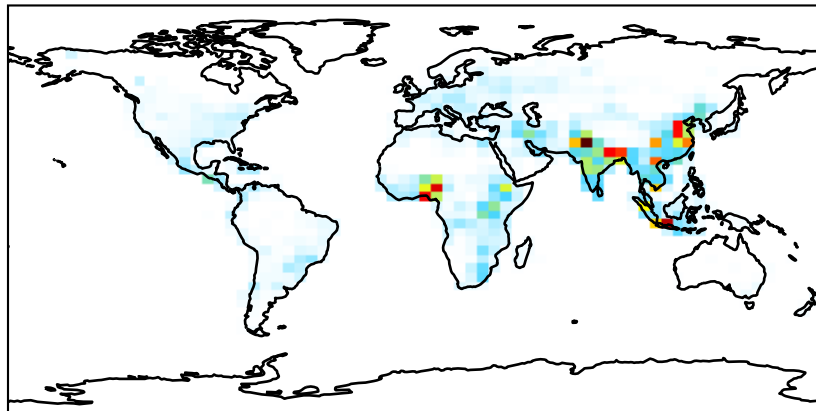
Ratio  
Dev/Ref, Fixed Range



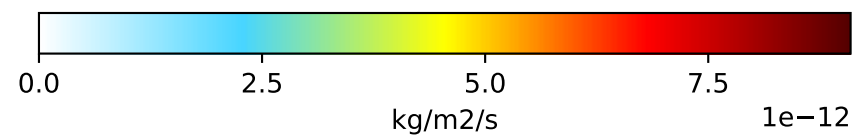
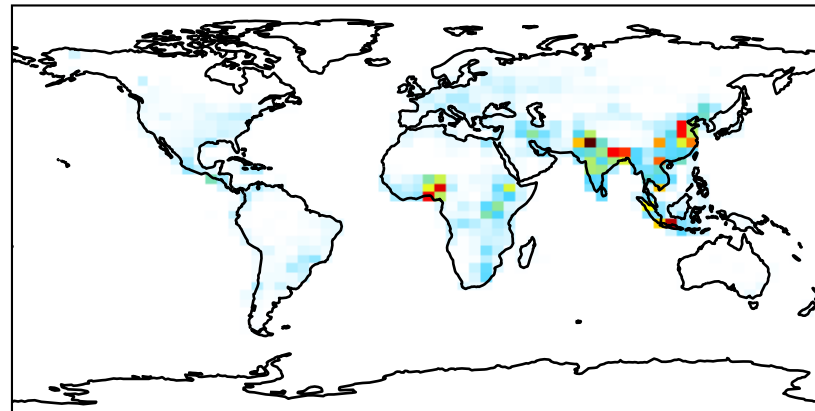


# EmisC2H2\_Anthro (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



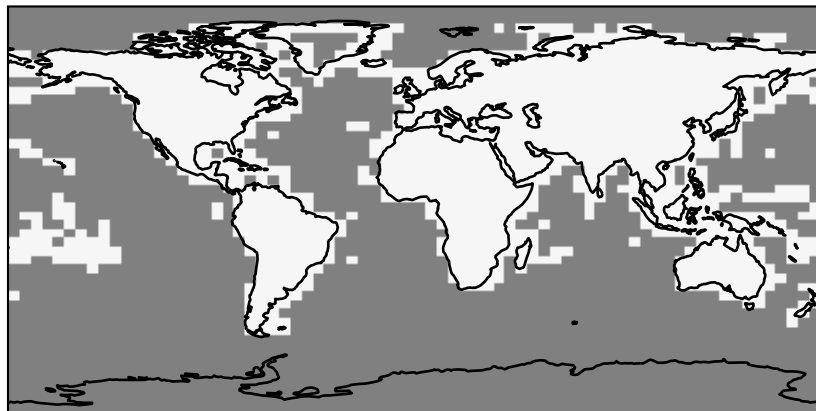
Difference  
Dev - Ref, Dynamic Range



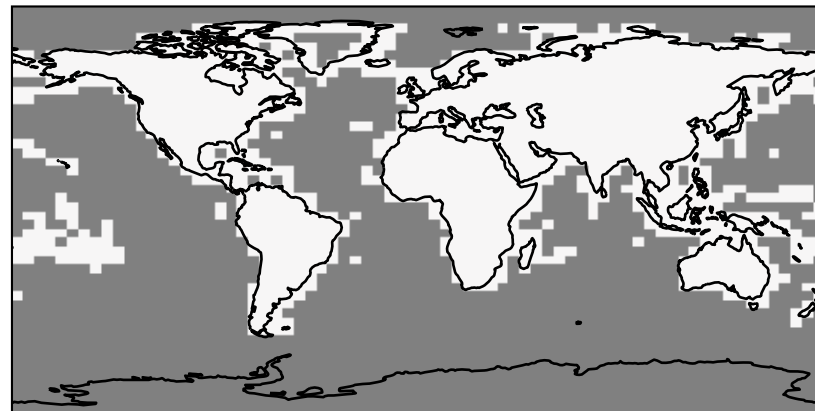
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

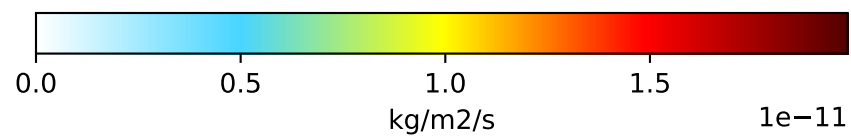
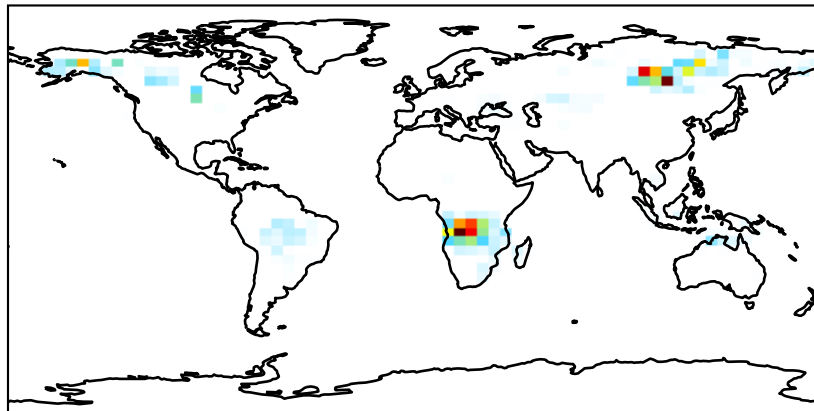


Ratio  
Dev/Ref, Fixed Range

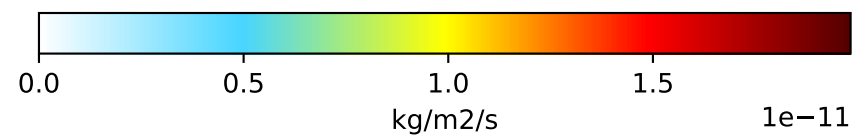
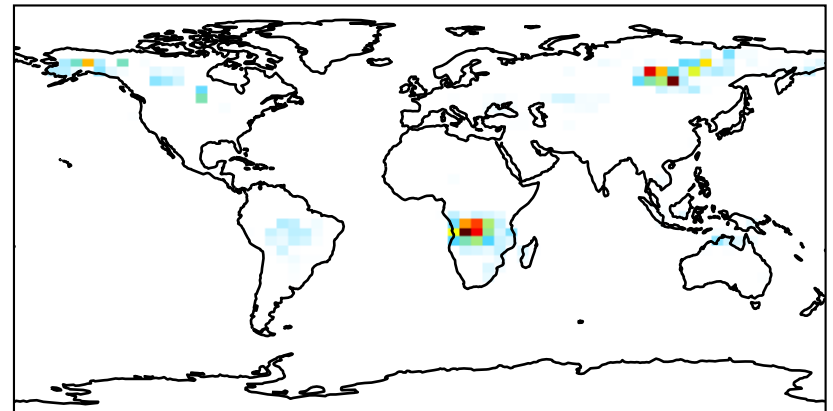


# EmisC2H2\_BioBurn (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



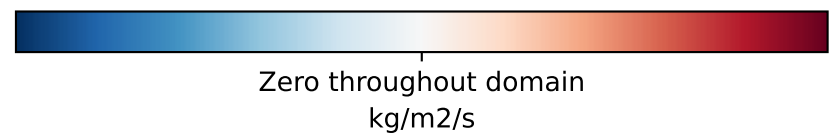
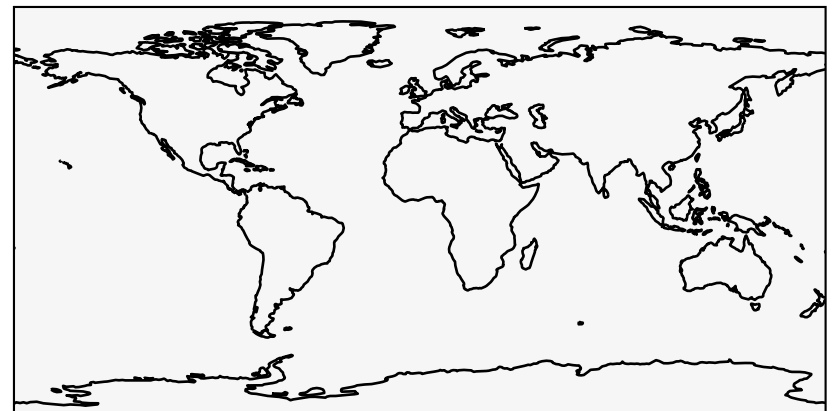
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



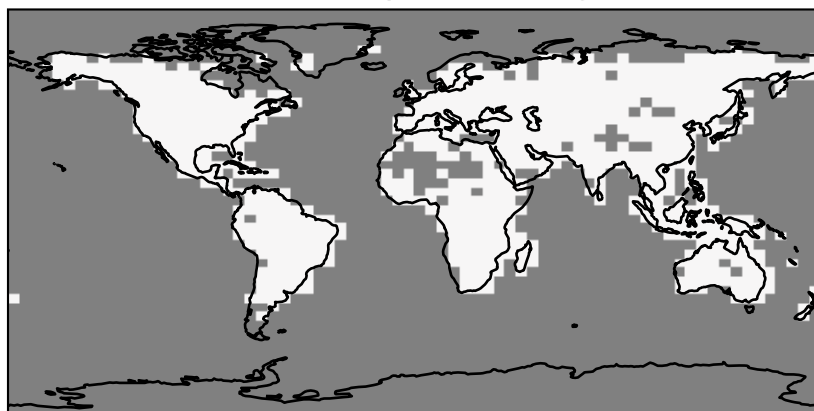
Difference  
Dev - Ref, Dynamic Range



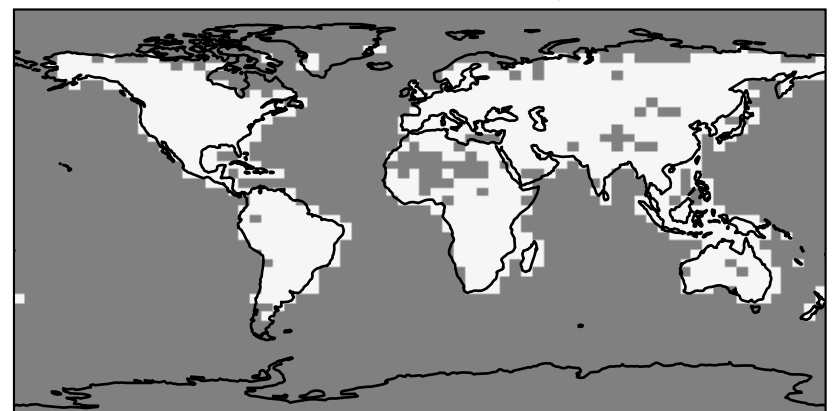
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

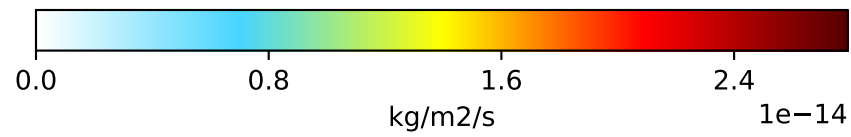
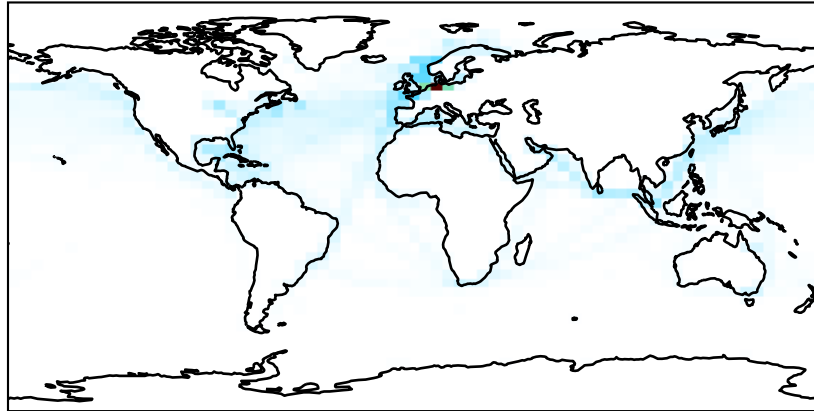


Ratio  
Dev/Ref, Fixed Range

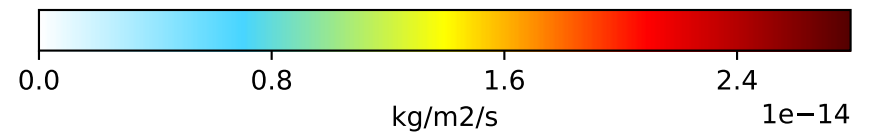
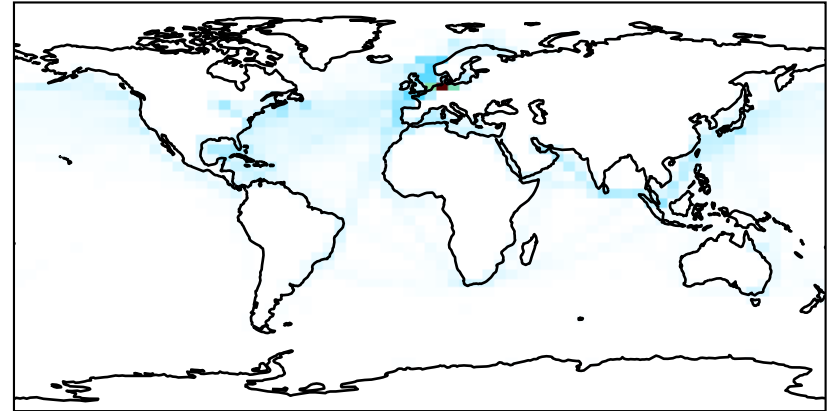


# EmisC2H2\_Ship (Jul2019)

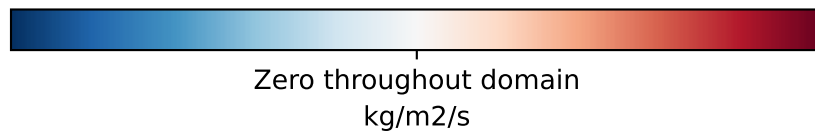
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



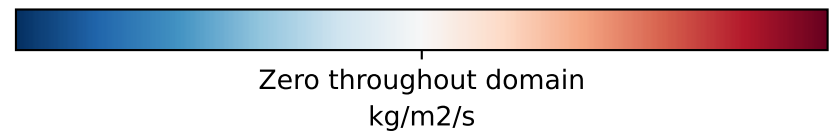
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



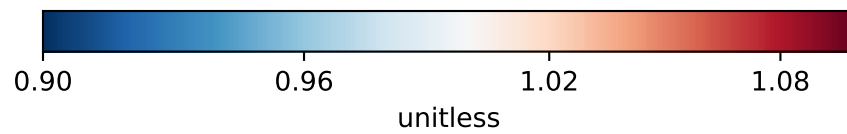
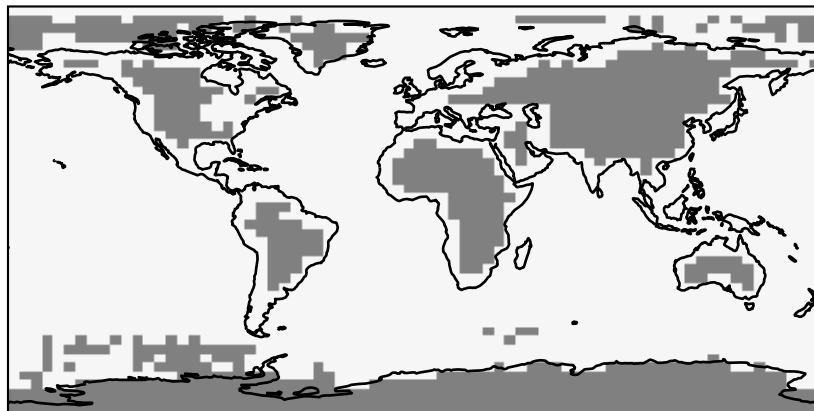
Difference  
Dev - Ref, Dynamic Range



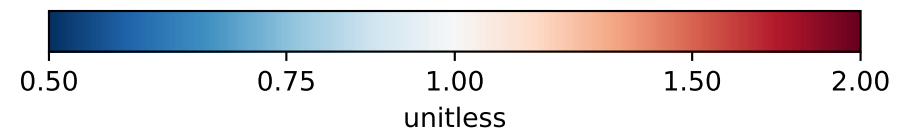
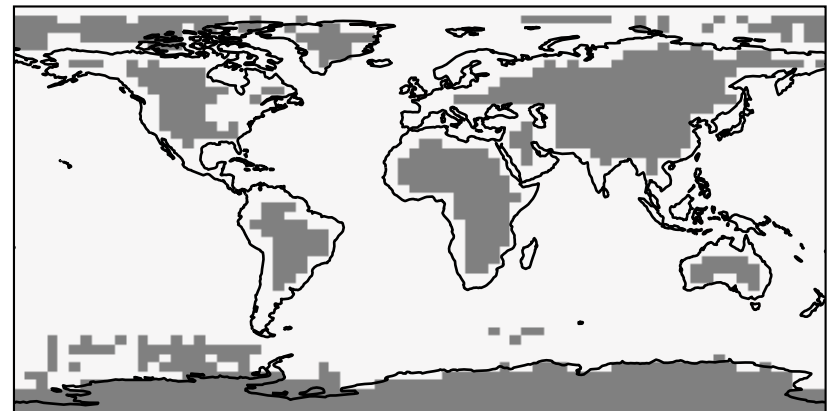
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

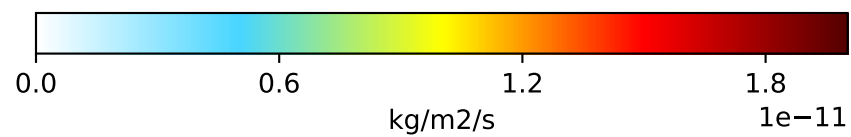
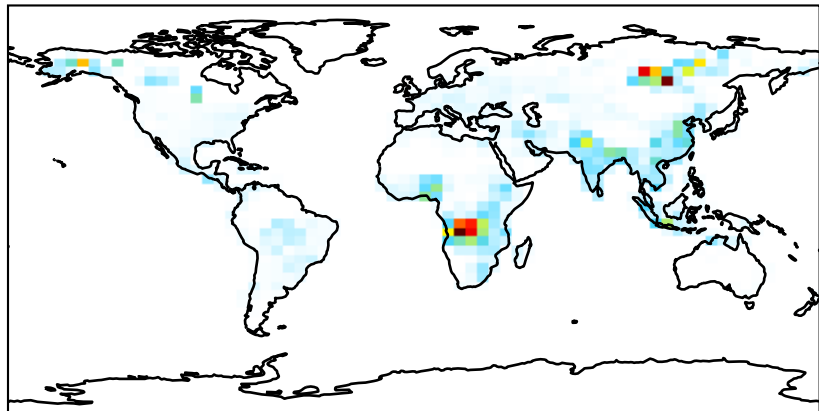


Ratio  
Dev/Ref, Fixed Range

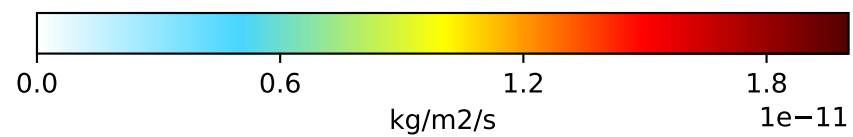
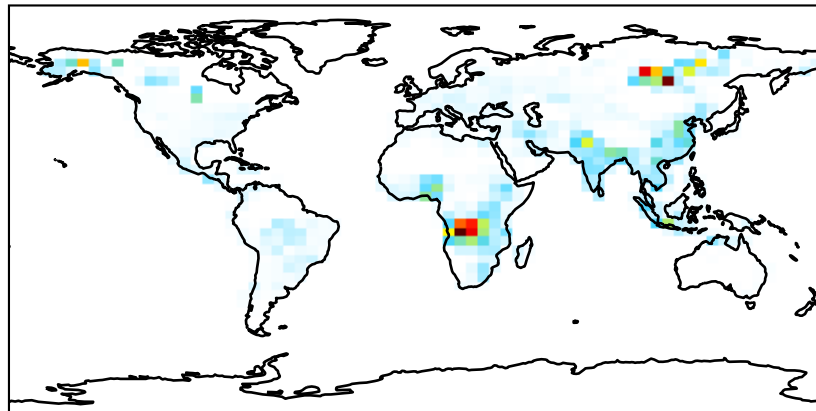


# EmisC2H2\_Total (Jul2019)

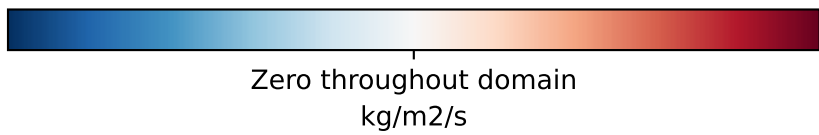
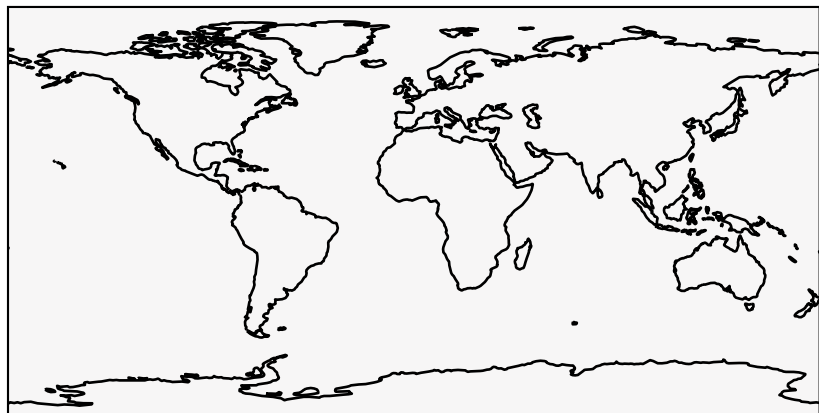
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



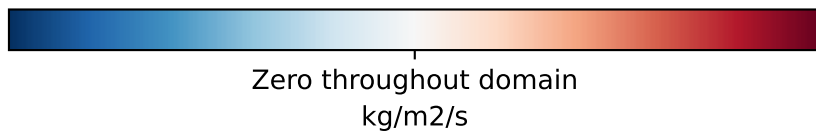
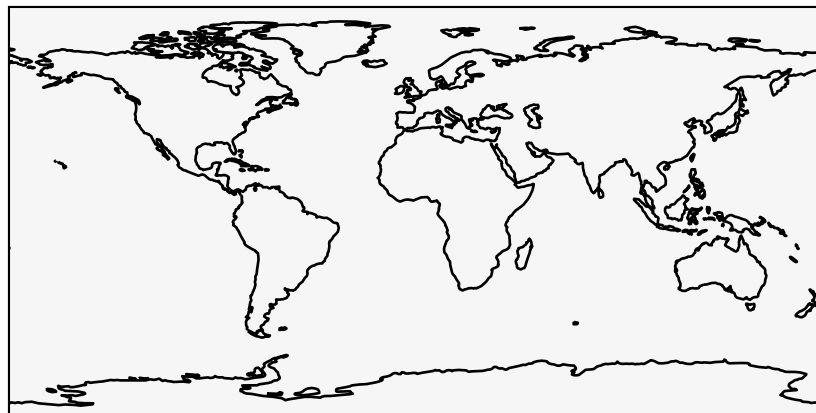
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



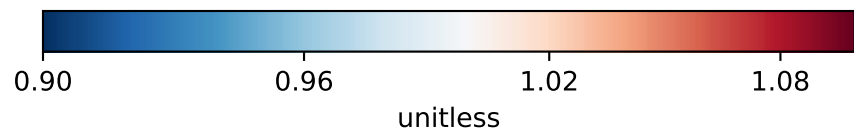
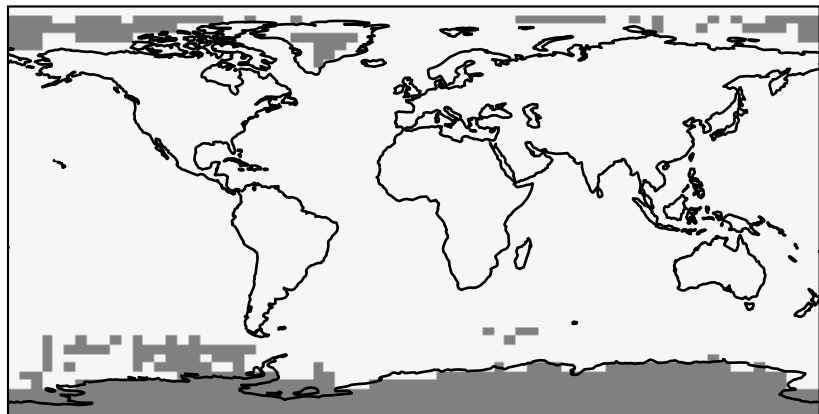
Difference  
Dev - Ref, Dynamic Range



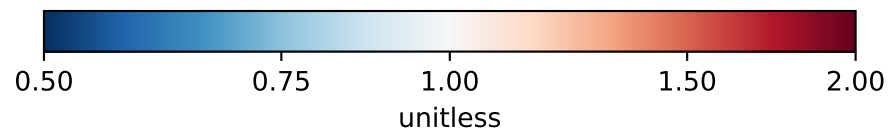
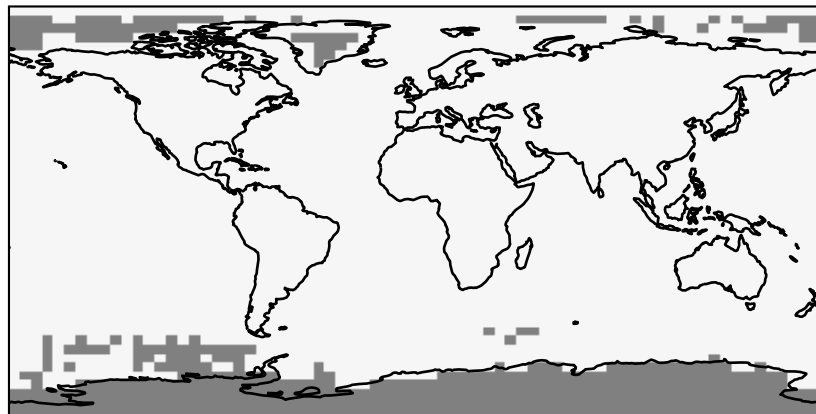
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

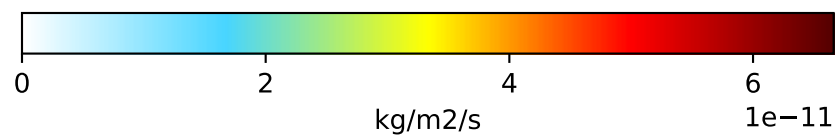
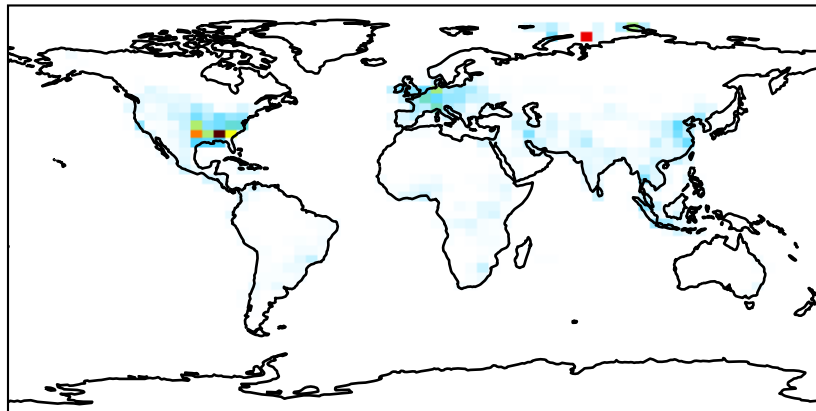


Ratio  
Dev/Ref, Fixed Range

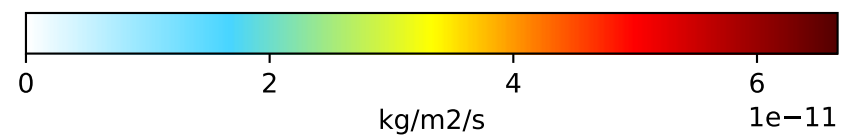
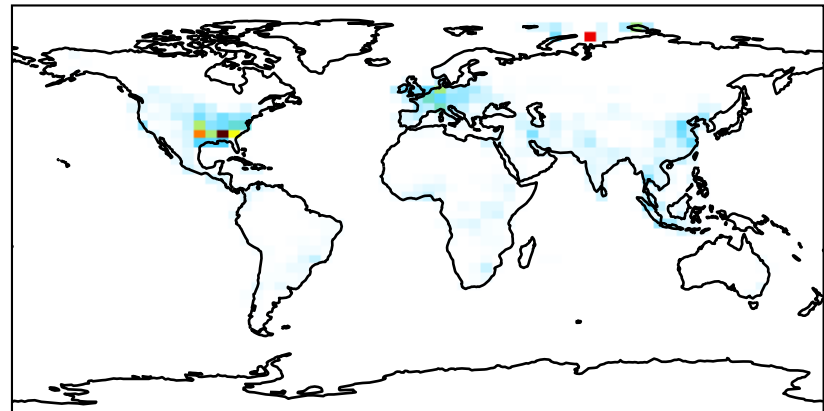


# EmisC2H4\_Anthro (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



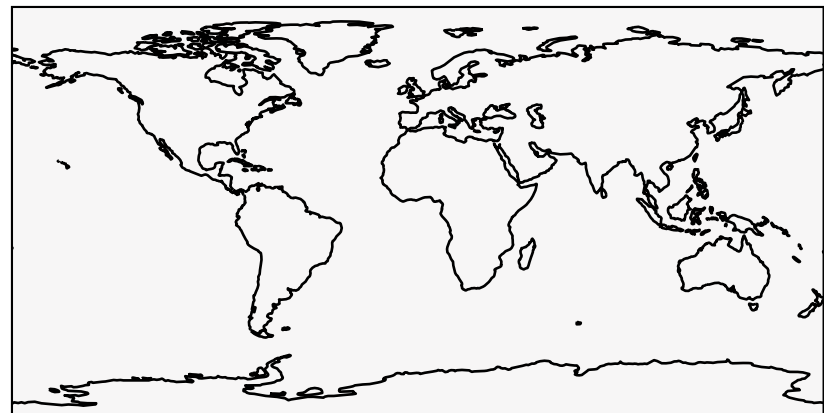
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



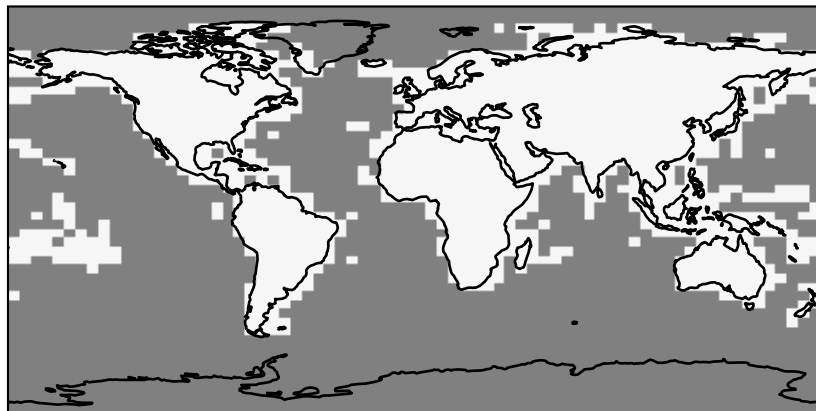
Difference  
Dev - Ref, Dynamic Range



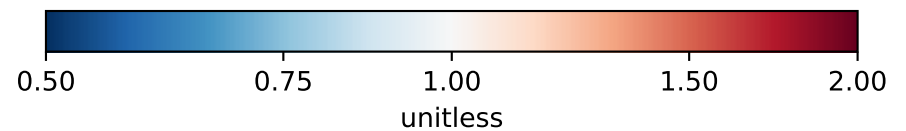
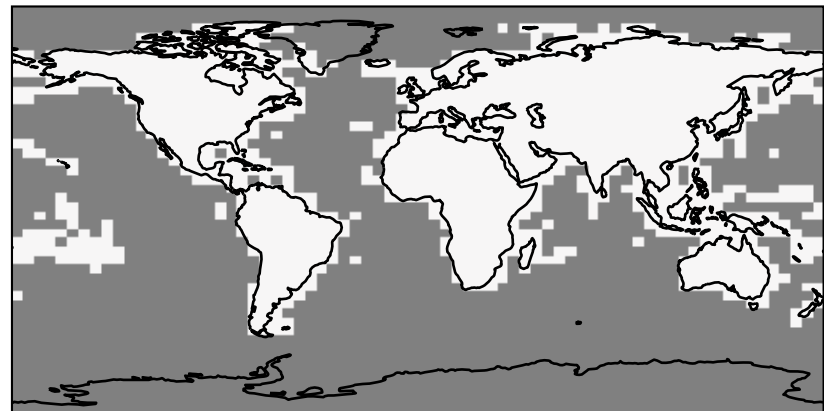
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



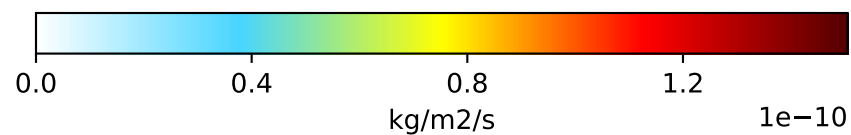
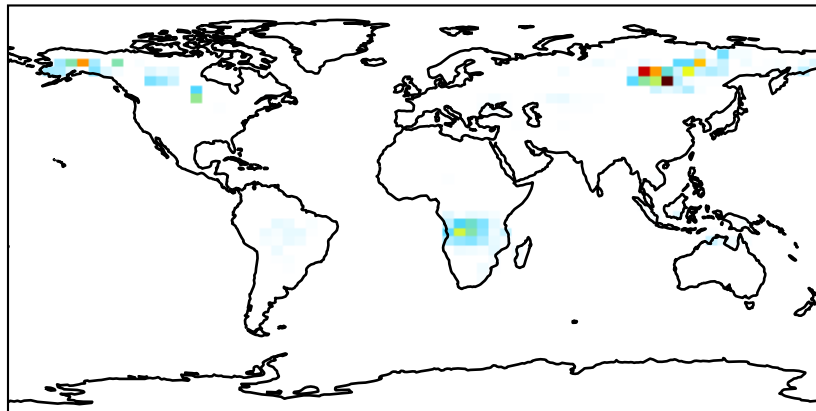
Ratio  
Dev/Ref, Fixed Range



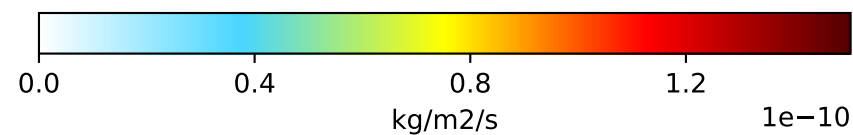
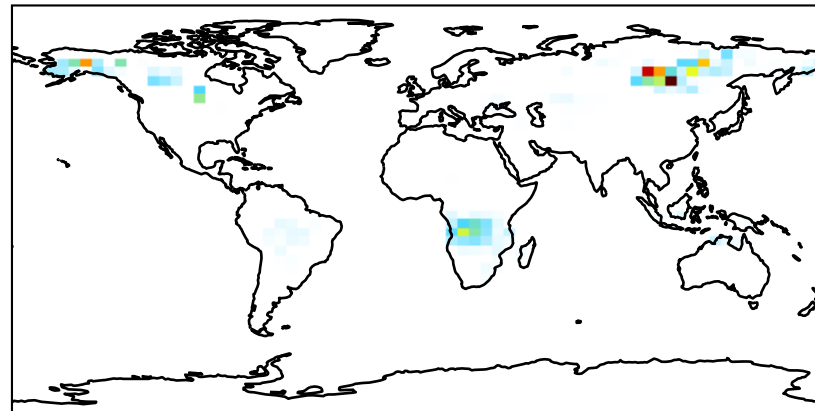


# EmisC2H4\_BioBurn (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



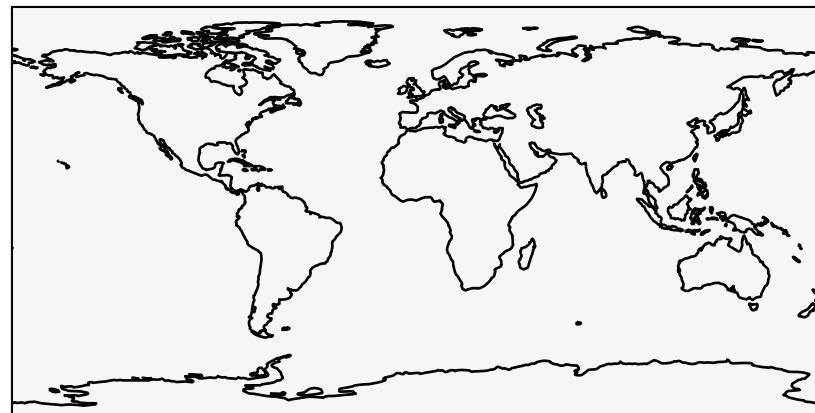
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



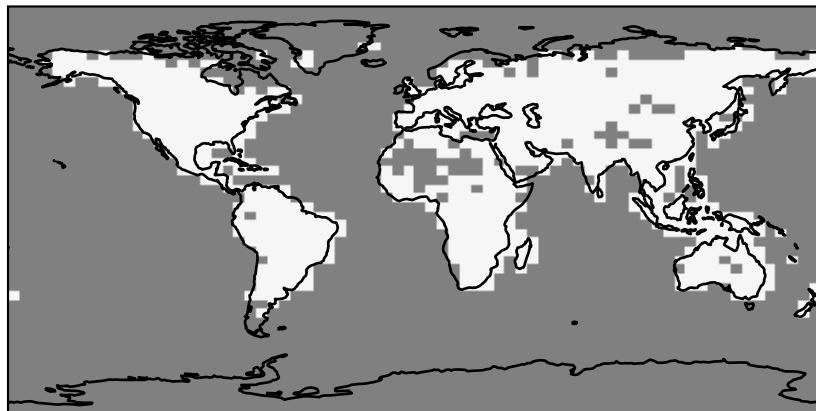
Difference  
Dev - Ref, Dynamic Range



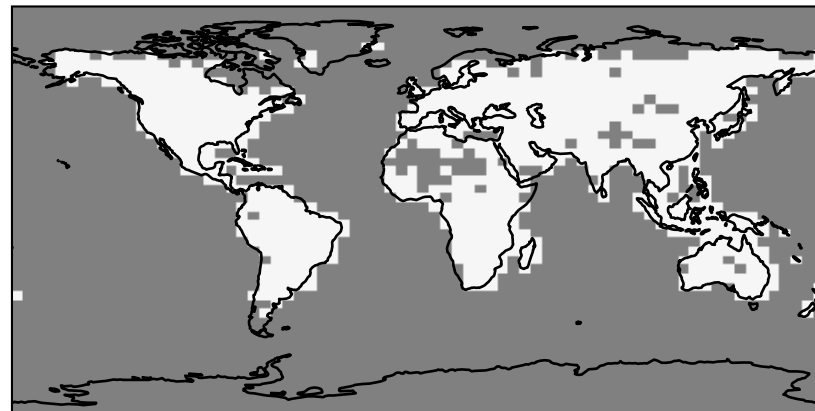
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

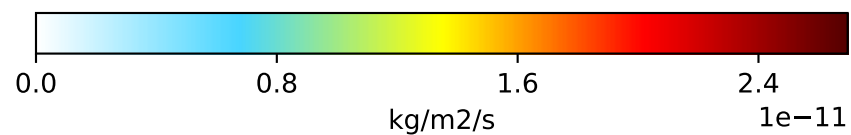
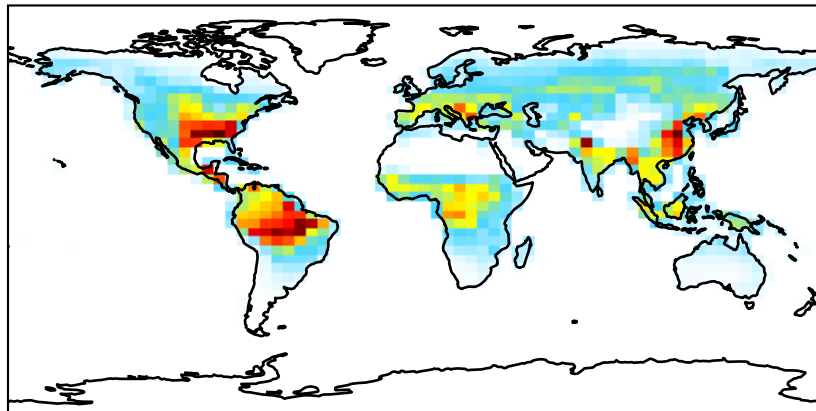


Ratio  
Dev/Ref, Fixed Range

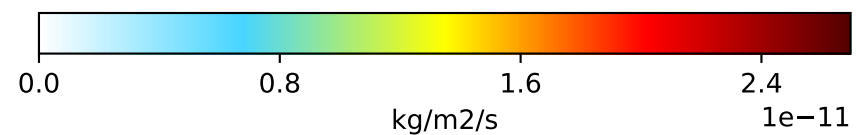
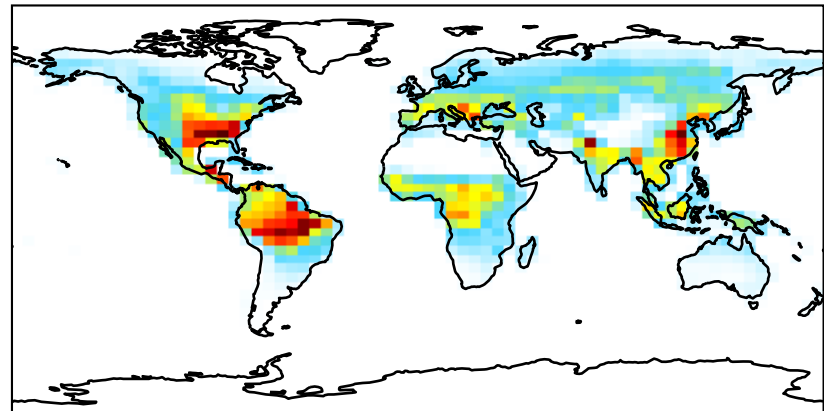


# EmisC2H4\_Biogenic (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



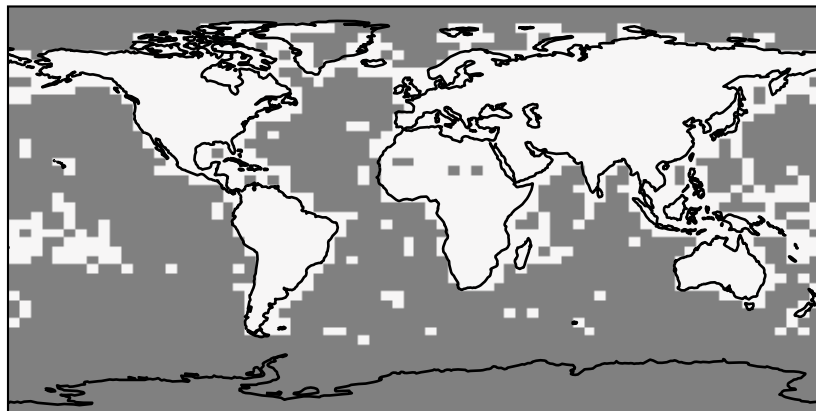
Difference  
Dev - Ref, Dynamic Range



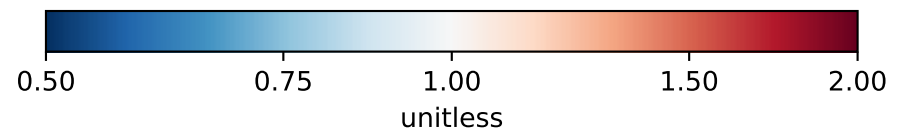
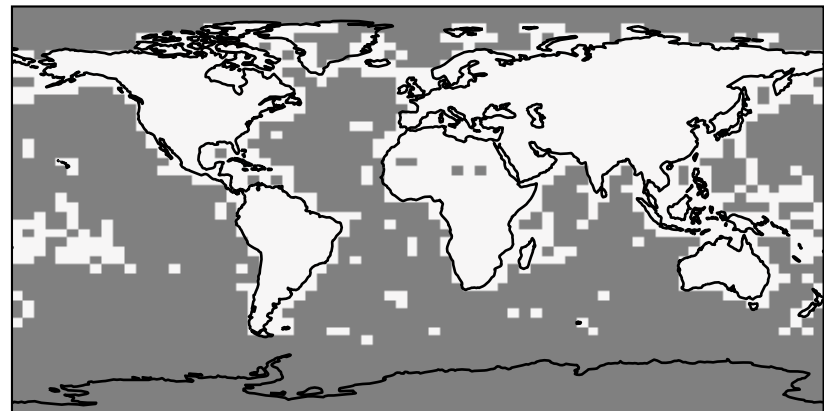
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

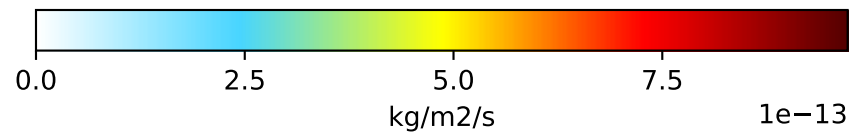
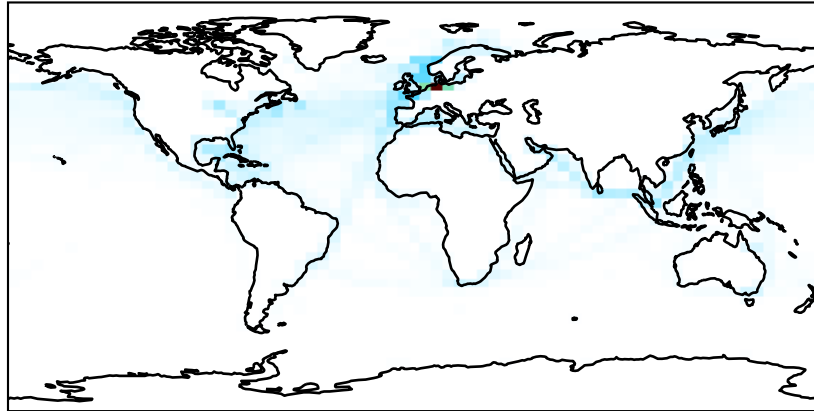


Ratio  
Dev/Ref, Fixed Range

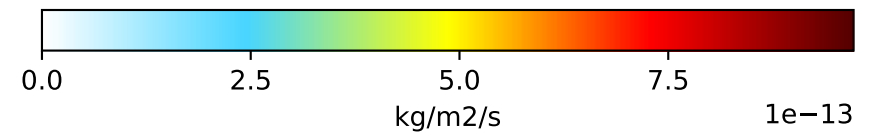
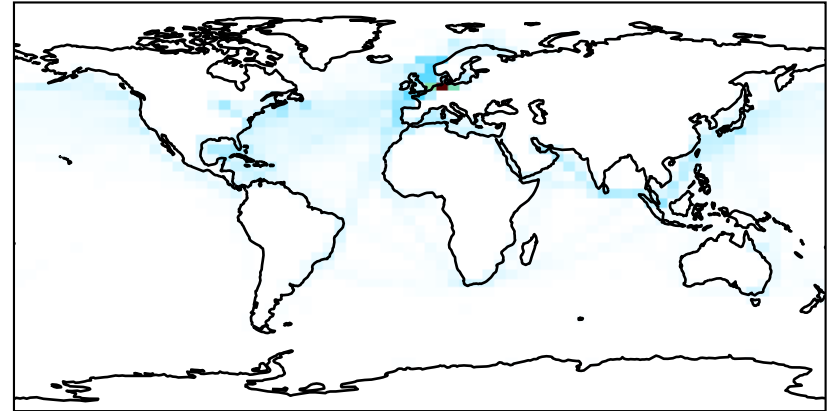


# EmisC2H4\_Ship (Jul2019)

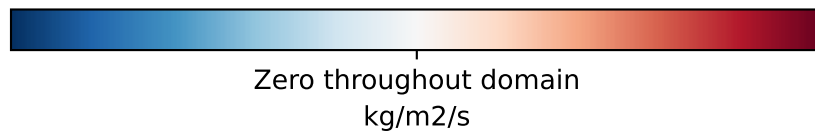
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



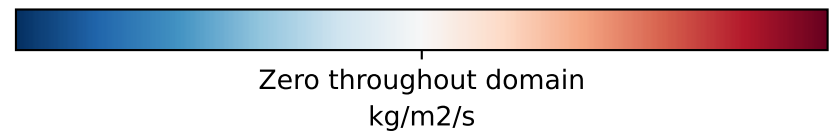
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



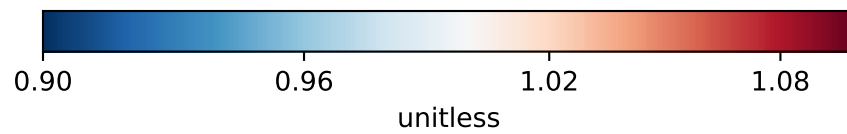
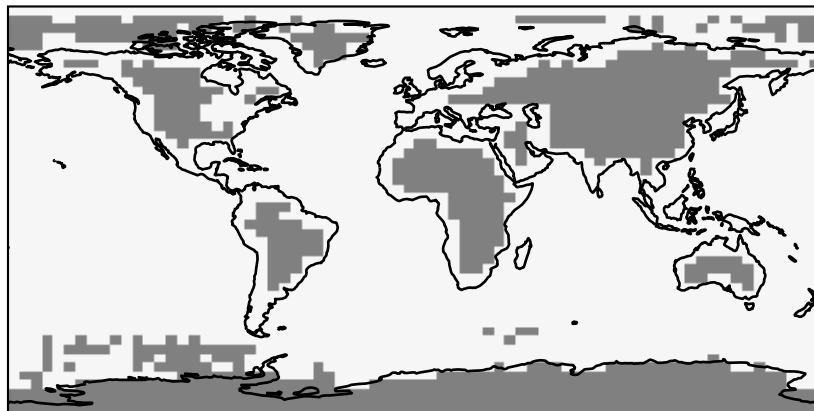
Difference  
Dev - Ref, Dynamic Range



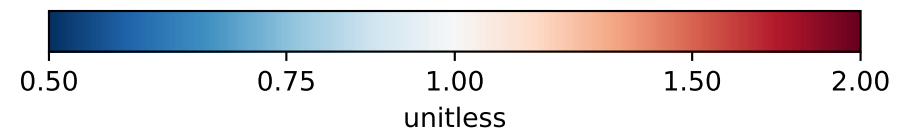
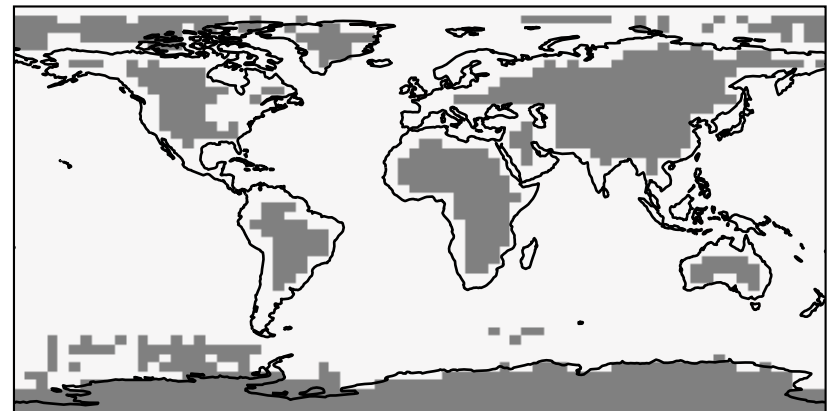
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

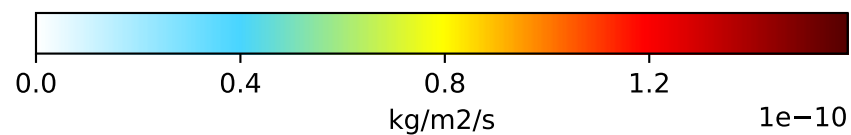
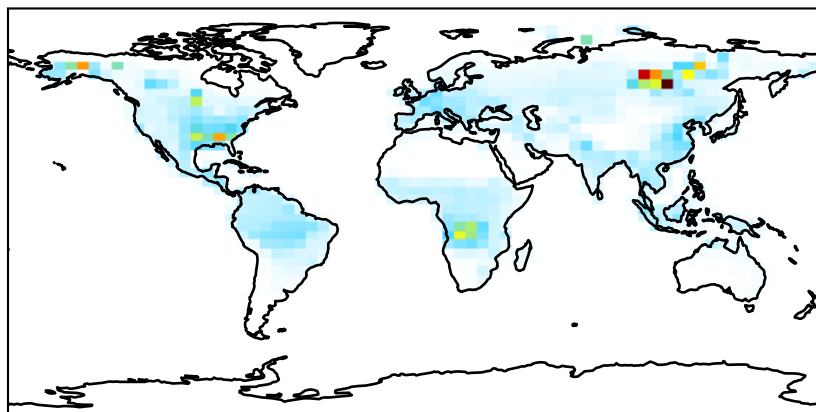


Ratio  
Dev/Ref, Fixed Range

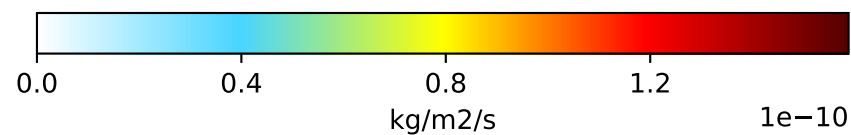
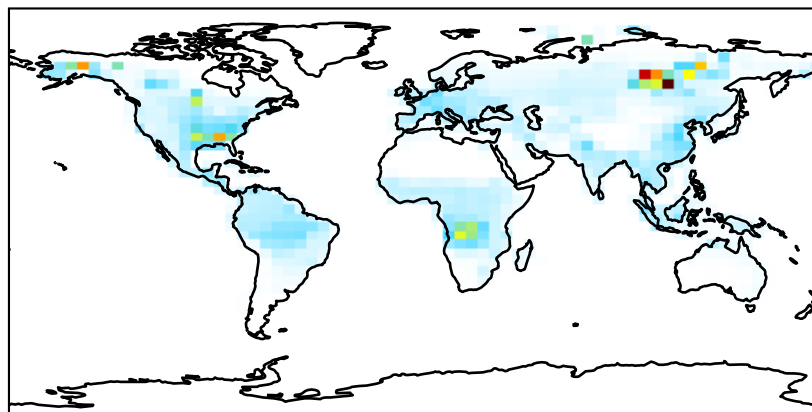


# EmisC2H4\_Total (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



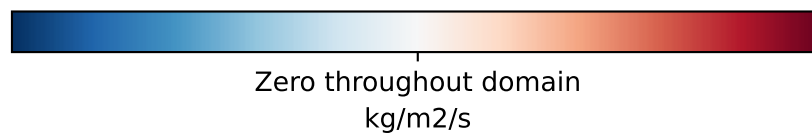
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



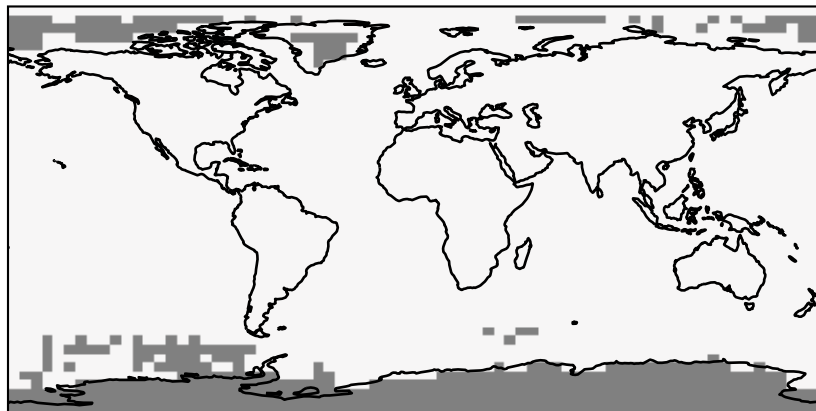
Difference  
Dev - Ref, Dynamic Range



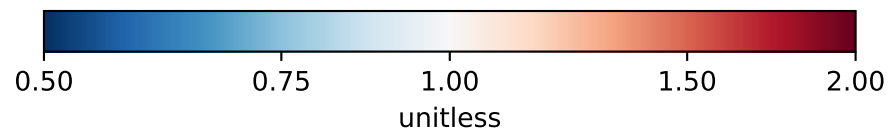
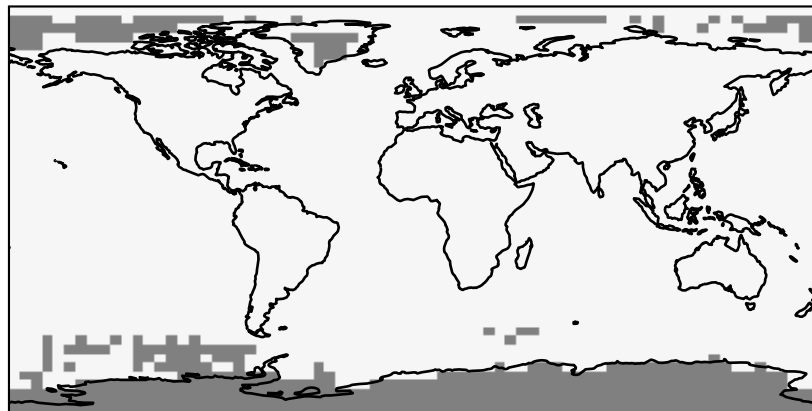
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

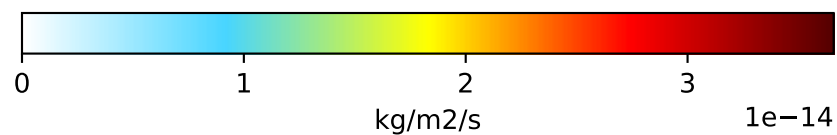
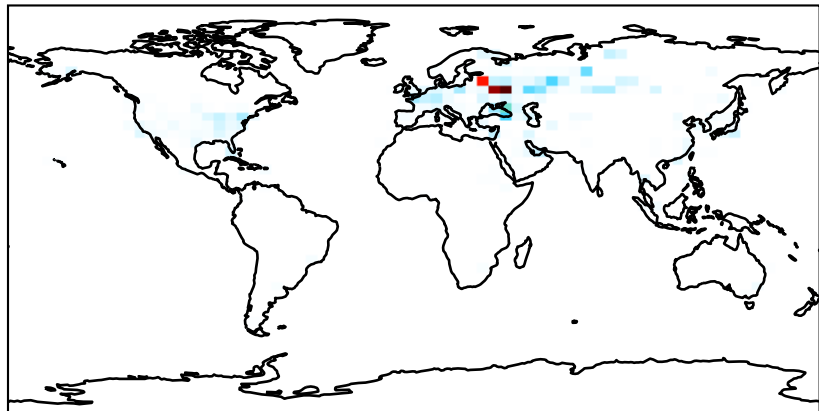


Ratio  
Dev/Ref, Fixed Range

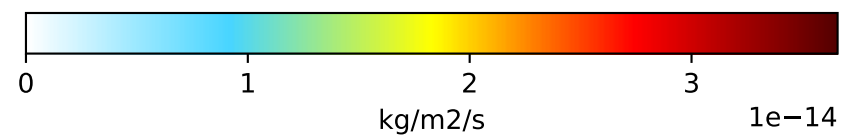


# EmisC2H6\_Aircraft (Jul2019)

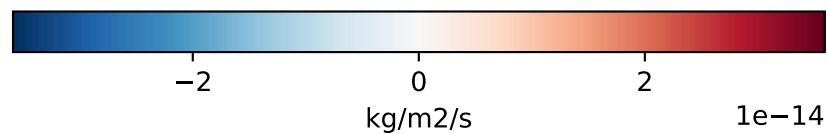
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



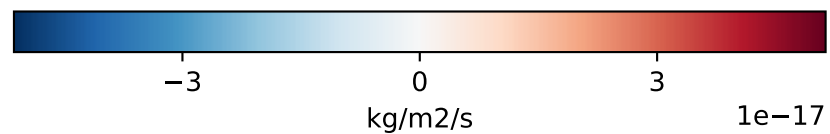
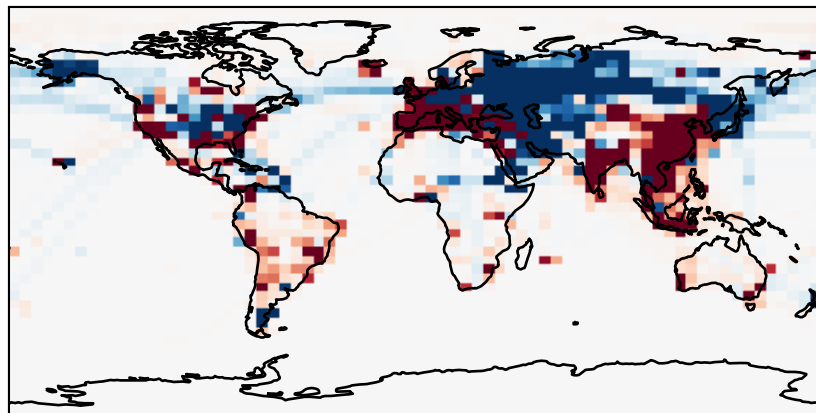
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



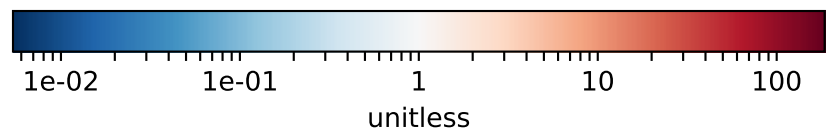
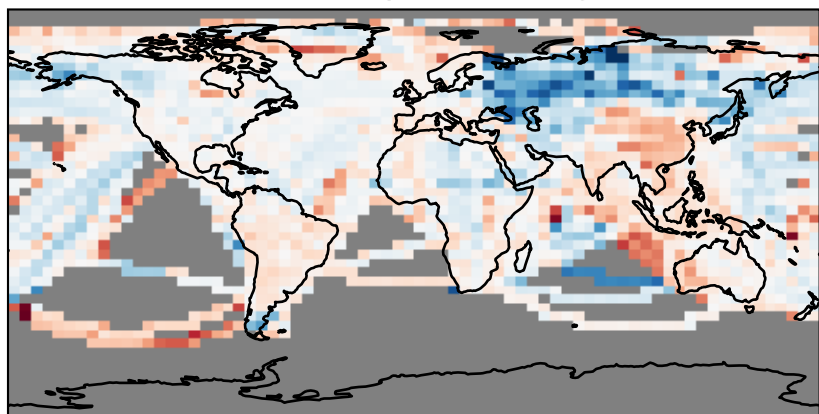
Difference  
Dev - Ref, Dynamic Range



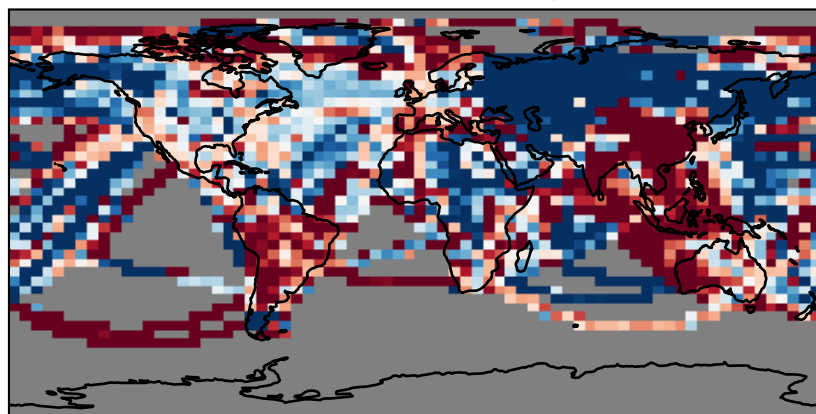
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



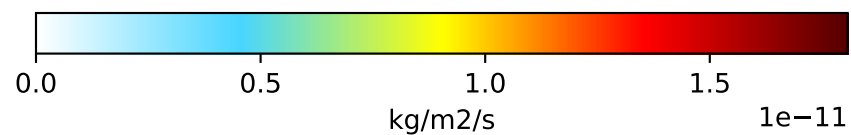
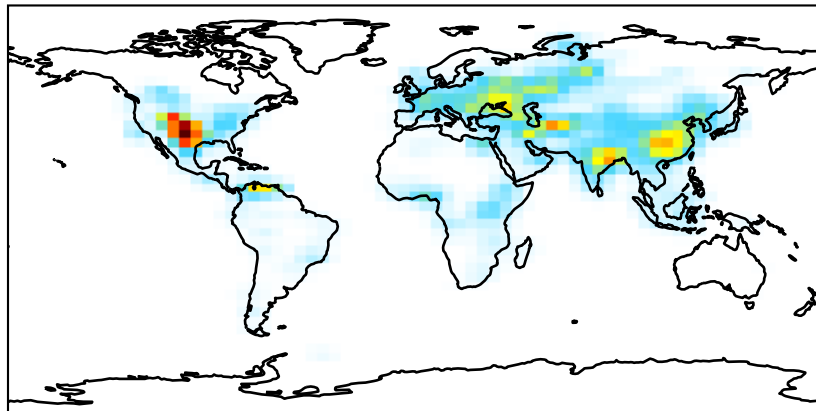
Ratio  
Dev/Ref, Fixed Range



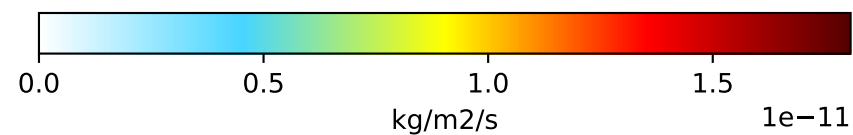
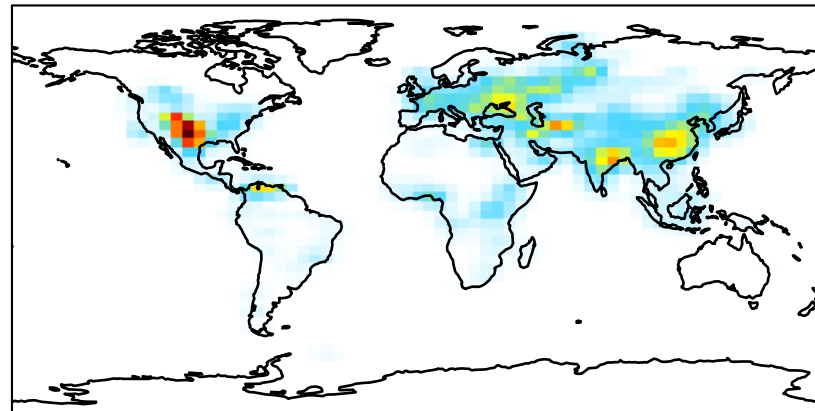


# EmisC2H6\_Anthro (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



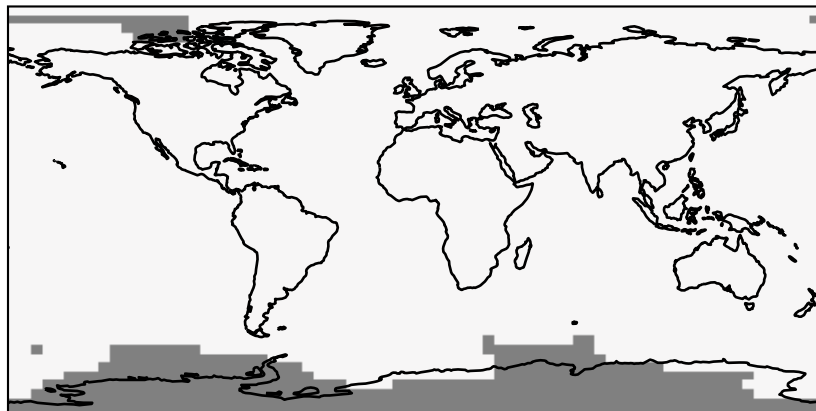
Difference  
Dev - Ref, Dynamic Range



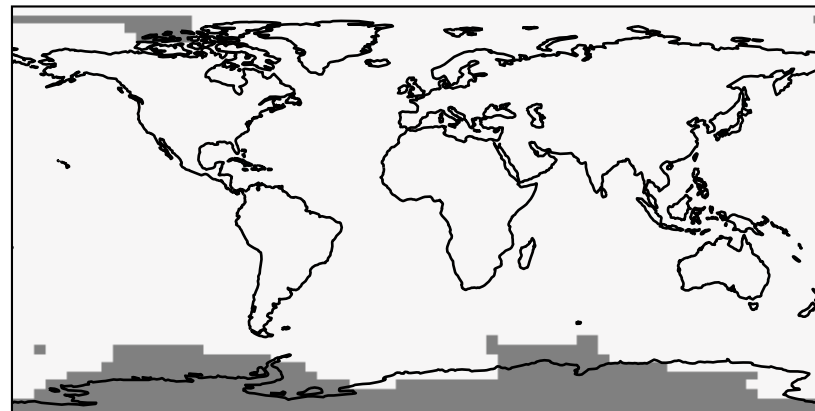
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

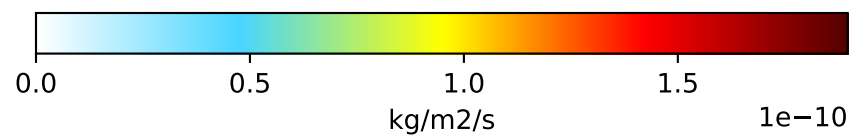
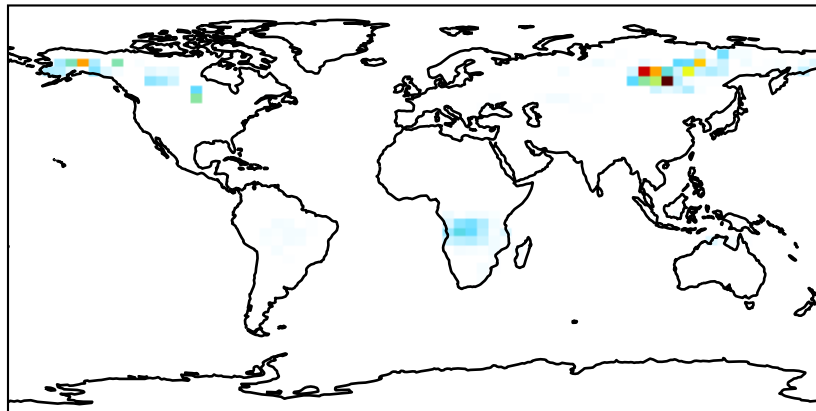


Ratio  
Dev/Ref, Fixed Range

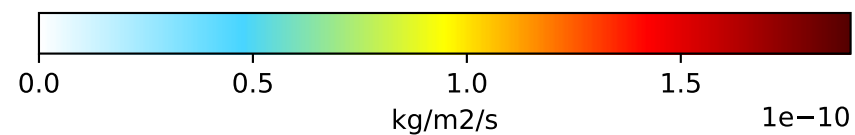
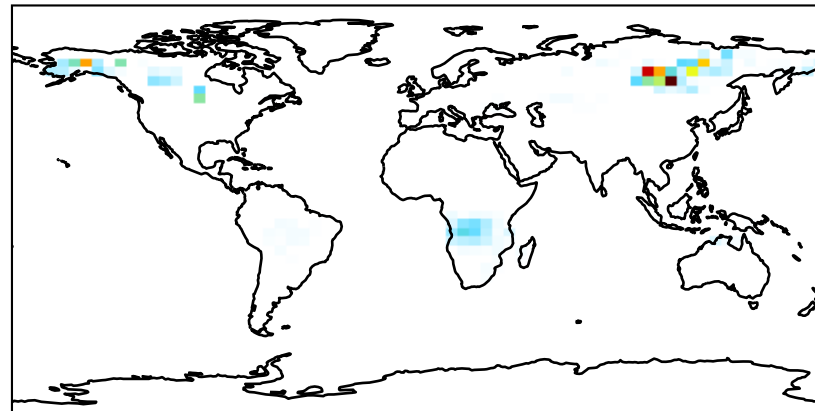


# EmisC2H6\_BioBurn (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



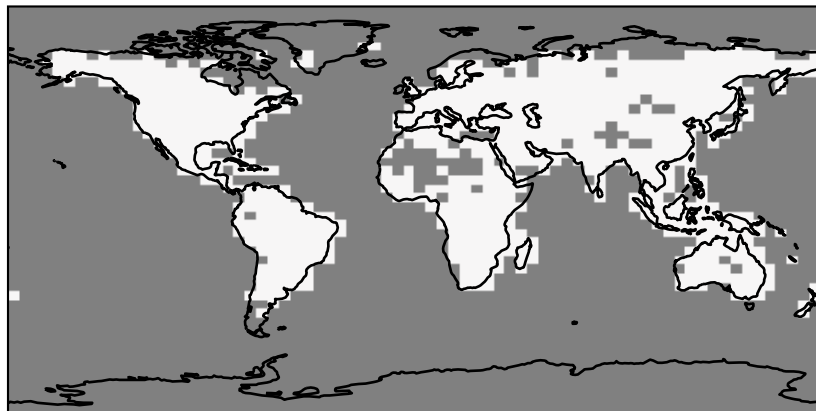
Difference  
Dev - Ref, Dynamic Range



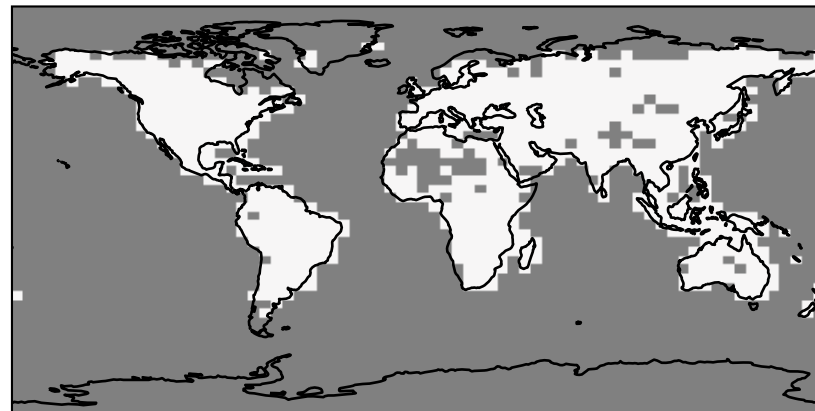
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

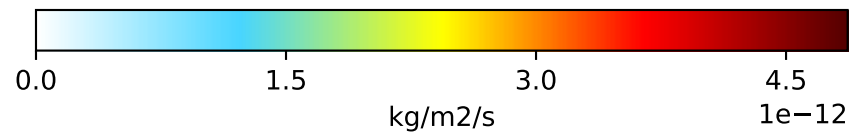


Ratio  
Dev/Ref, Fixed Range

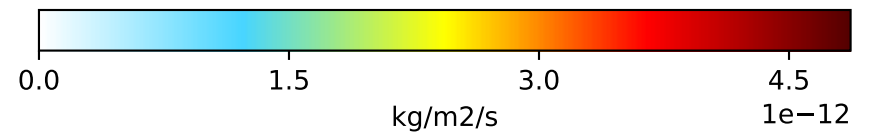
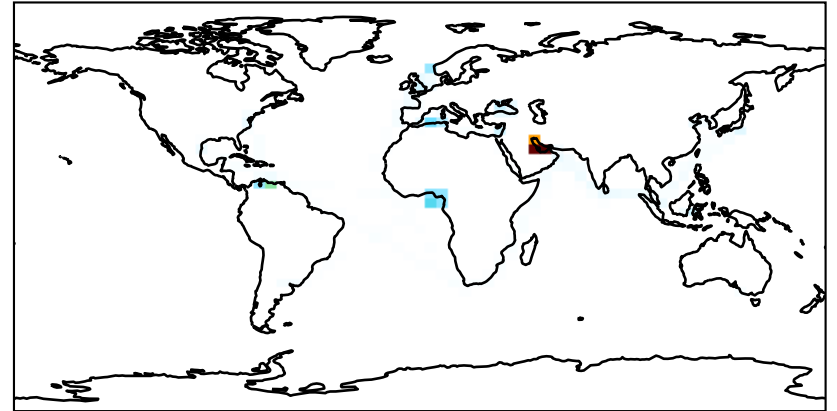


# EmisC2H6\_Ship (Jul2019)

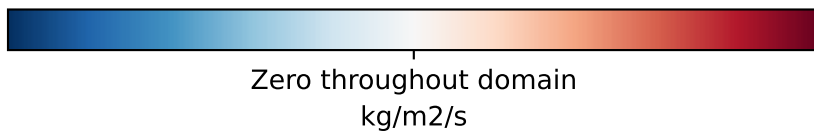
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



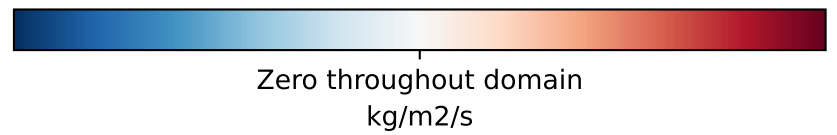
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



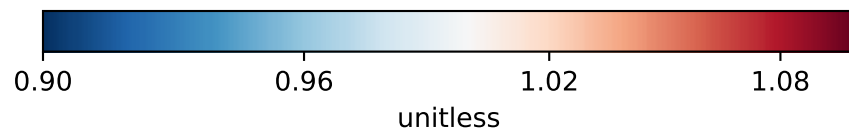
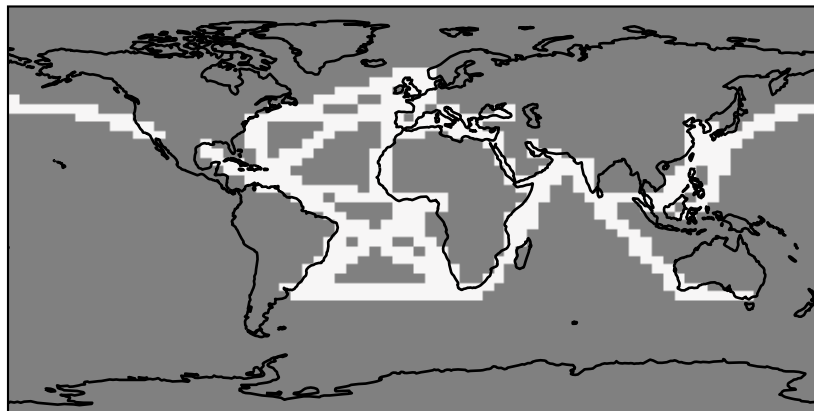
Difference  
Dev - Ref, Dynamic Range



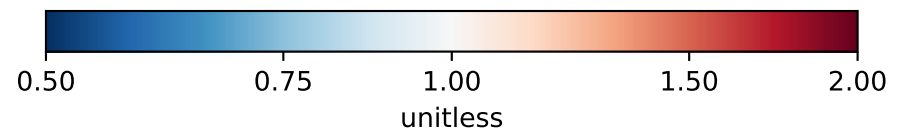
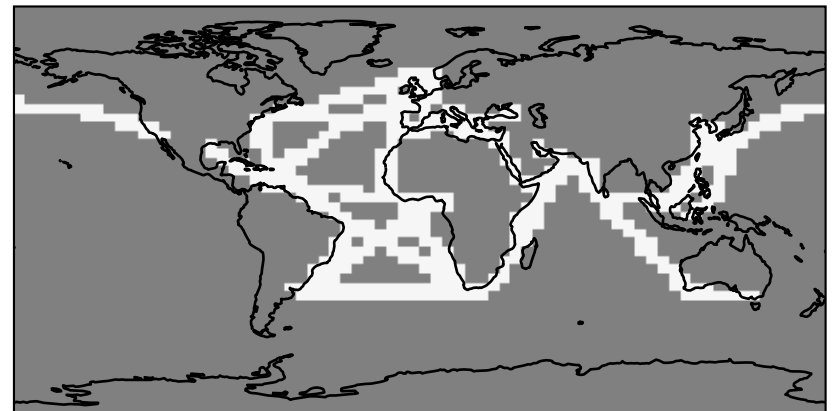
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

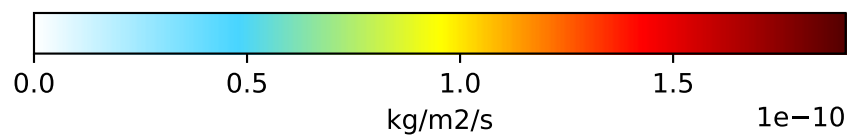
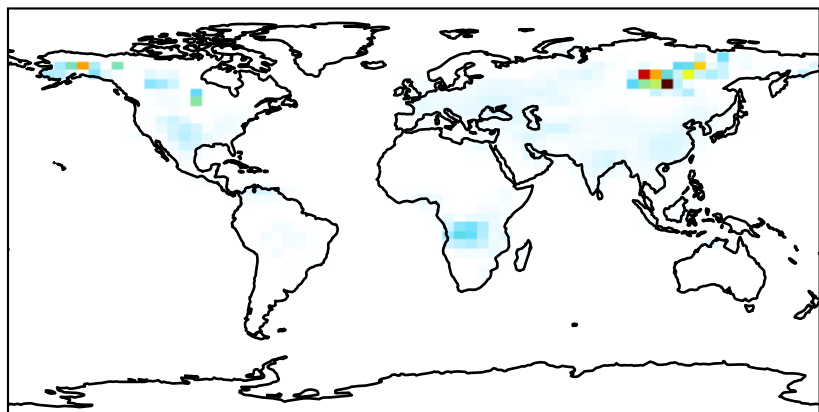


Ratio  
Dev/Ref, Fixed Range

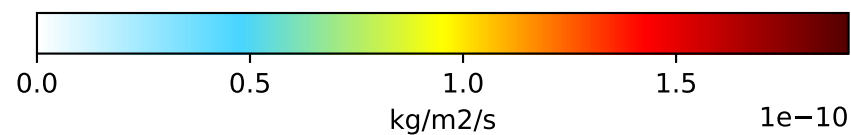
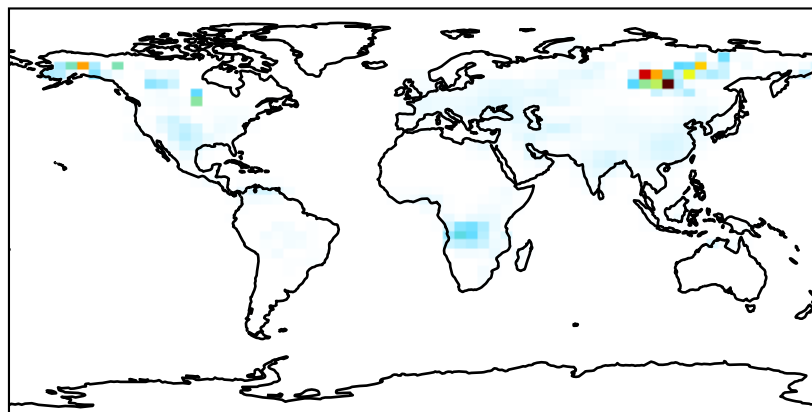


# EmisC2H6\_Total (Jul2019)

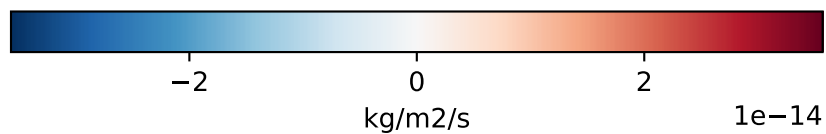
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



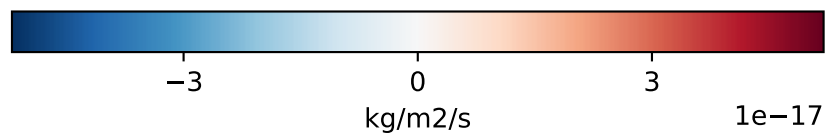
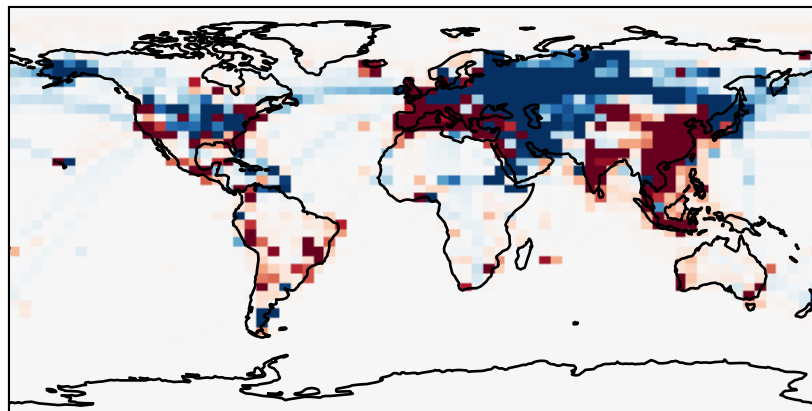
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



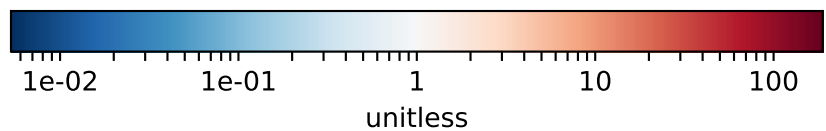
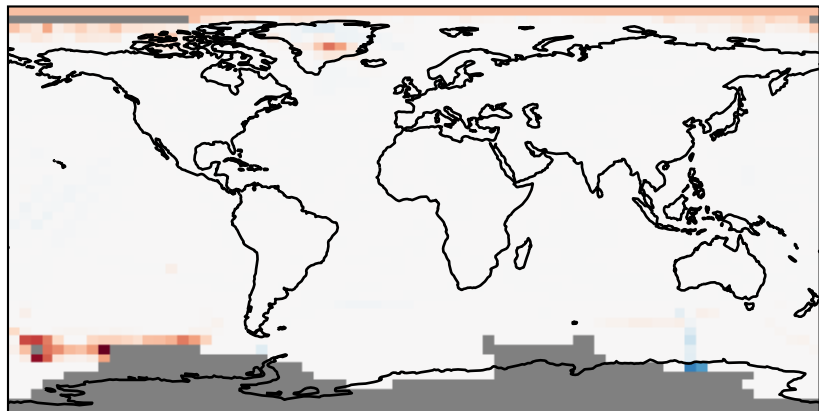
Difference  
Dev - Ref, Dynamic Range



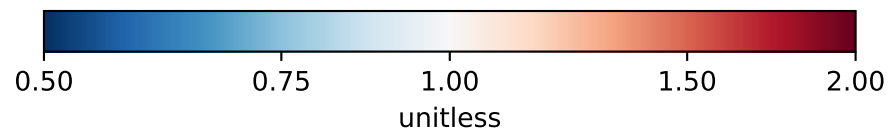
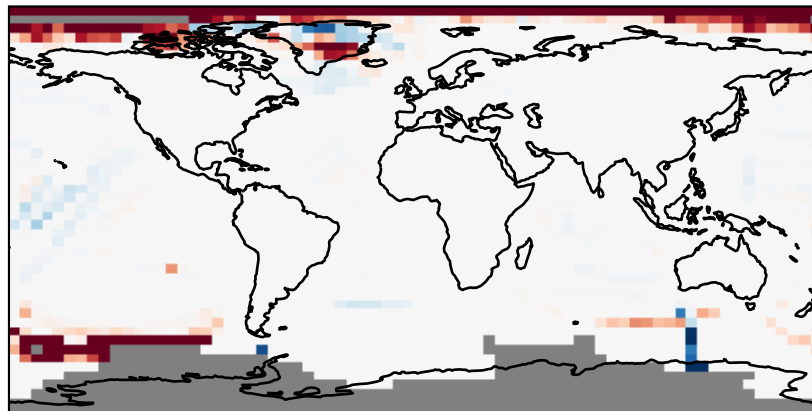
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

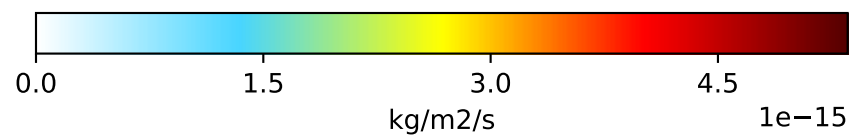
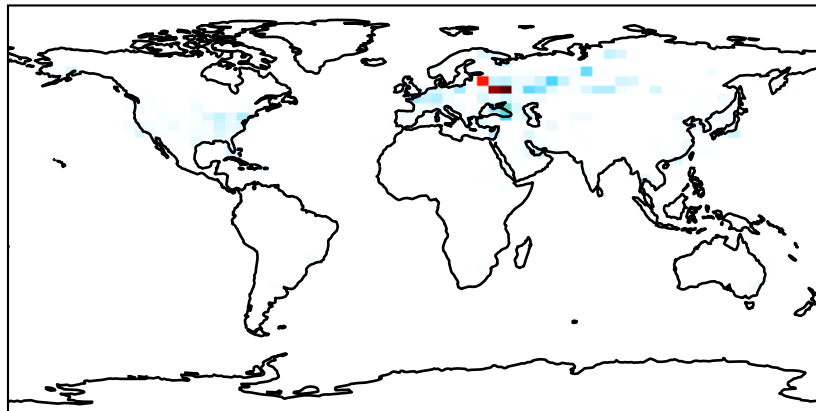


Ratio  
Dev/Ref, Fixed Range

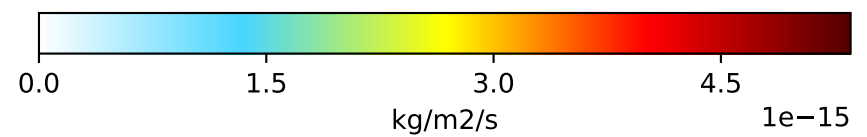


# EmisC3H8\_Aircraft (Jul2019)

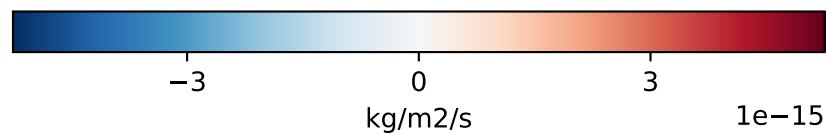
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



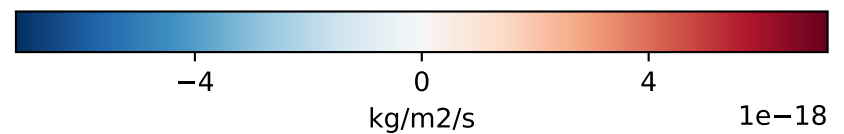
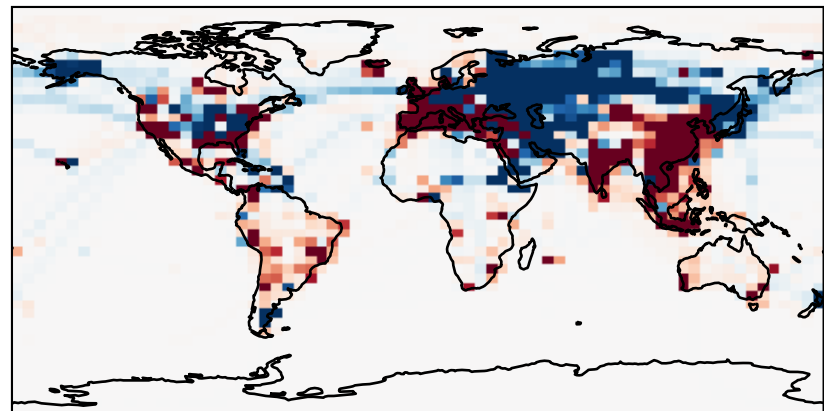
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



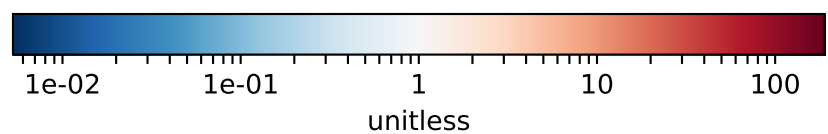
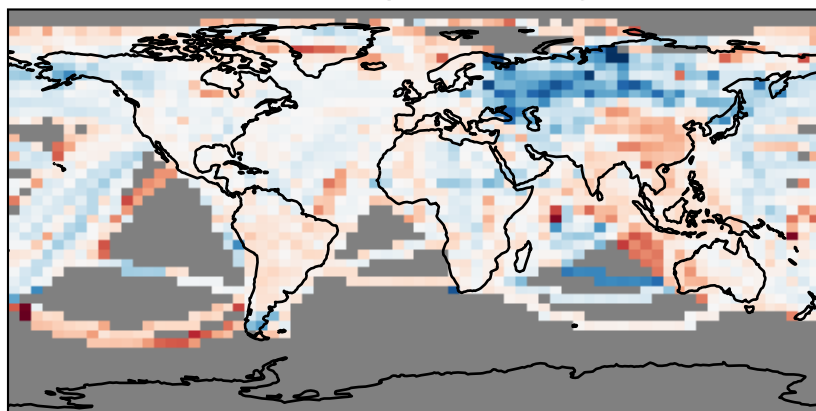
Difference  
Dev - Ref, Dynamic Range



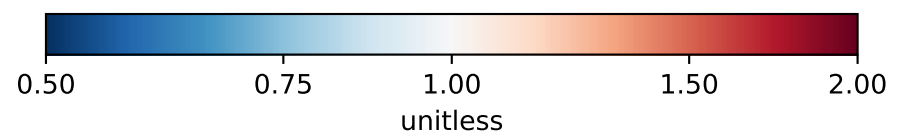
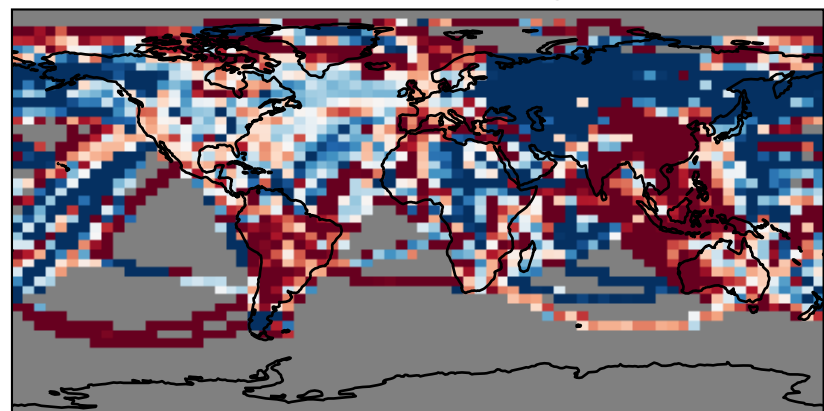
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



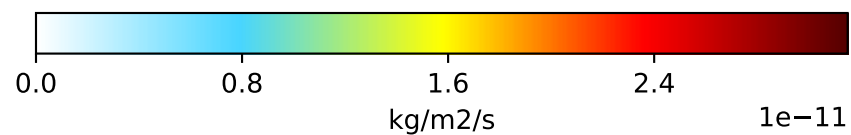
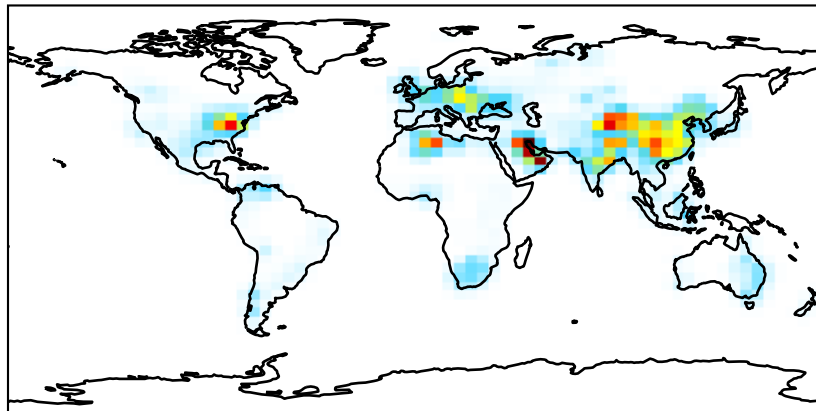
Ratio  
Dev/Ref, Fixed Range



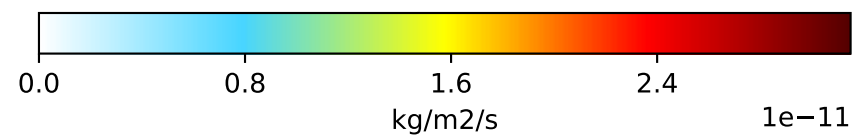
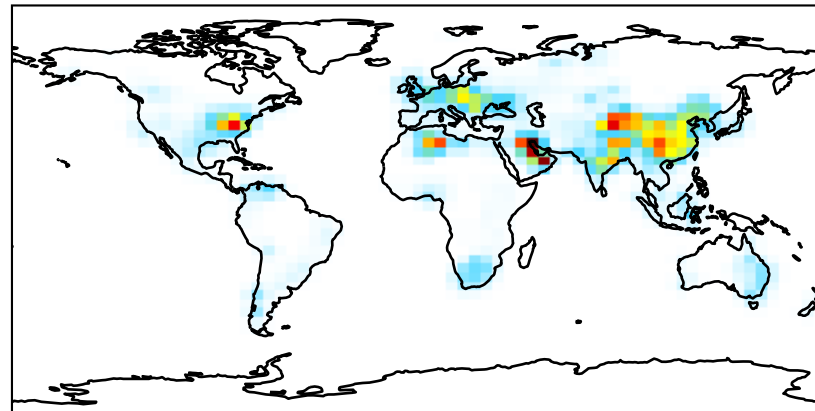


# EmisC3H8\_Anthro (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



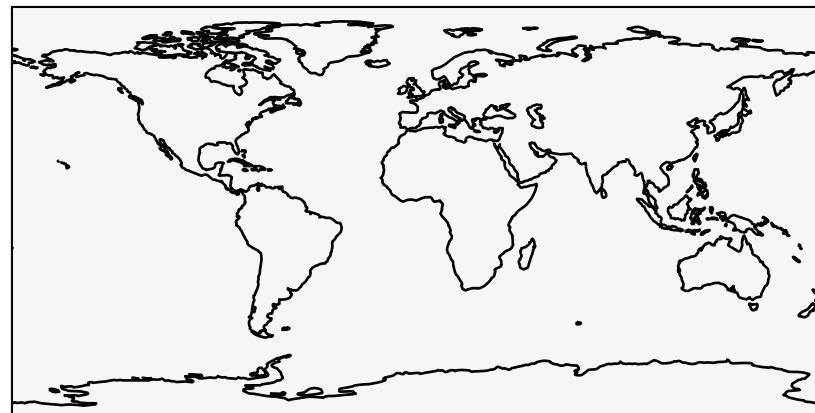
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



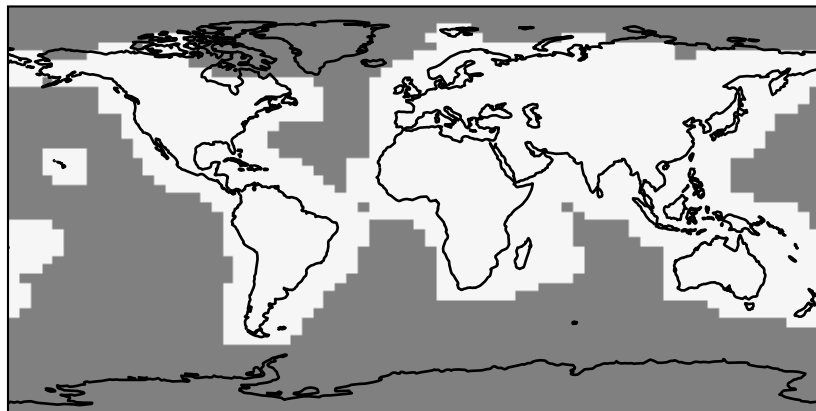
Difference  
Dev - Ref, Dynamic Range



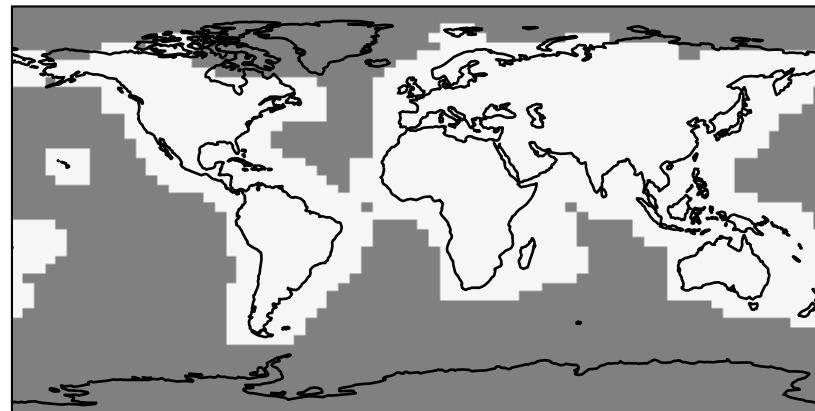
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

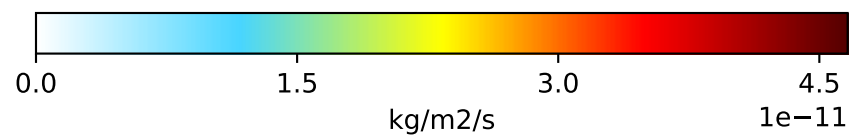
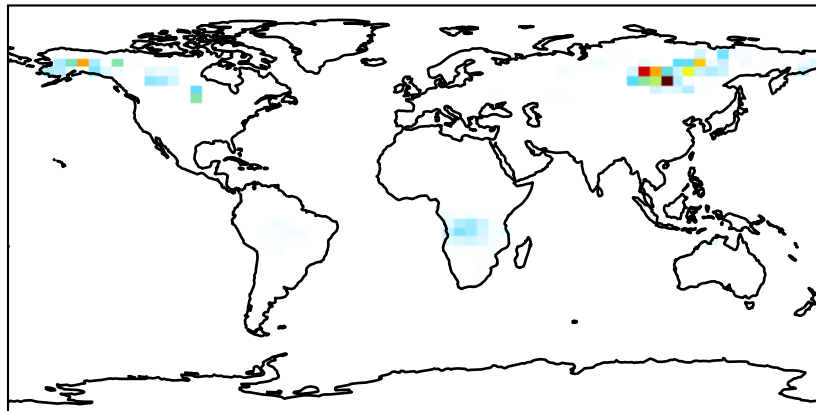


Ratio  
Dev/Ref, Fixed Range

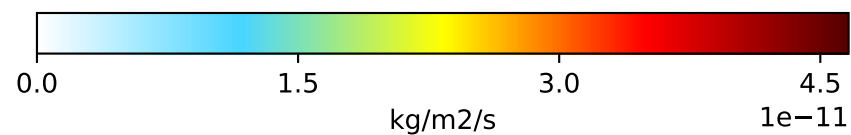
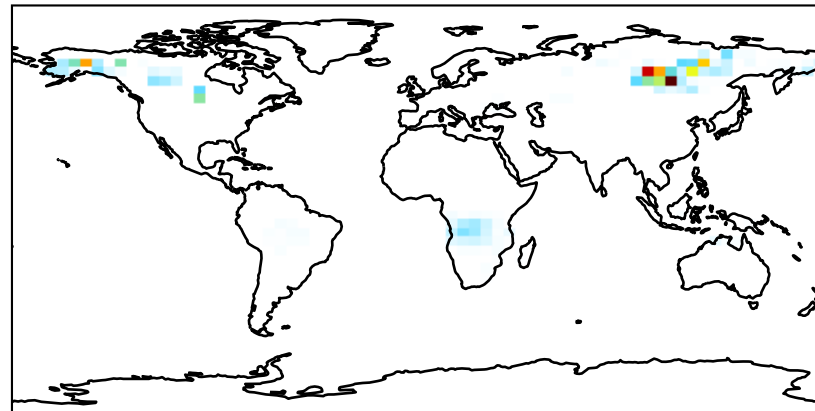


# EmisC3H8\_BioBurn (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



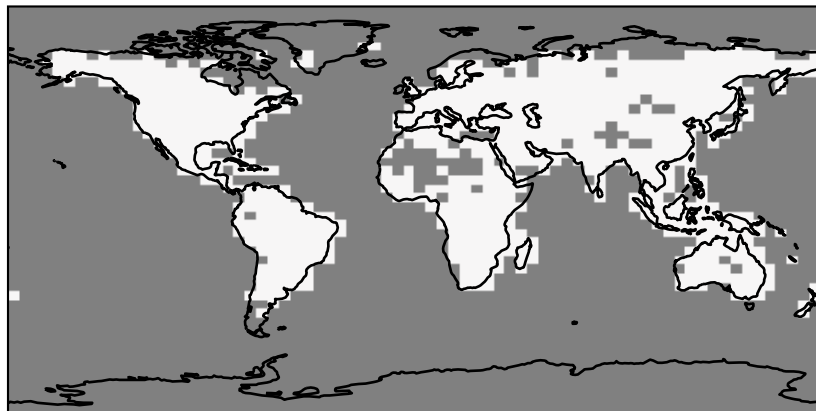
Difference  
Dev - Ref, Dynamic Range



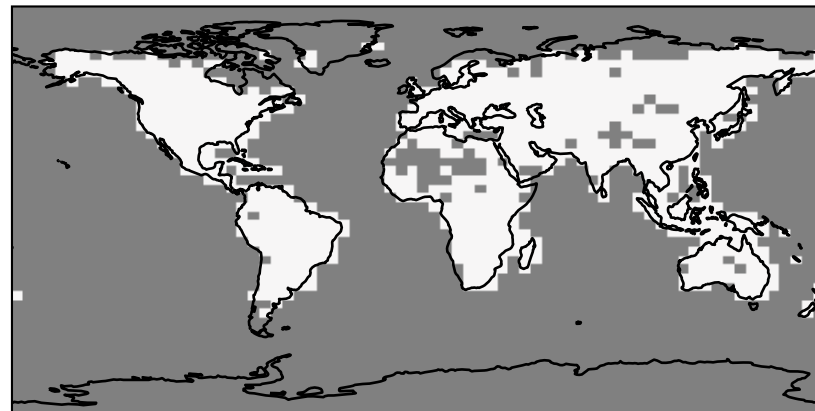
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

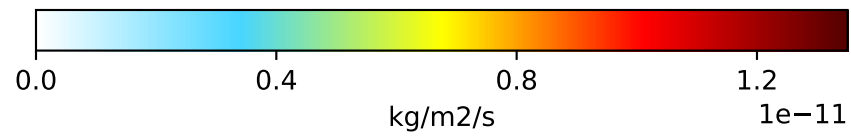


Ratio  
Dev/Ref, Fixed Range

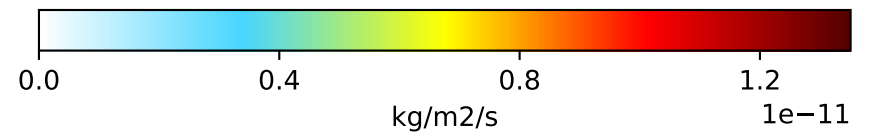
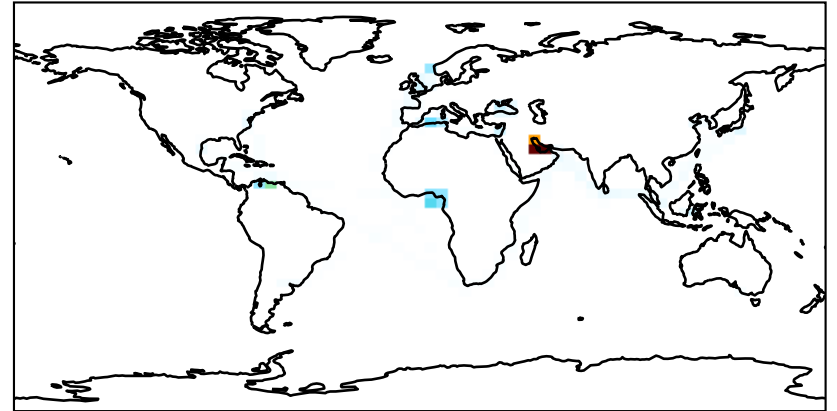


# EmisC3H8\_Ship (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



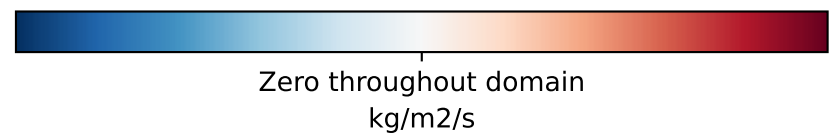
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



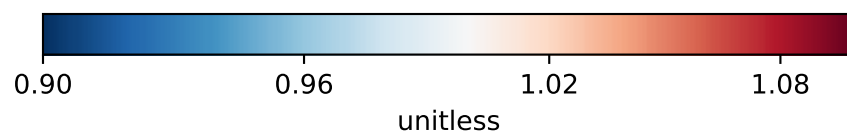
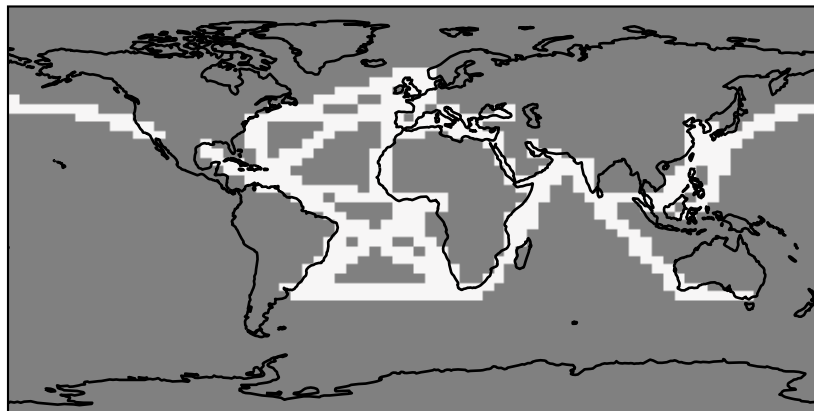
Difference  
Dev - Ref, Dynamic Range



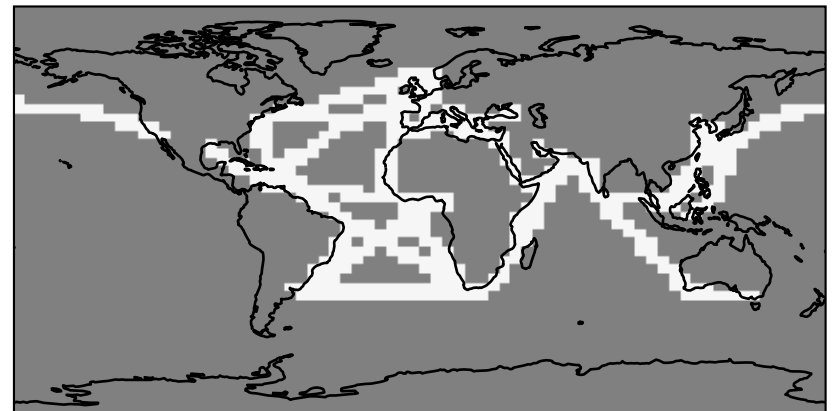
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

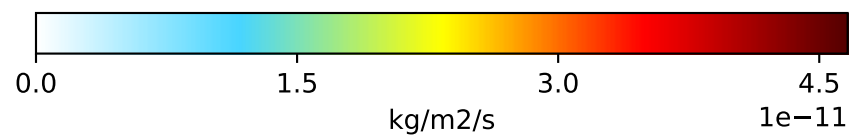
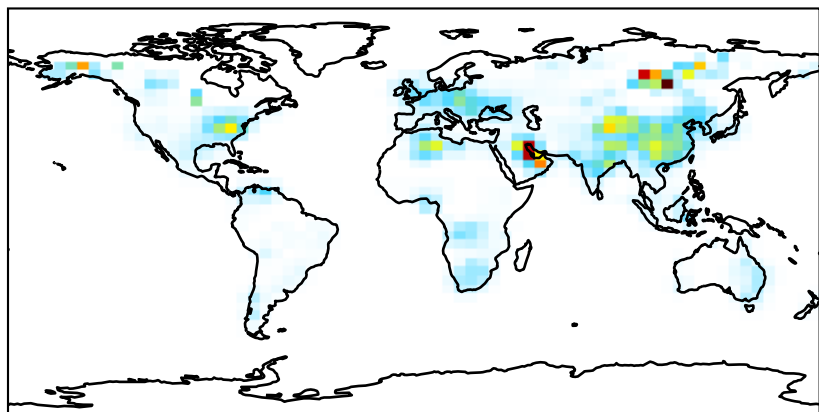


Ratio  
Dev/Ref, Fixed Range

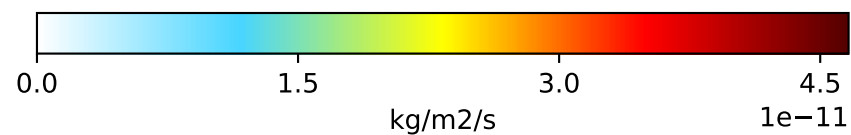
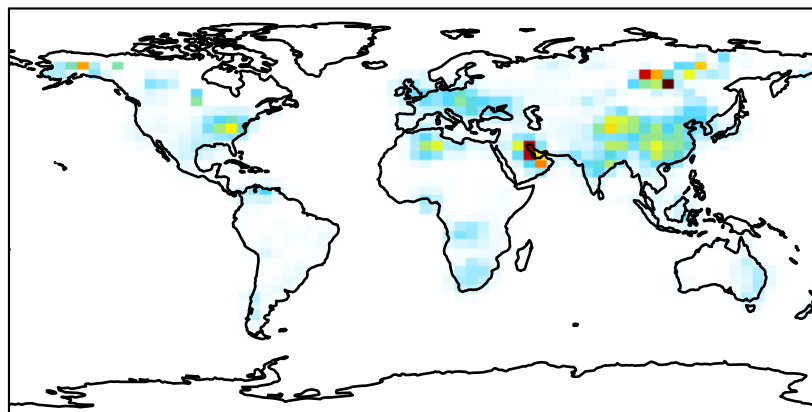


# EmisC3H8\_Total (Jul2019)

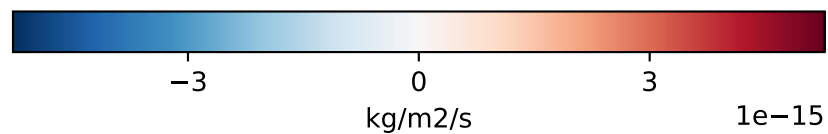
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



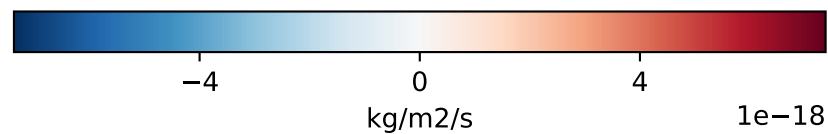
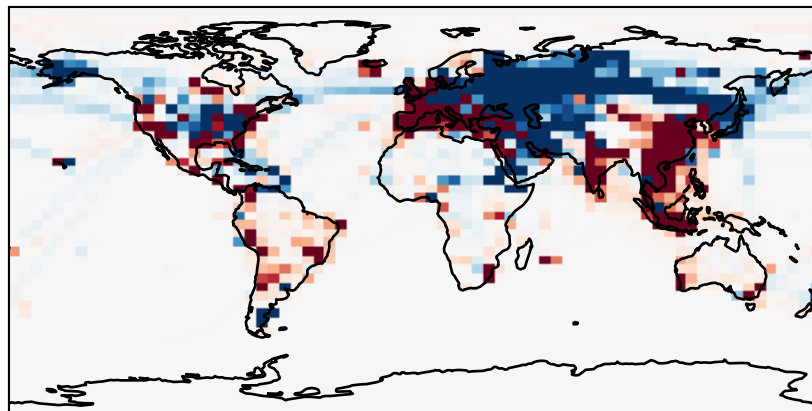
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



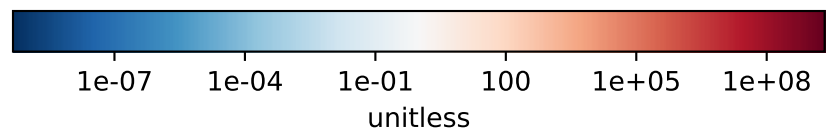
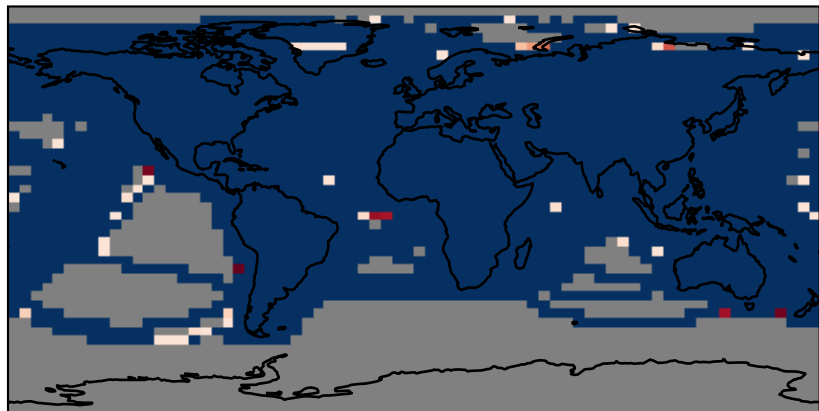
Difference  
Dev - Ref, Dynamic Range



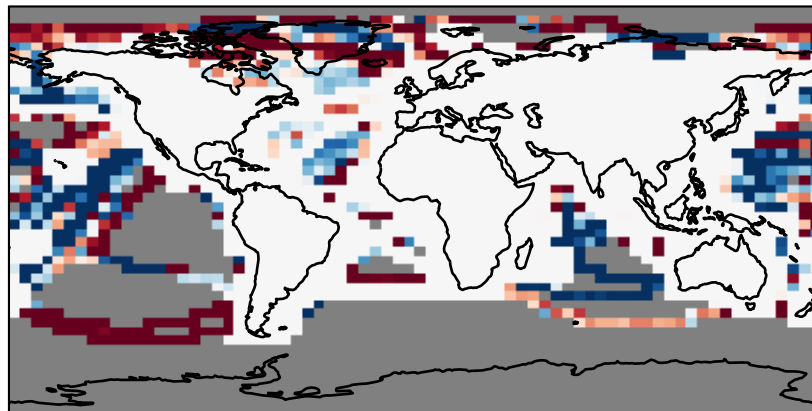
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

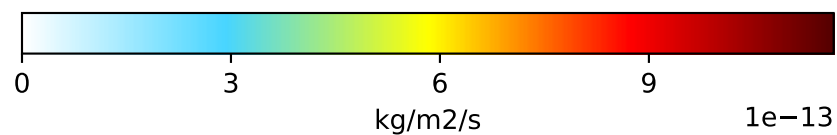
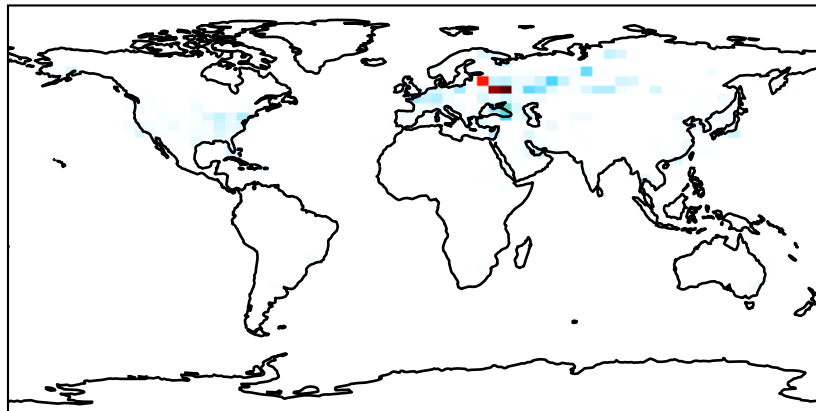


Ratio  
Dev/Ref, Fixed Range

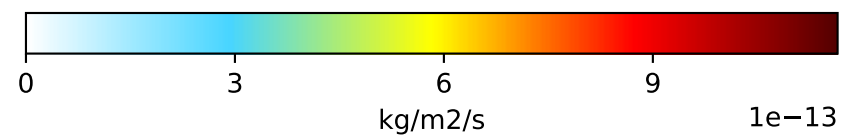


# EmisPRPE\_Aircraft (Jul2019)

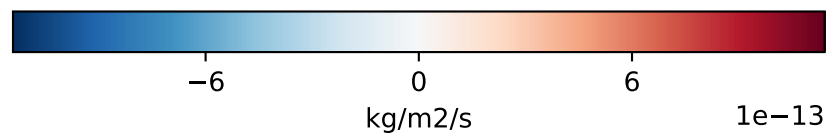
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



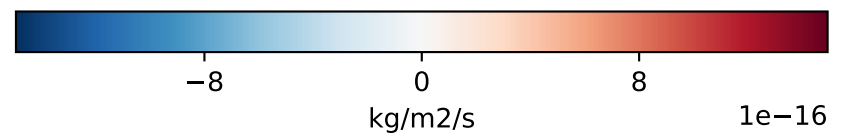
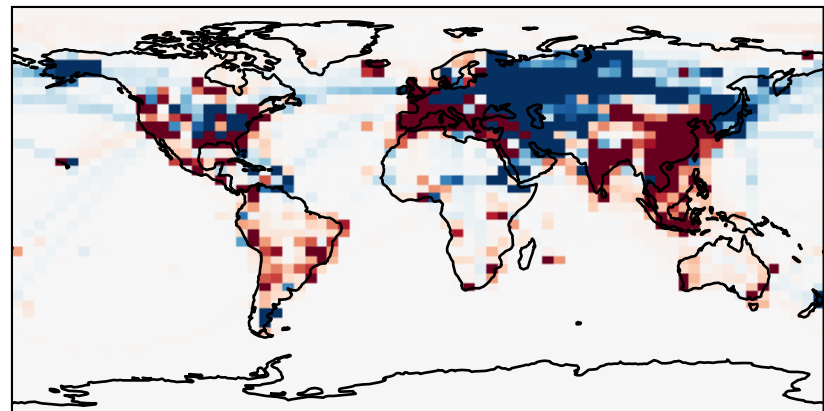
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



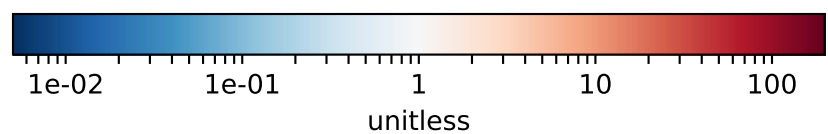
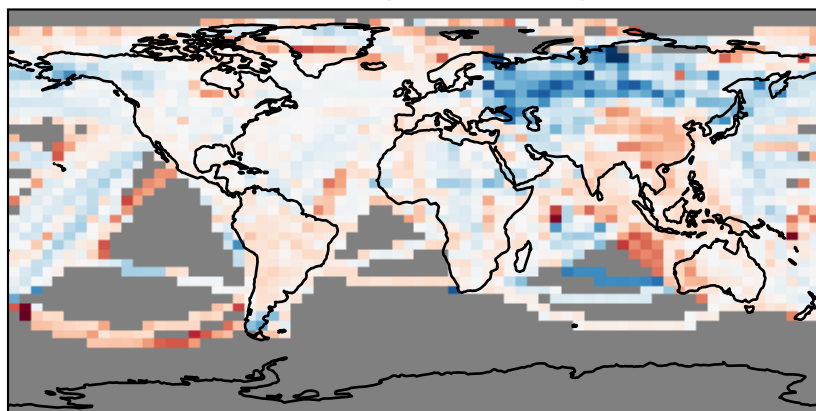
Difference  
Dev - Ref, Dynamic Range



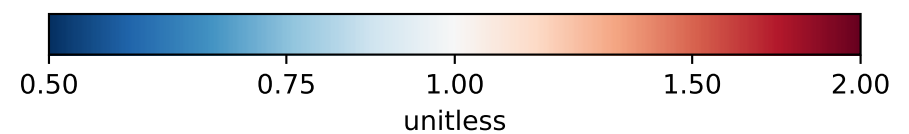
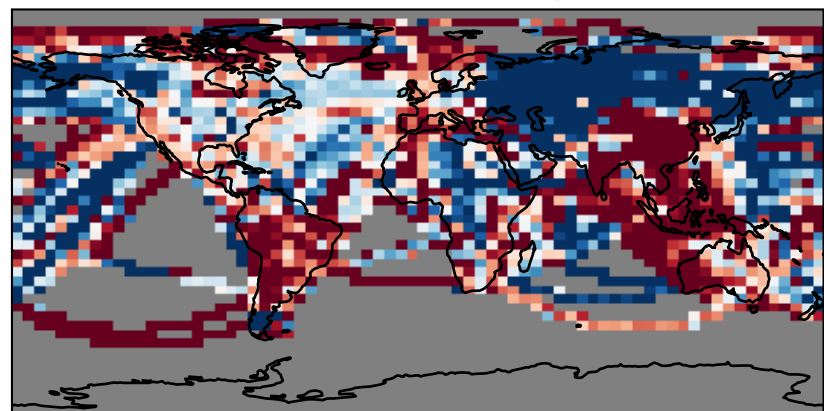
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



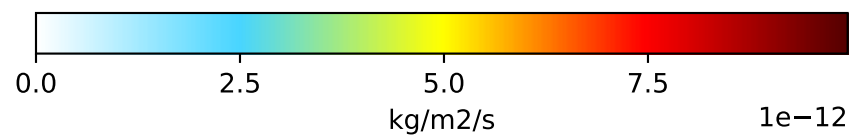
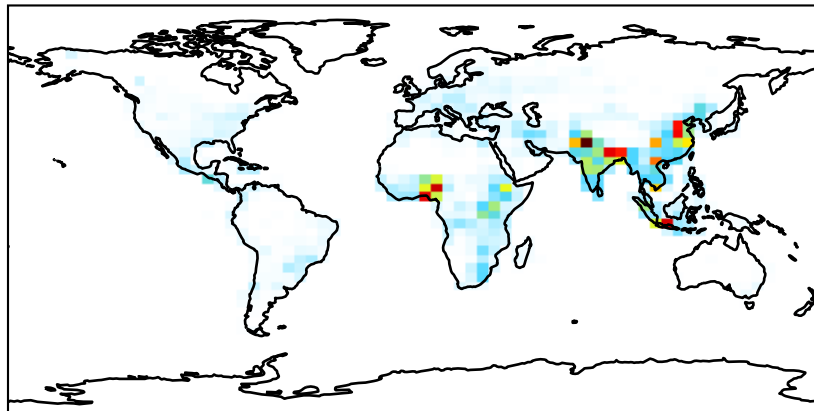
Ratio  
Dev/Ref, Fixed Range



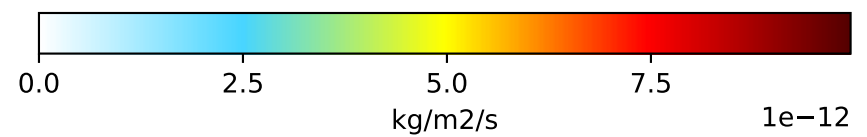
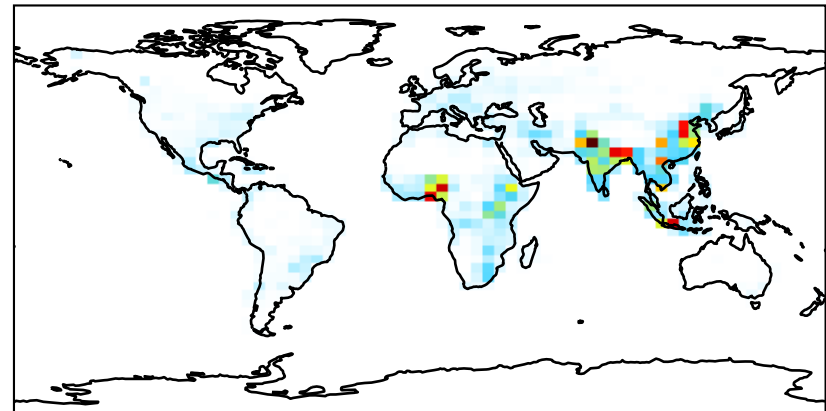


# EmisPRPE\_Anthro (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



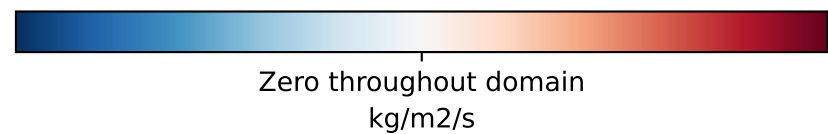
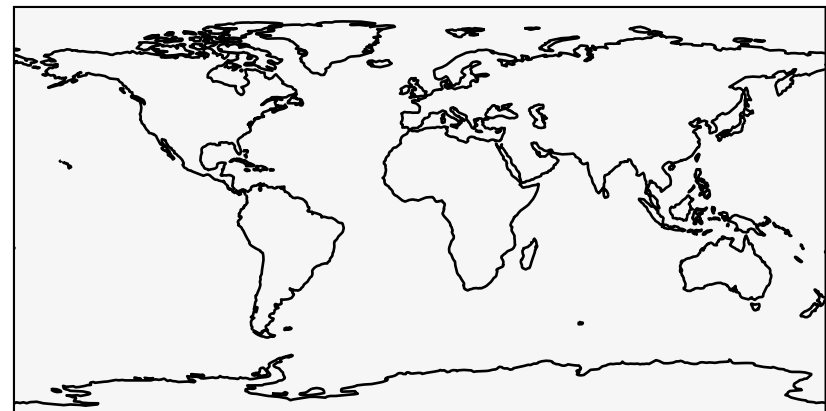
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



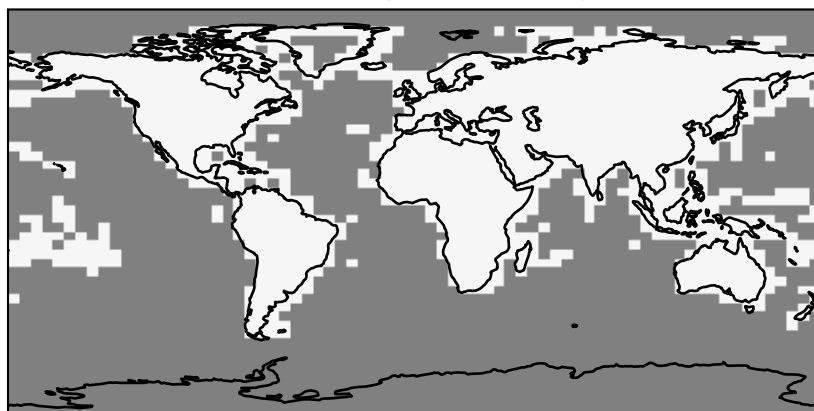
Difference  
Dev - Ref, Dynamic Range



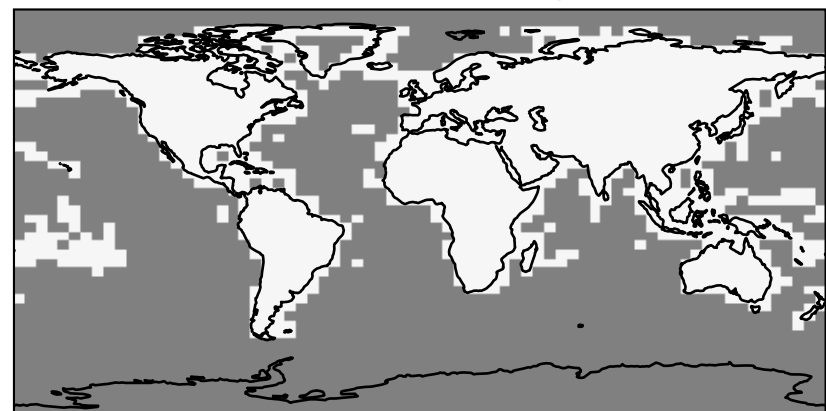
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

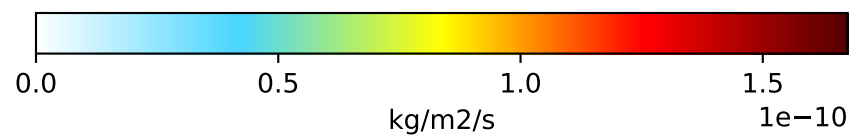
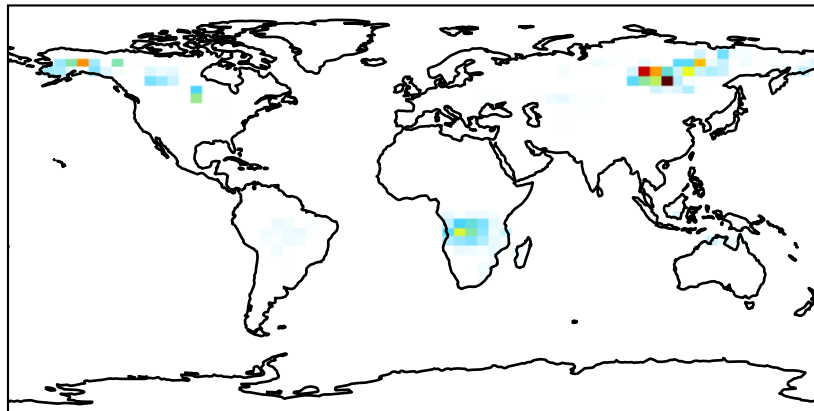


Ratio  
Dev/Ref, Fixed Range

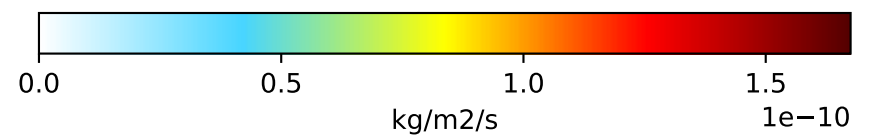
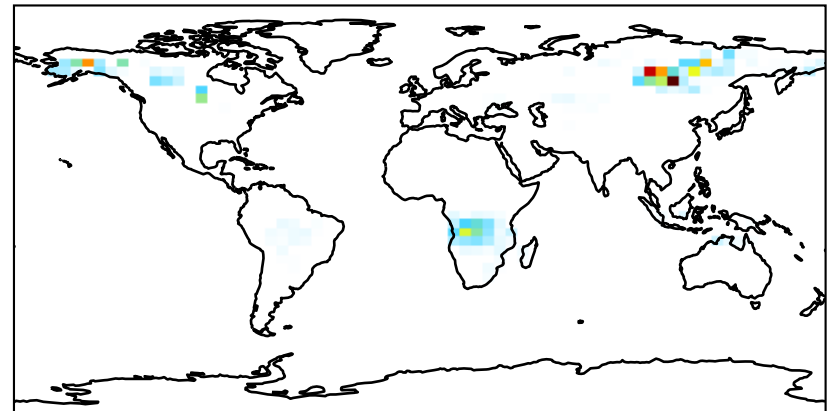


# EmisPRPE\_BioBurn (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



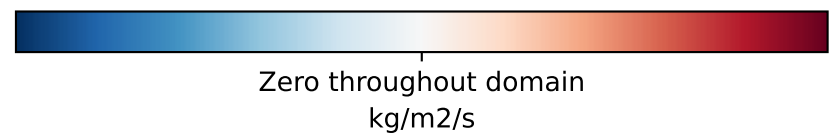
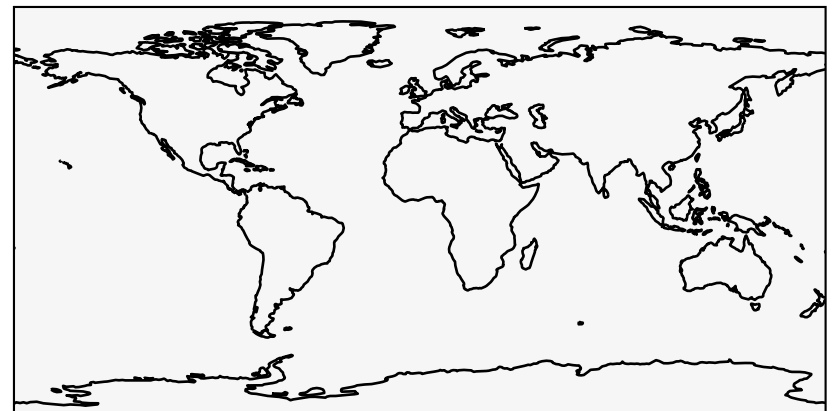
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



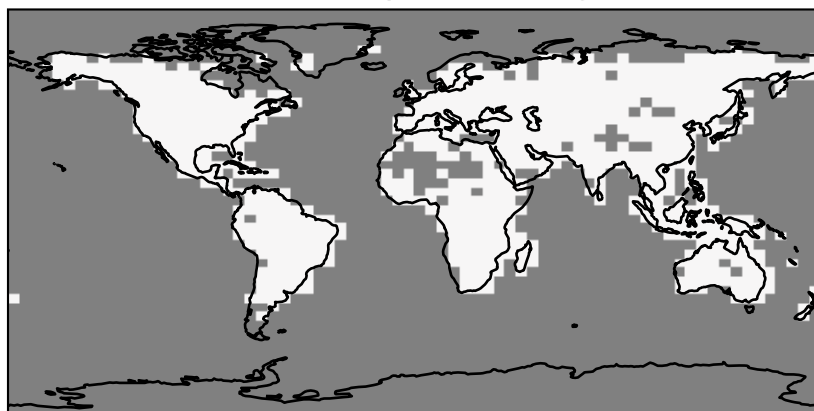
Difference  
Dev - Ref, Dynamic Range



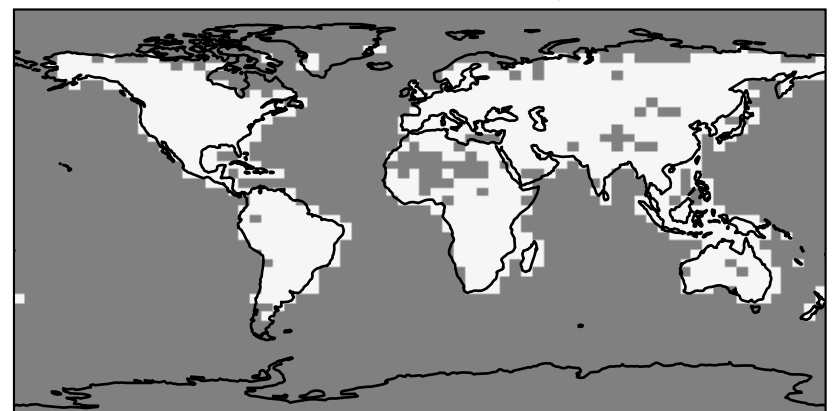
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

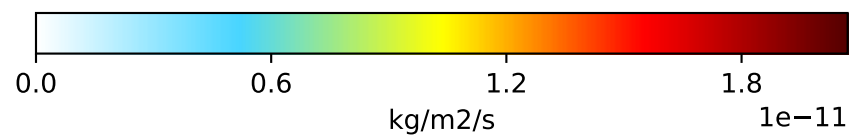
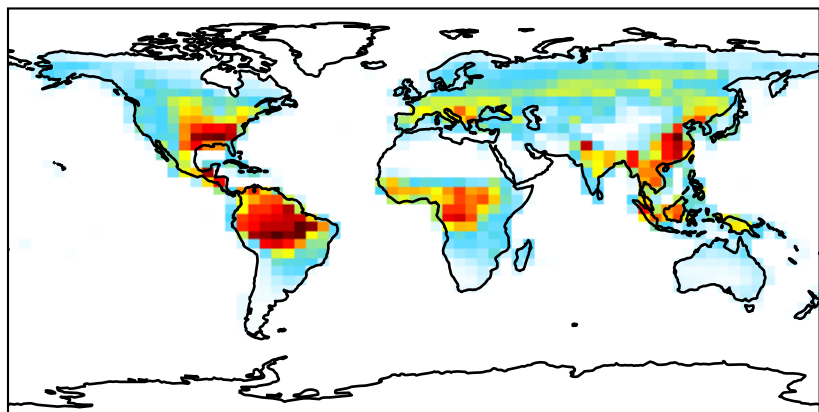


Ratio  
Dev/Ref, Fixed Range

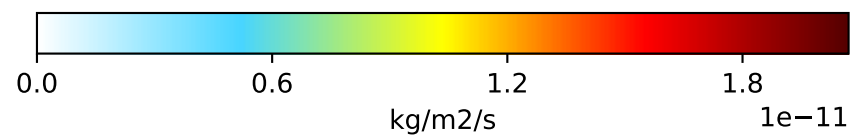
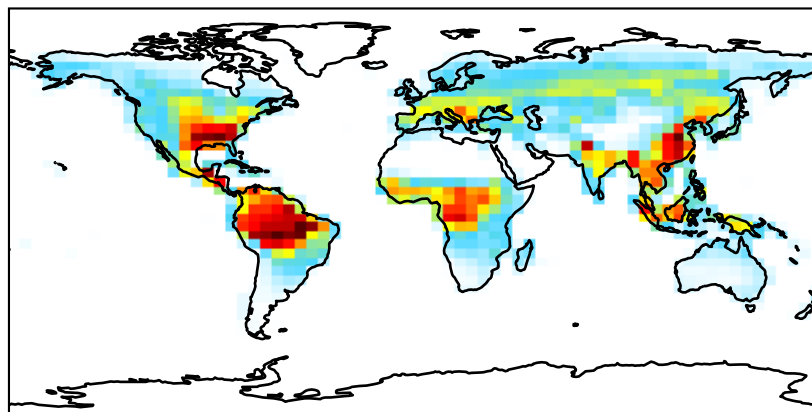


# EmisPRPE\_Biogenic (Jul2019)

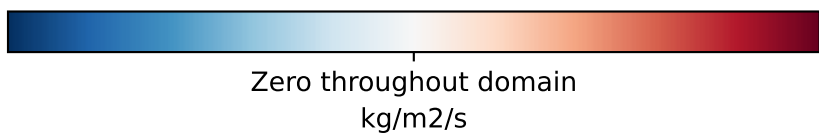
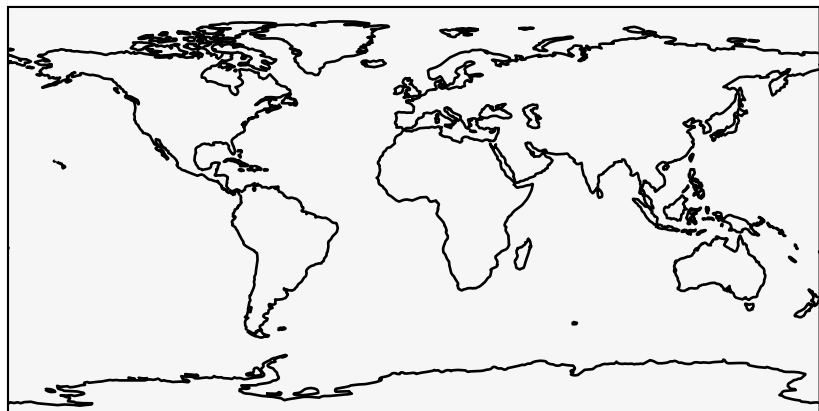
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



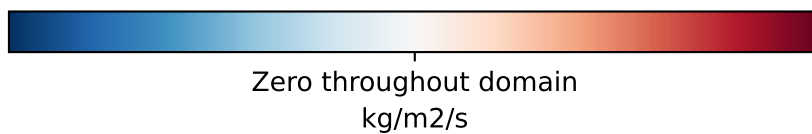
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



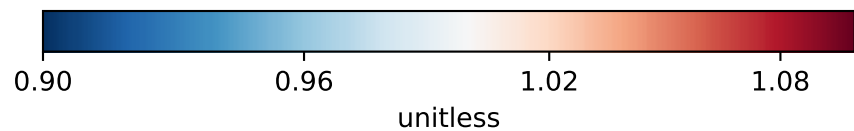
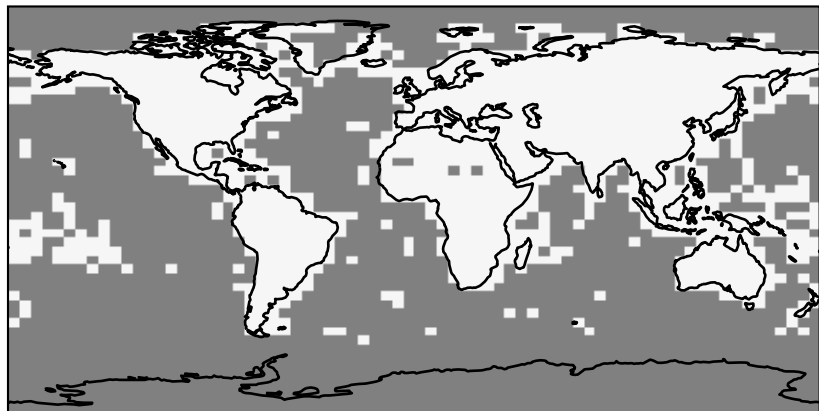
Difference  
Dev - Ref, Dynamic Range



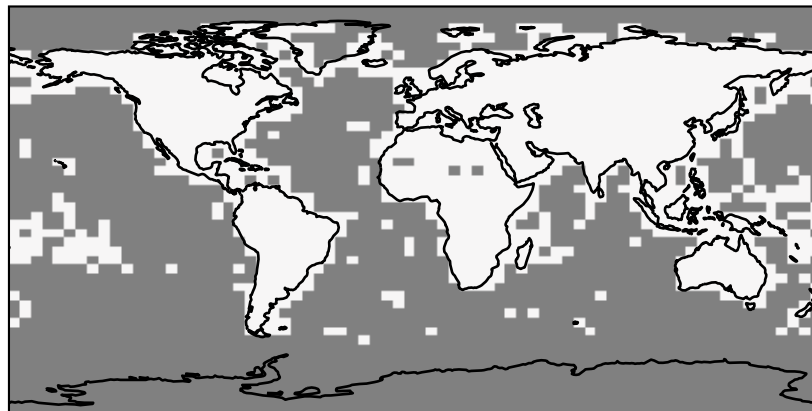
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

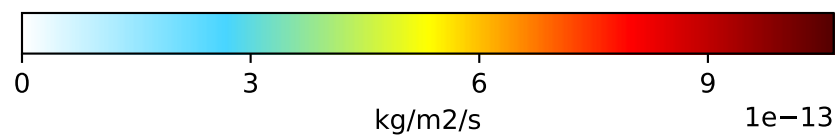
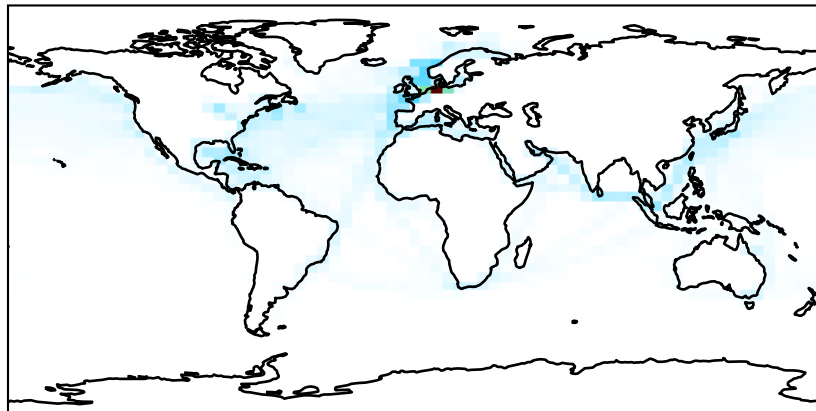


Ratio  
Dev/Ref, Fixed Range

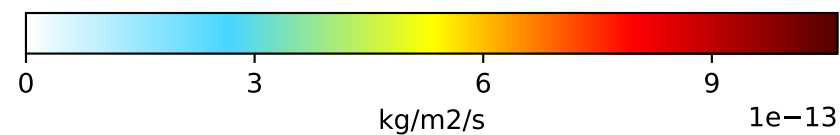
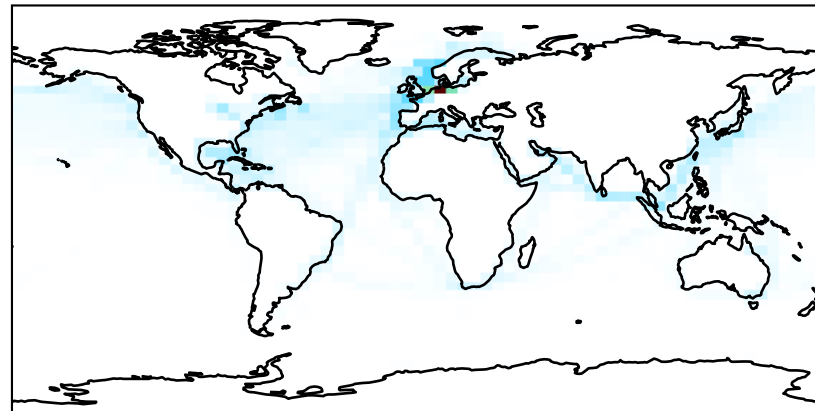


# EmisPRPE\_Ship (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



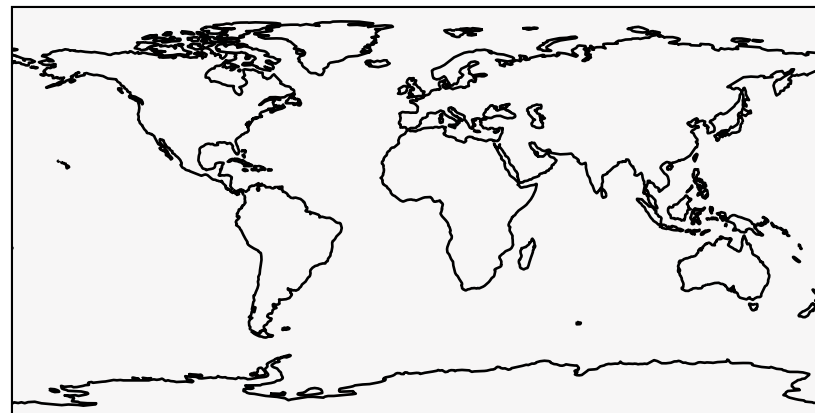
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



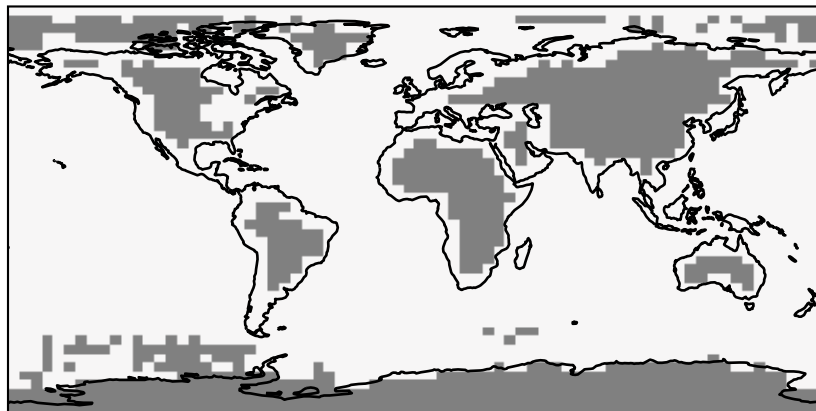
Difference  
Dev - Ref, Dynamic Range



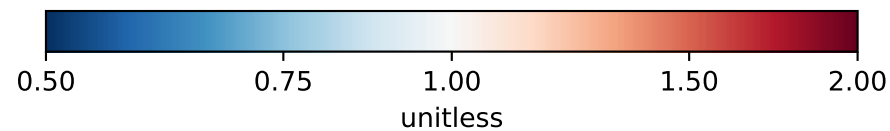
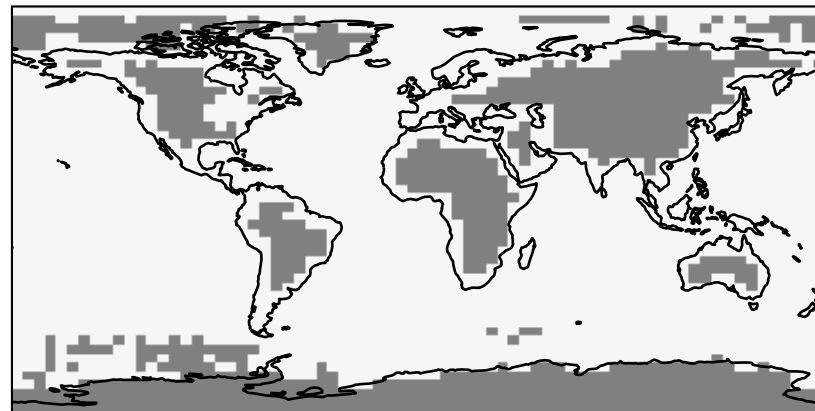
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

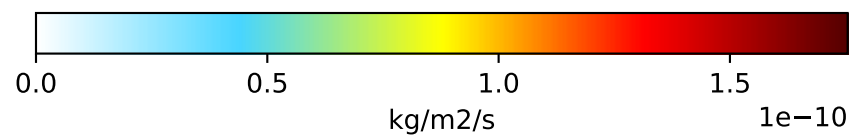
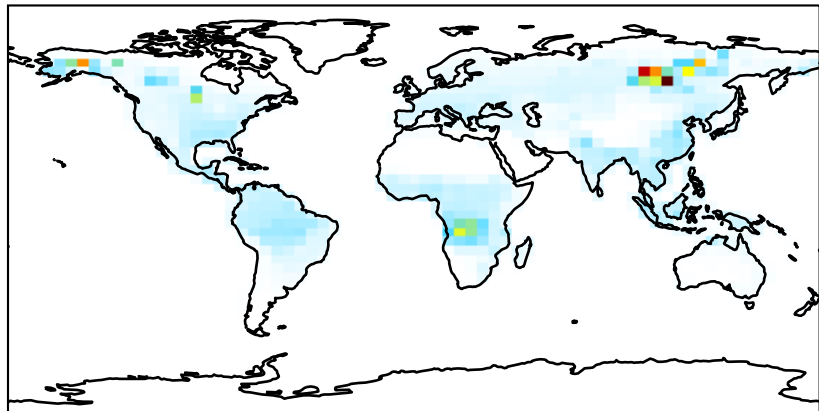


Ratio  
Dev/Ref, Fixed Range

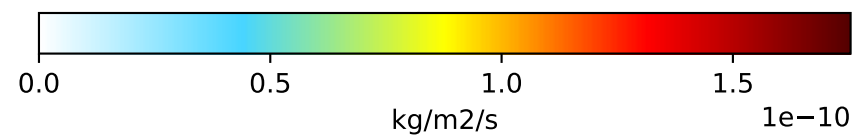
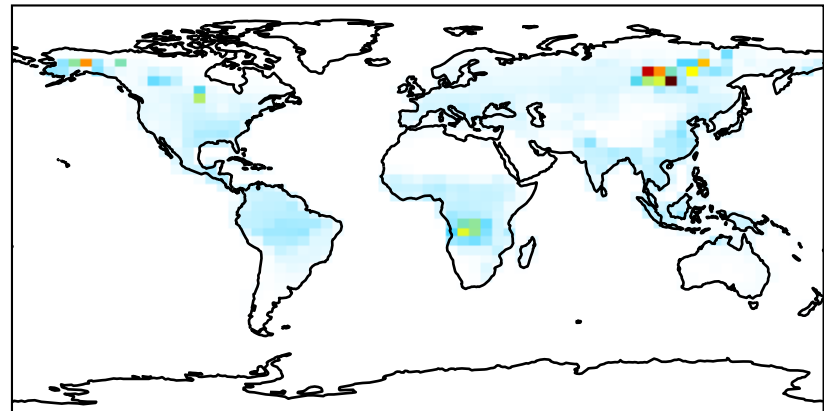


# EmisPRPE\_Total (Jul2019)

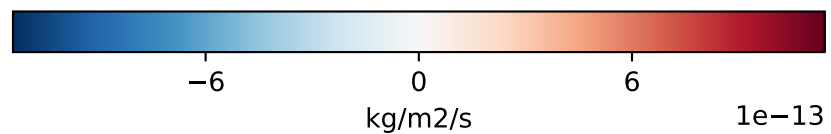
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



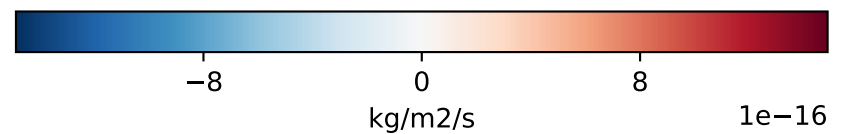
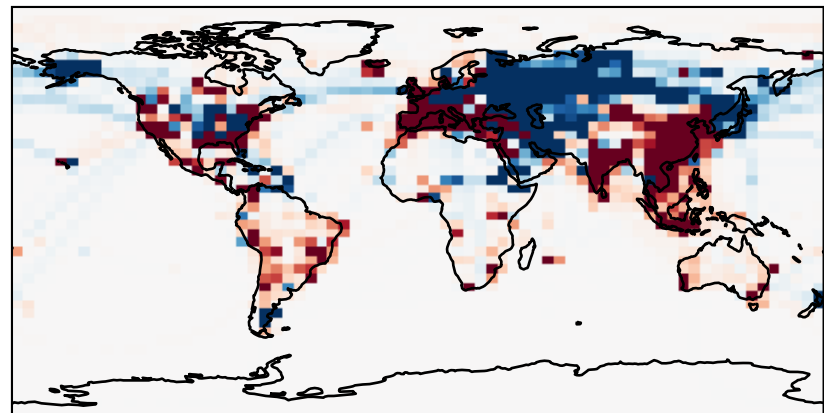
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



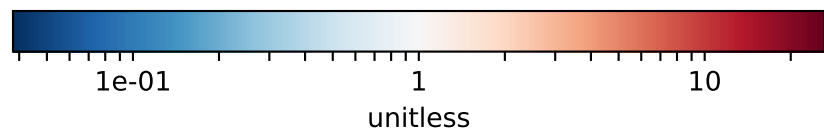
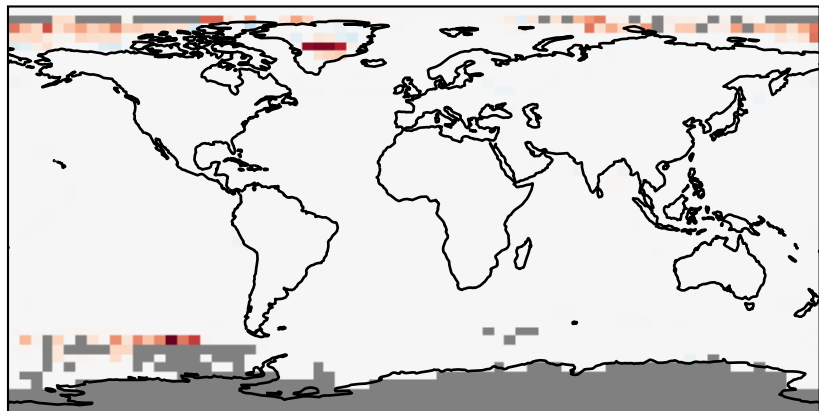
Difference  
Dev - Ref, Dynamic Range



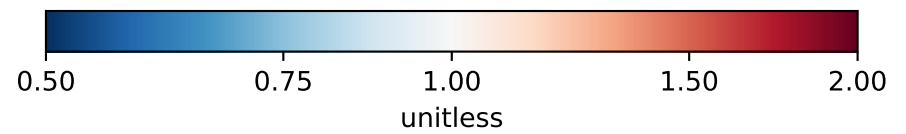
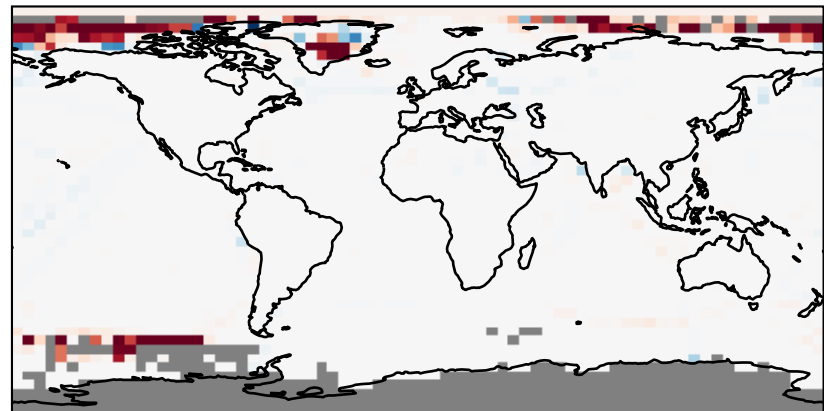
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



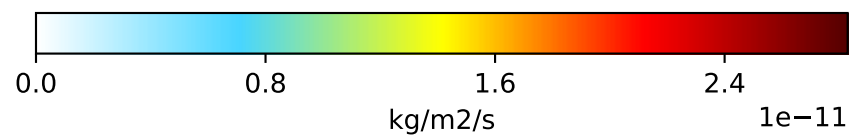
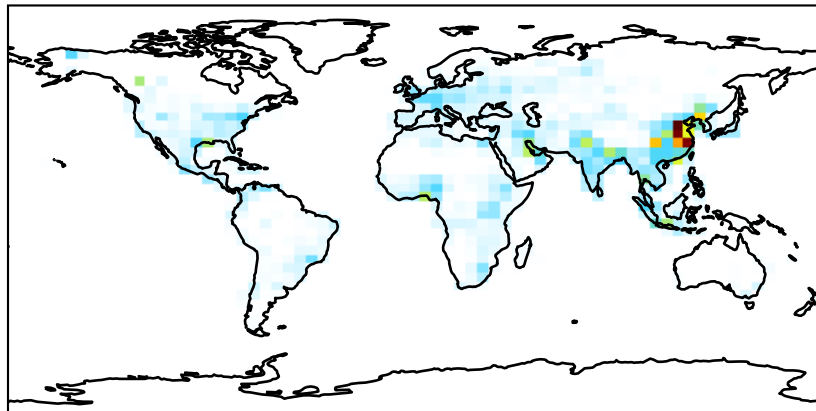
Ratio  
Dev/Ref, Fixed Range



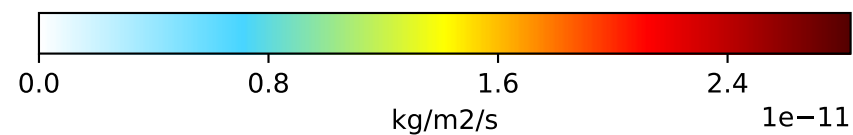
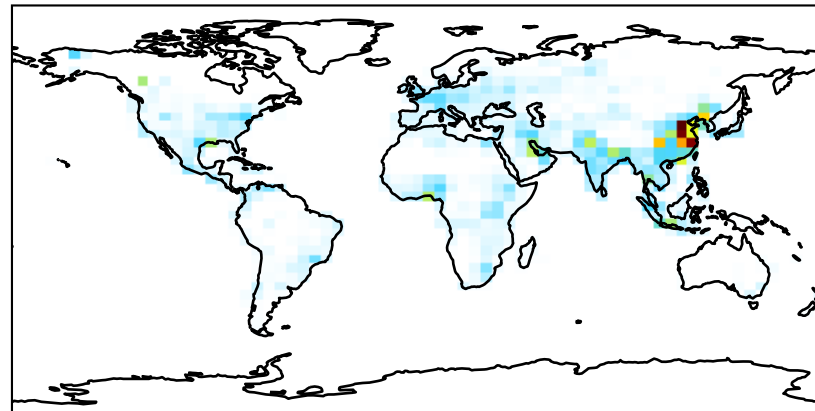


# EmisTOLU\_Anthro (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



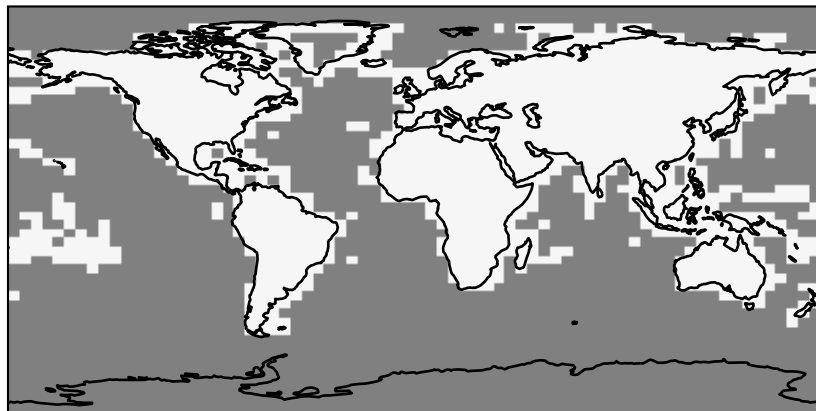
Difference  
Dev - Ref, Dynamic Range



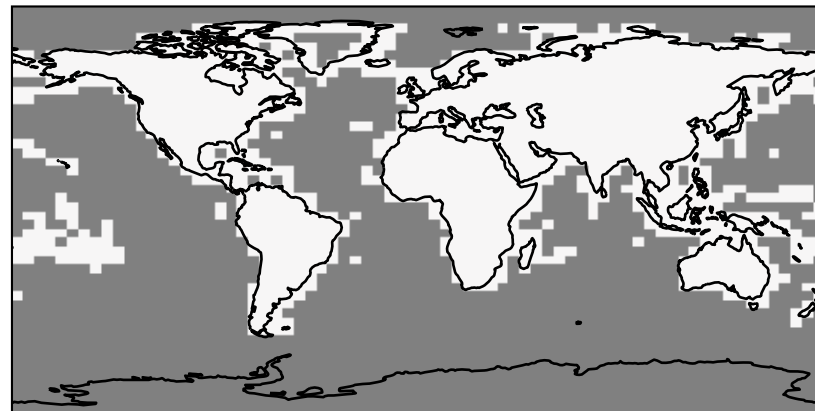
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

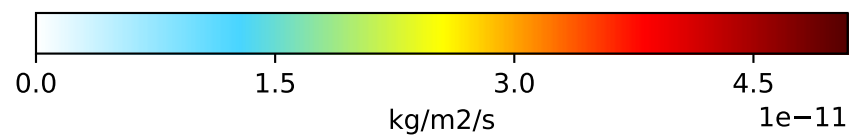
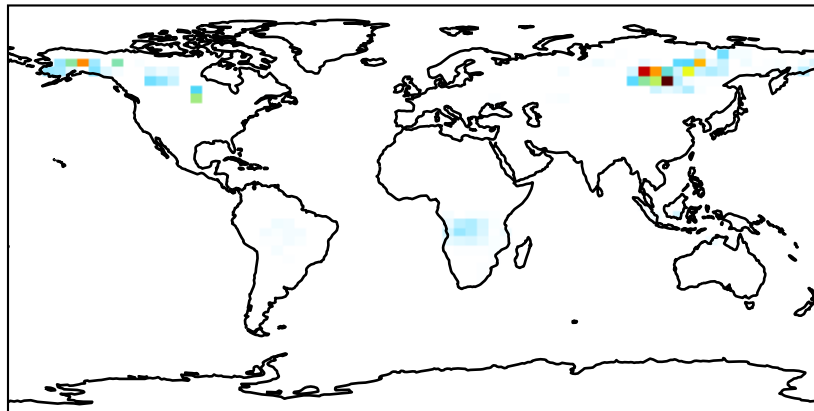


Ratio  
Dev/Ref, Fixed Range

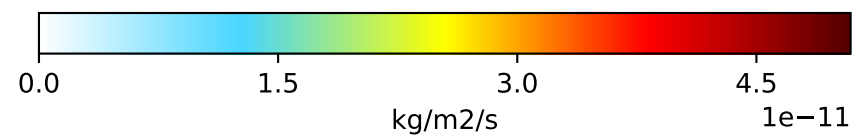
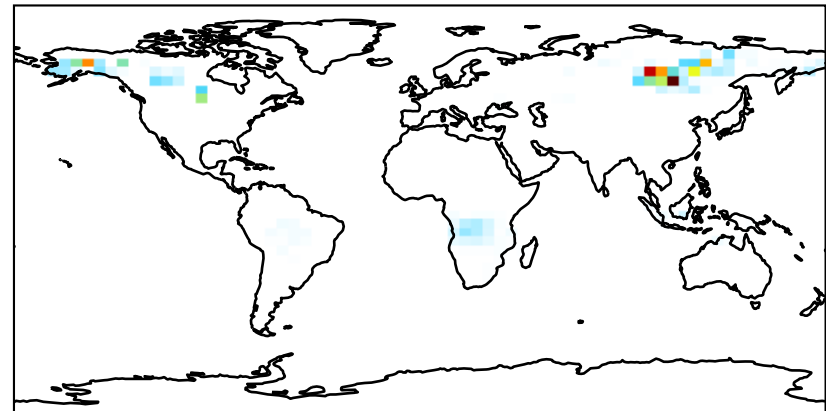


# EmisTOLU\_BioBurn (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



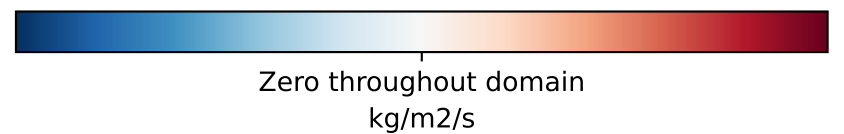
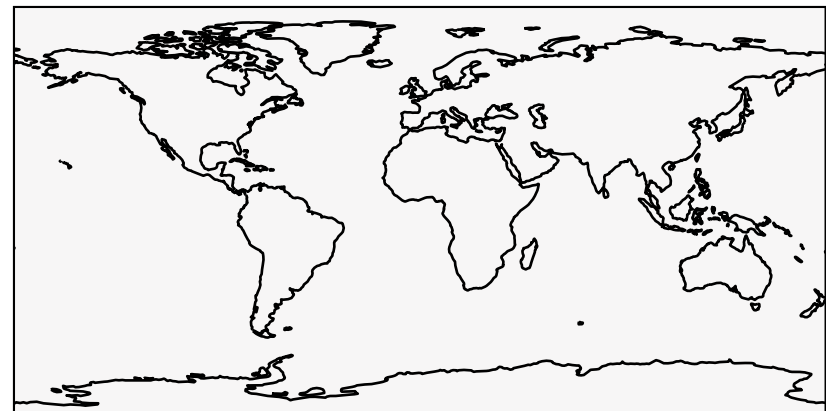
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



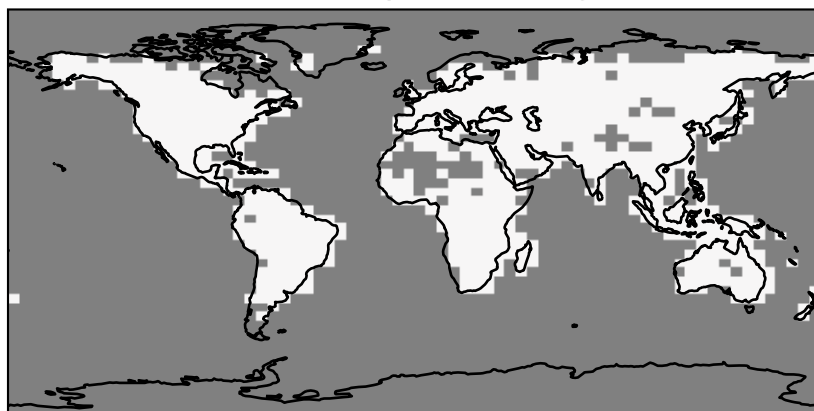
Difference  
Dev - Ref, Dynamic Range



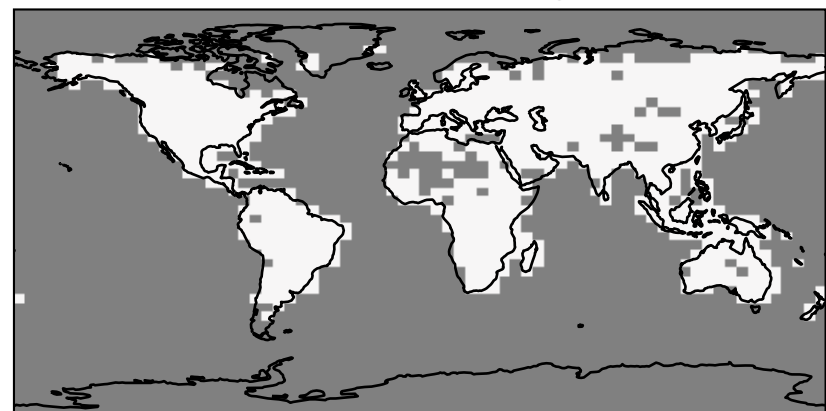
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

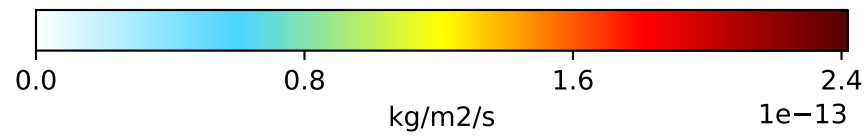
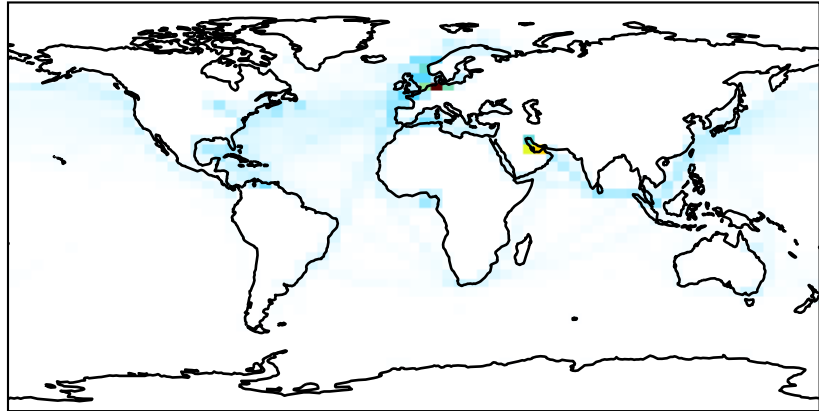


Ratio  
Dev/Ref, Fixed Range

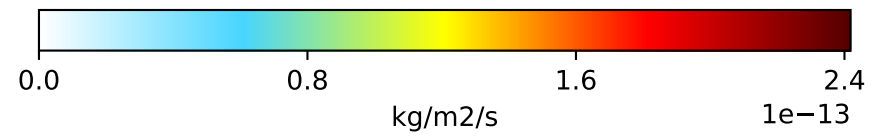
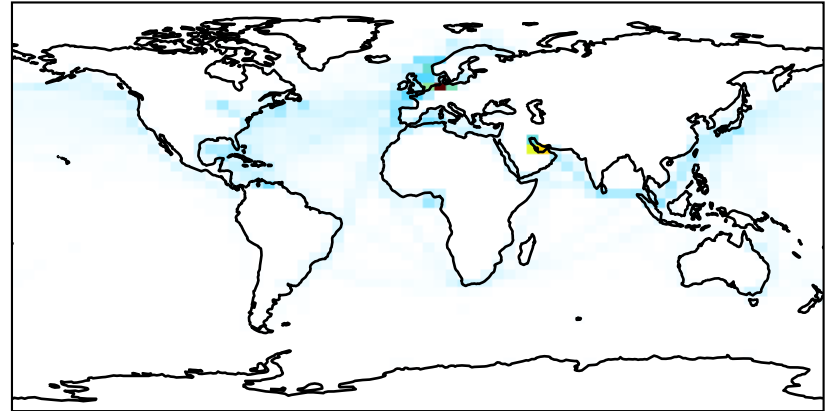


# EmisTOLU\_Ship (Jul2019)

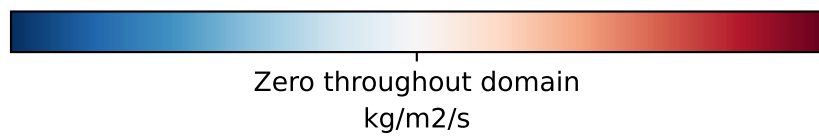
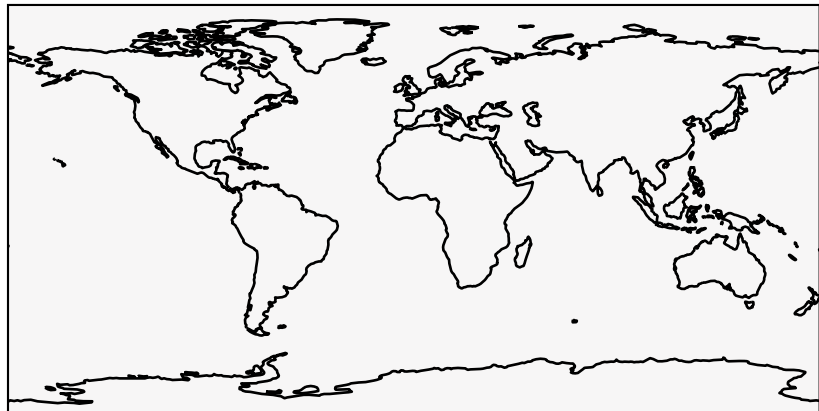
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



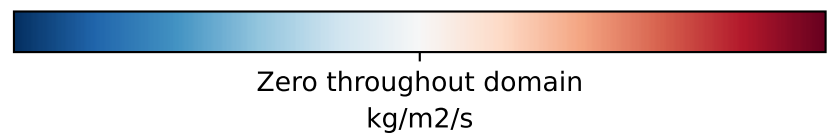
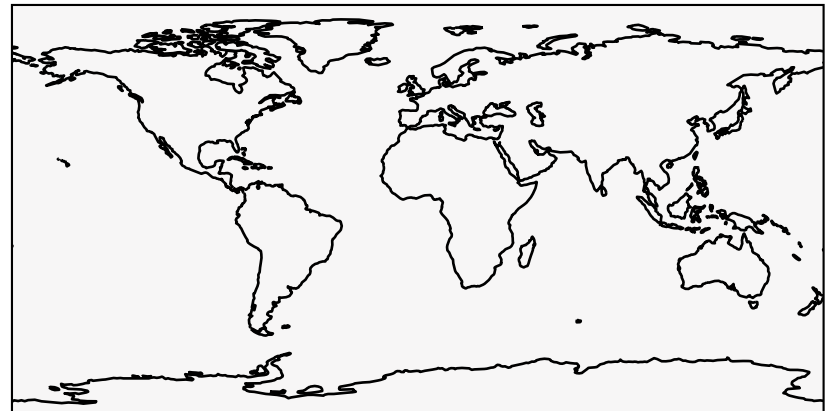
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



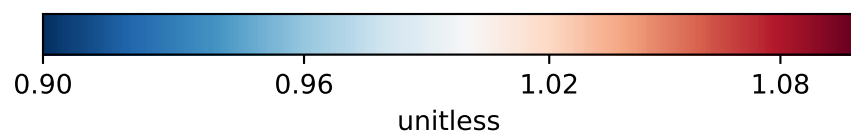
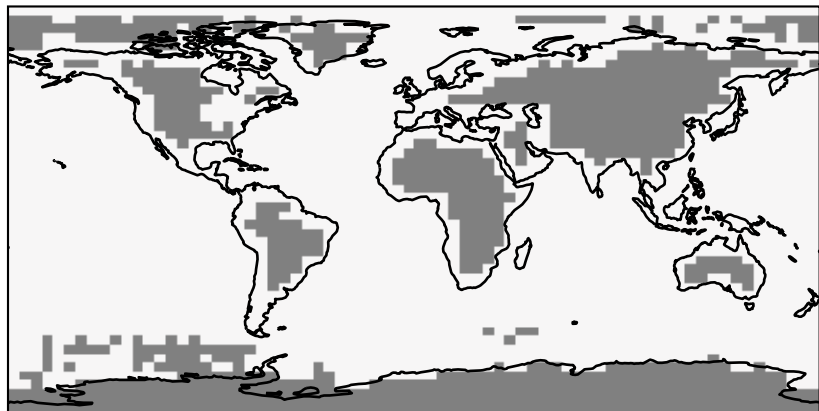
Difference  
Dev - Ref, Dynamic Range



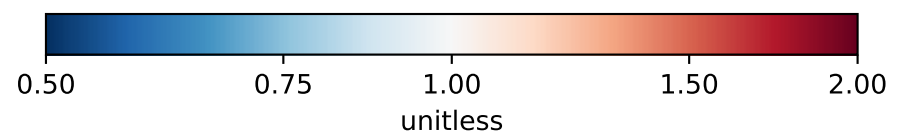
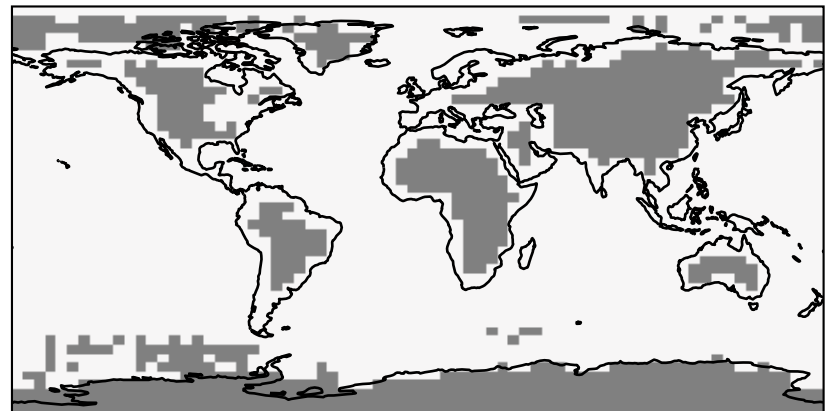
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

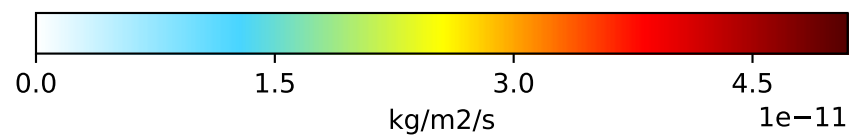
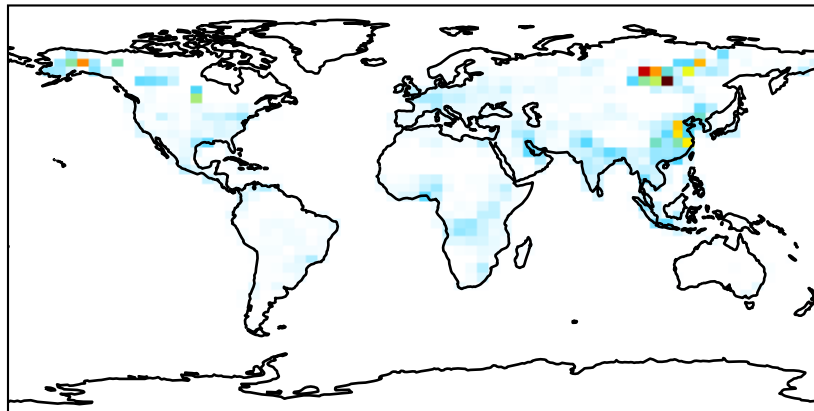


Ratio  
Dev/Ref, Fixed Range

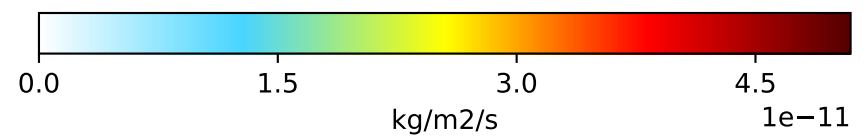
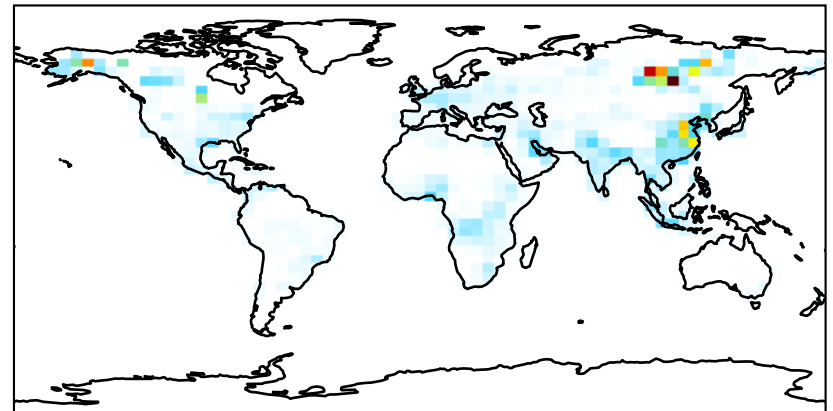


# EmisTOLU\_Total (Jul2019)

GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



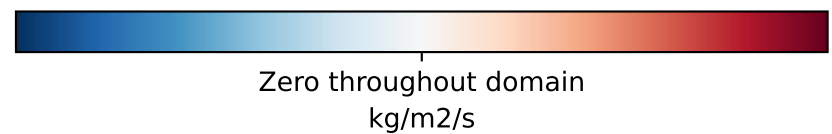
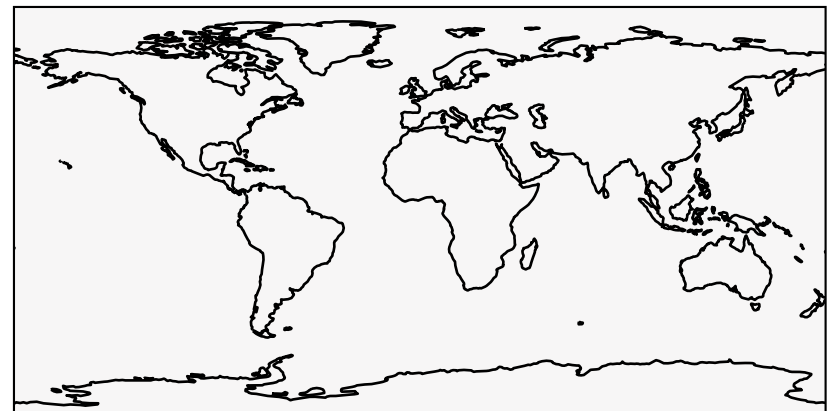
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



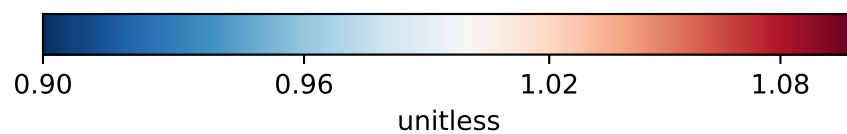
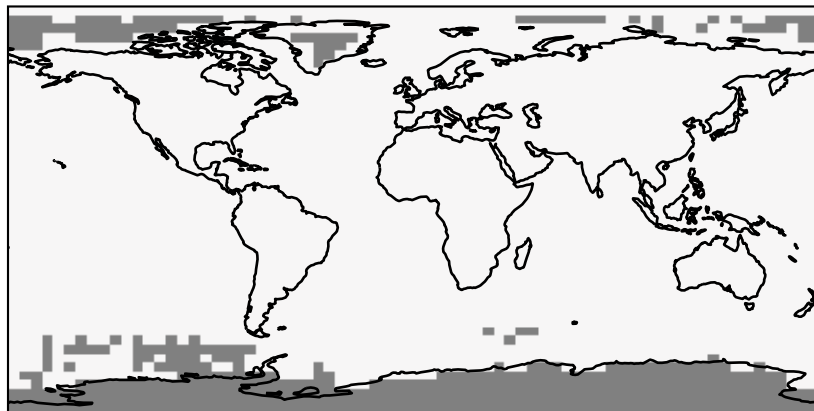
Difference  
Dev - Ref, Dynamic Range



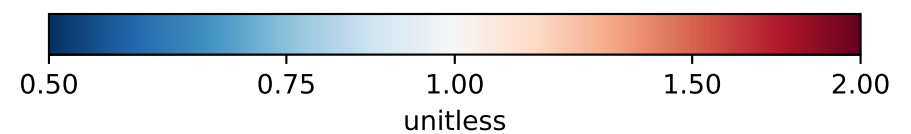
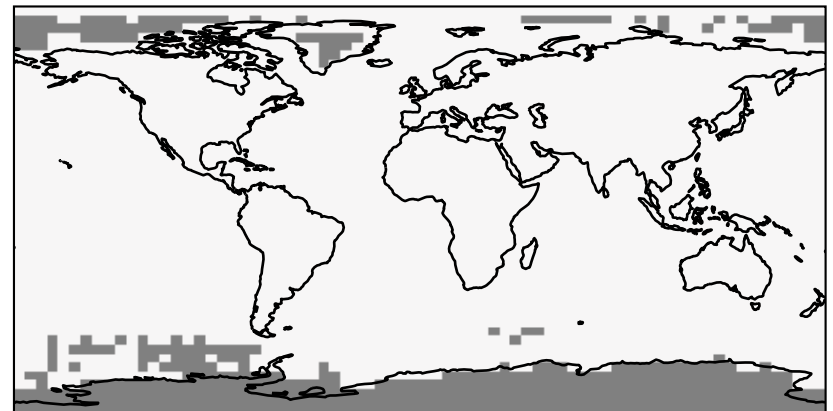
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

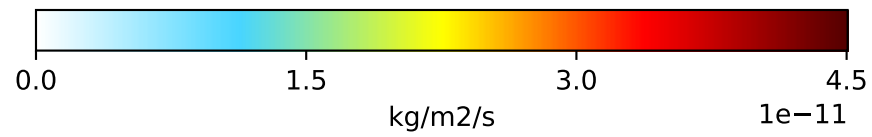
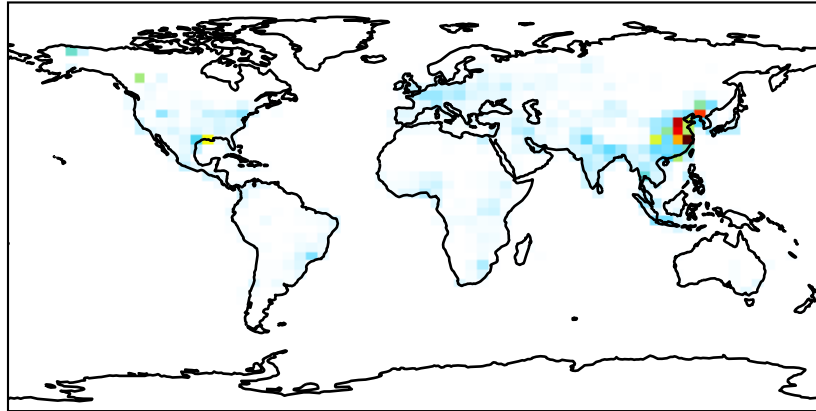


Ratio  
Dev/Ref, Fixed Range

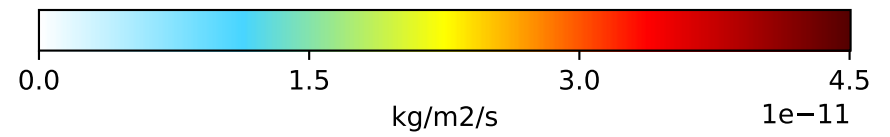
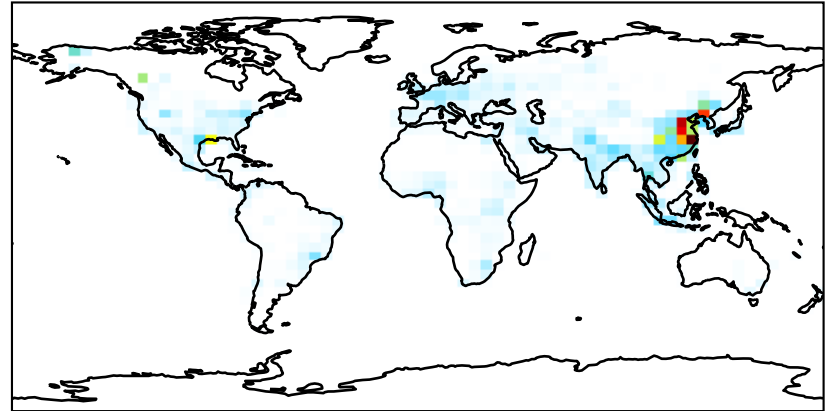


# EmisXYLE\_Anthro (Jul2019)

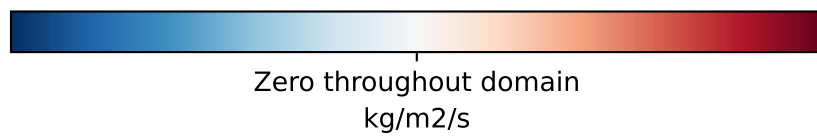
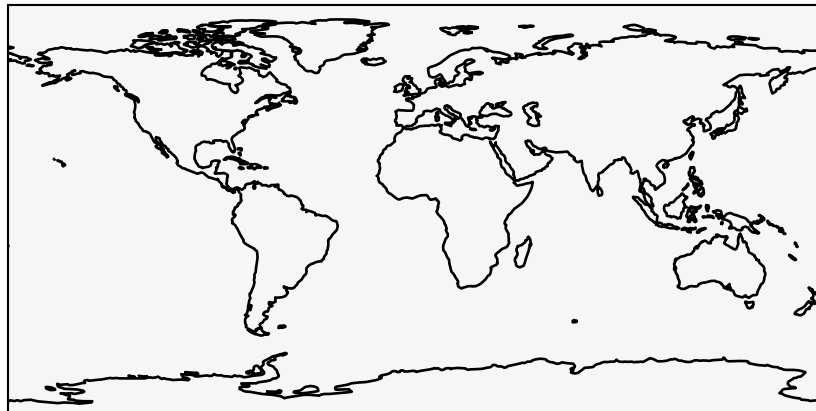
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



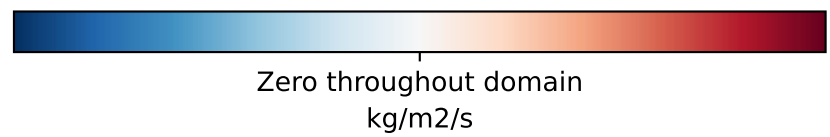
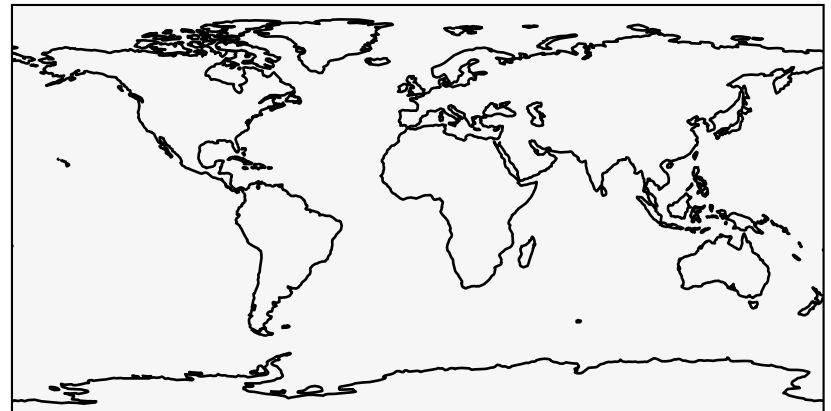
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



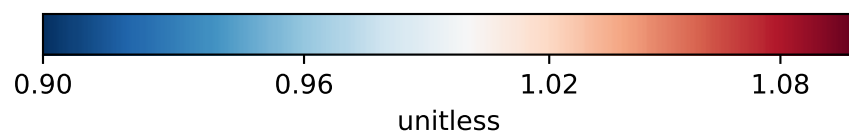
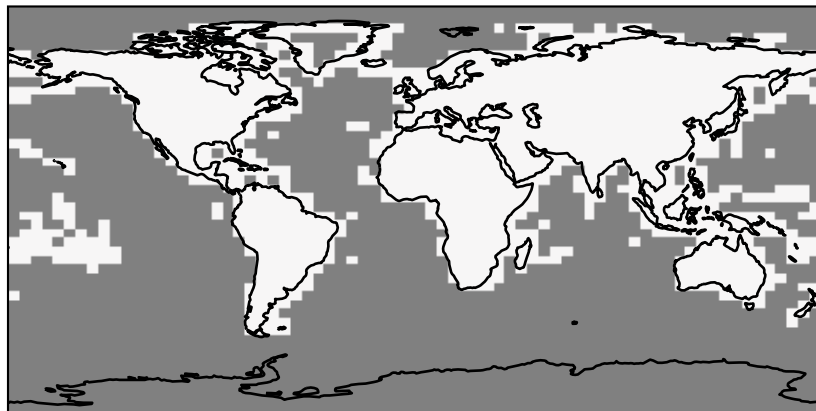
Difference  
Dev - Ref, Dynamic Range



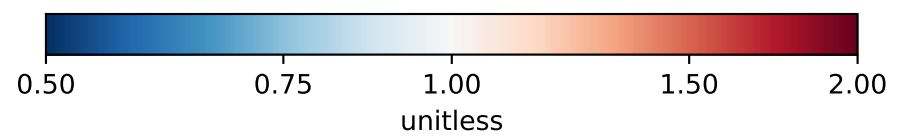
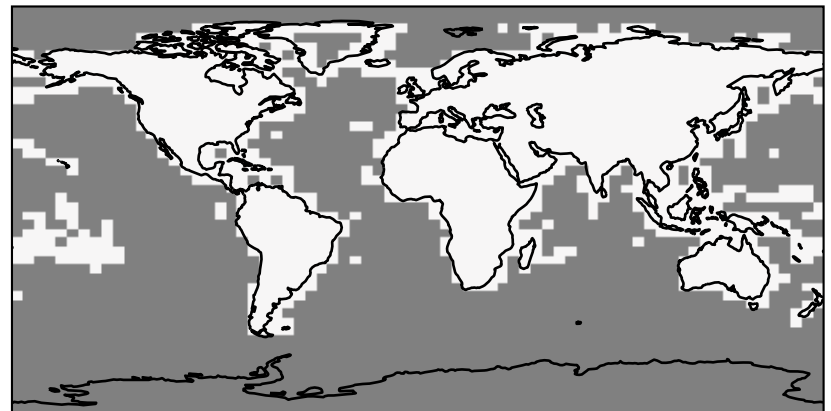
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



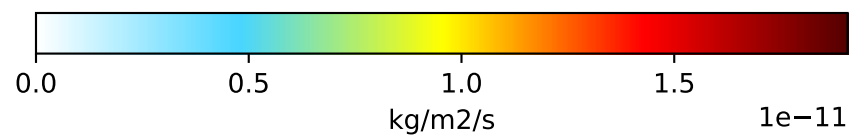
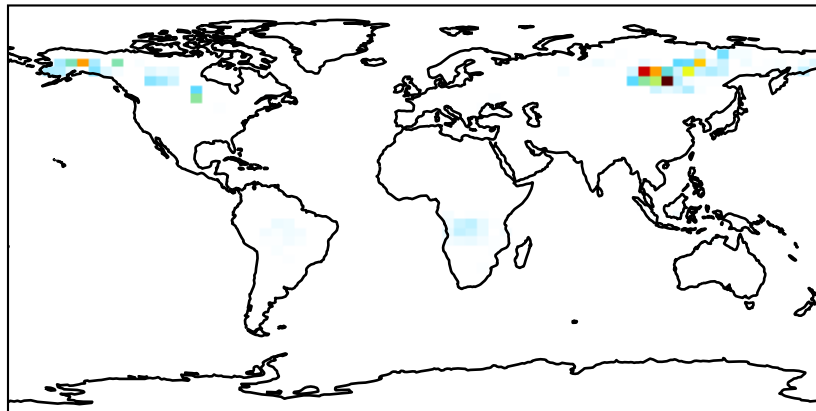
Ratio  
Dev/Ref, Fixed Range



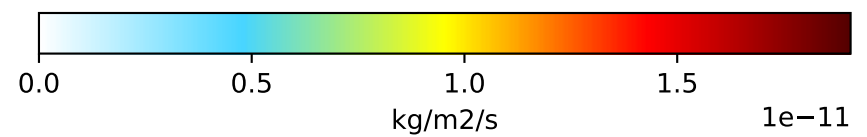
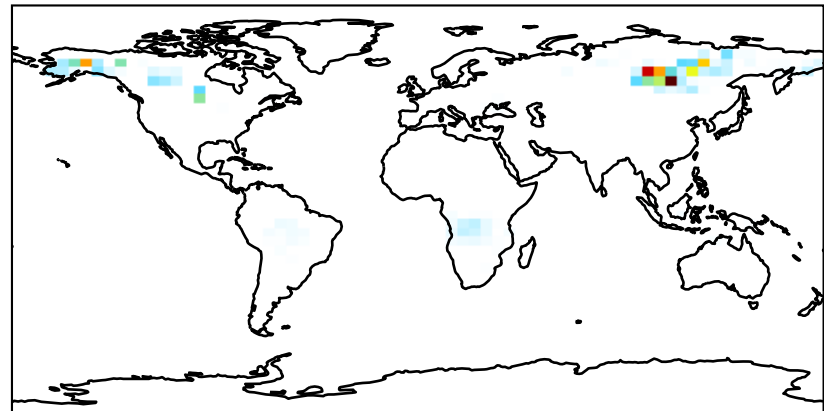


# EmisXYLE\_BioBurn (Jul2019)

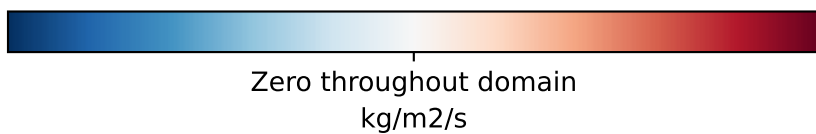
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



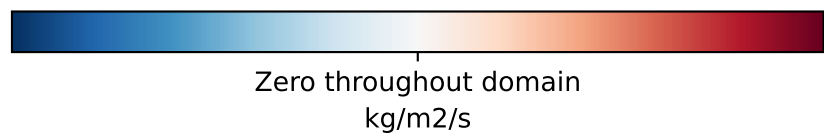
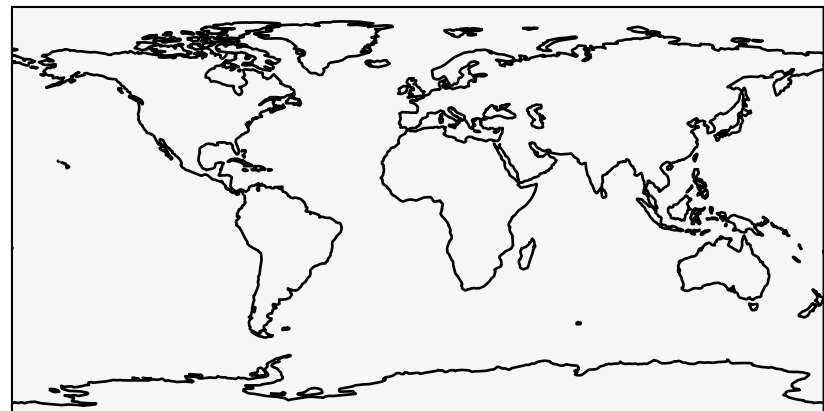
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



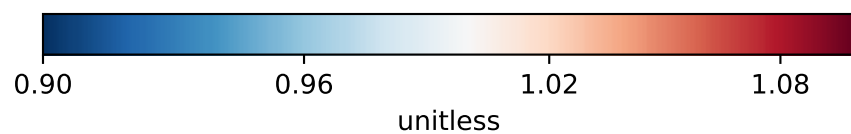
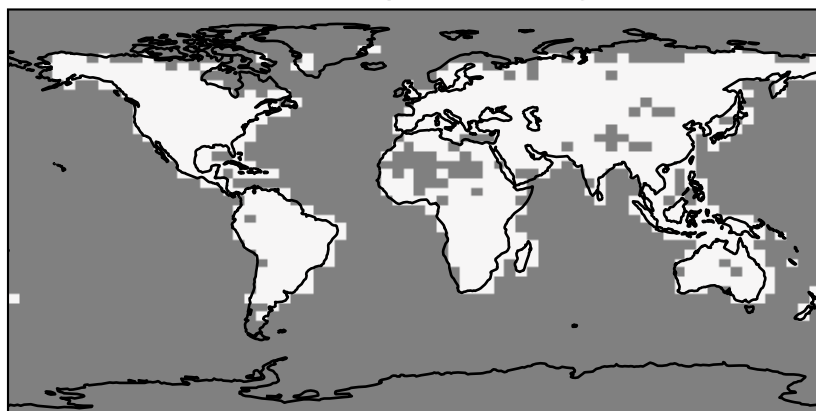
Difference  
Dev - Ref, Dynamic Range



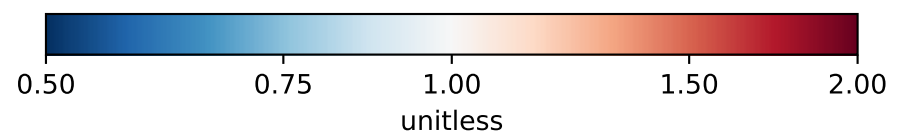
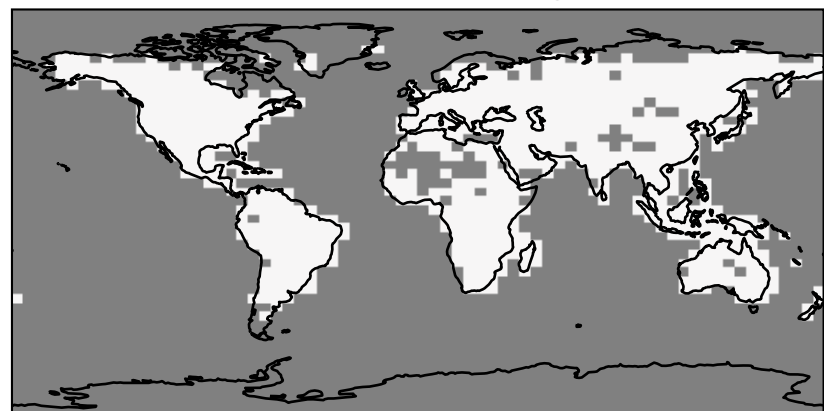
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

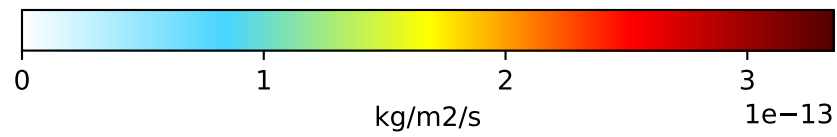
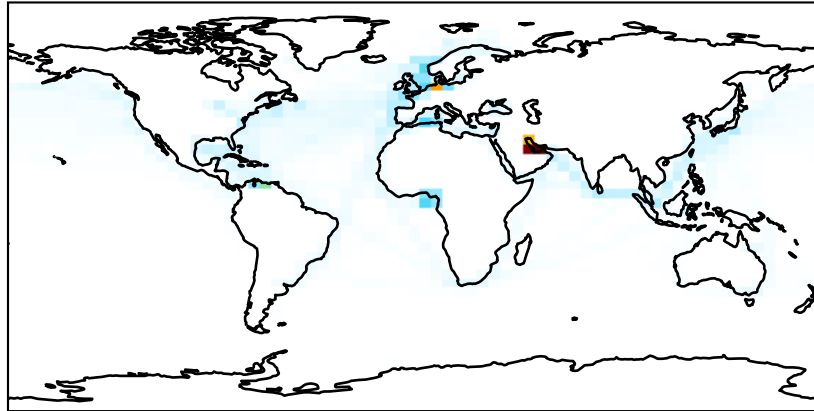


Ratio  
Dev/Ref, Fixed Range

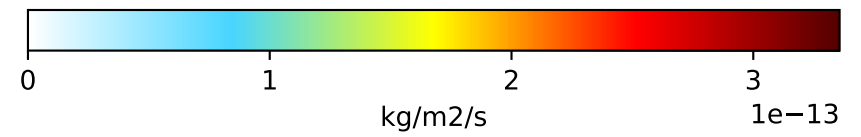
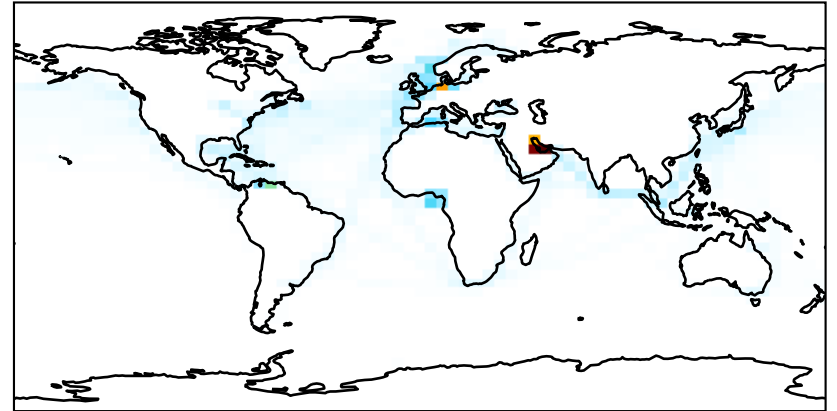


# EmisXYLE\_Ship (Jul2019)

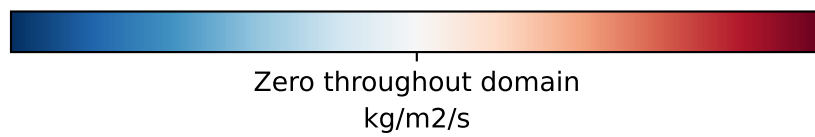
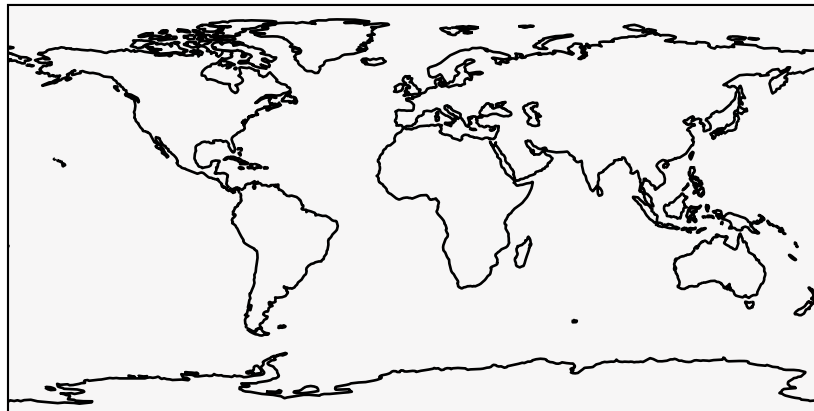
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



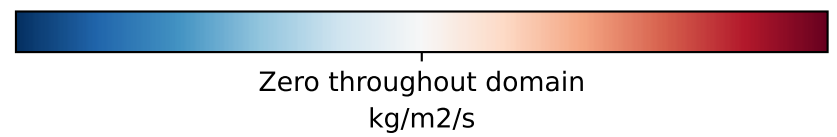
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



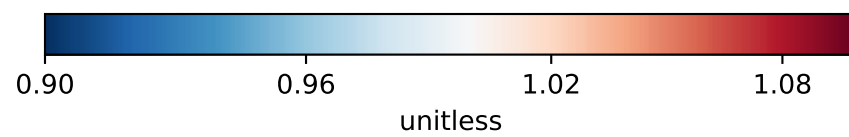
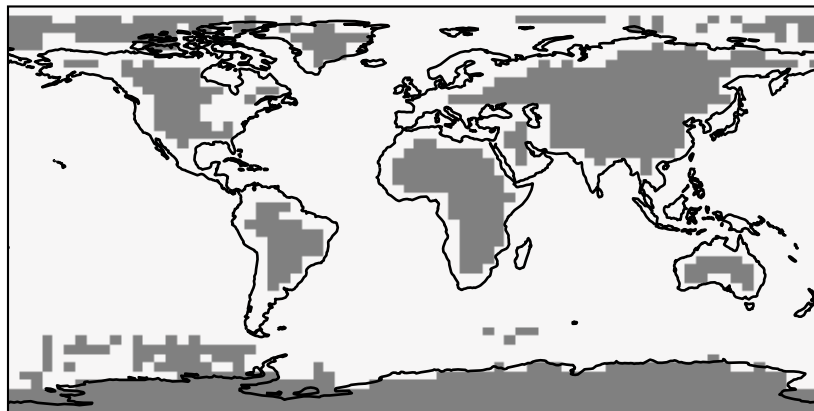
Difference  
Dev - Ref, Dynamic Range



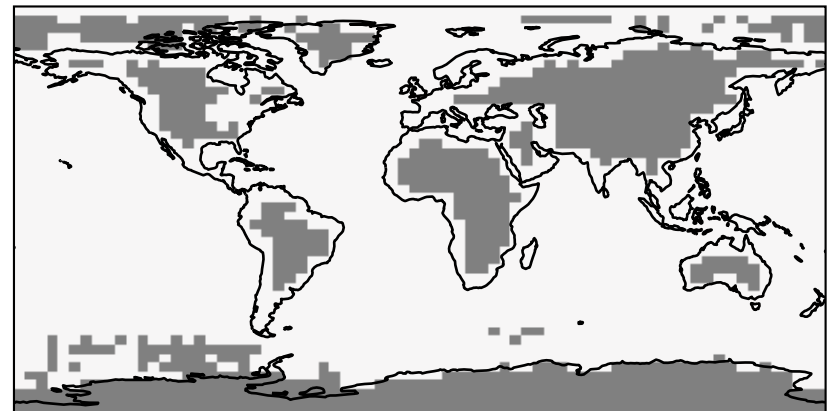
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

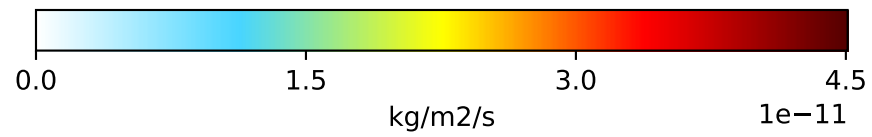
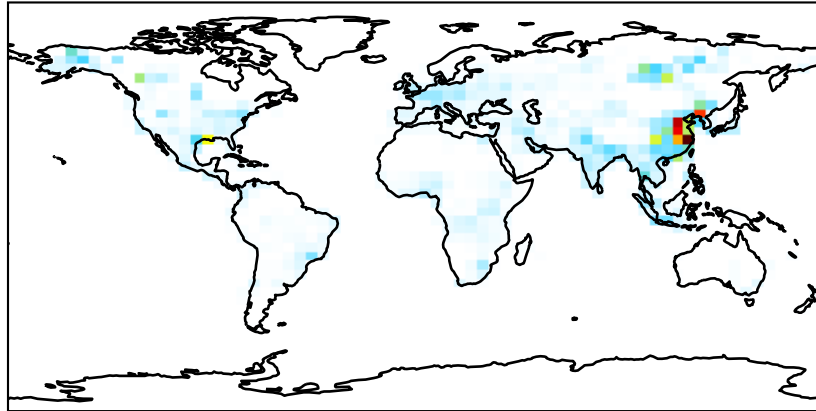


Ratio  
Dev/Ref, Fixed Range

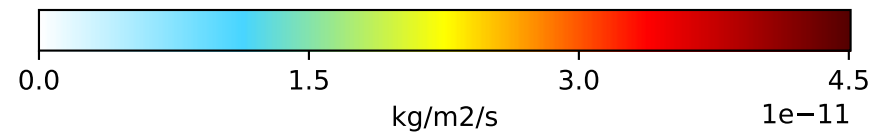
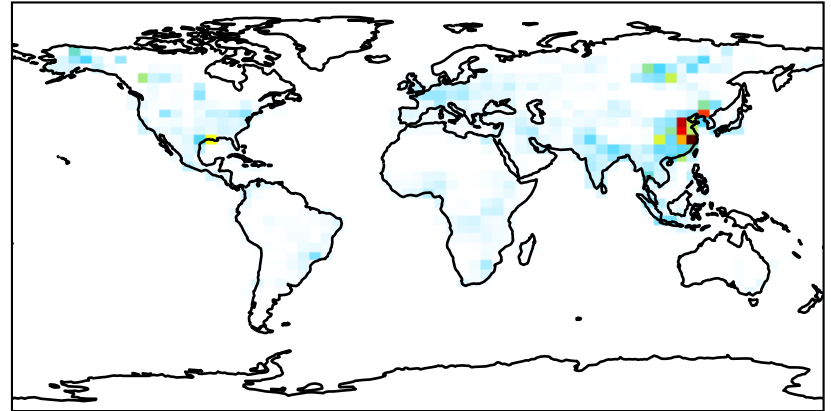


# EmisXYLE\_Total (Jul2019)

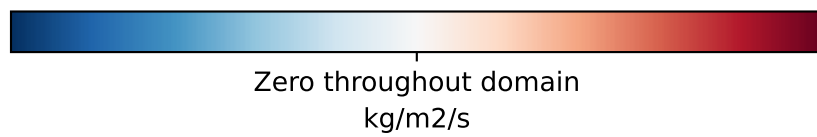
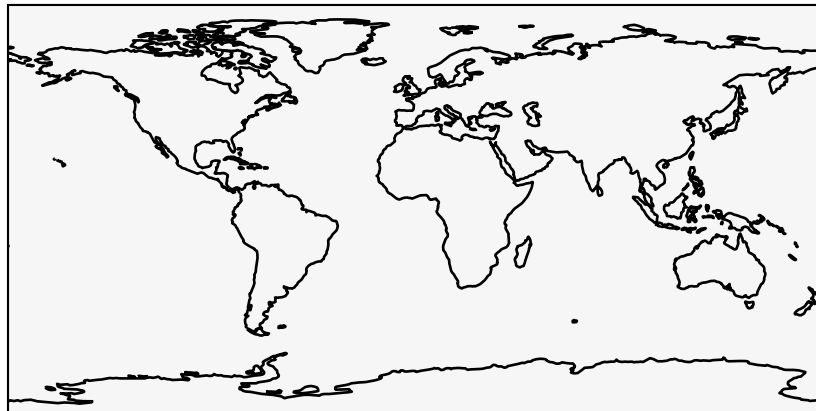
GCC\_13.3.0-rc.0 (Ref)  
4.0x5.0



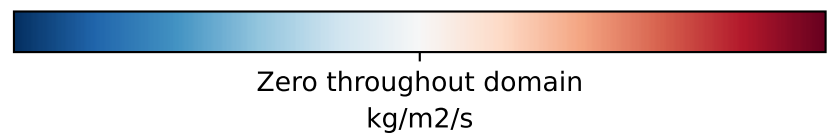
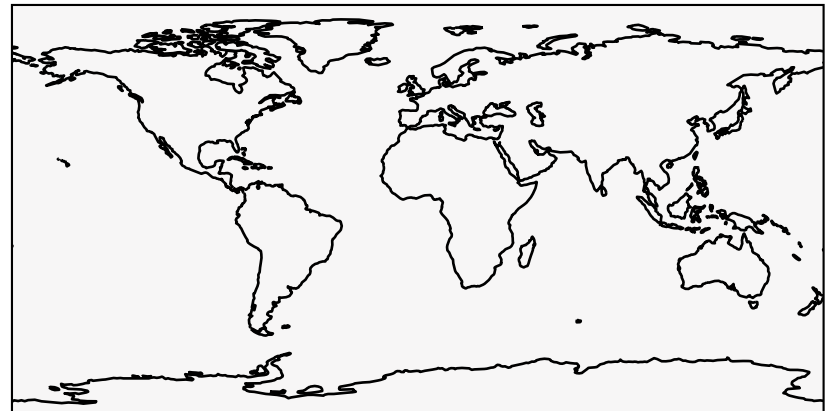
GCC\_13.4.0-rc.4 (Dev)  
4.0x5.0



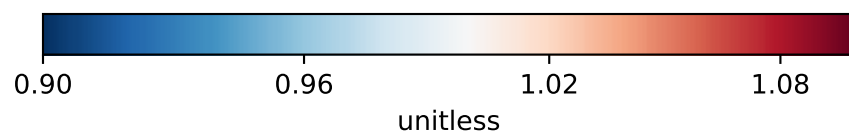
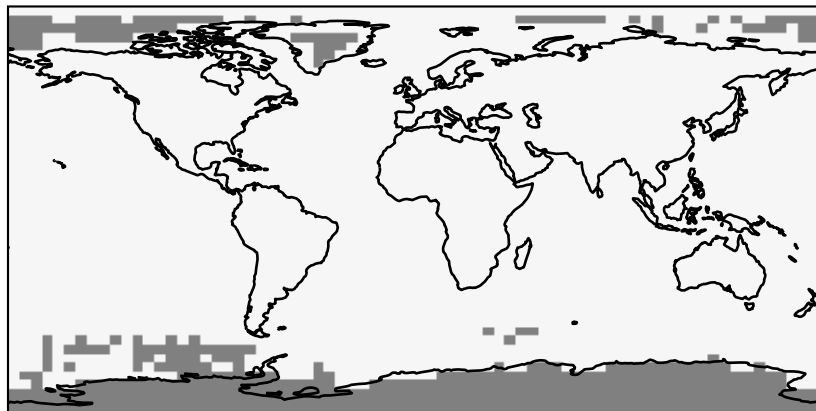
Difference  
Dev - Ref, Dynamic Range



Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

