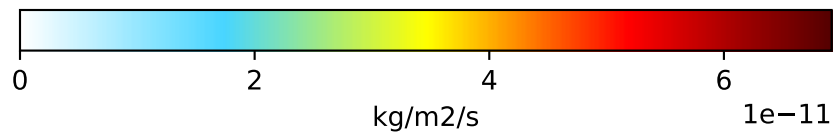
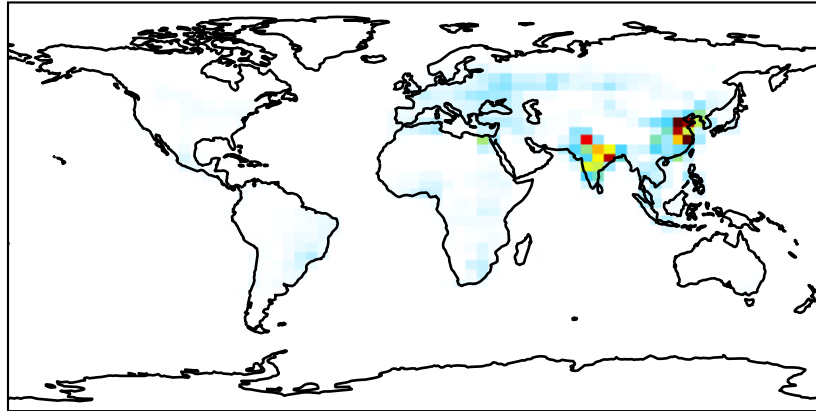
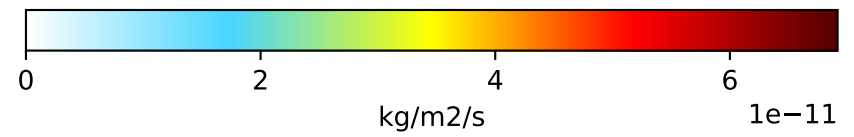
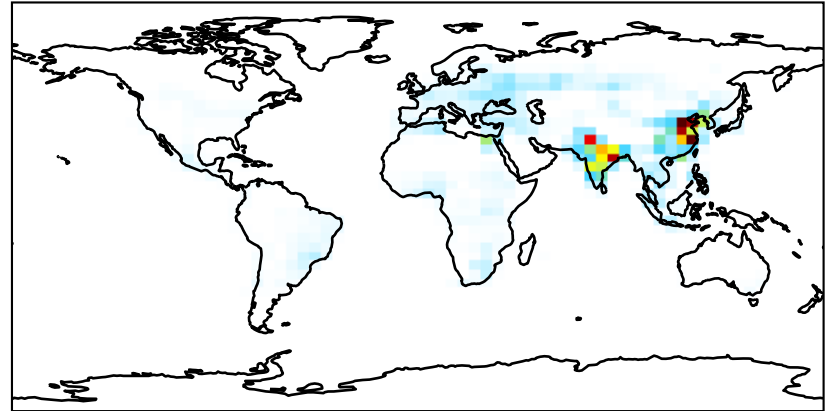


# EmisDST1\_Anthro (Apr2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



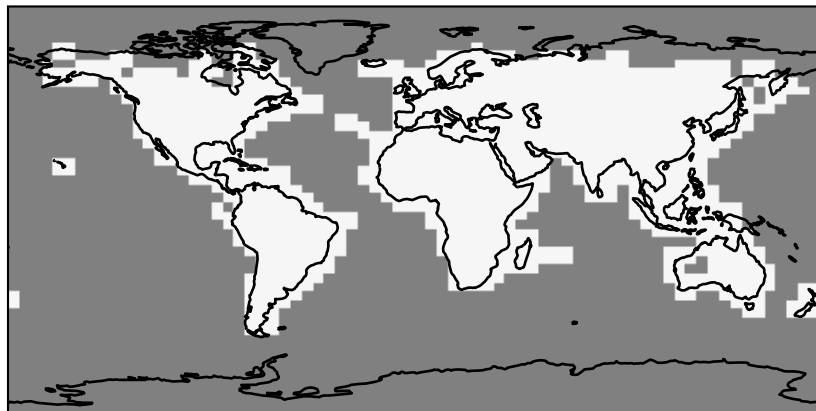
Difference  
Dev - Ref, Dynamic Range



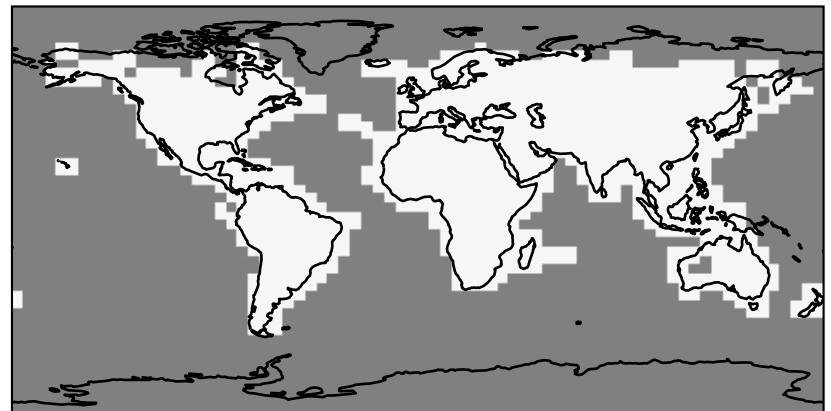
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

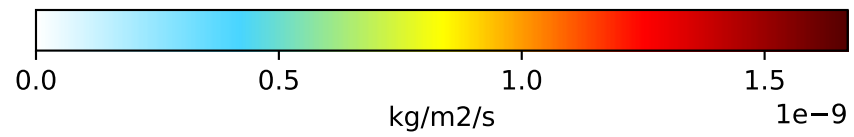
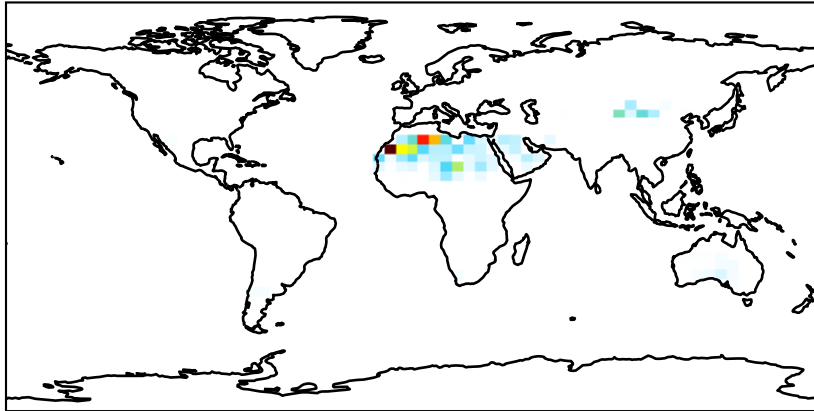


Ratio  
Dev/Ref, Fixed Range

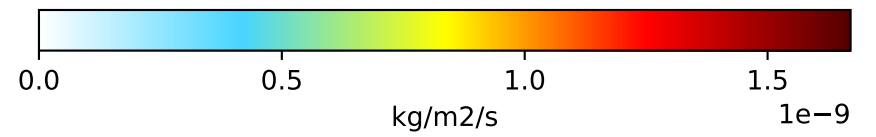
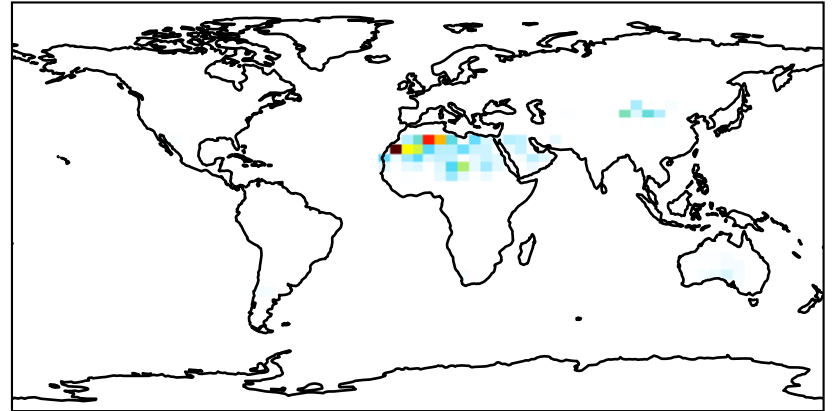


# EmisDST1\_Natural (Apr2019)

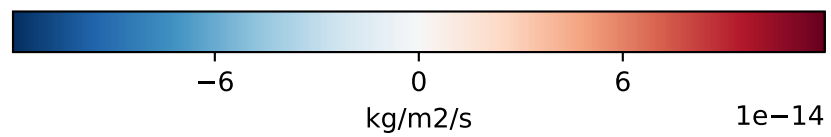
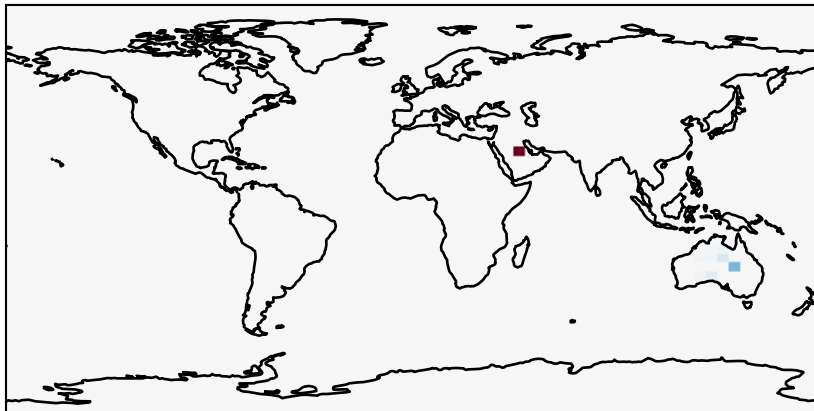
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



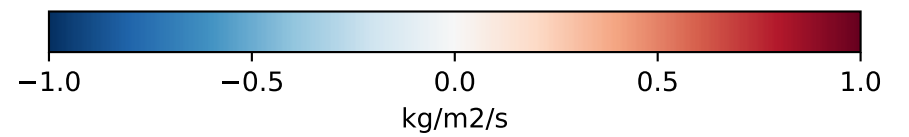
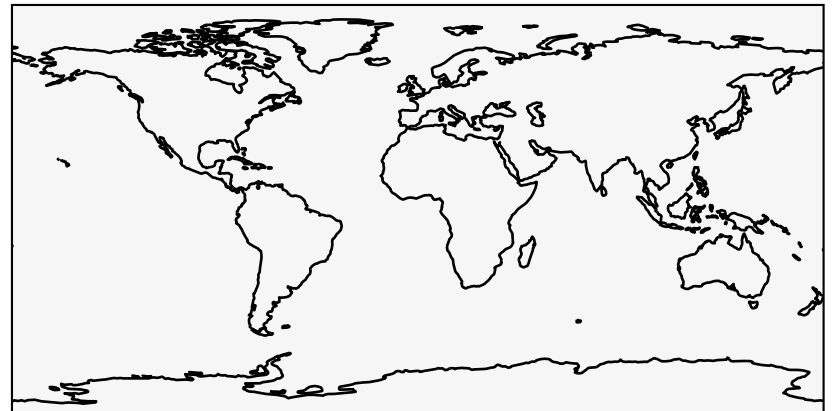
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



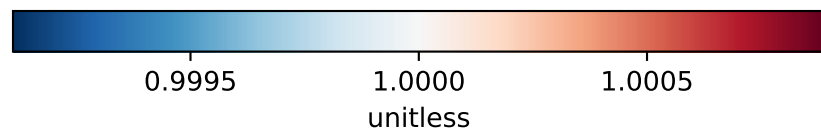
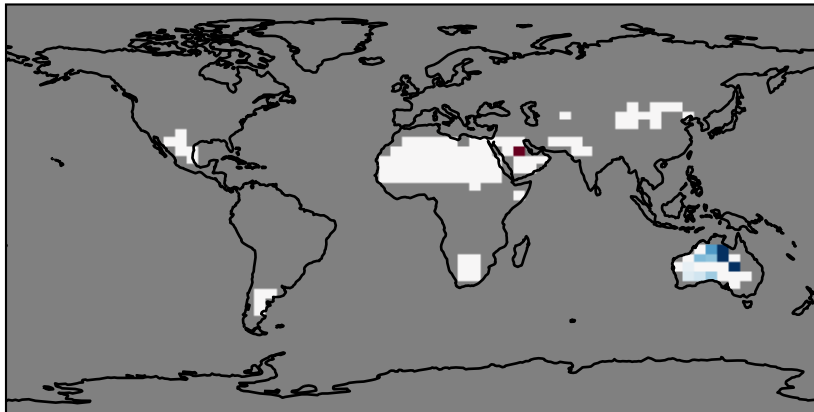
Difference  
Dev - Ref, Dynamic Range



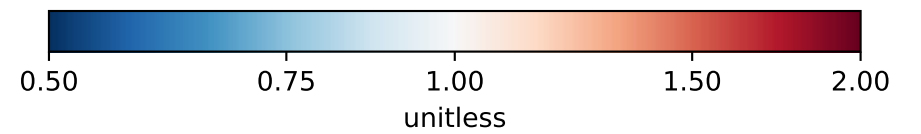
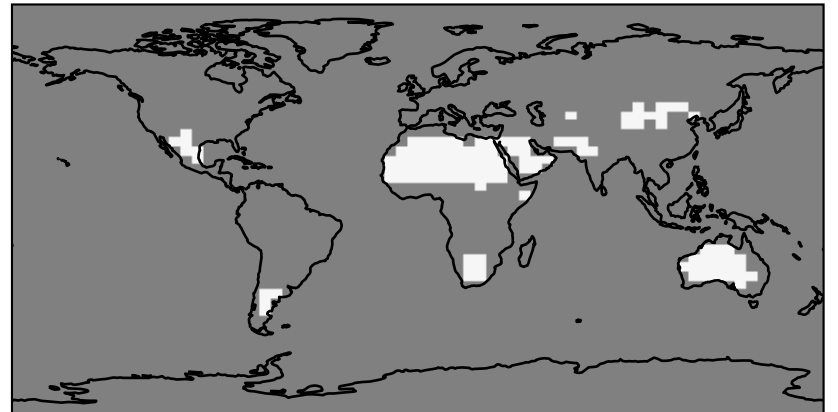
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

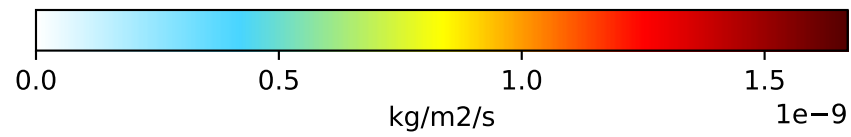
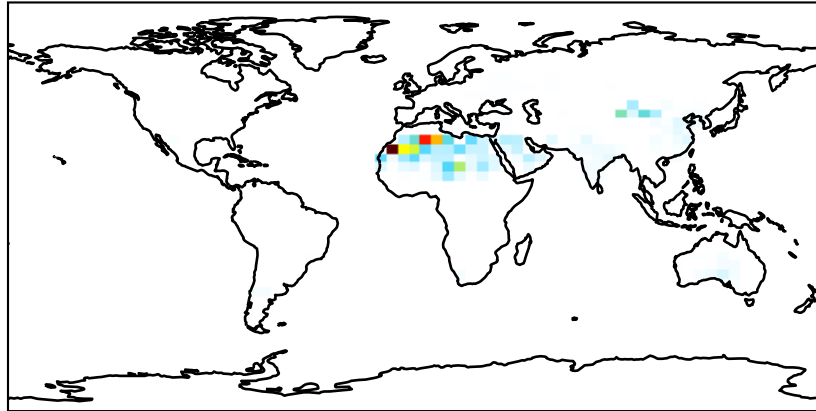


Ratio  
Dev/Ref, Fixed Range

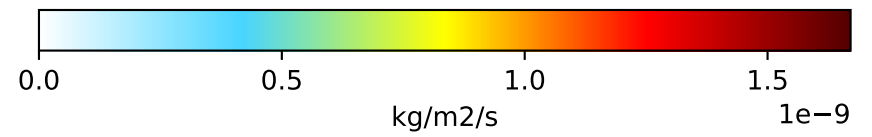
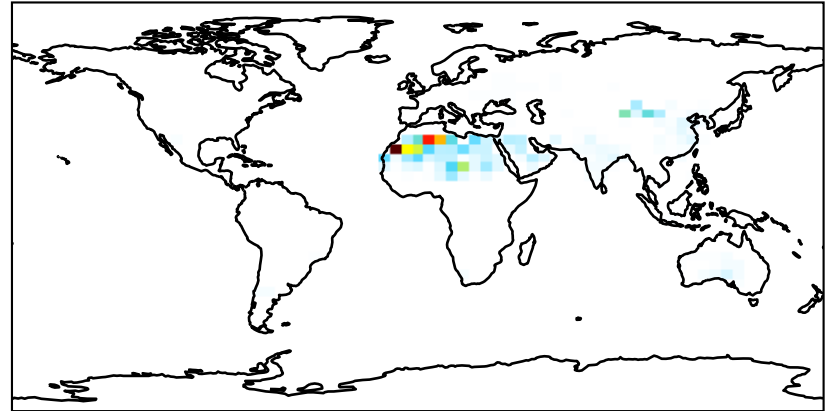


# EmisDST1\_Total (Apr2019)

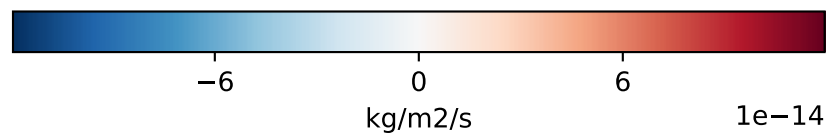
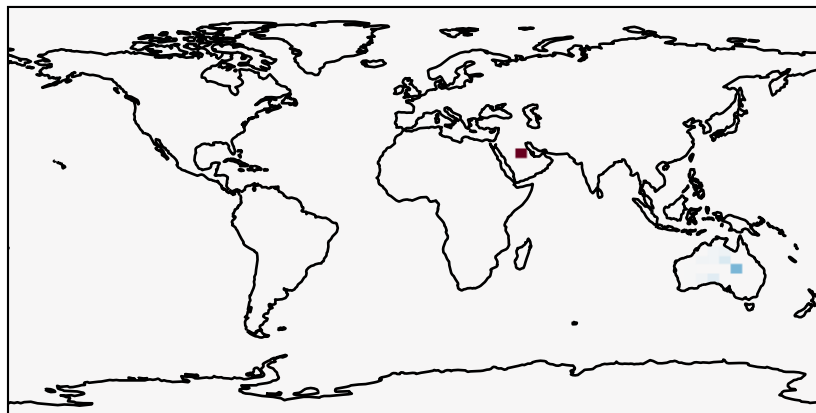
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



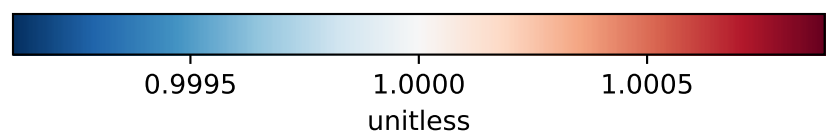
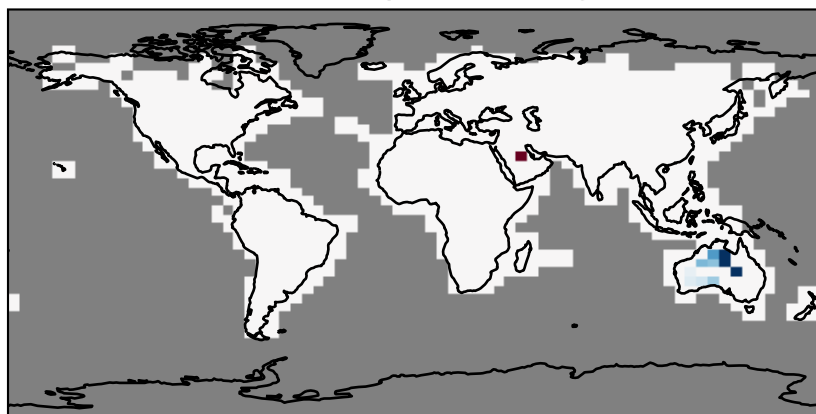
Difference  
Dev - Ref, Dynamic Range



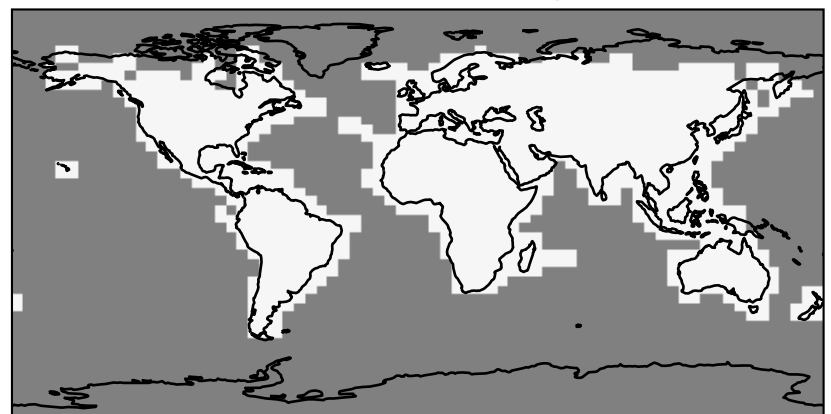
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

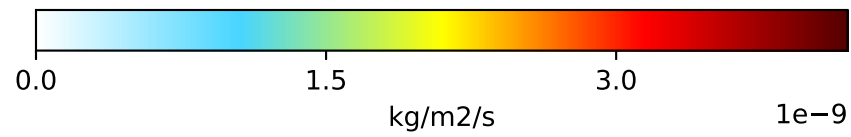
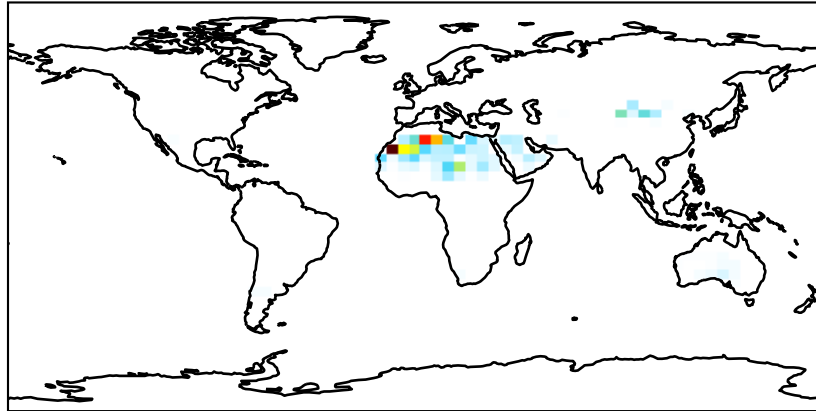


Ratio  
Dev/Ref, Fixed Range

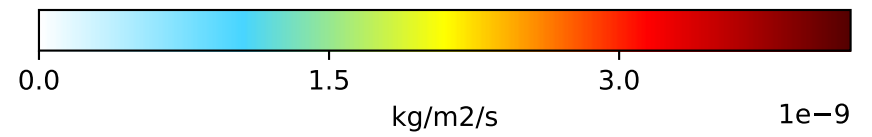
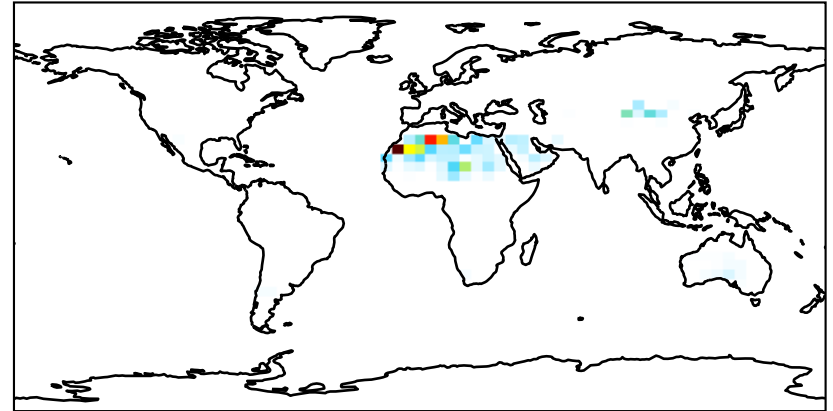


# EmisDST2\_Natural (Apr2019)

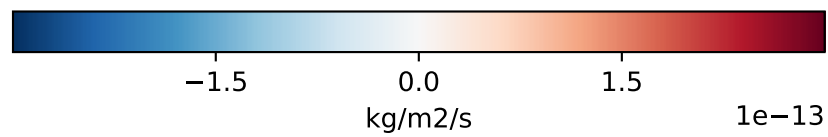
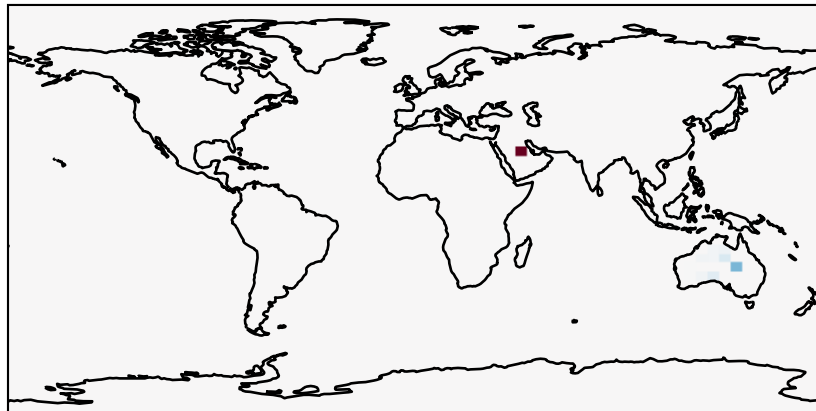
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



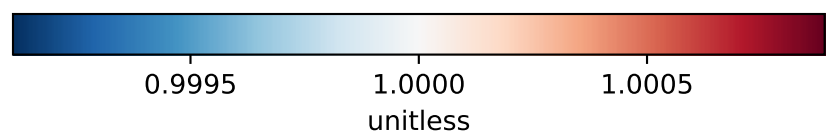
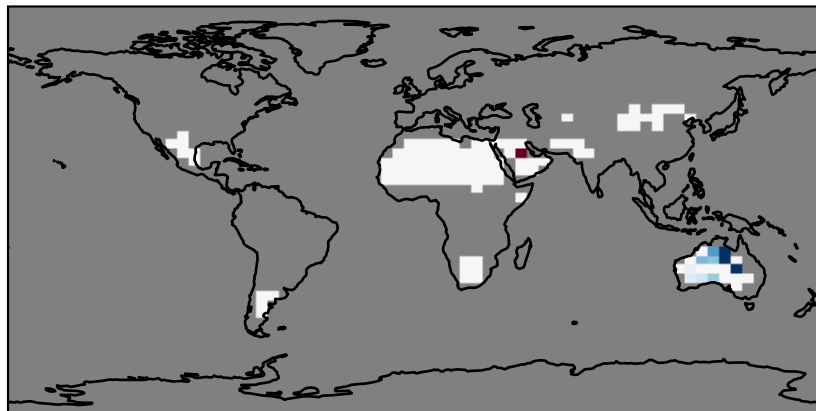
Difference  
Dev - Ref, Dynamic Range



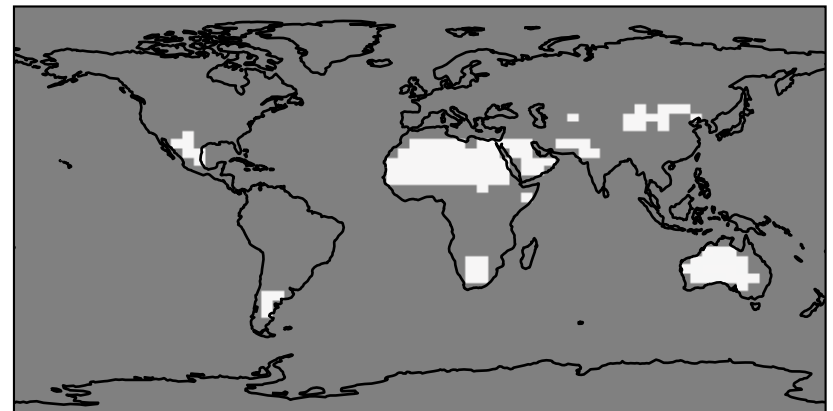
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

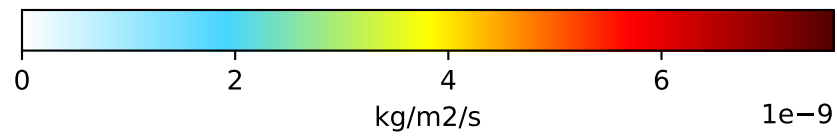
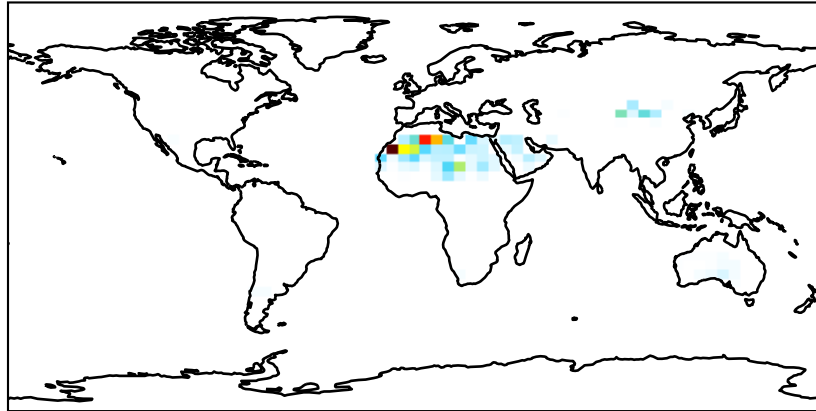


Ratio  
Dev/Ref, Fixed Range

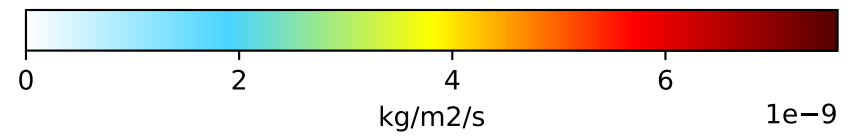
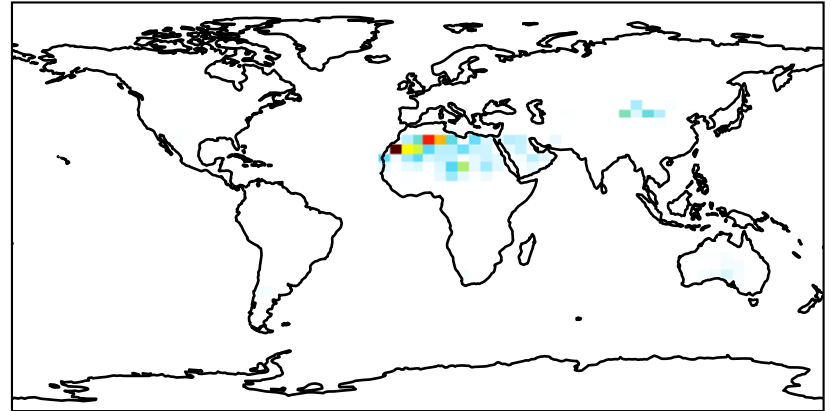


# EmisDST3\_Natural (Apr2019)

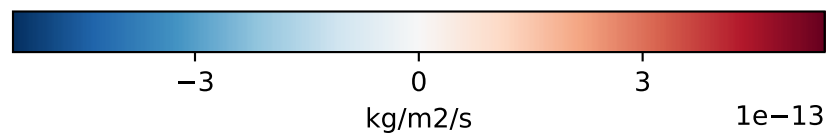
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



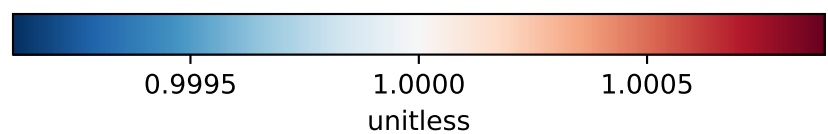
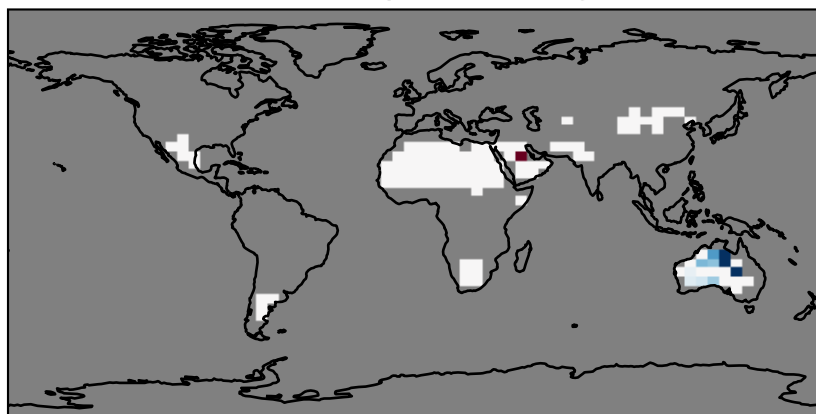
Difference  
Dev - Ref, Dynamic Range



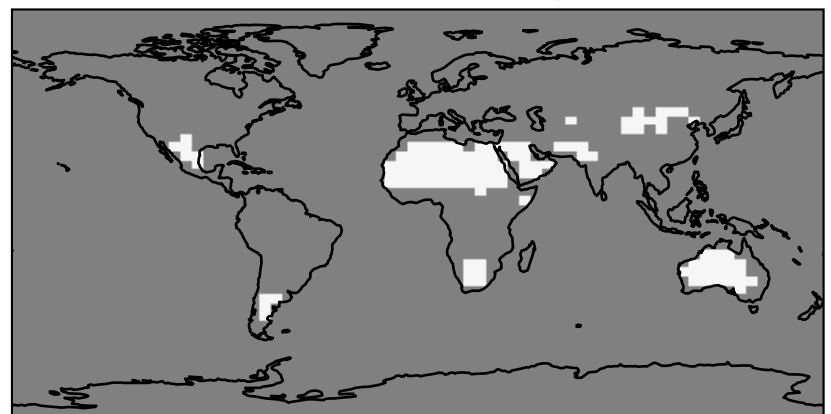
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

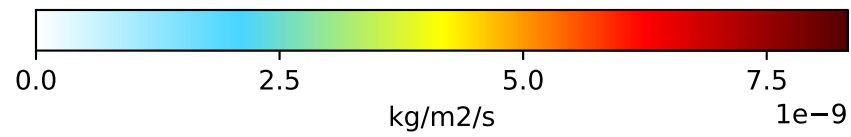
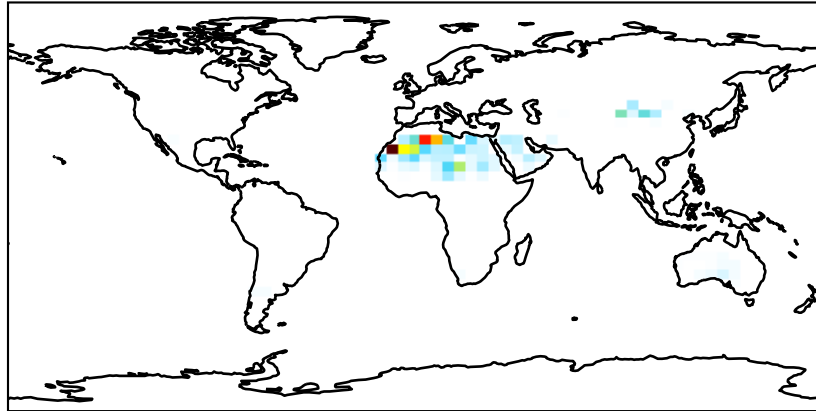


Ratio  
Dev/Ref, Fixed Range

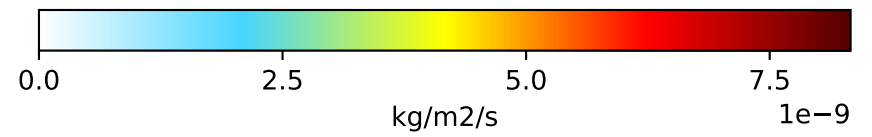
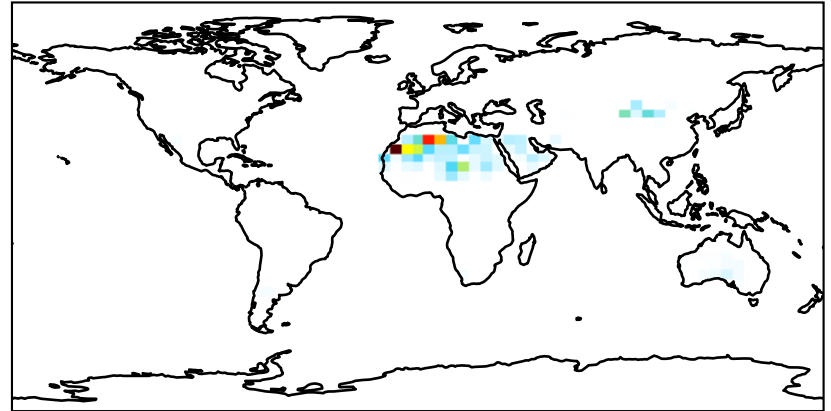


# EmisDST4\_Natural (Apr2019)

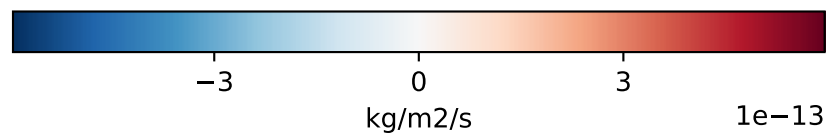
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



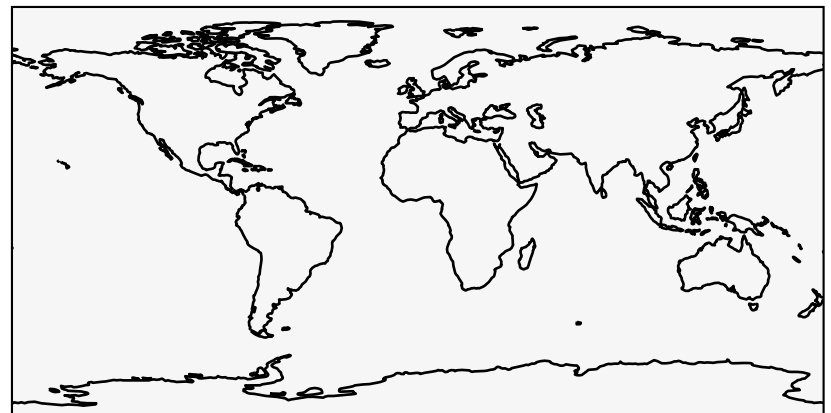
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



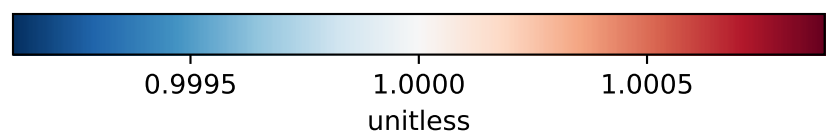
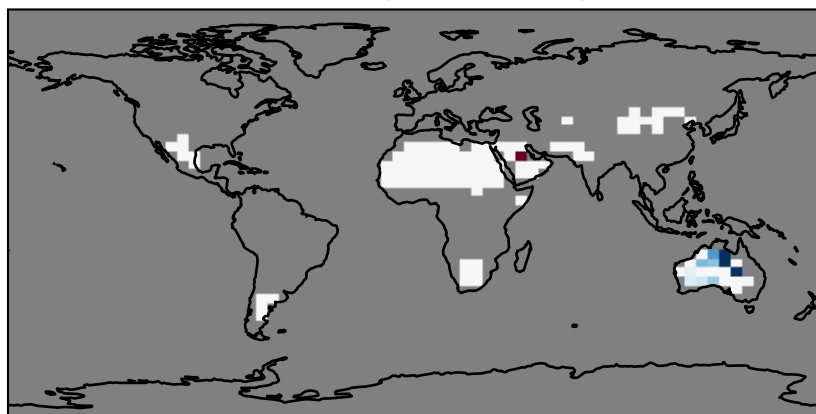
Difference  
Dev - Ref, Dynamic Range



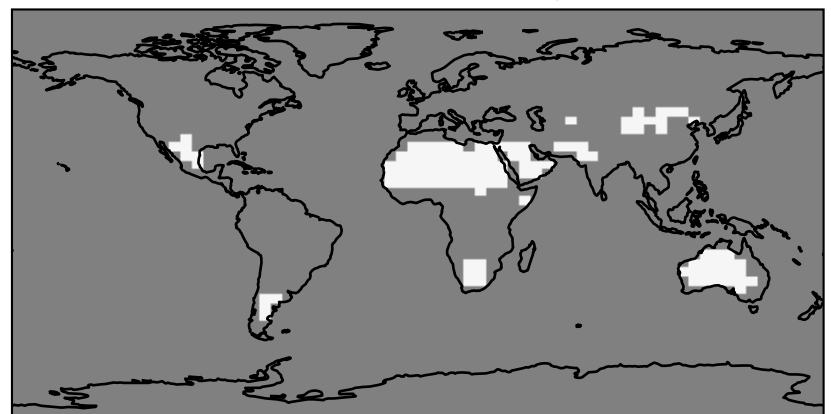
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

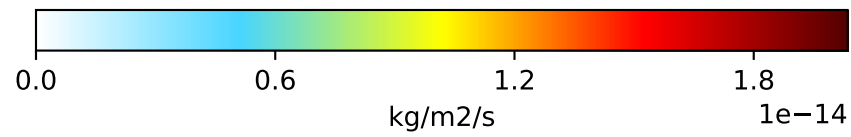
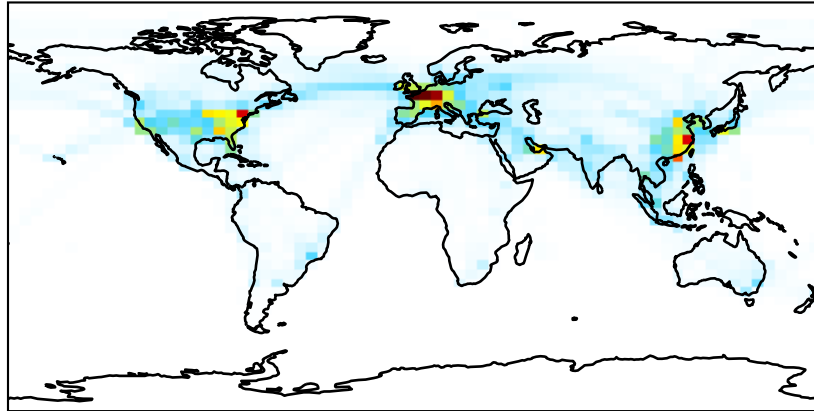


Ratio  
Dev/Ref, Fixed Range

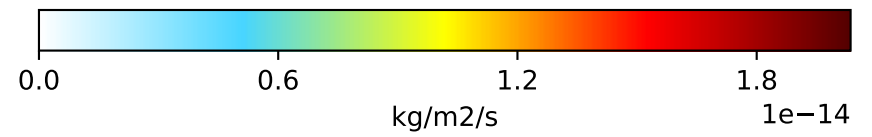
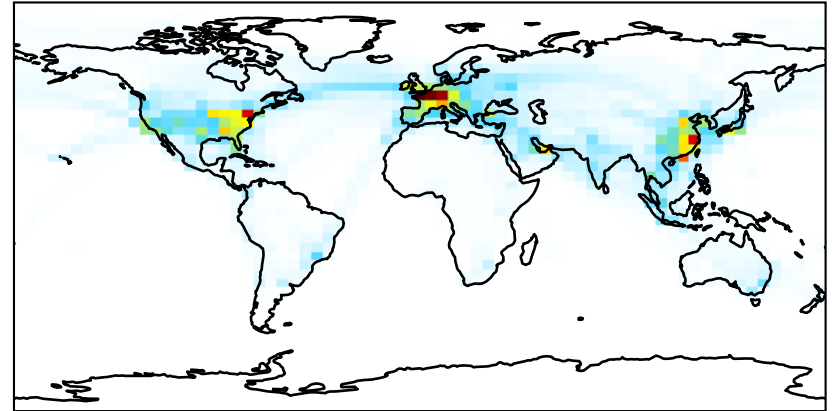


# EmisSO4\_Aircraft (Apr2019)

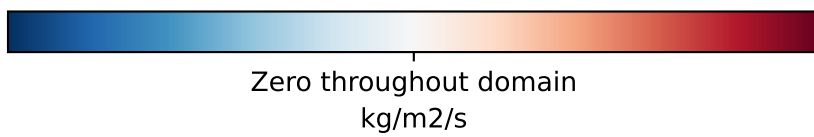
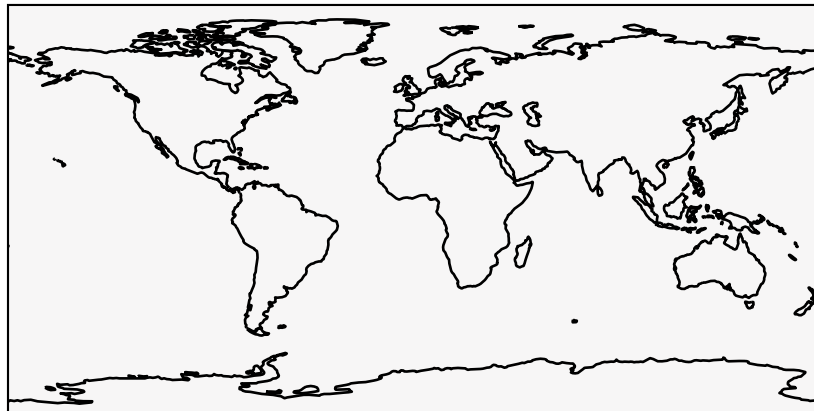
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



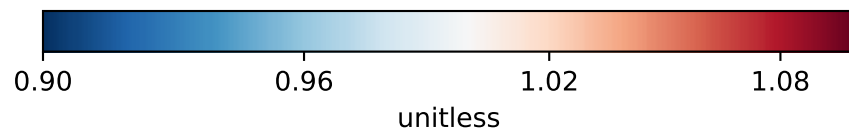
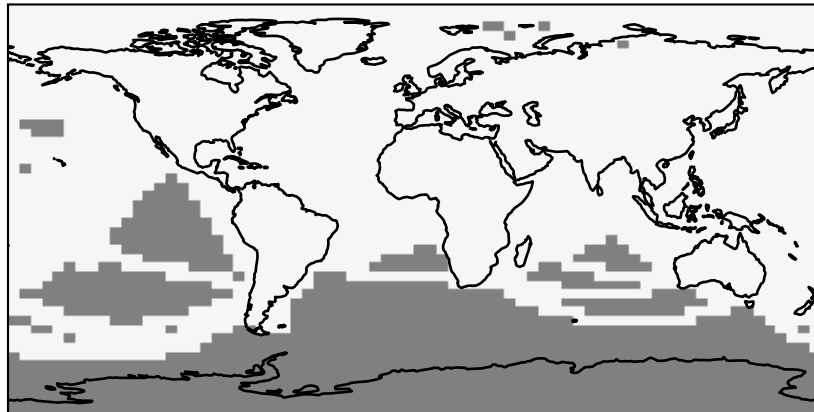
Difference  
Dev - Ref, Dynamic Range



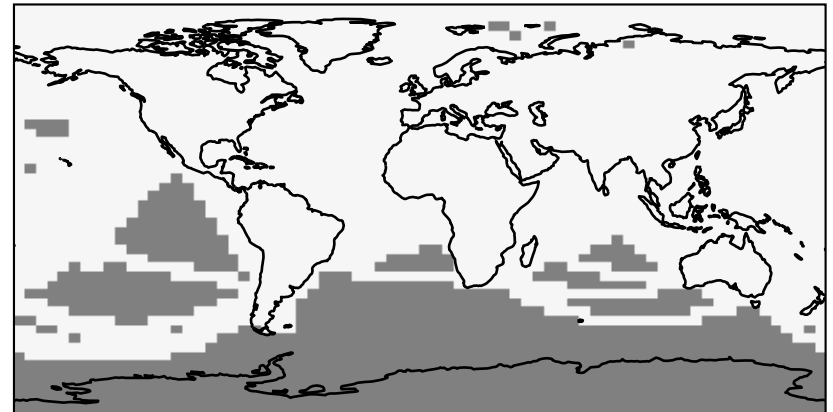
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

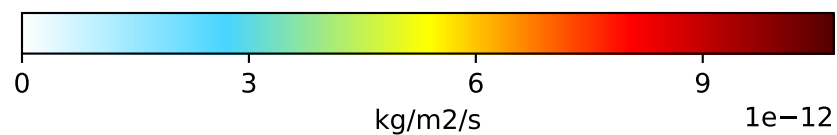
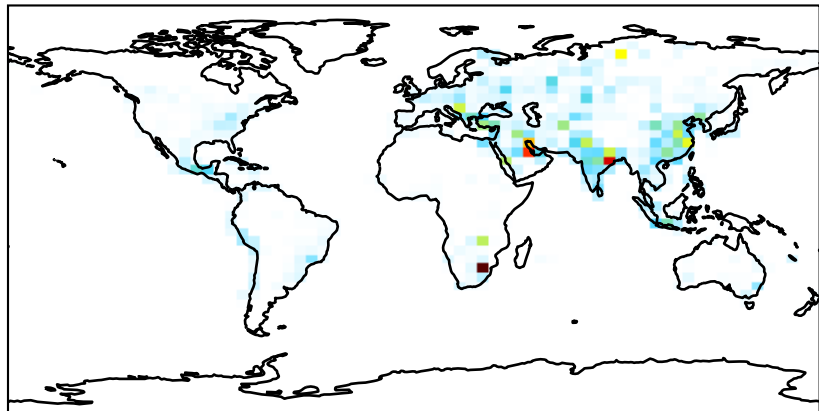


Ratio  
Dev/Ref, Fixed Range

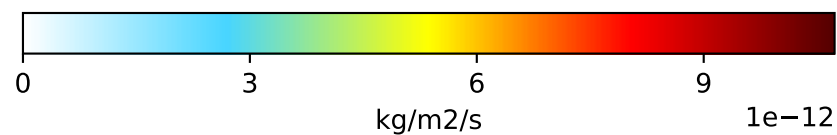
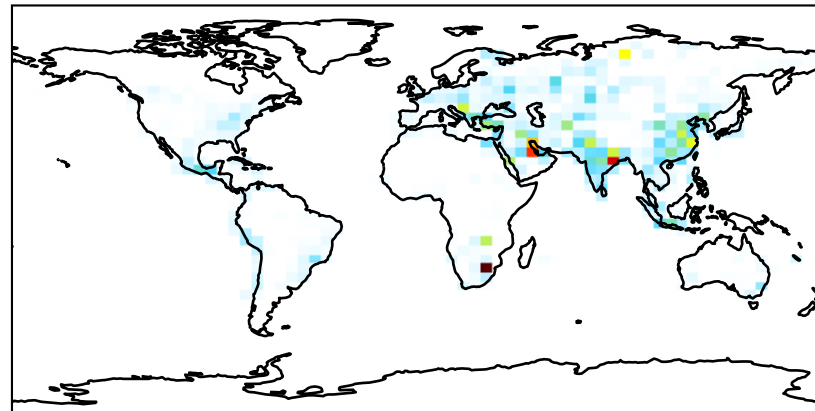


# EmisSO4\_Anthro (Apr2019)

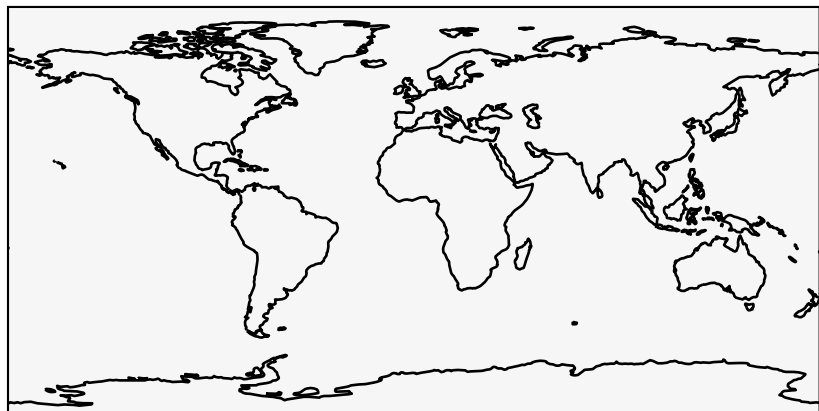
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



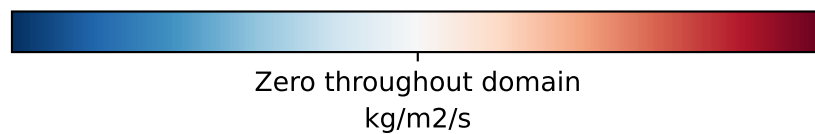
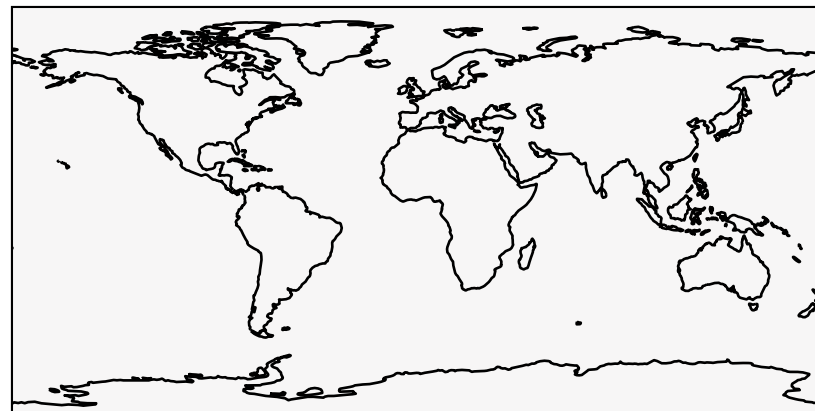
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



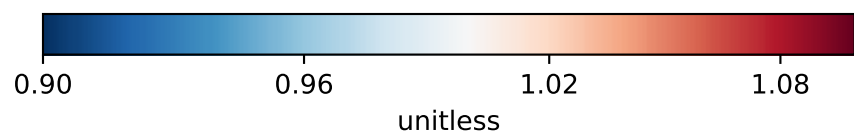
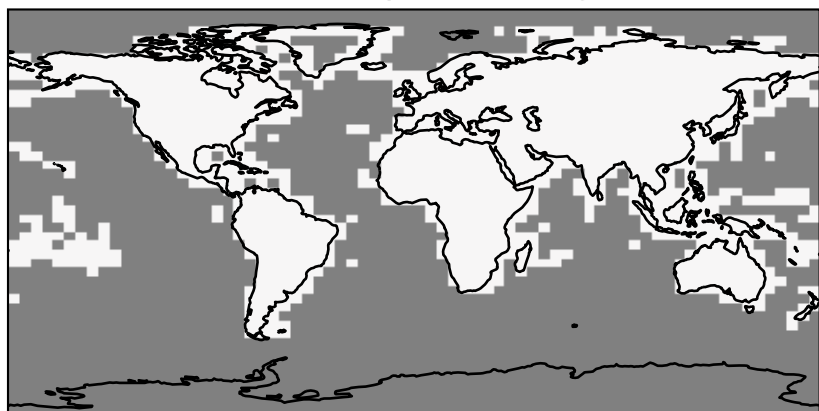
Difference  
Dev - Ref, Dynamic Range



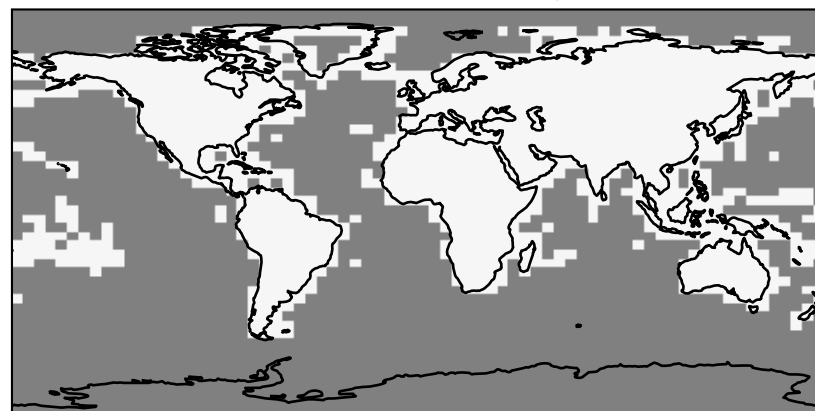
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



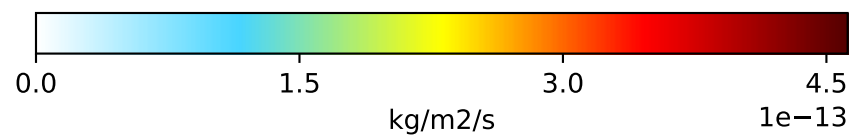
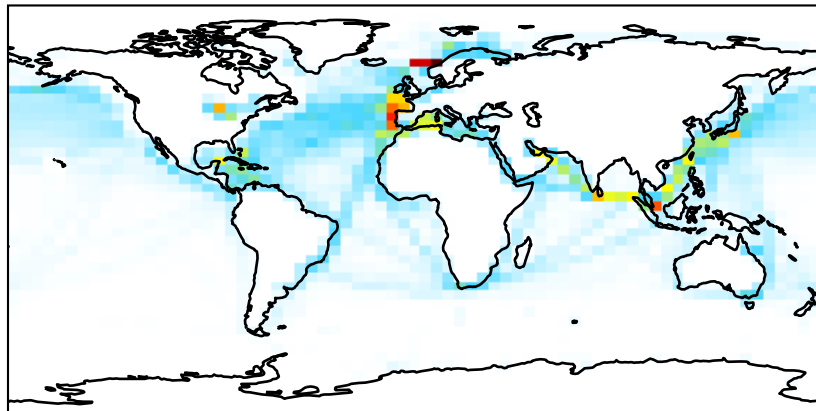
Ratio  
Dev/Ref, Fixed Range



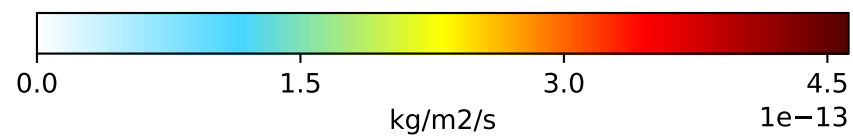
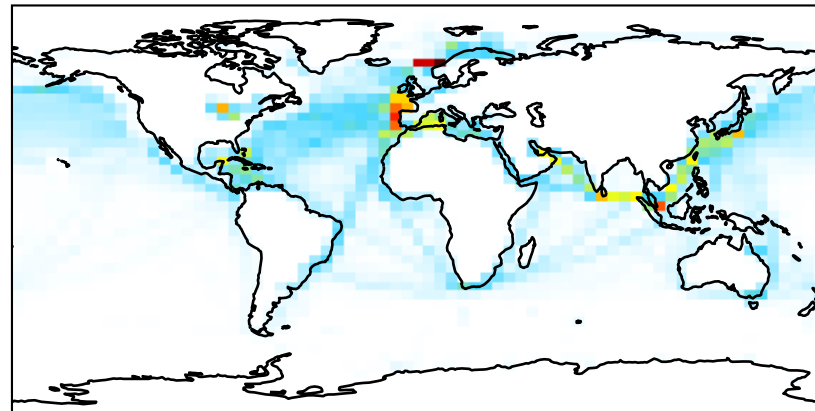


# EmisSO4\_Ship (Apr2019)

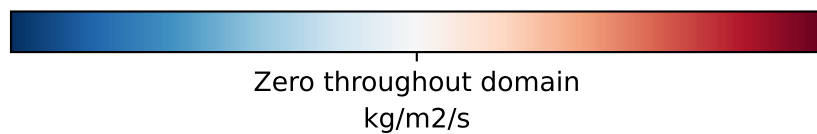
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



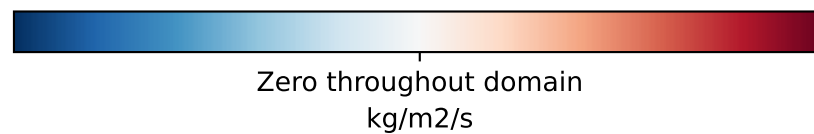
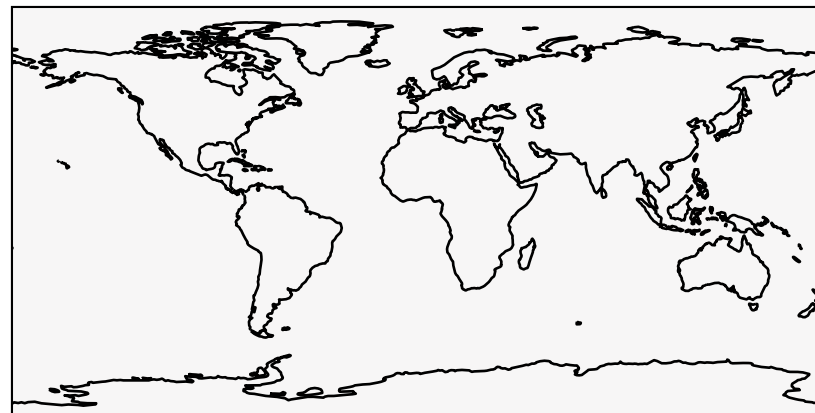
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



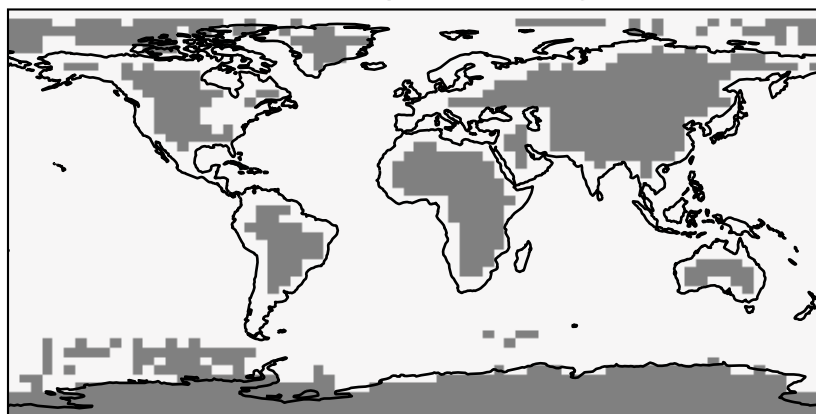
Difference  
Dev - Ref, Dynamic Range



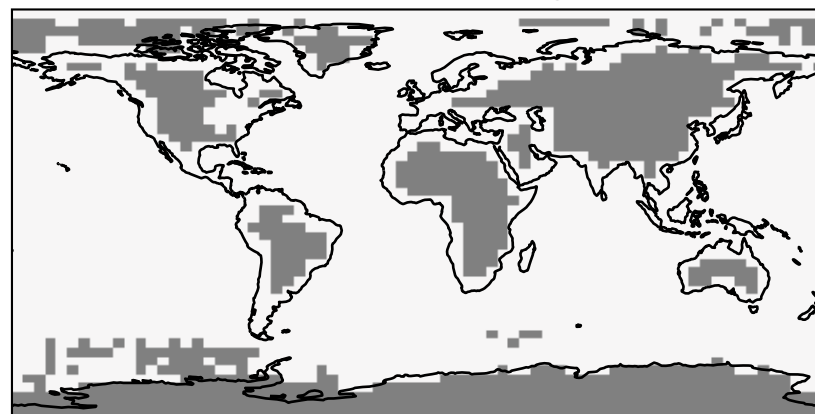
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

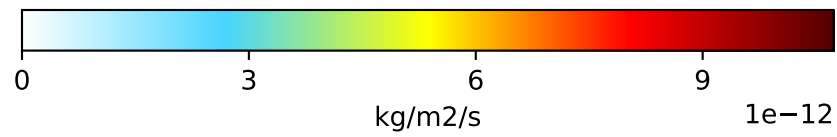
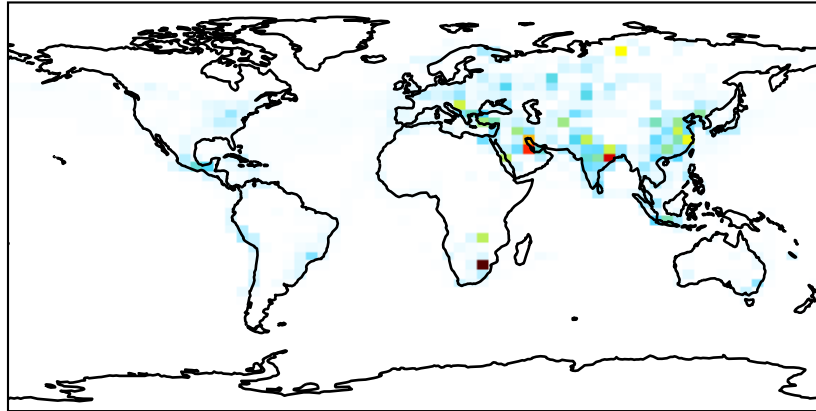


Ratio  
Dev/Ref, Fixed Range

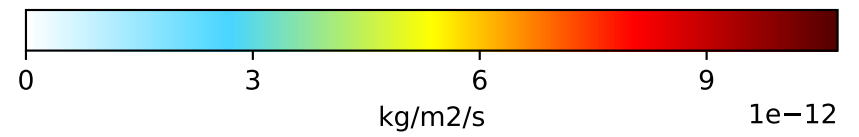
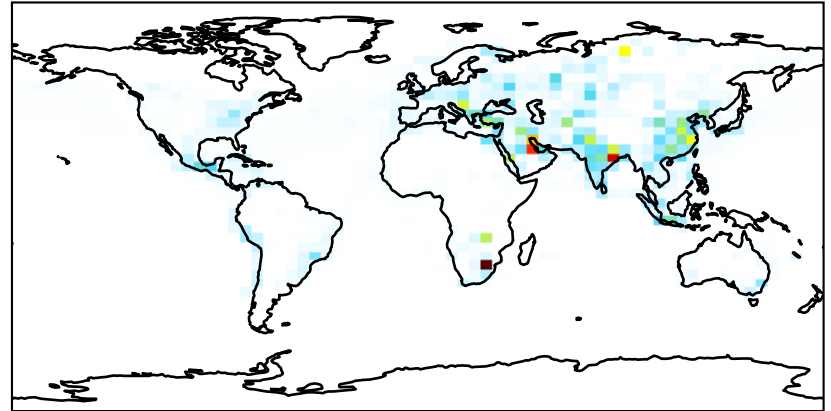


# EmisSO4\_Total (Apr2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



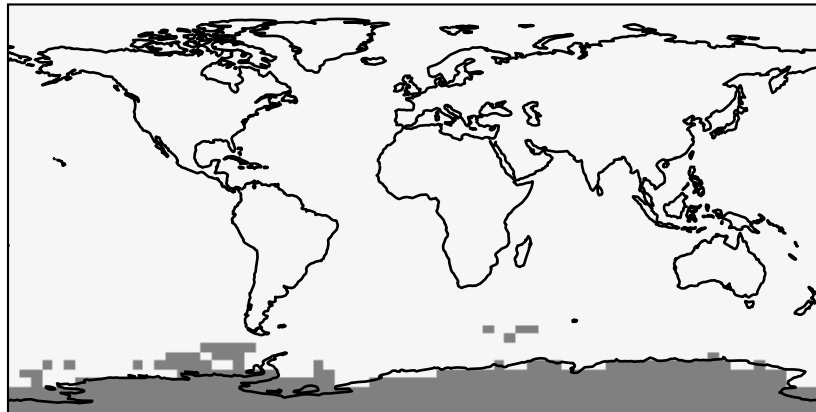
Difference  
Dev - Ref, Dynamic Range



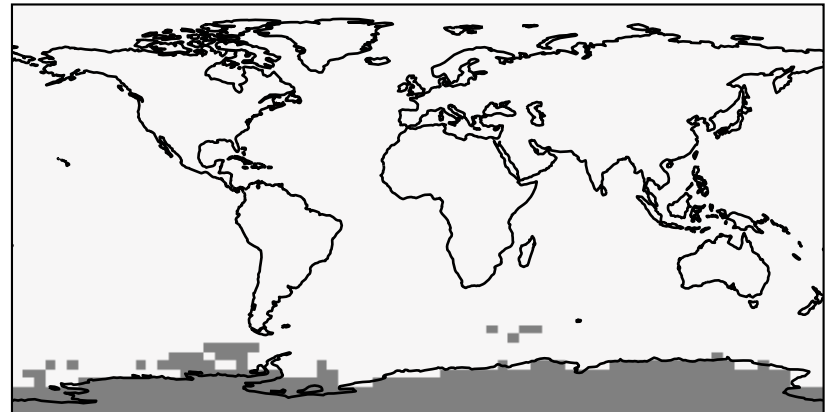
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

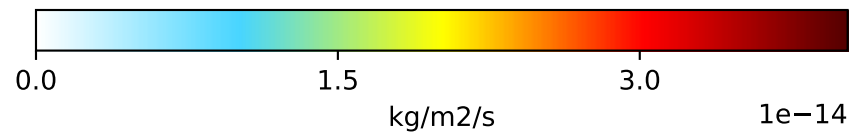
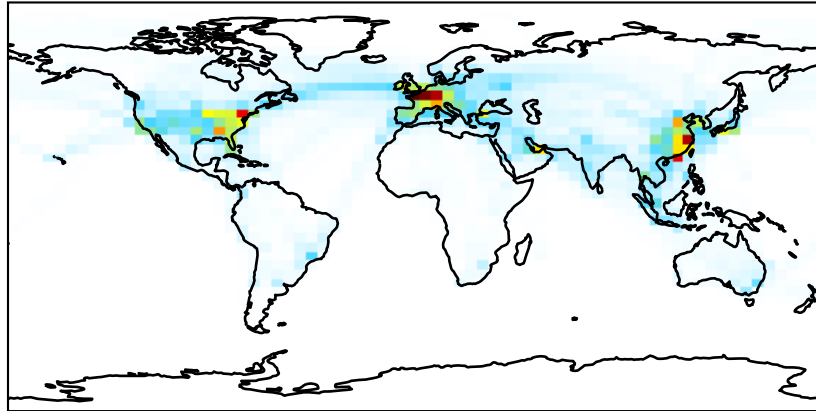


Ratio  
Dev/Ref, Fixed Range

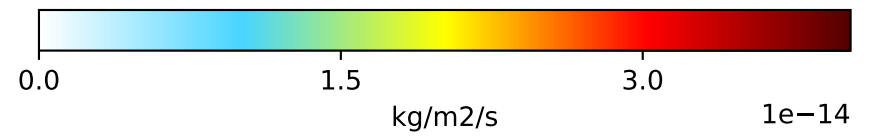
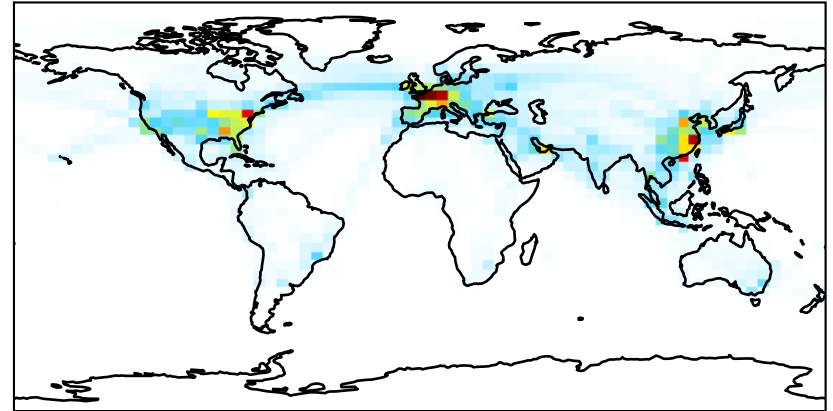


# EmisBCPI\_Aircraft (Apr2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



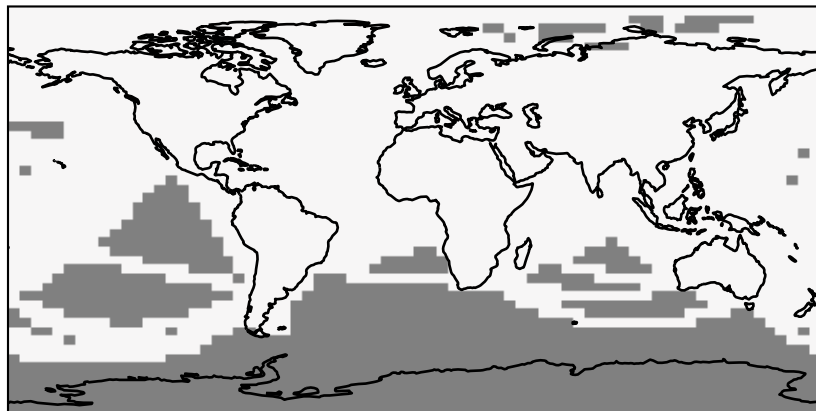
Difference  
Dev - Ref, Dynamic Range



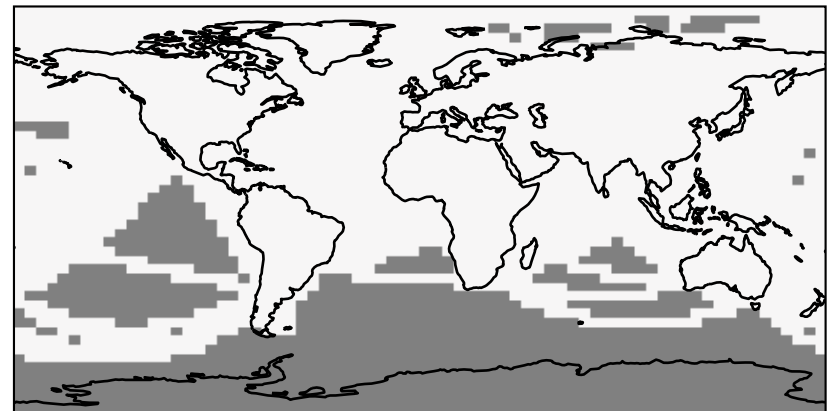
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

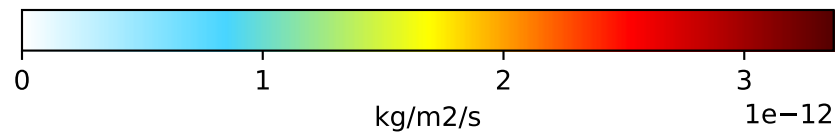
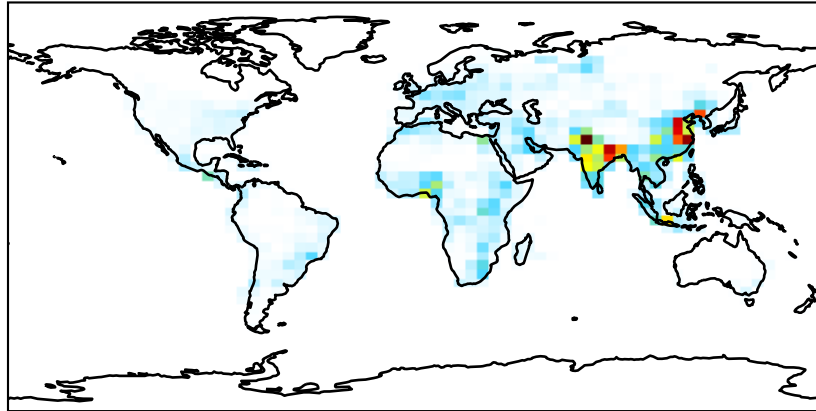


Ratio  
Dev/Ref, Fixed Range

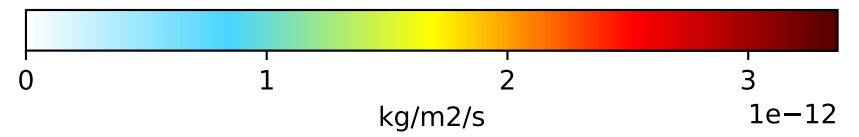
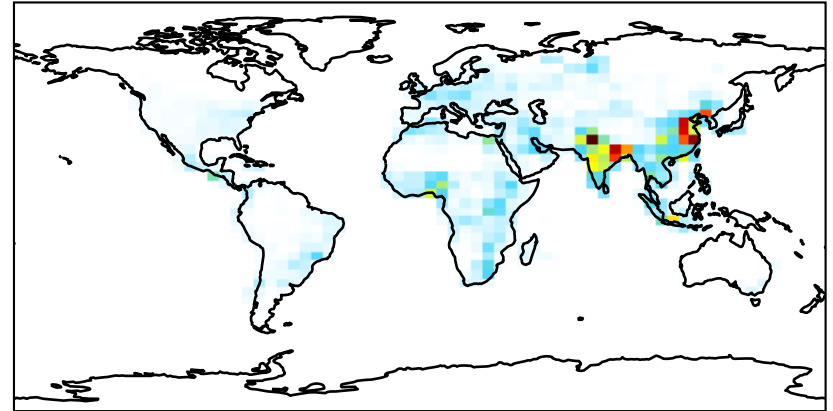


# EmisBCPI\_Anthro (Apr2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



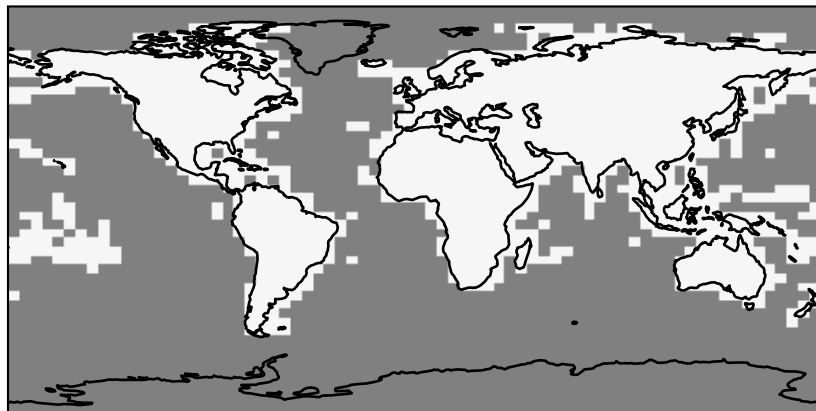
Difference  
Dev - Ref, Dynamic Range



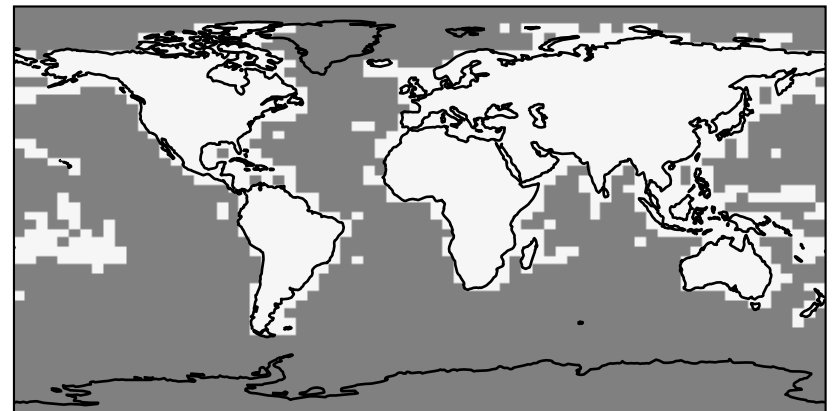
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

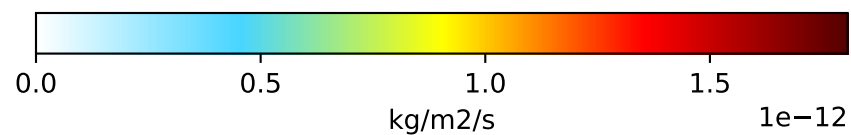
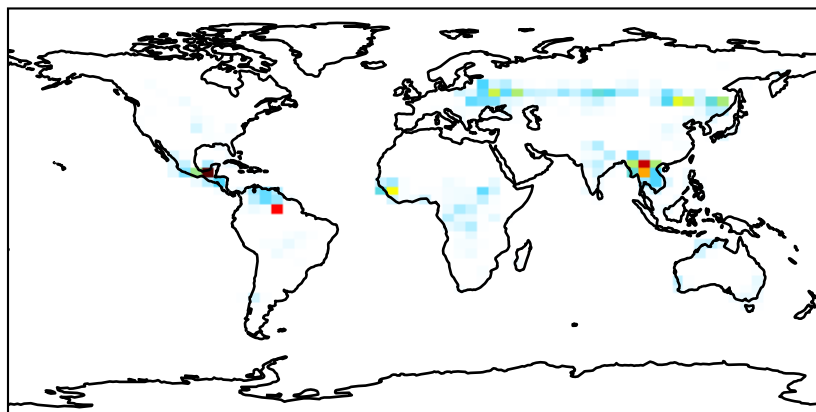


Ratio  
Dev/Ref, Fixed Range

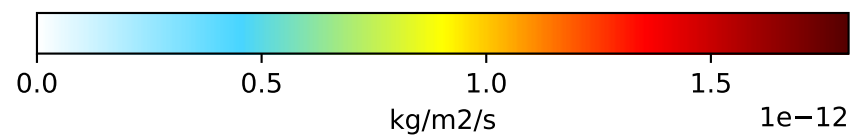
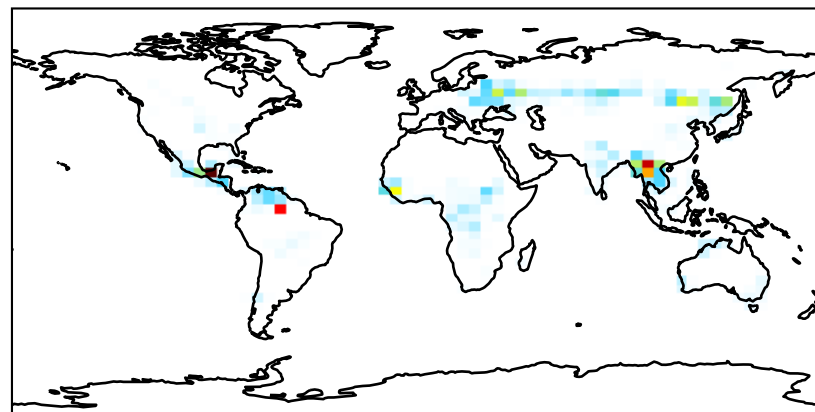


# EmisBCPI\_BioBurn (Apr2019)

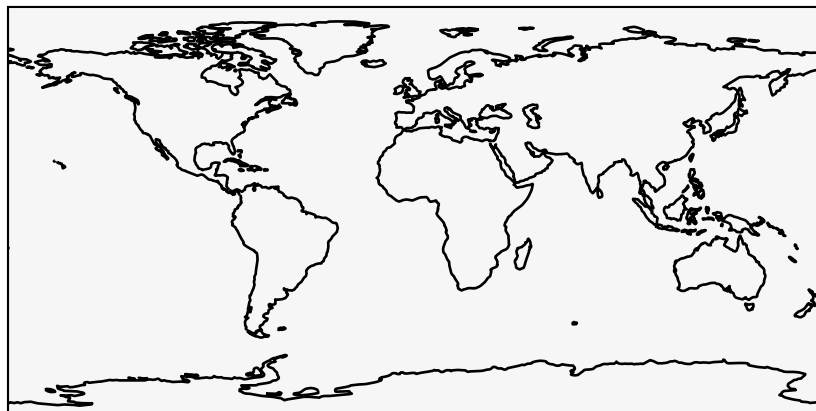
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



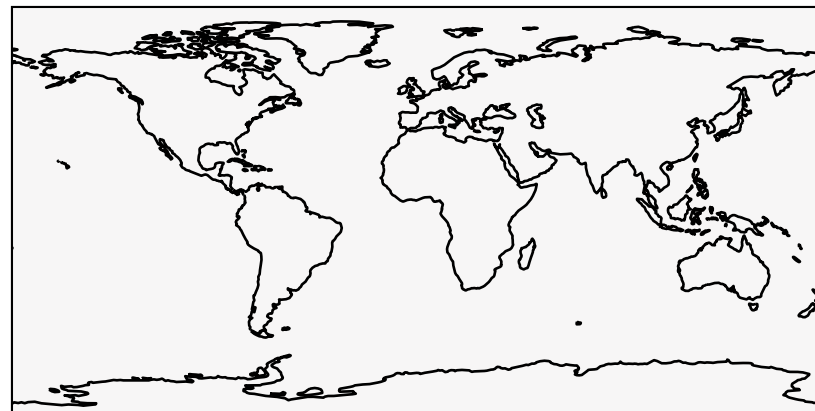
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



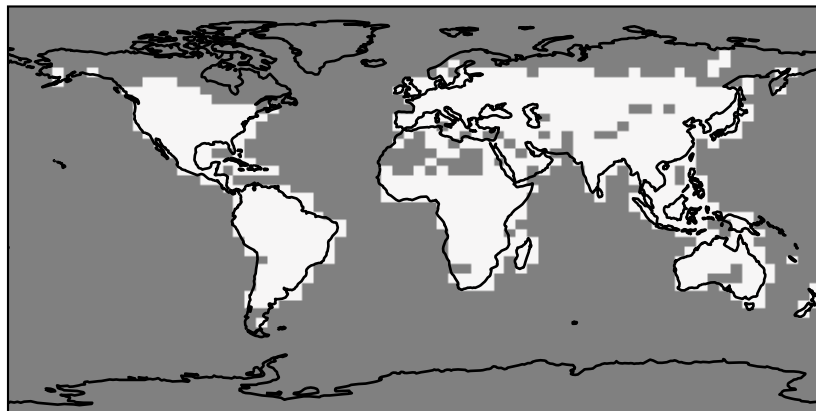
Difference  
Dev - Ref, Dynamic Range



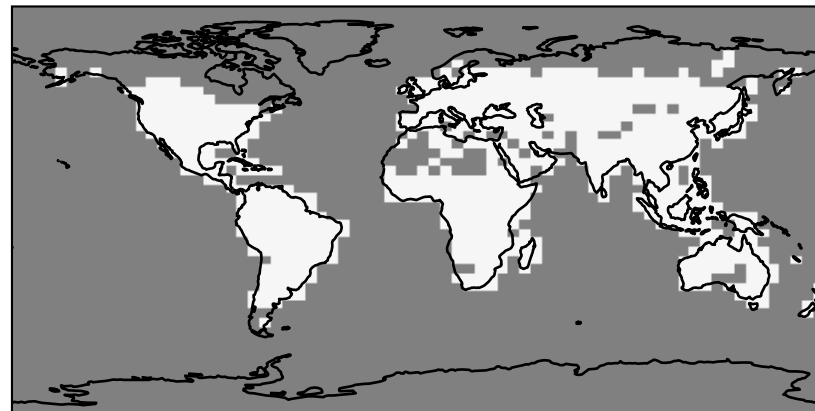
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

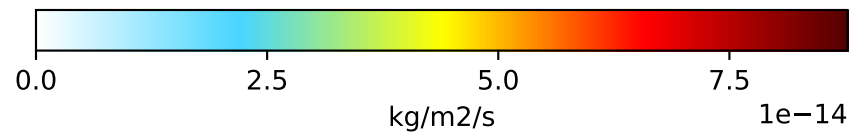
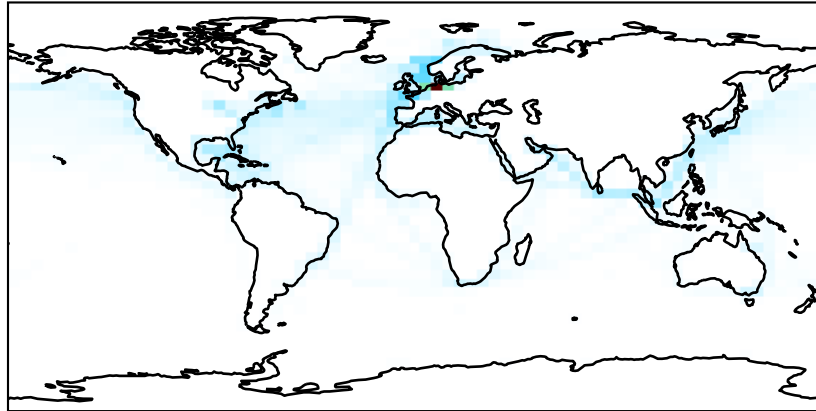


Ratio  
Dev/Ref, Fixed Range

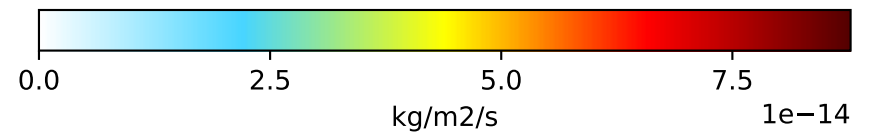
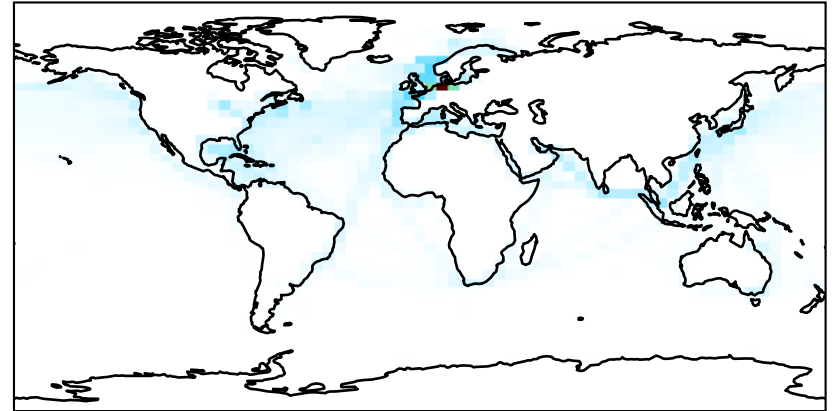


# EmisBCPI\_Ship (Apr2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



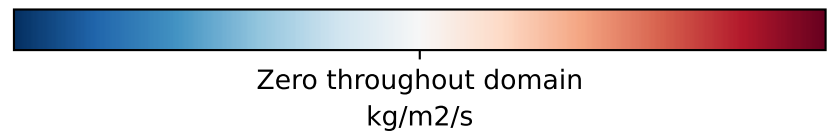
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



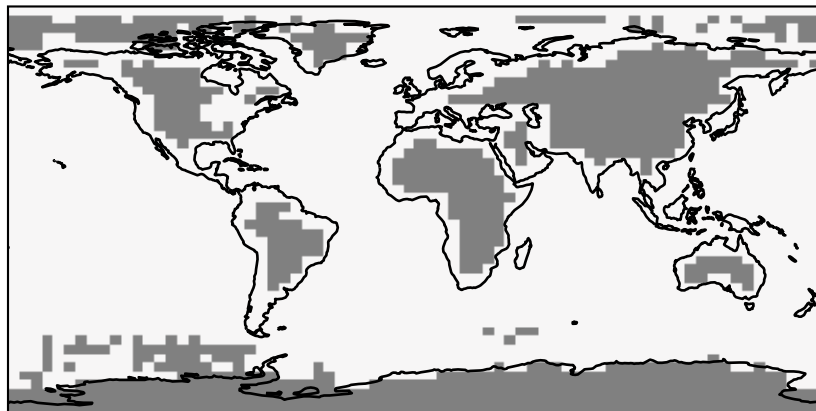
Difference  
Dev - Ref, Dynamic Range



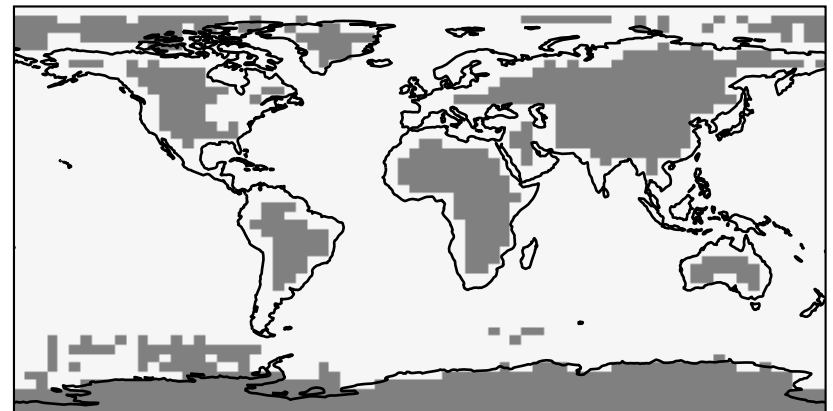
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

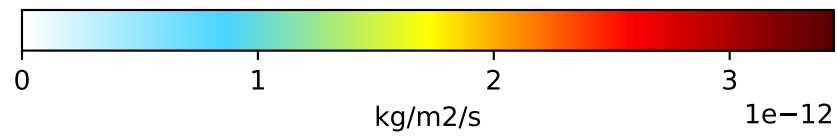
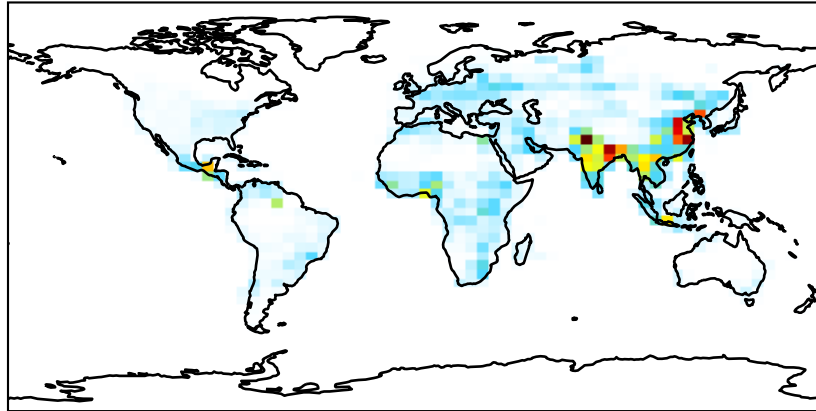


Ratio  
Dev/Ref, Fixed Range

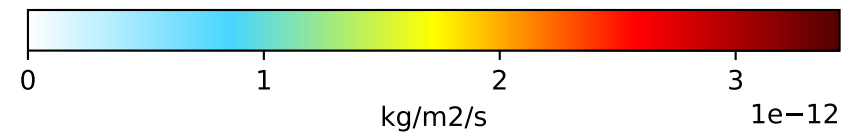
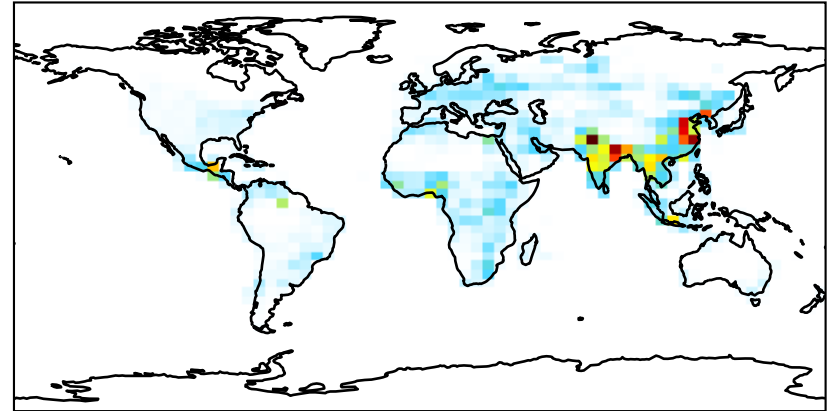


# EmisBCPI\_Total (Apr2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



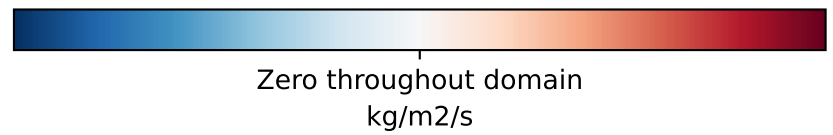
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



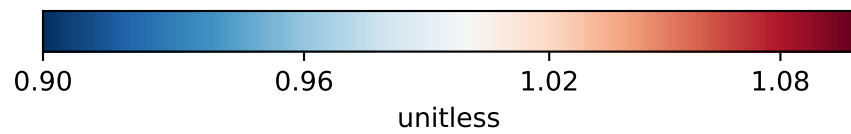
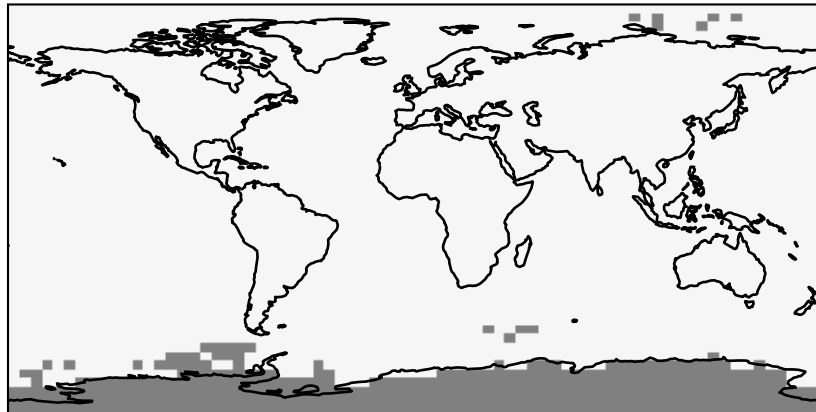
Difference  
Dev - Ref, Dynamic Range



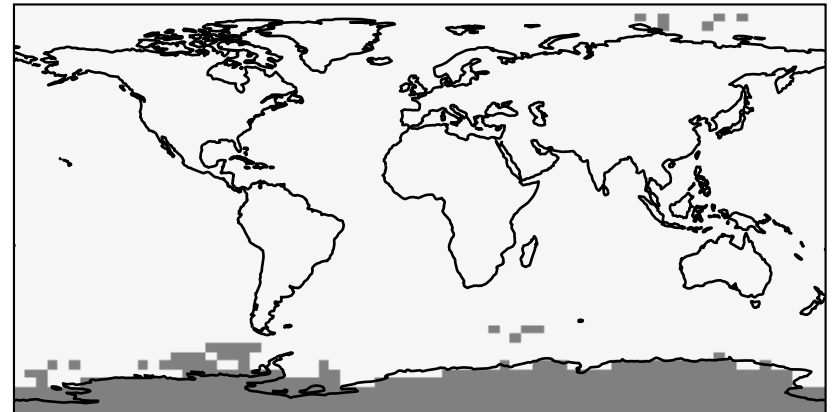
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

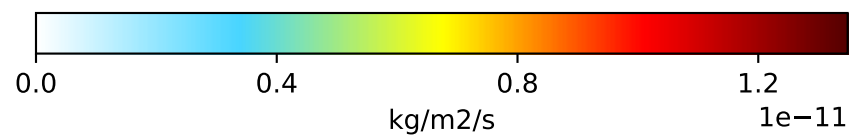
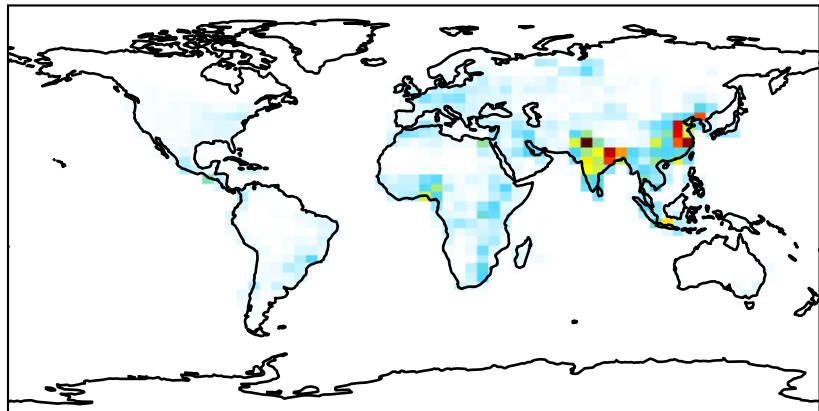


Ratio  
Dev/Ref, Fixed Range

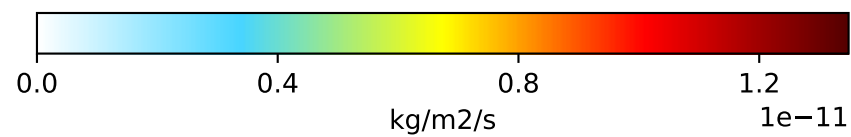
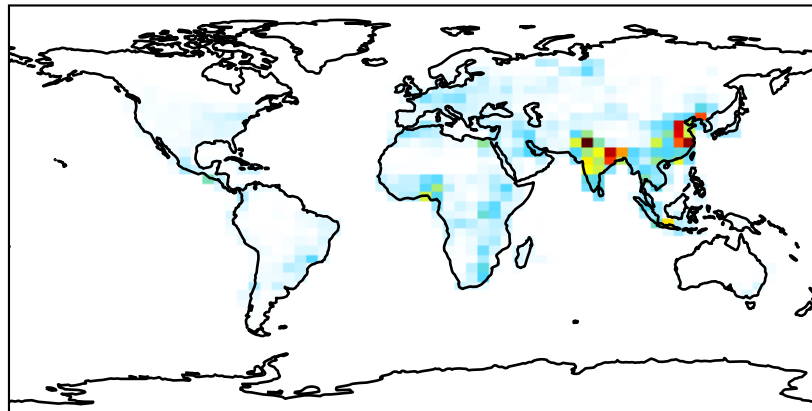


# EmisBCPO\_Anthro (Apr2019)

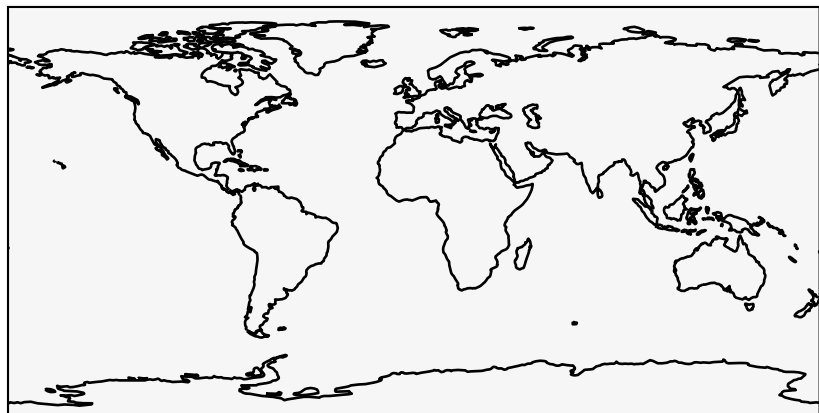
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



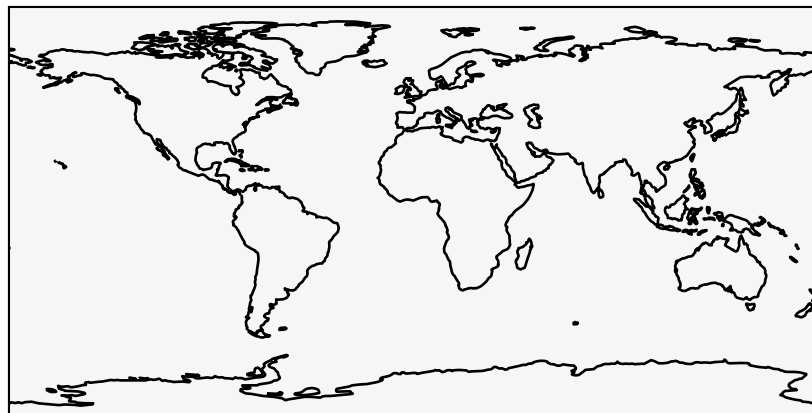
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



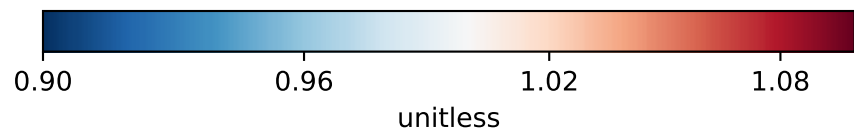
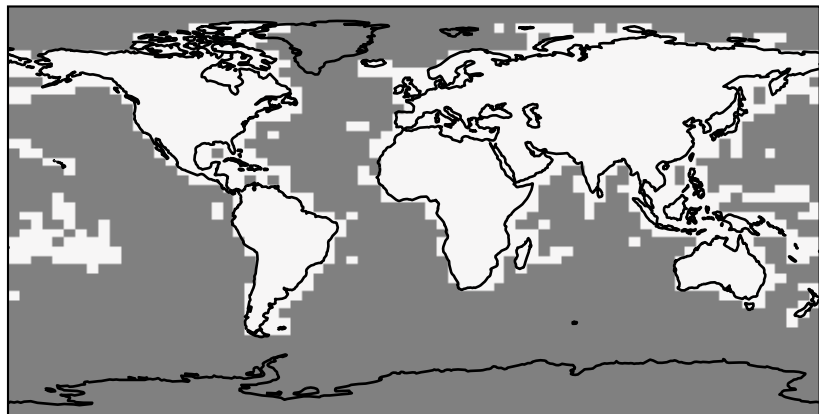
Difference  
Dev - Ref, Dynamic Range



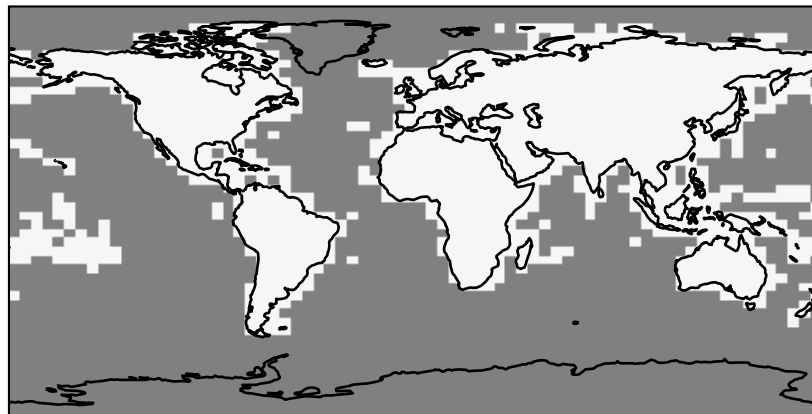
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



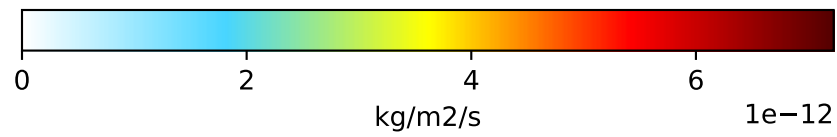
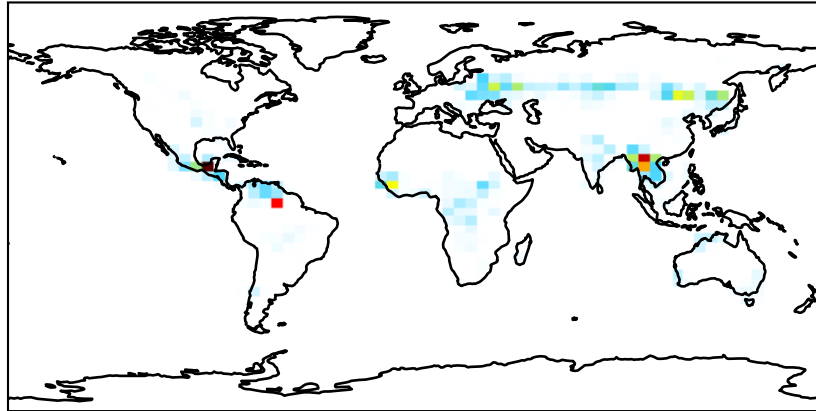
Ratio  
Dev/Ref, Fixed Range



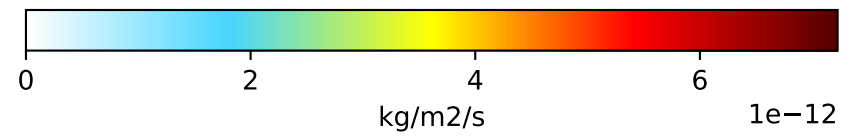
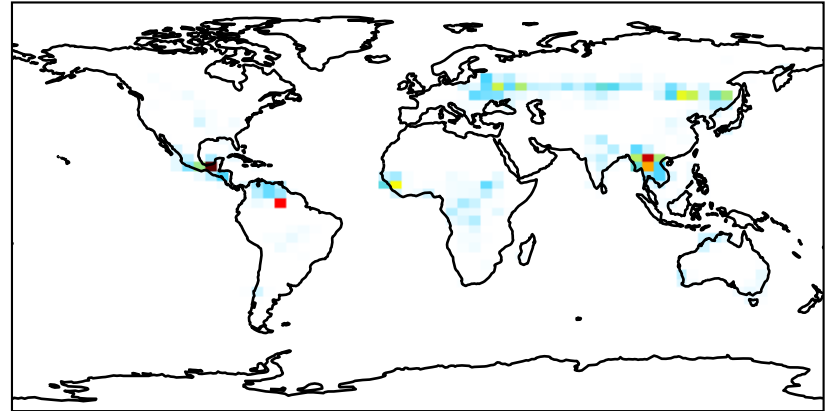


# EmisBCPO\_BioBurn (Apr2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



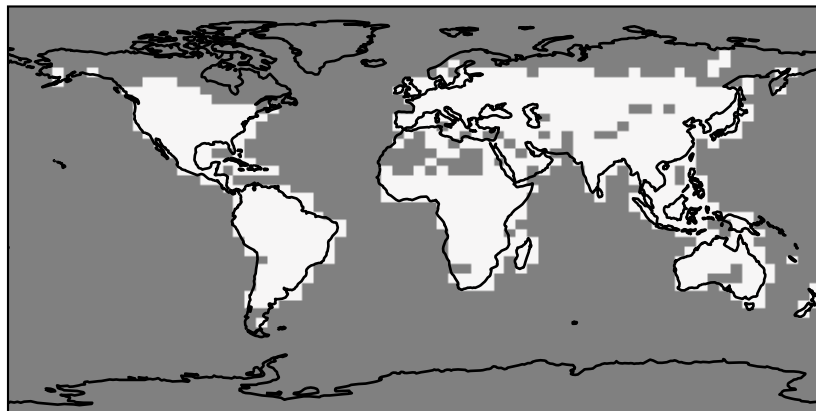
Difference  
Dev - Ref, Dynamic Range



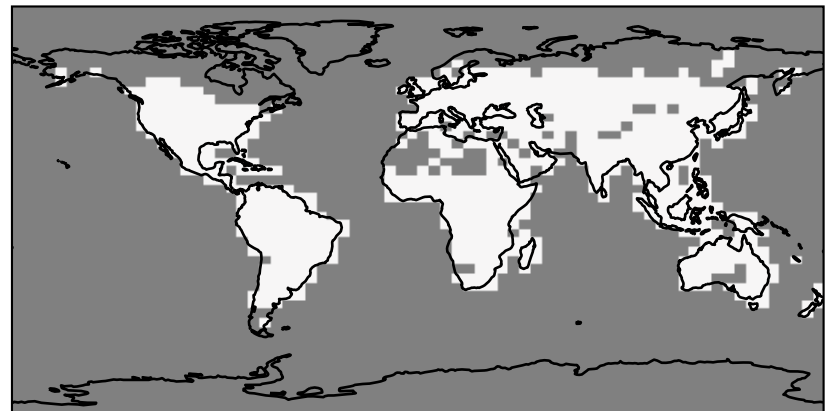
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

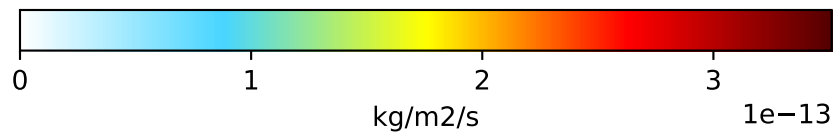
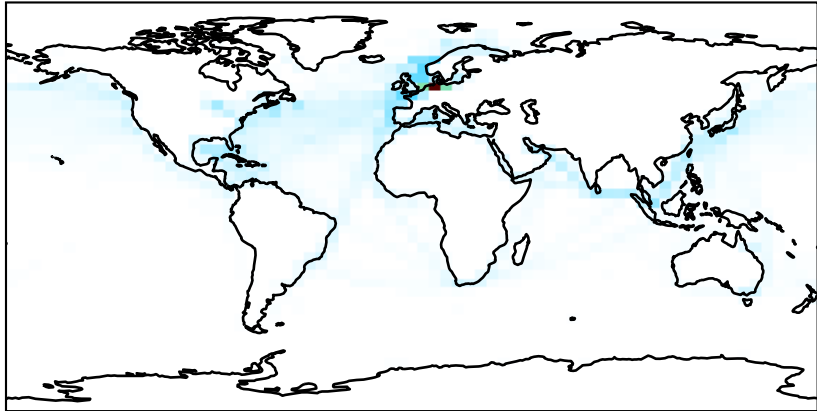


Ratio  
Dev/Ref, Fixed Range

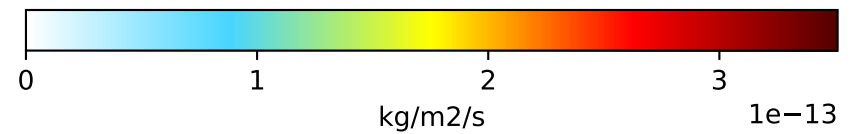
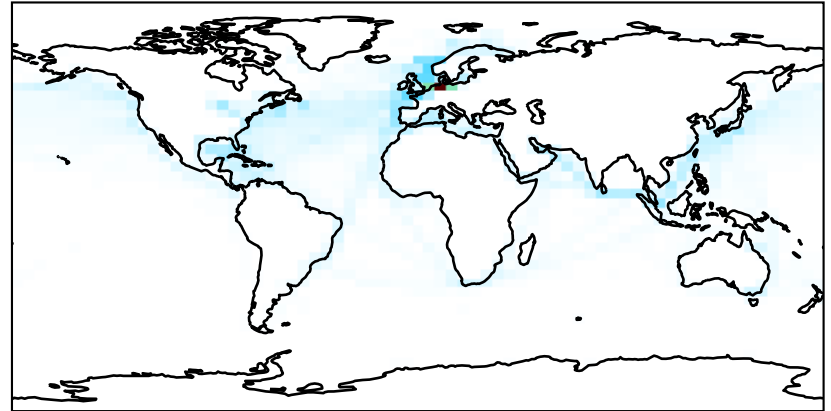


# EmisBCPO\_Ship (Apr2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



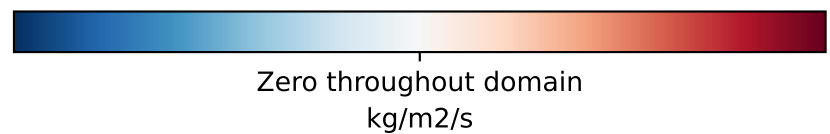
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



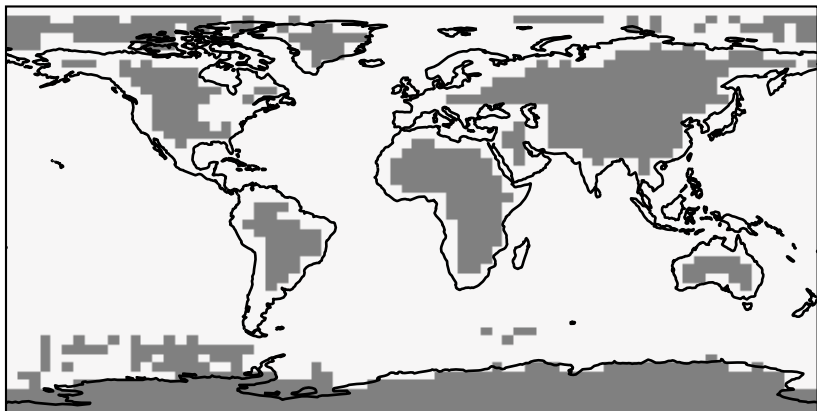
Difference  
Dev - Ref, Dynamic Range



Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

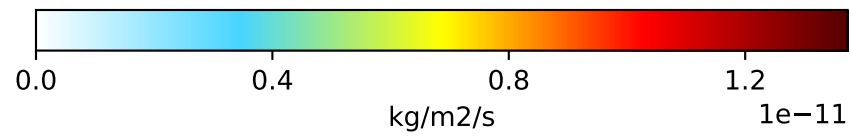
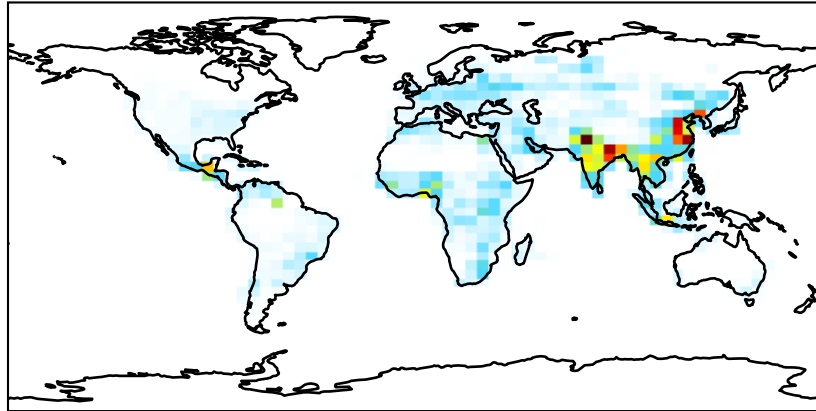


Ratio  
Dev/Ref, Fixed Range

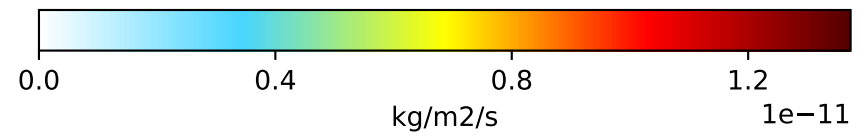
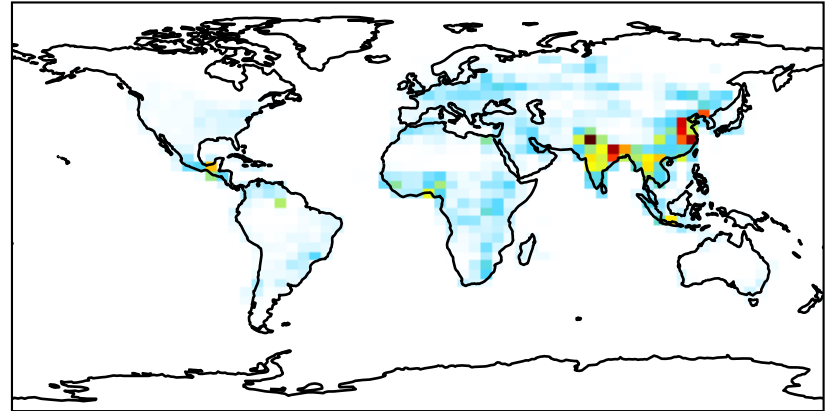


# EmisBCPO\_Total (Apr2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



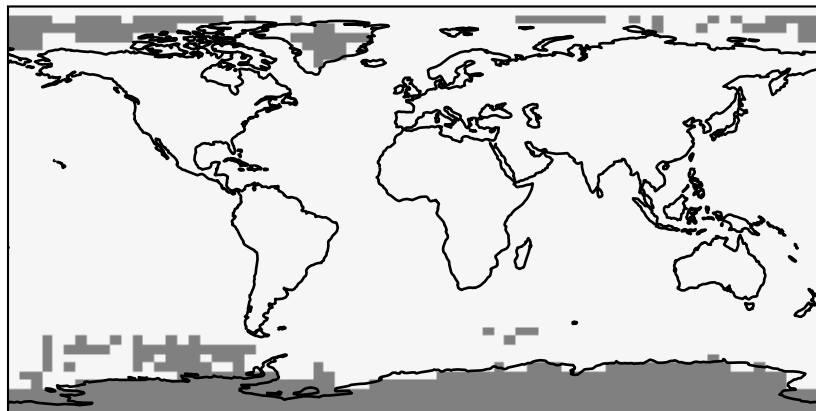
Difference  
Dev - Ref, Dynamic Range



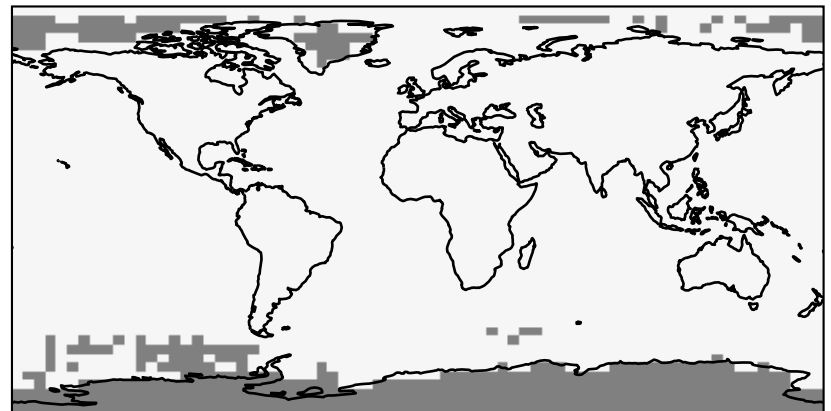
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

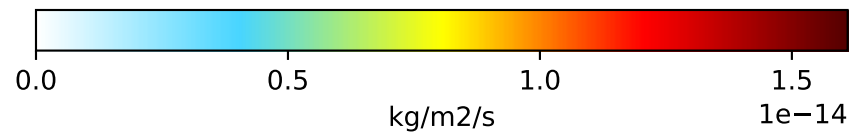
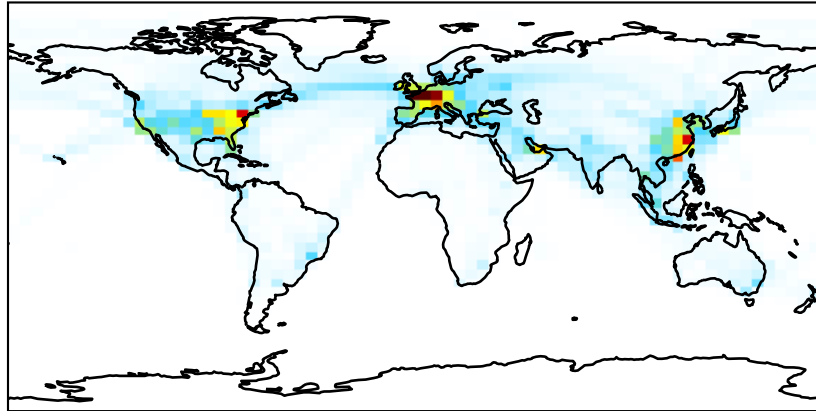


Ratio  
Dev/Ref, Fixed Range

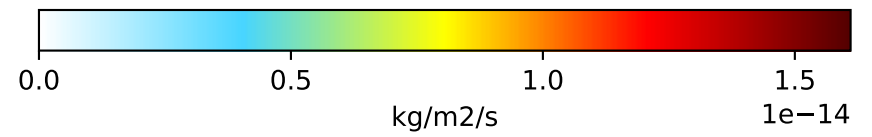
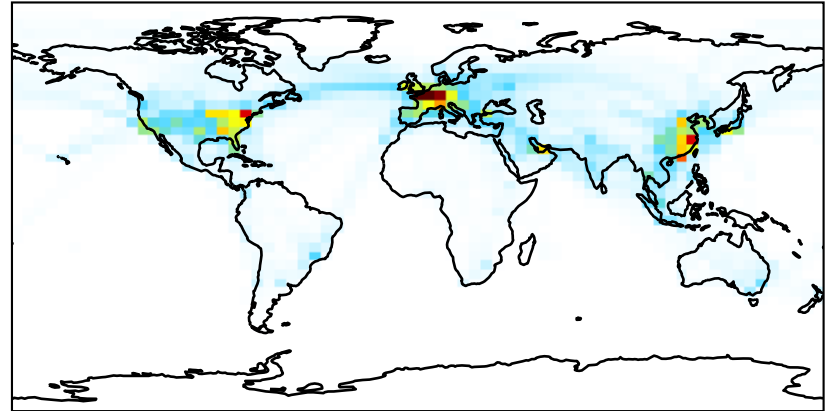


# EmisOCPI\_Aircraft (Apr2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



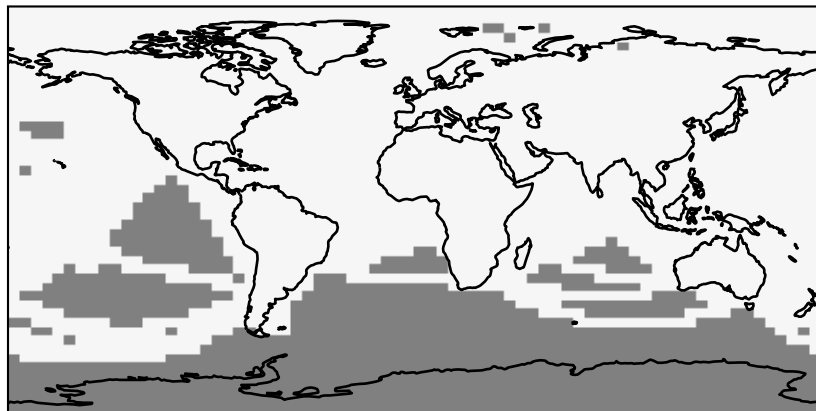
Difference  
Dev - Ref, Dynamic Range



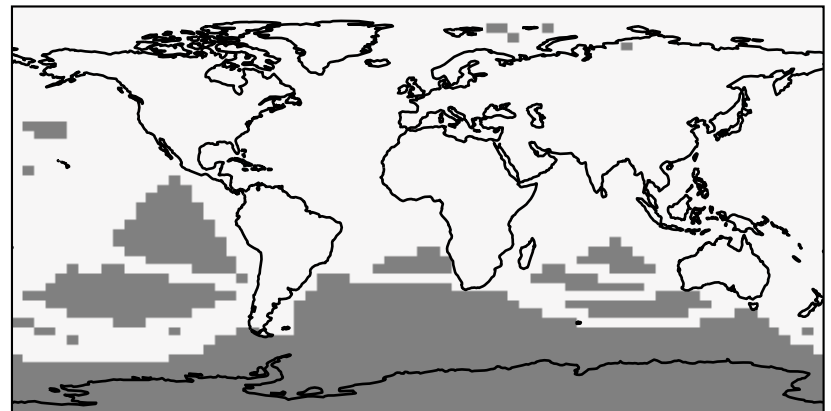
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

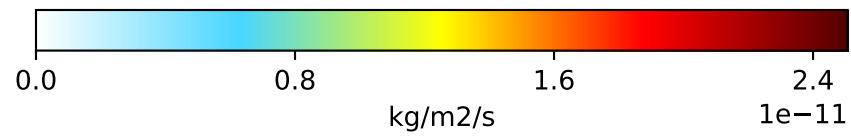
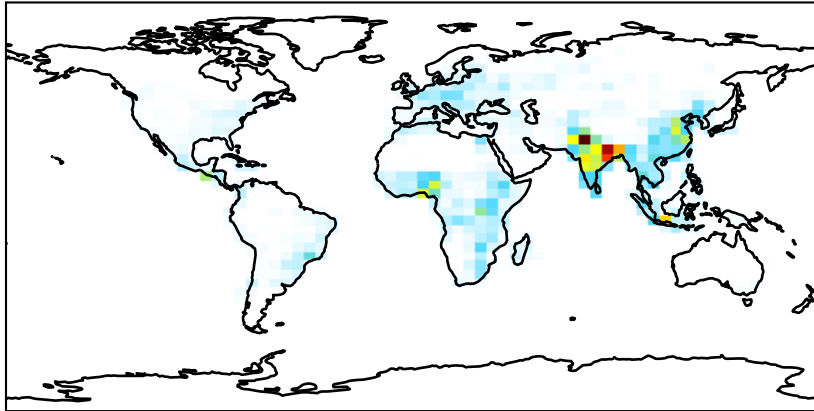


Ratio  
Dev/Ref, Fixed Range

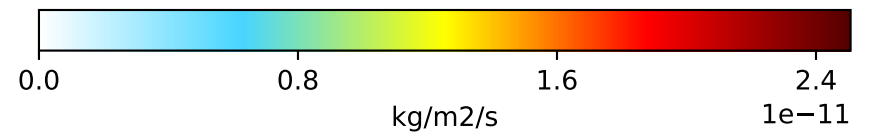
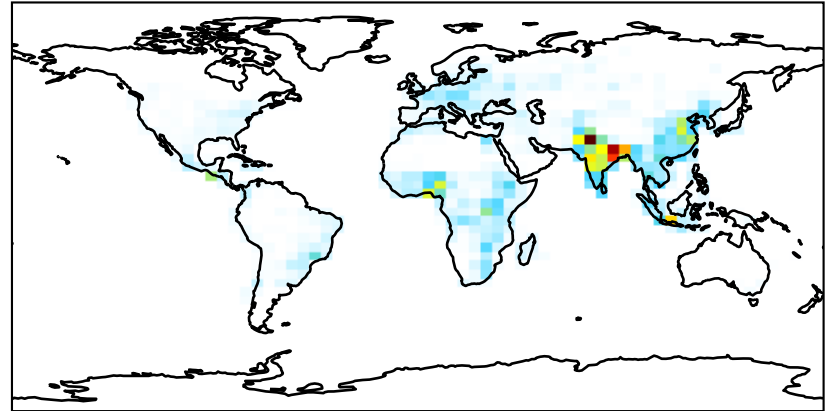


# EmisOCPI\_Anthro (Apr2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



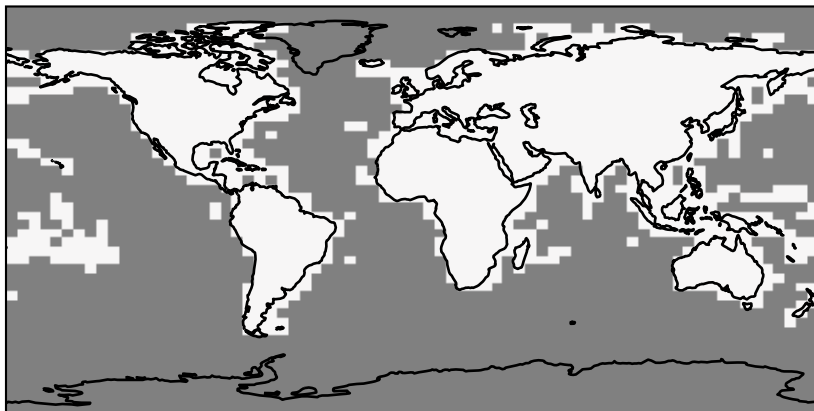
Difference  
Dev - Ref, Dynamic Range



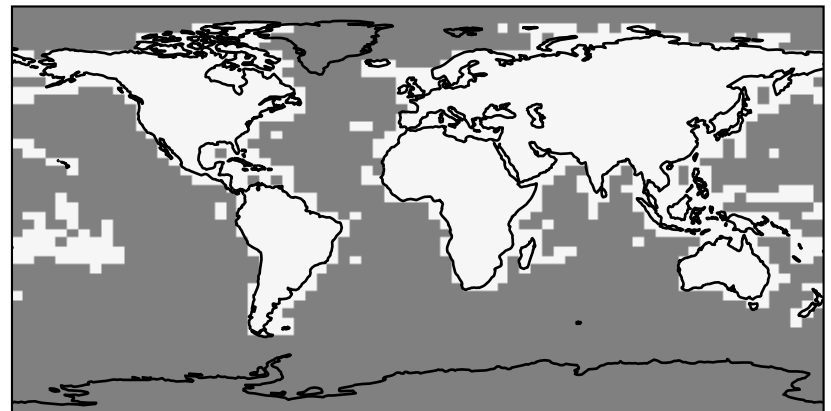
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

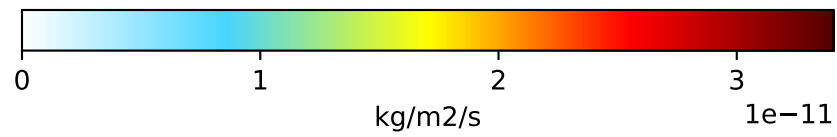
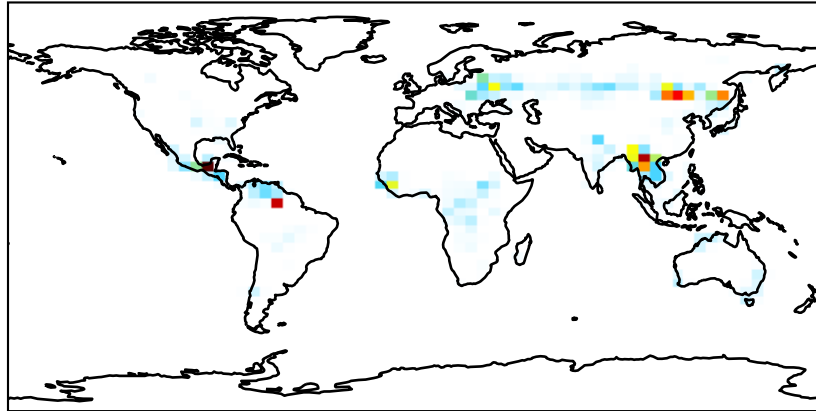


Ratio  
Dev/Ref, Fixed Range

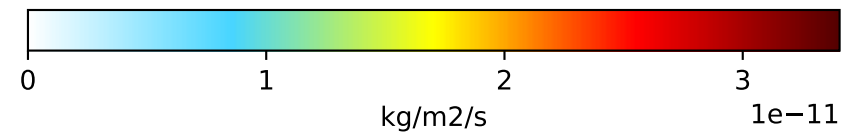
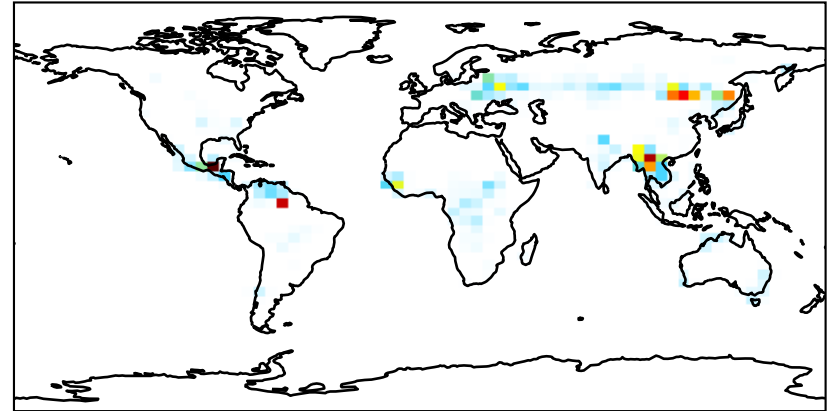


# EmisOCPI\_BioBurn (Apr2019)

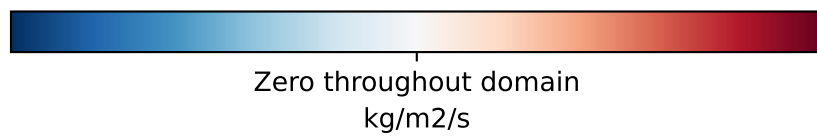
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



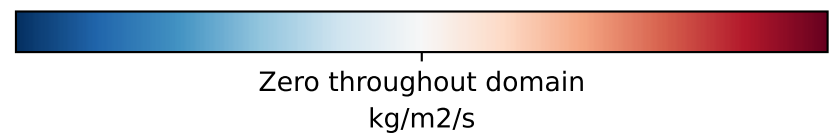
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



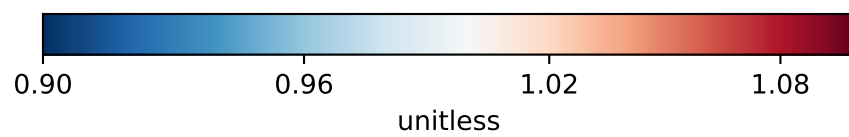
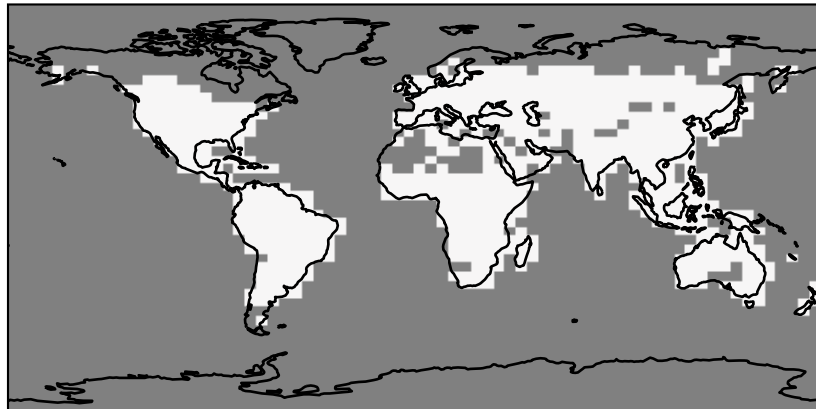
Difference  
Dev - Ref, Dynamic Range



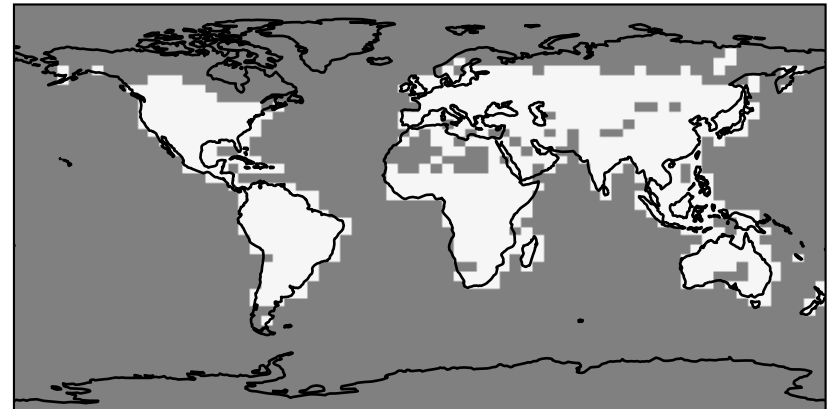
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

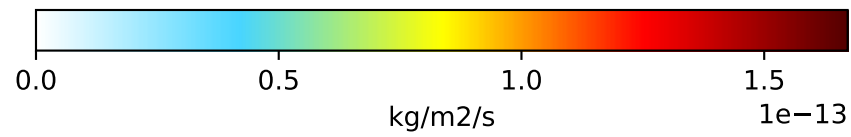
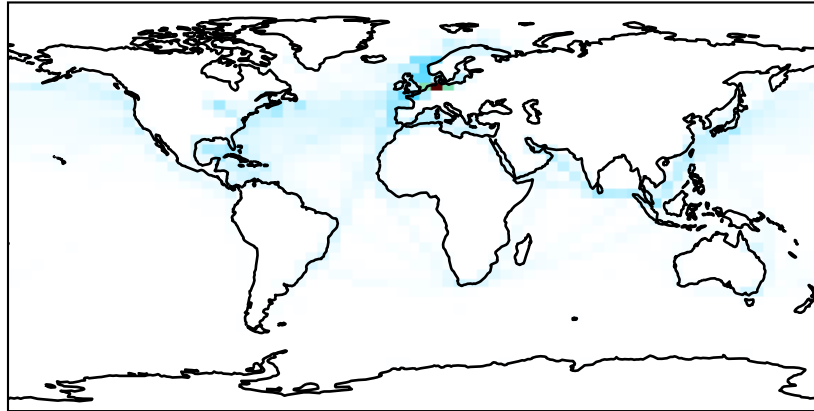


Ratio  
Dev/Ref, Fixed Range

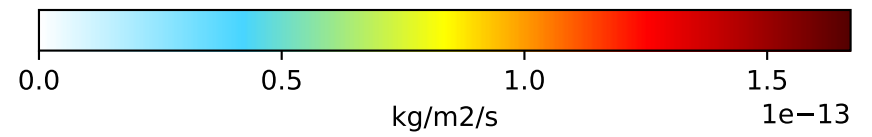
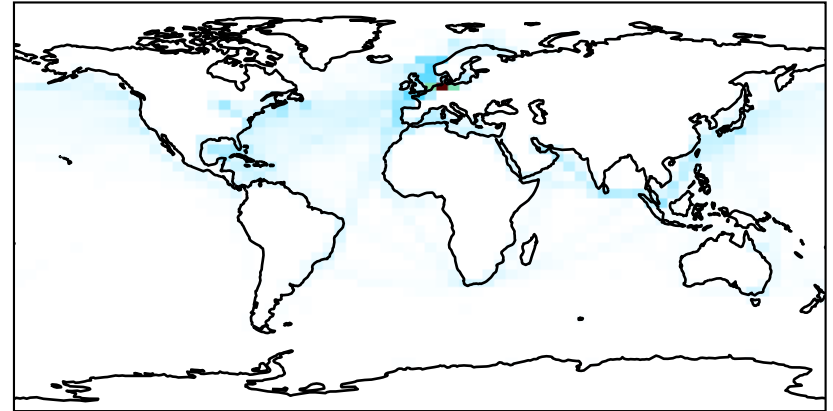


# EmisOCPI\_Ship (Apr2019)

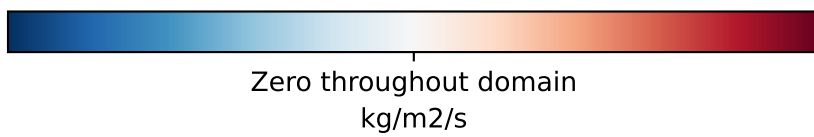
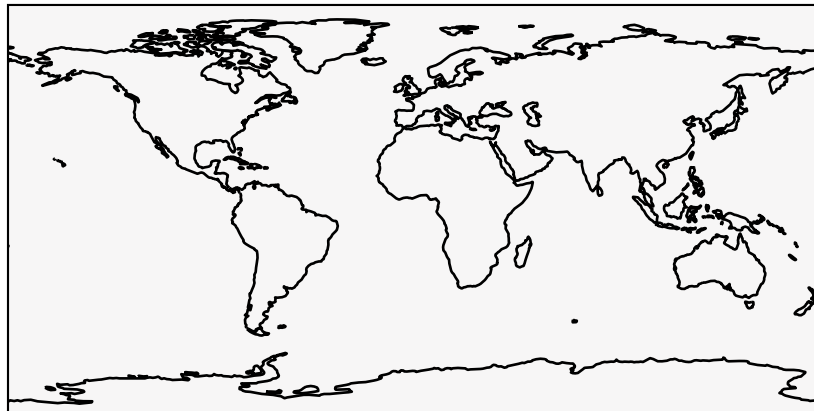
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



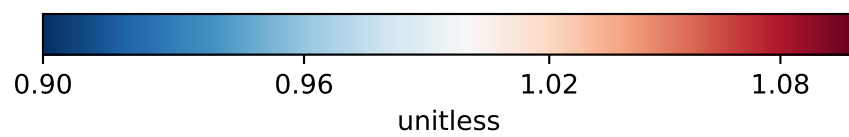
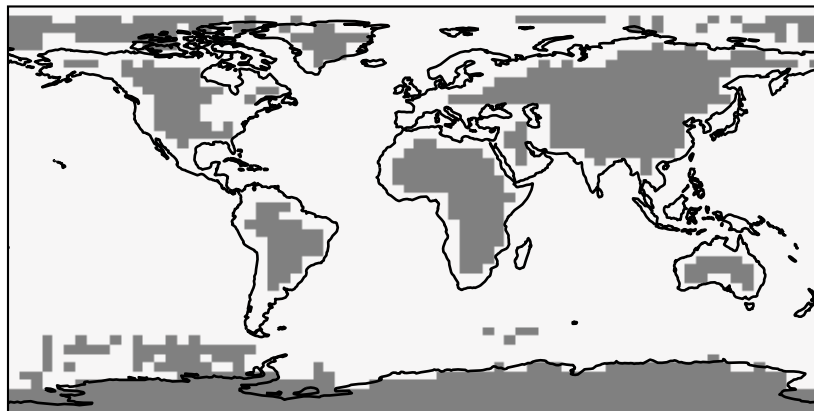
Difference  
Dev - Ref, Dynamic Range



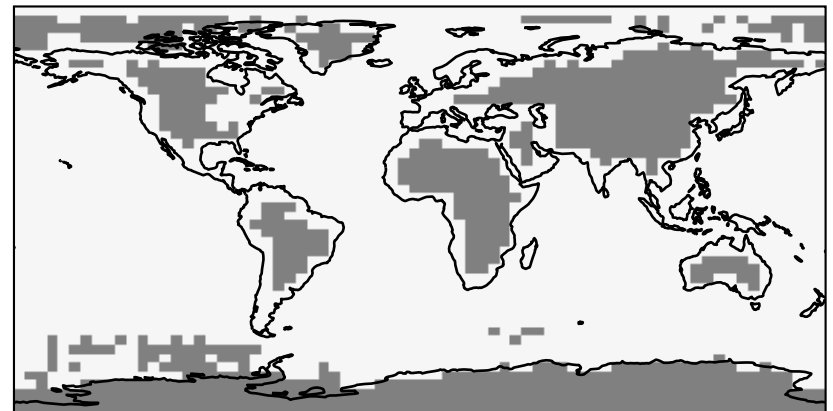
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

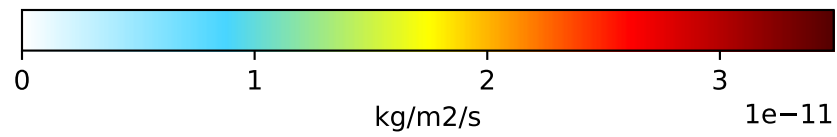
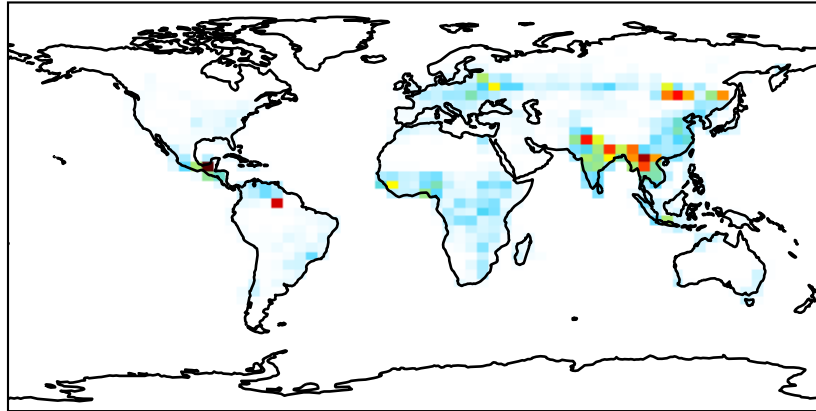


Ratio  
Dev/Ref, Fixed Range

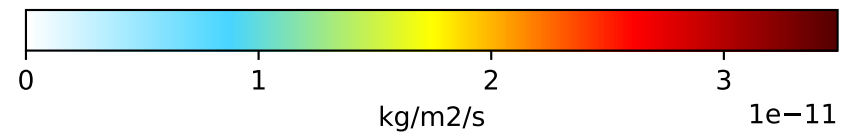
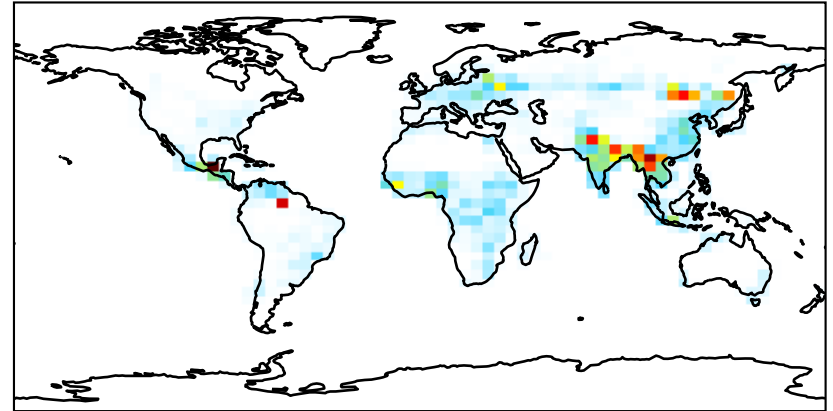


# EmisOCPI\_Total (Apr2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



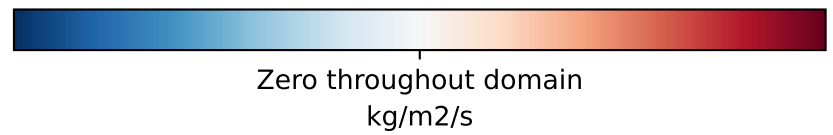
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



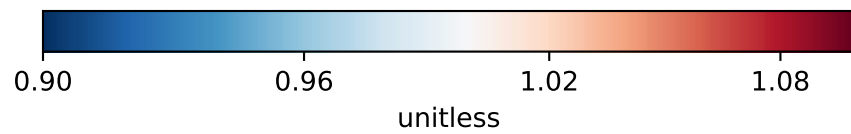
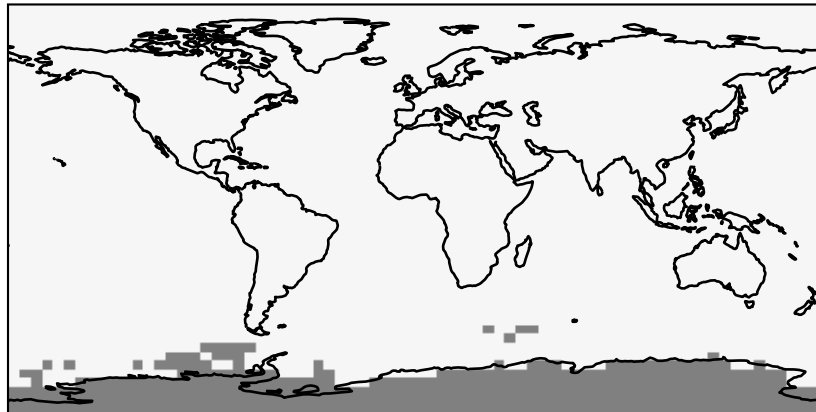
Difference  
Dev - Ref, Dynamic Range



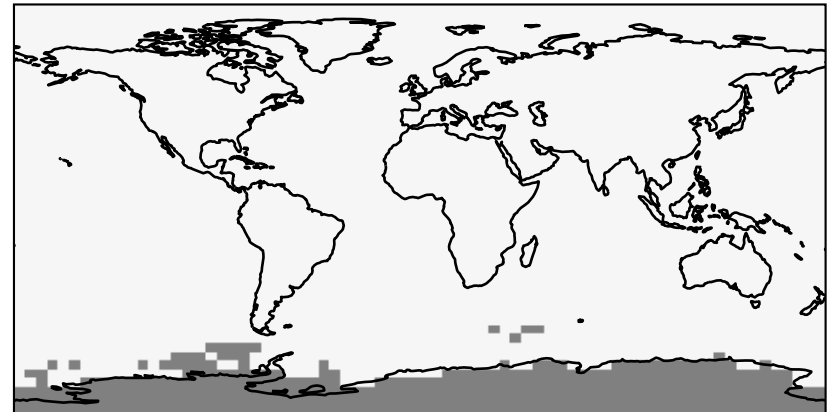
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



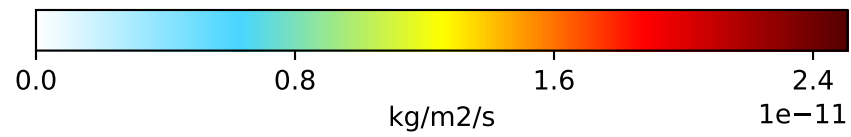
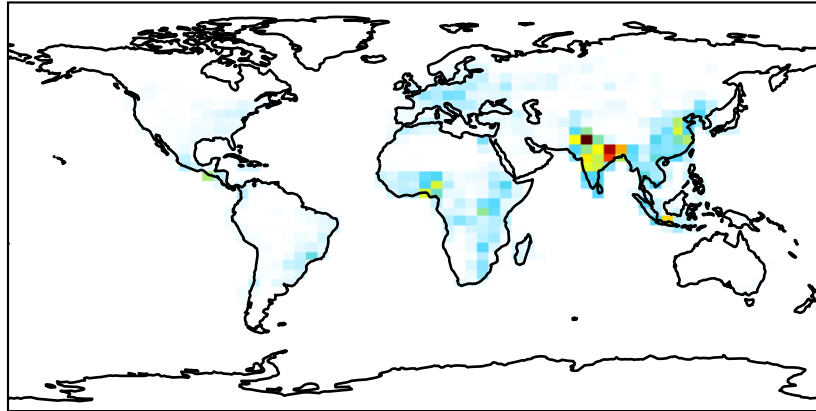
Ratio  
Dev/Ref, Fixed Range



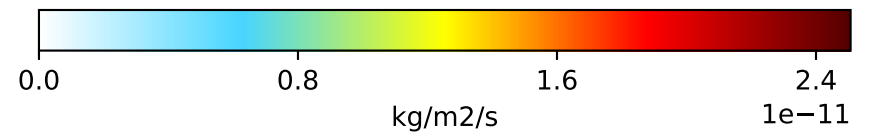
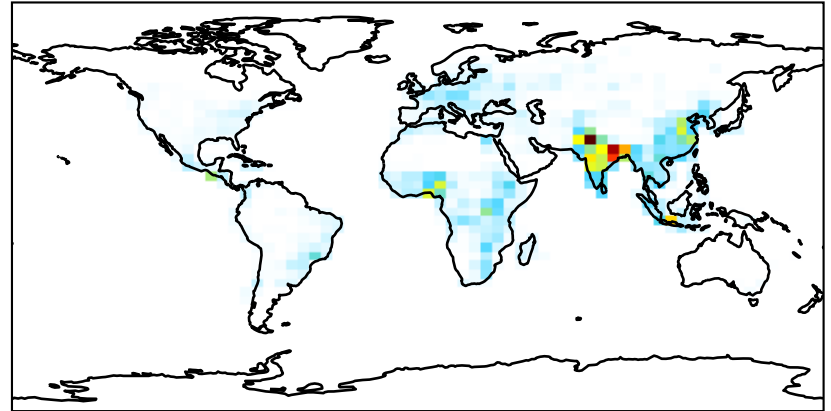


# EmisOCPO\_Anthro (Apr2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



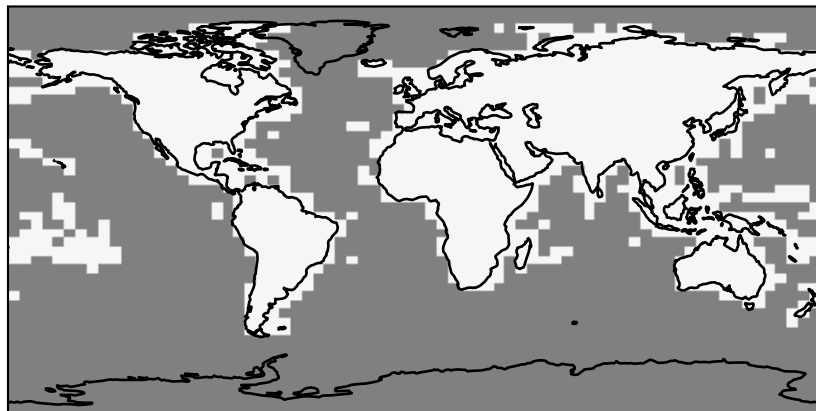
Difference  
Dev - Ref, Dynamic Range



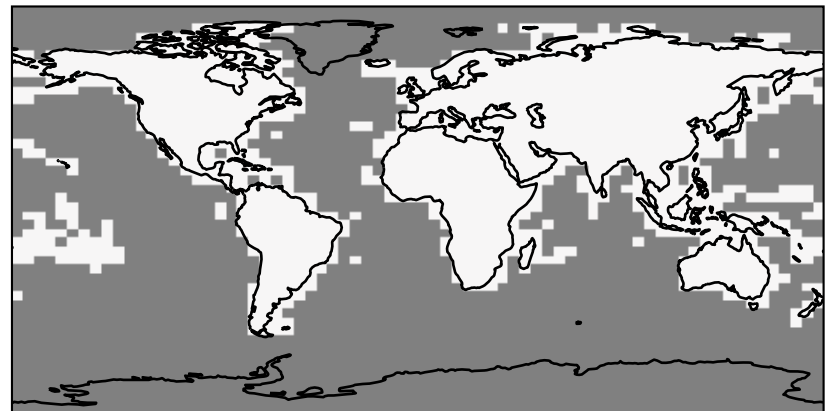
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

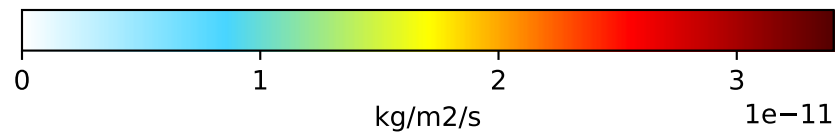
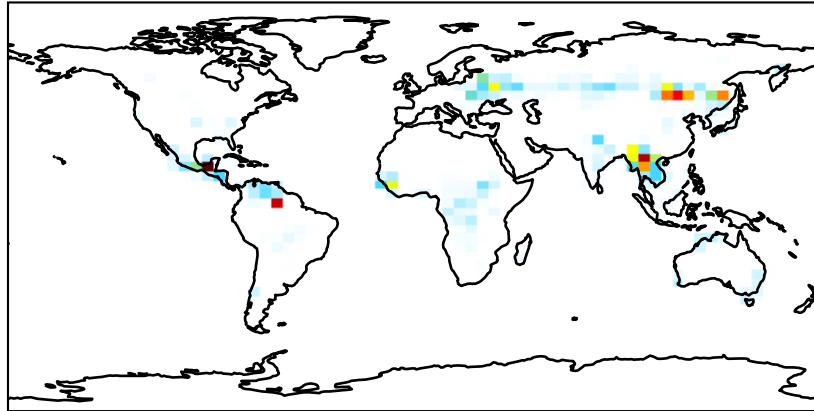


Ratio  
Dev/Ref, Fixed Range

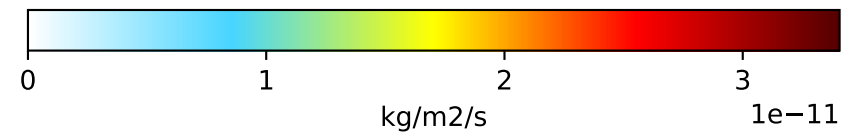
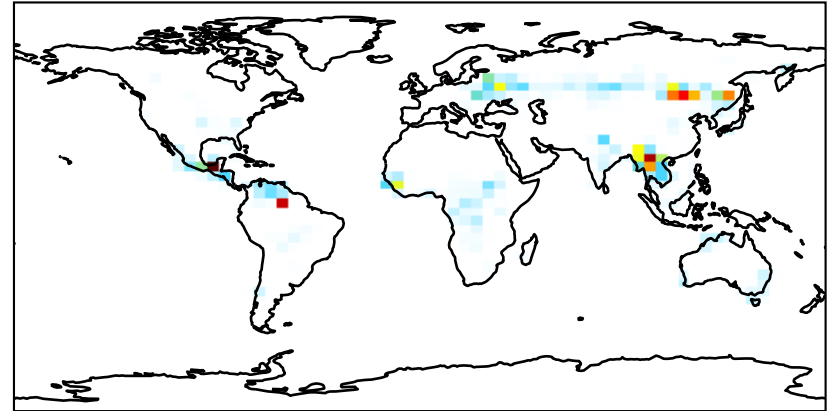


# EmisOCPO\_BioBurn (Apr2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



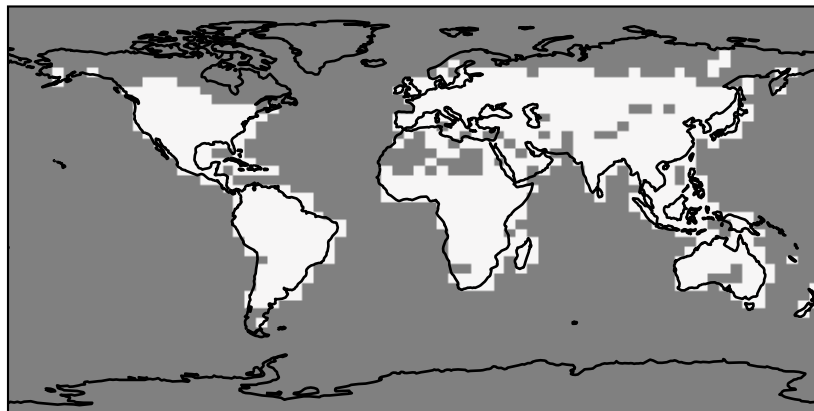
Difference  
Dev - Ref, Dynamic Range



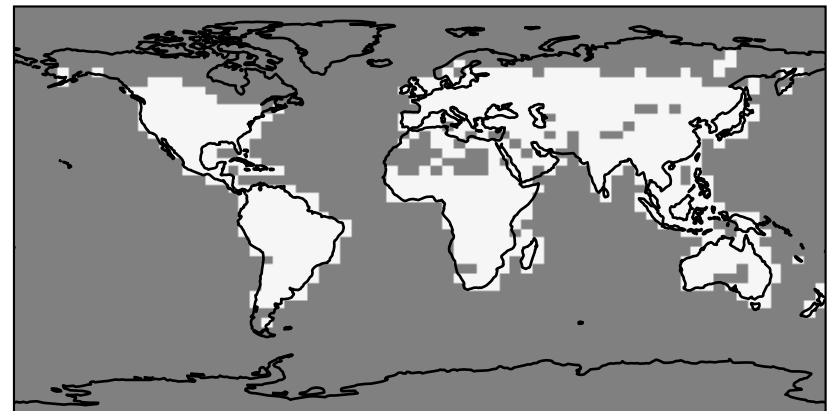
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

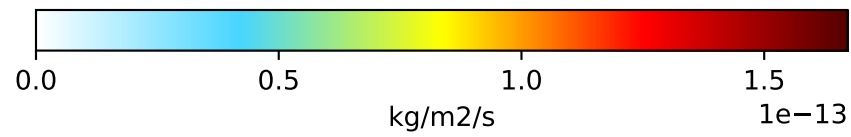
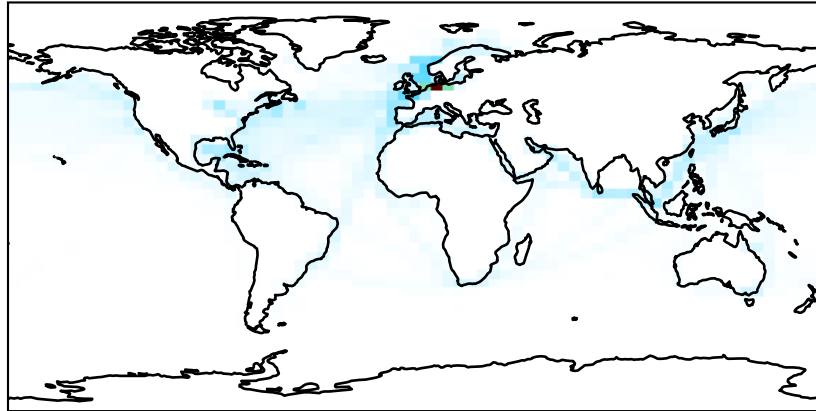


Ratio  
Dev/Ref, Fixed Range

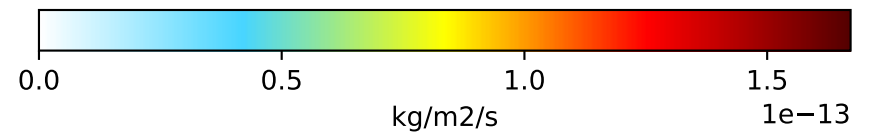
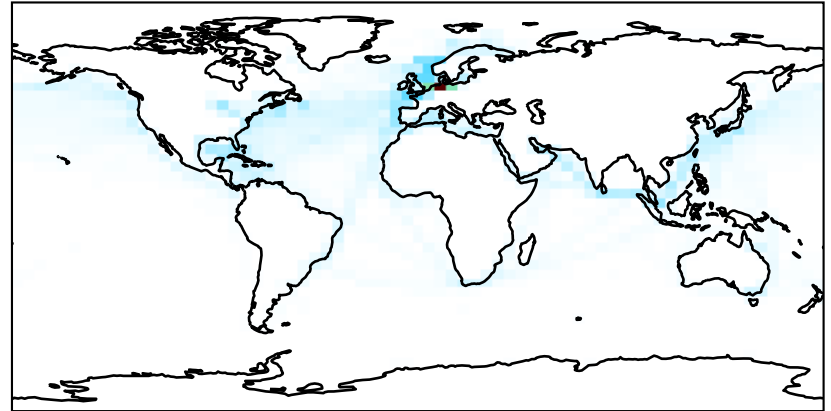


# EmisOCPO\_Ship (Apr2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



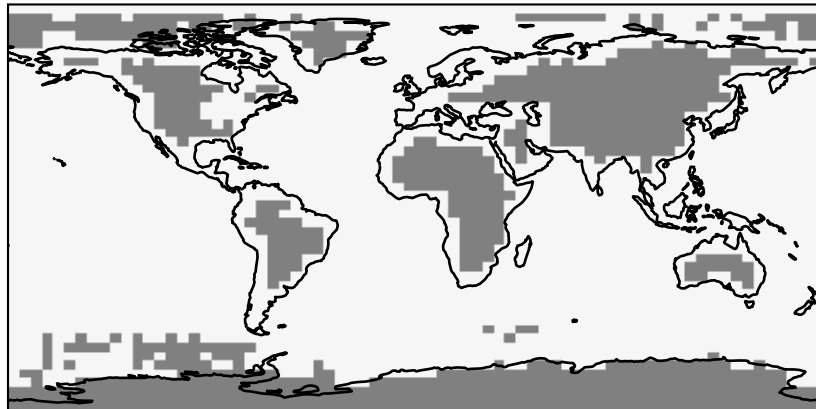
Difference  
Dev - Ref, Dynamic Range



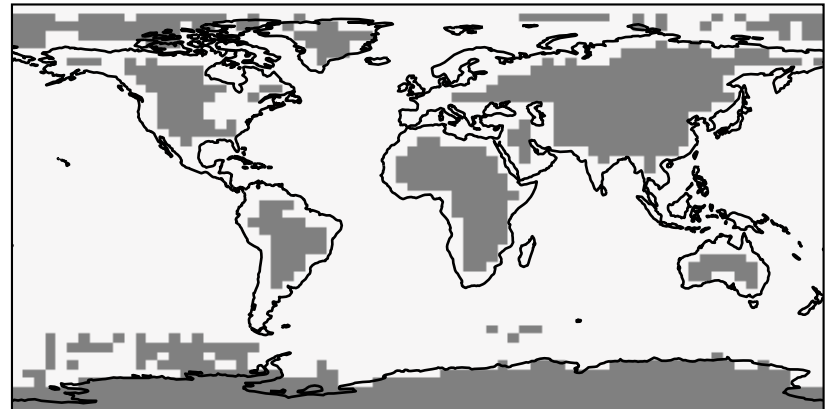
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

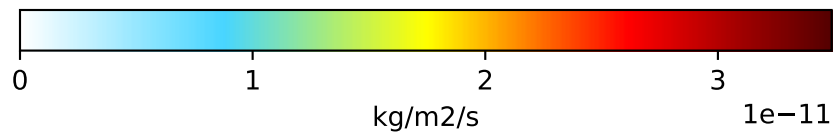
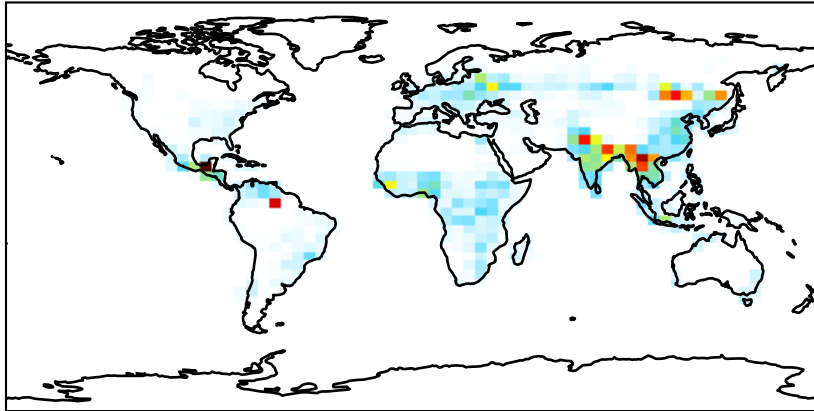


Ratio  
Dev/Ref, Fixed Range

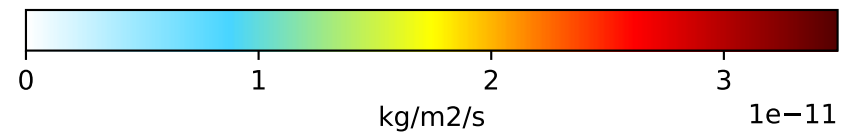
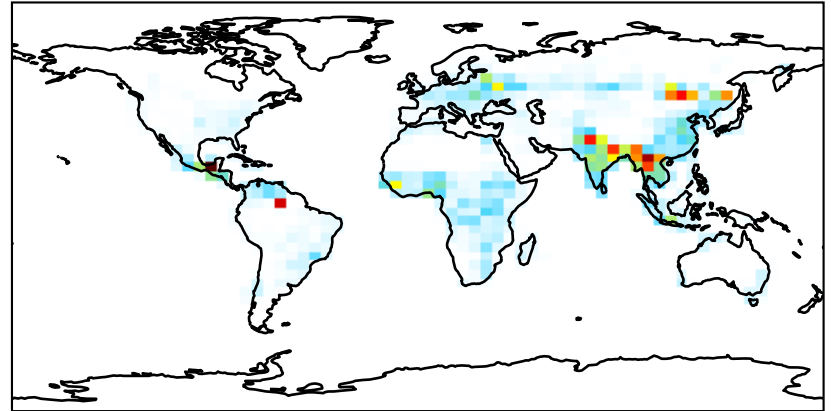


# EmisOCPO\_Total (Apr2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



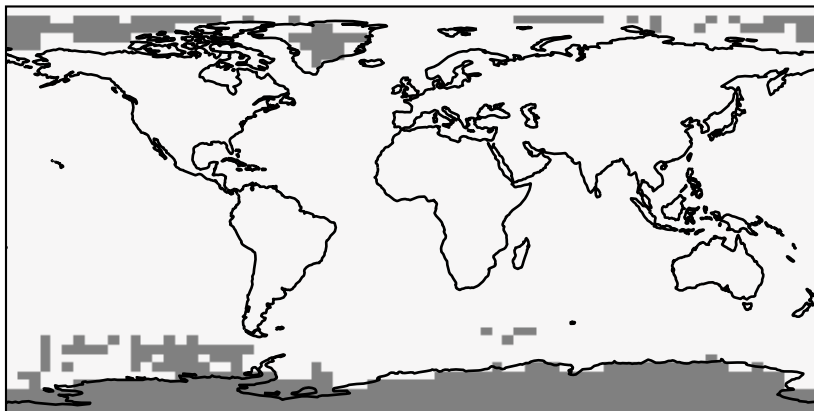
Difference  
Dev - Ref, Dynamic Range



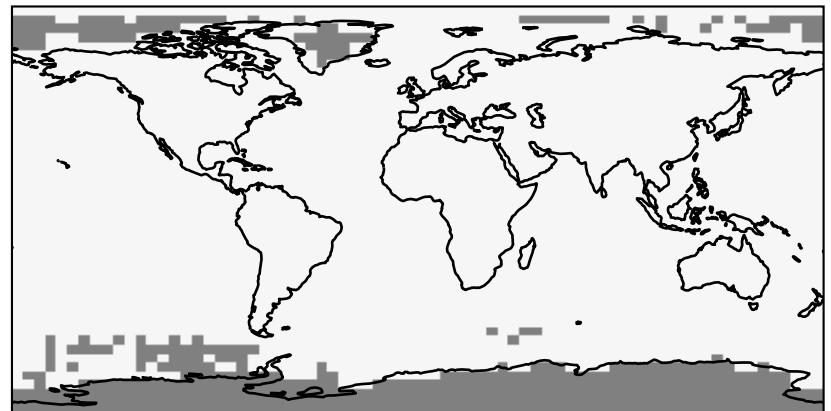
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

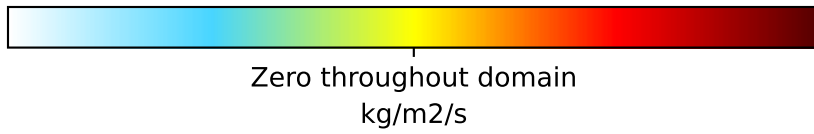


Ratio  
Dev/Ref, Fixed Range

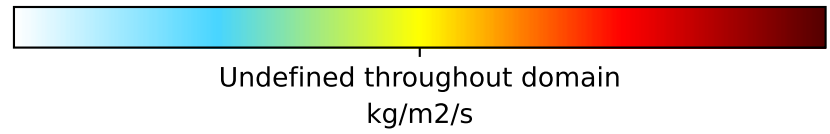
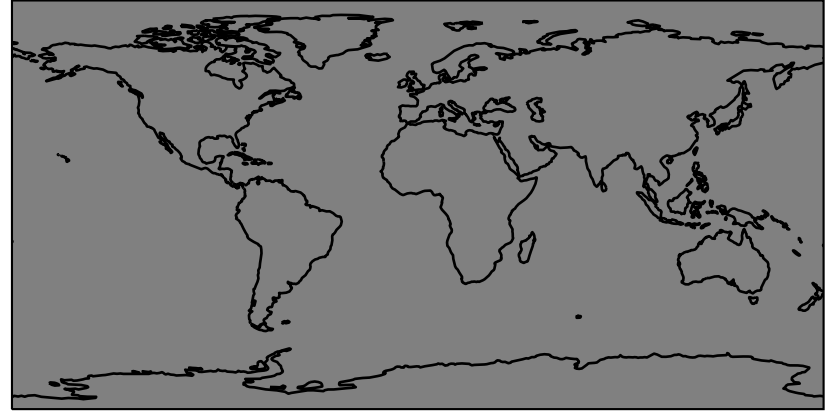


# EmisBrSALA\_Natural (Apr2019)

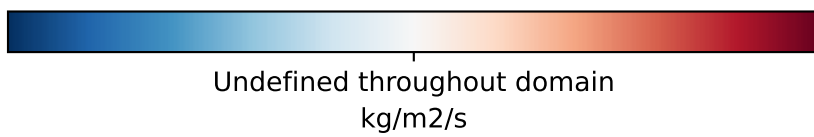
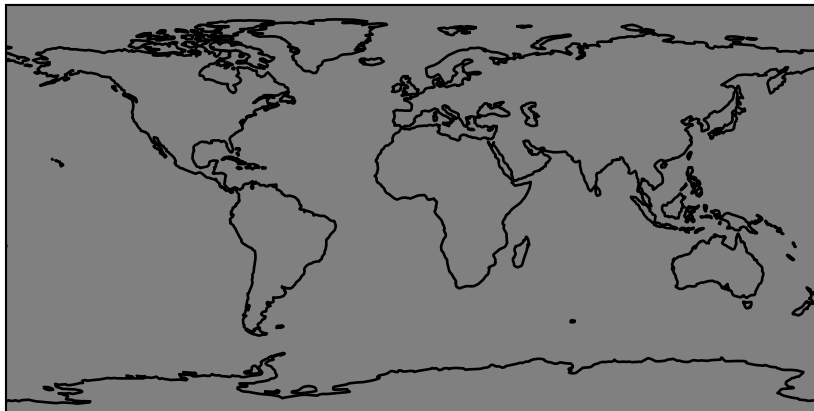
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



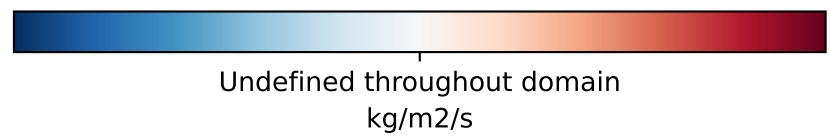
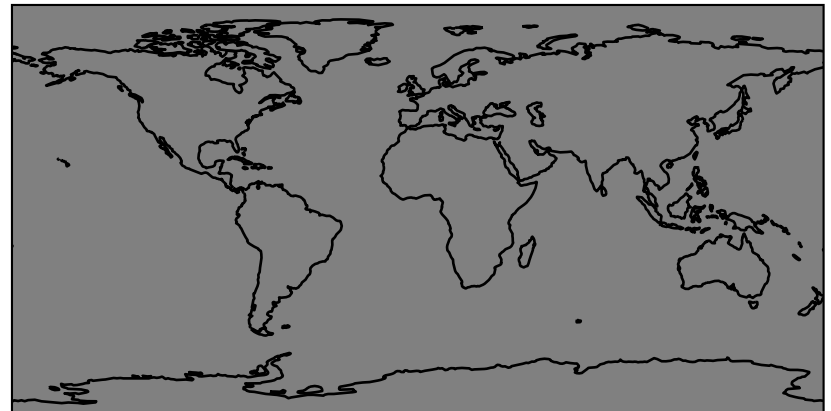
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



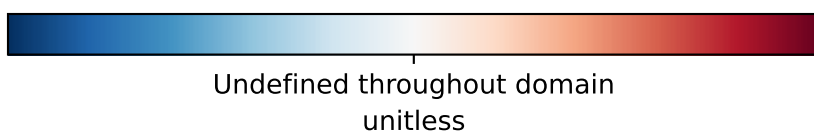
Difference  
Dev - Ref, Dynamic Range



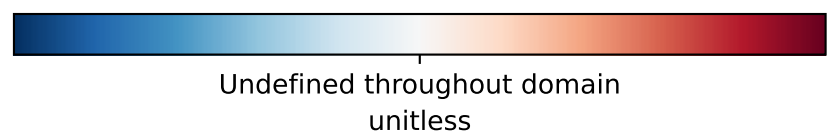
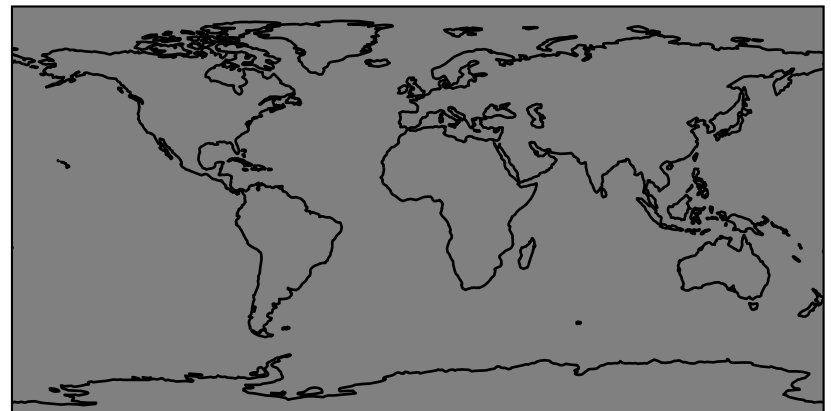
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range



# EmisBrSALC\_Natural (Apr2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



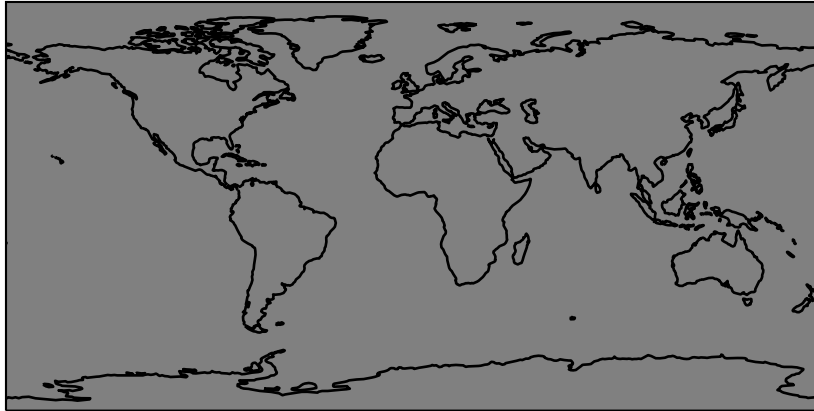
Zero throughout domain  
kg/m2/s

GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



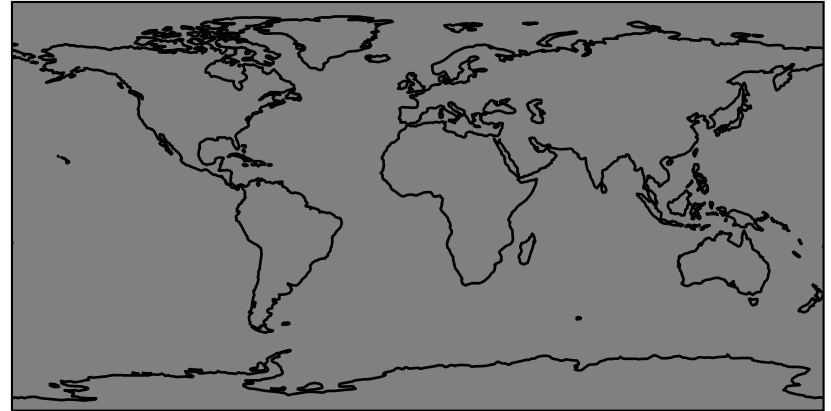
Undefined throughout domain  
kg/m2/s

Difference  
Dev - Ref, Dynamic Range



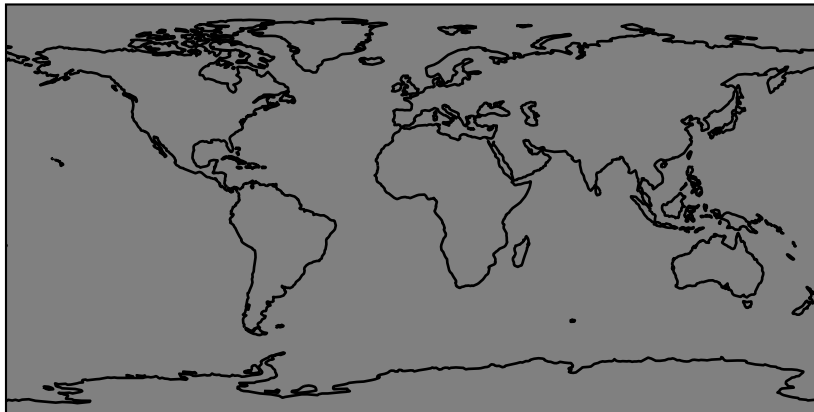
Undefined throughout domain  
kg/m2/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



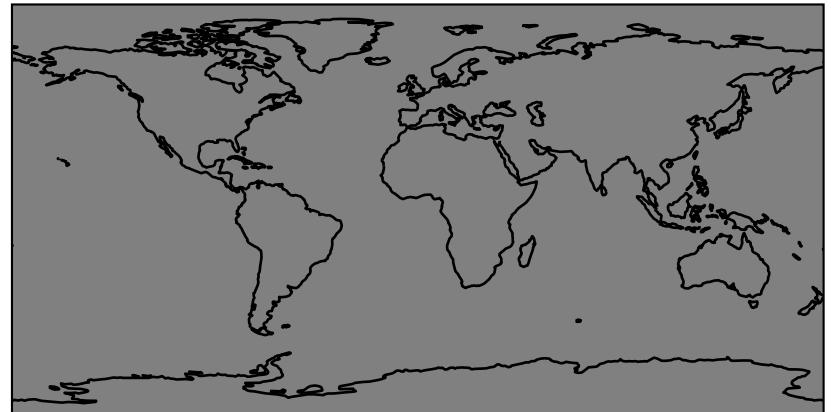
Undefined throughout domain  
kg/m2/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

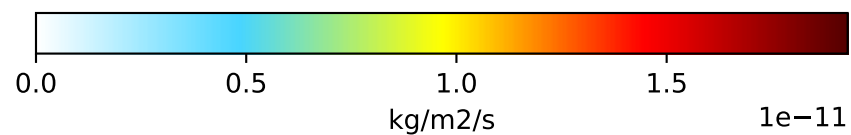
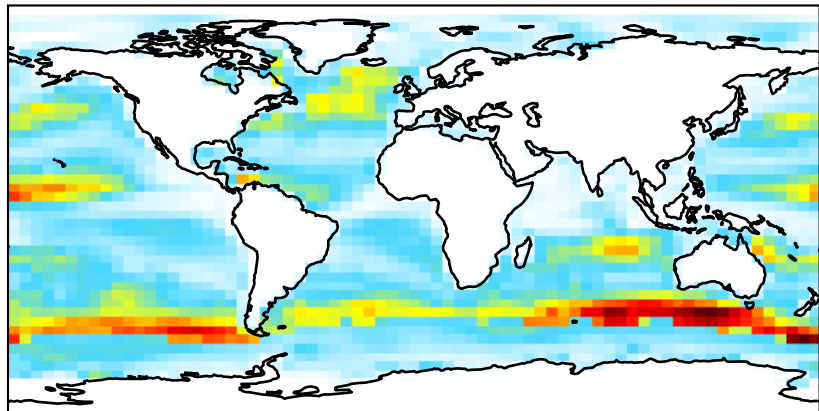
Ratio  
Dev/Ref, Fixed Range



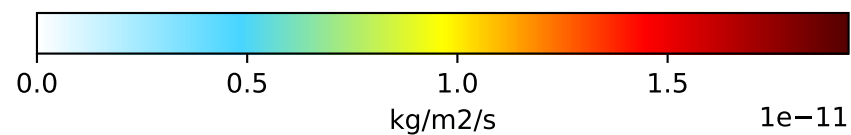
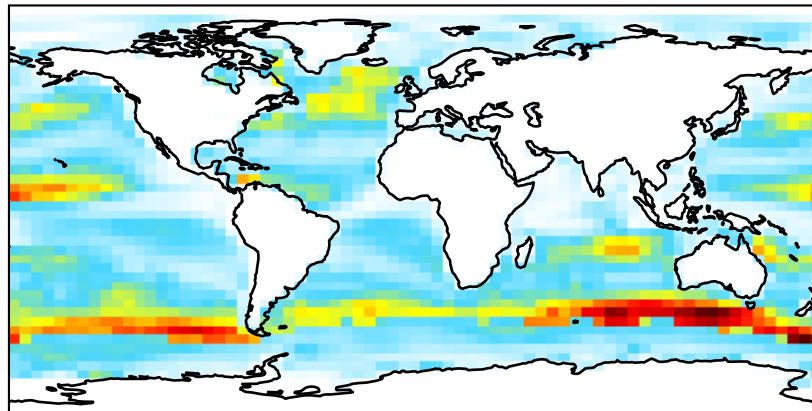
Undefined throughout domain  
unitless

# EmisSALA\_Natural (Apr2019)

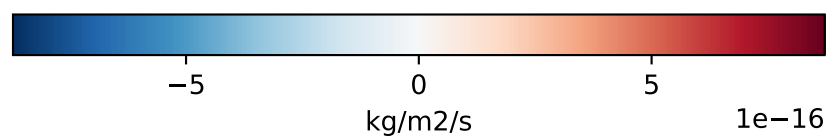
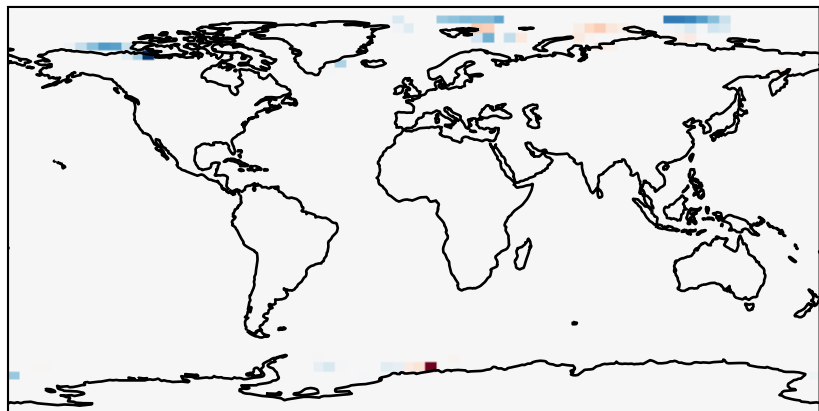
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



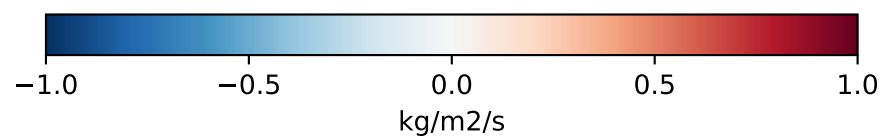
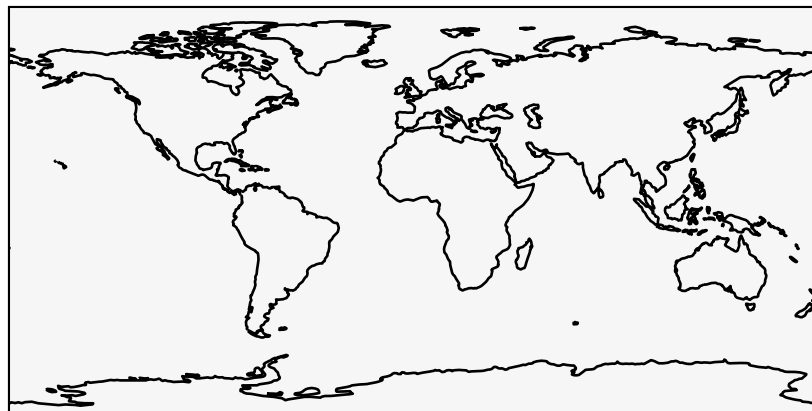
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



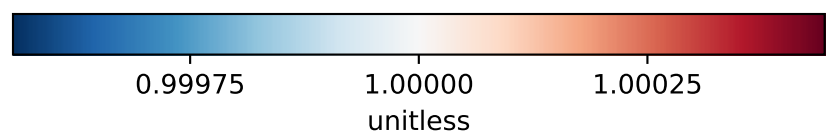
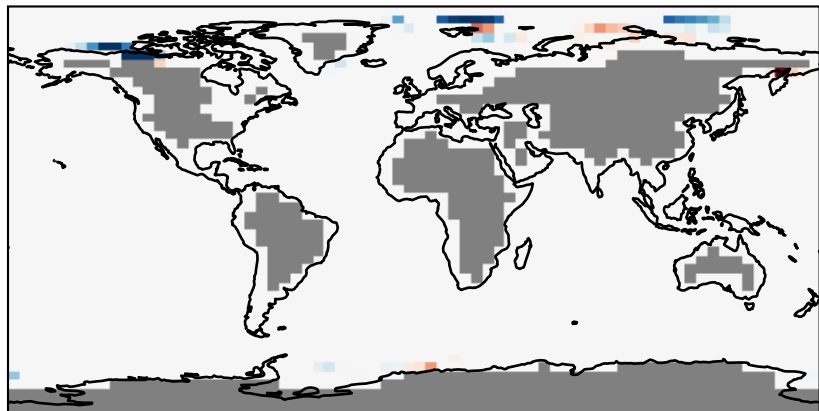
Difference  
Dev - Ref, Dynamic Range



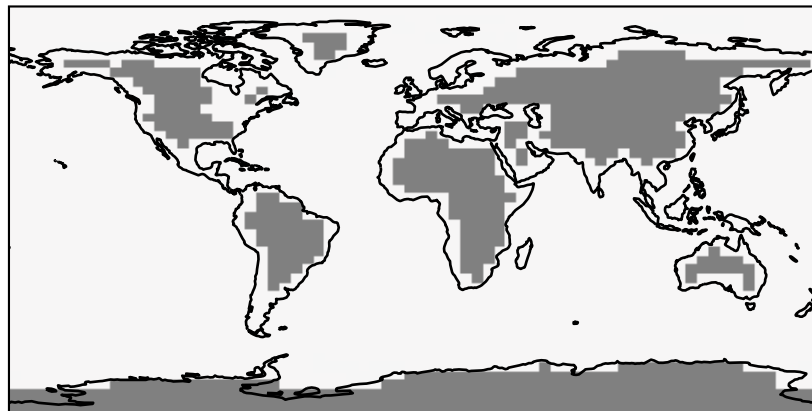
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

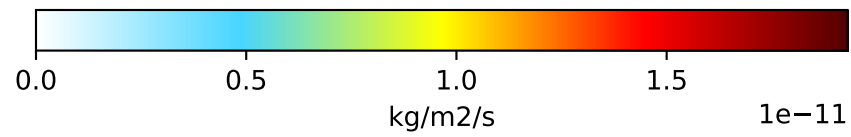
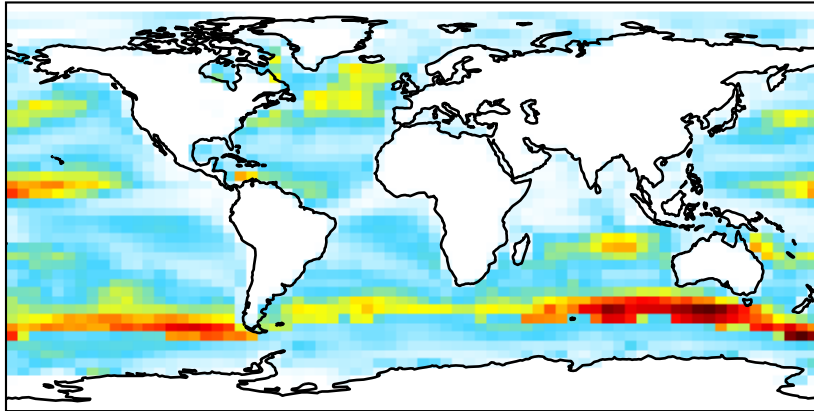


Ratio  
Dev/Ref, Fixed Range

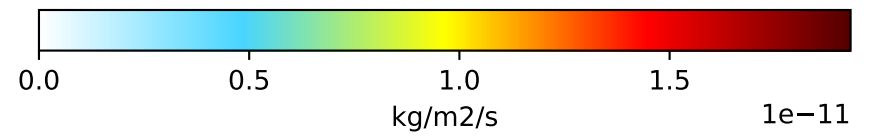
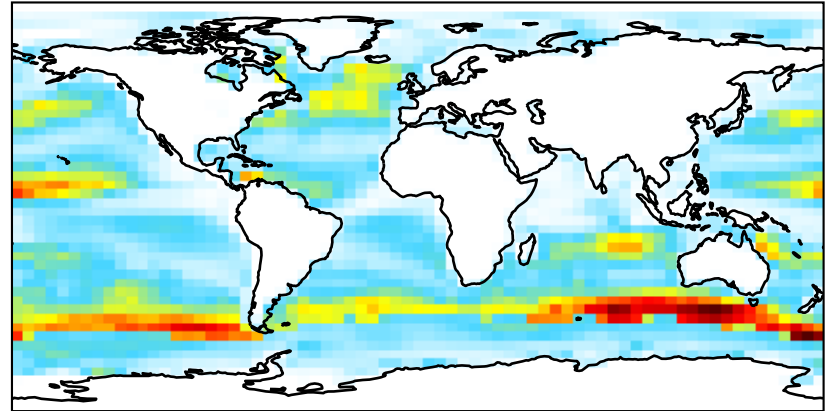


# EmisSALAAL\_Natural (Apr2019)

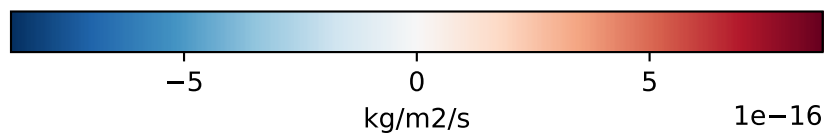
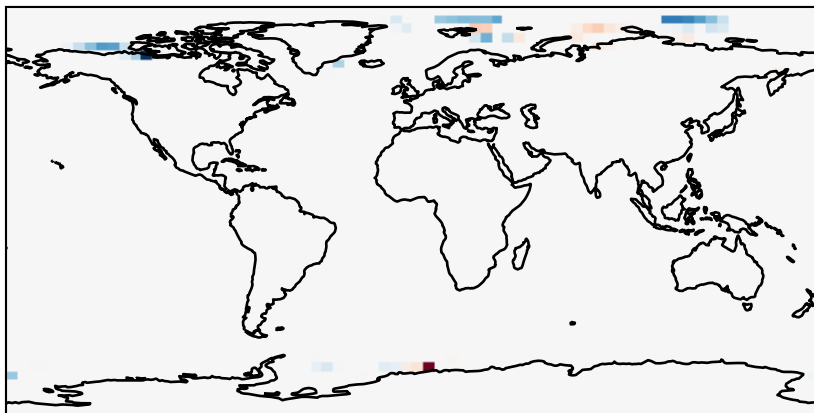
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



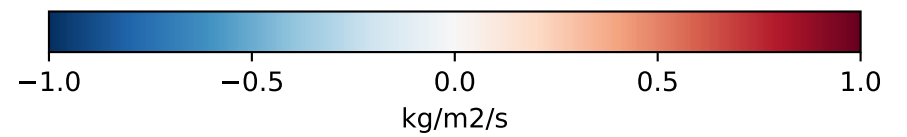
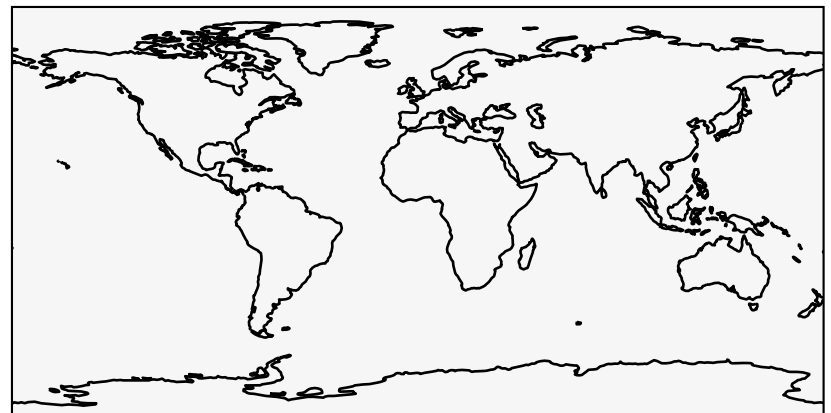
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



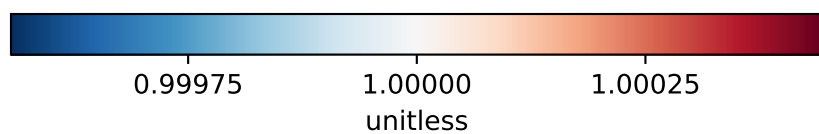
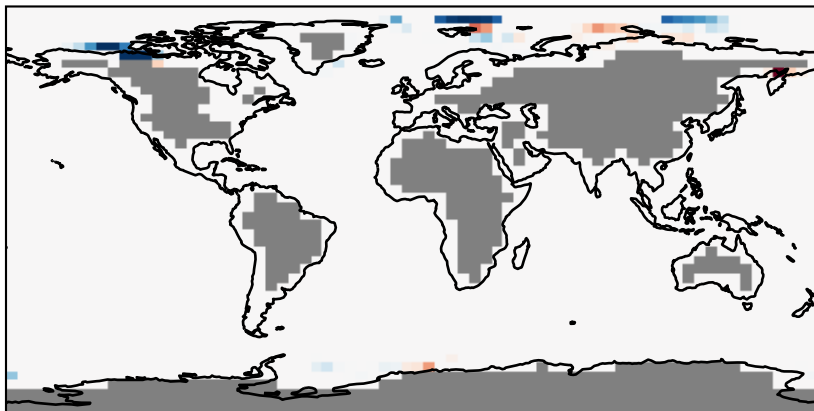
Difference  
Dev - Ref, Dynamic Range



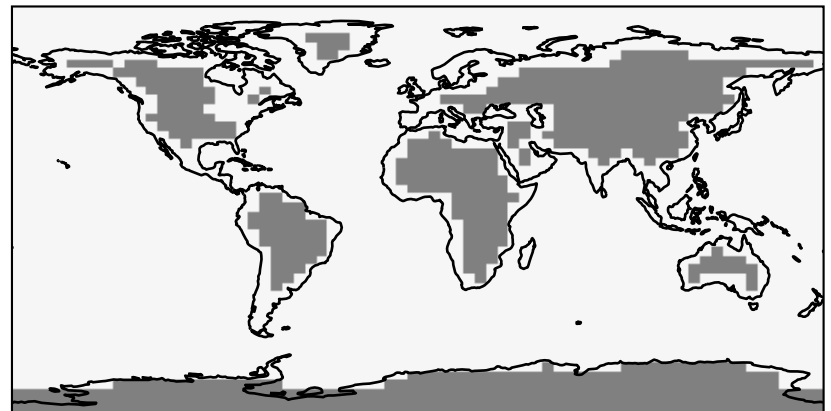
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



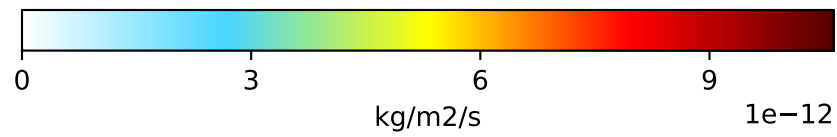
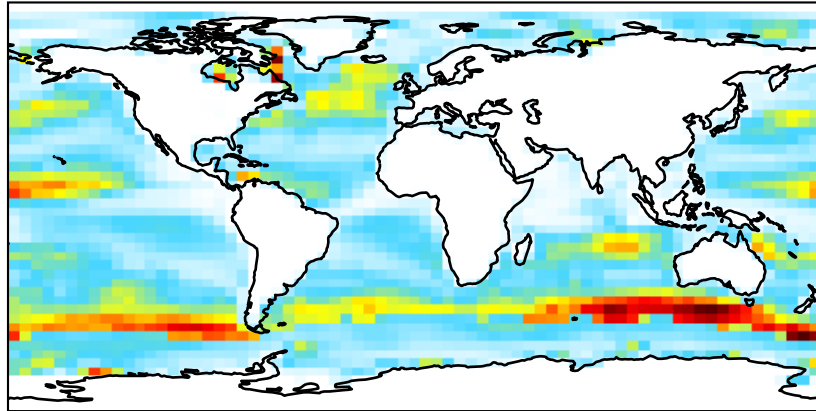
Ratio  
Dev/Ref, Fixed Range



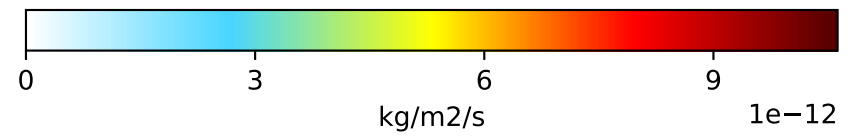
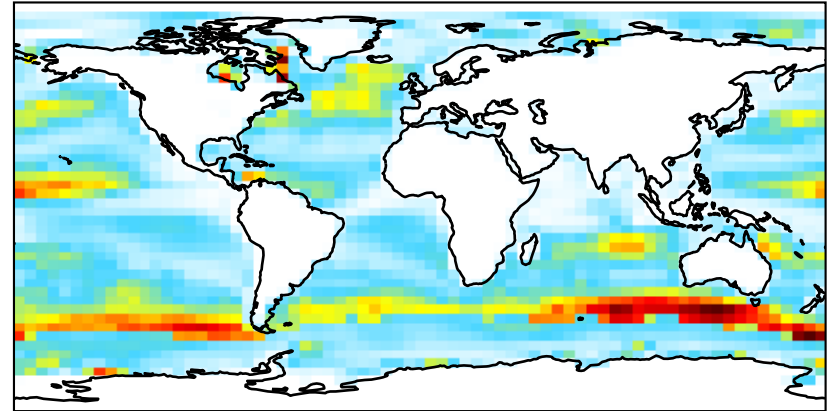


# EmisSALACL\_Natural (Apr2019)

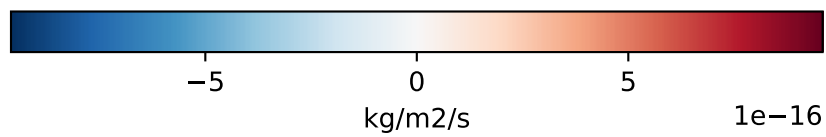
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



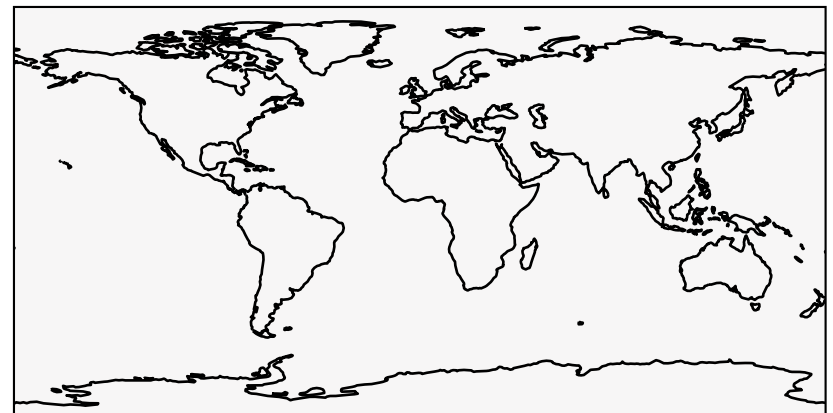
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



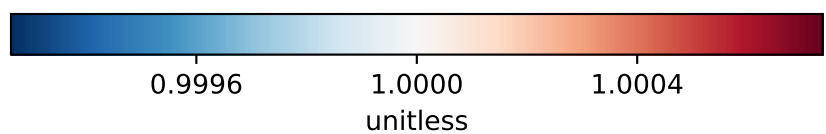
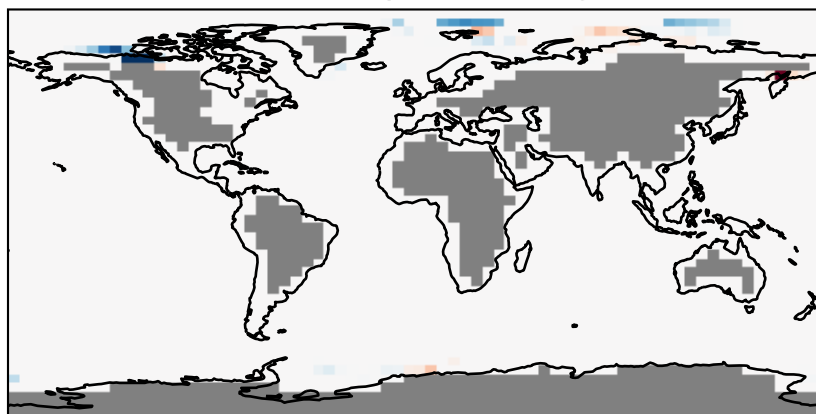
Difference  
Dev - Ref, Dynamic Range



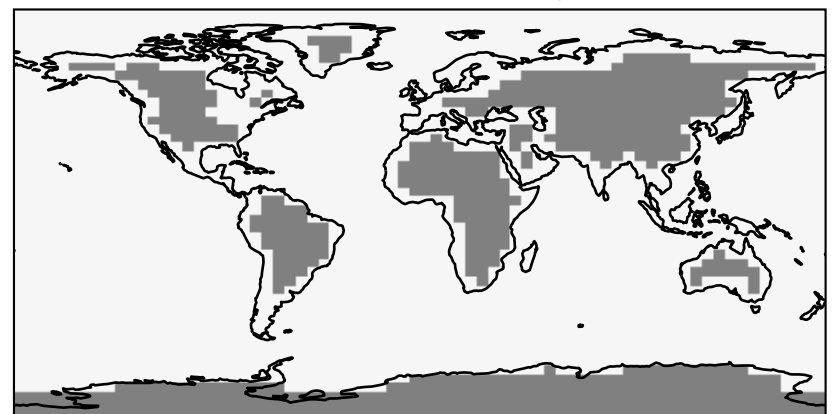
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

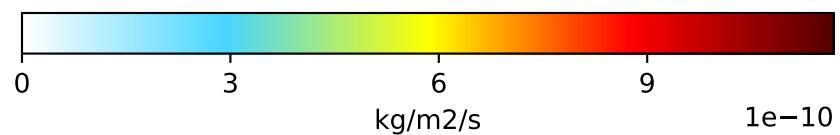
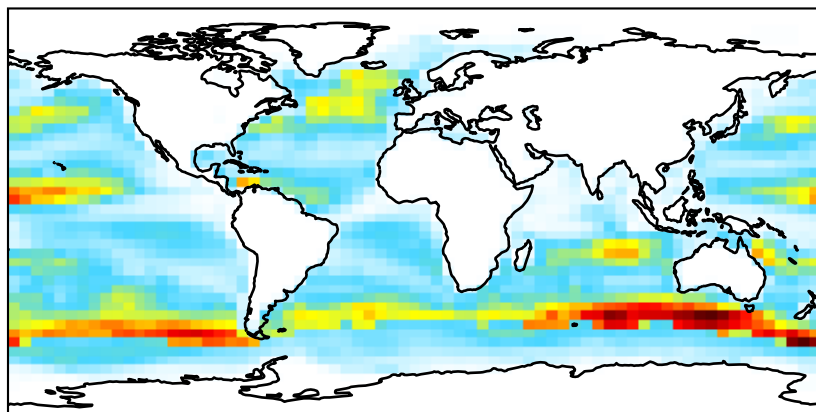


Ratio  
Dev/Ref, Fixed Range

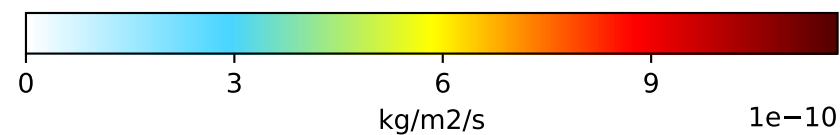
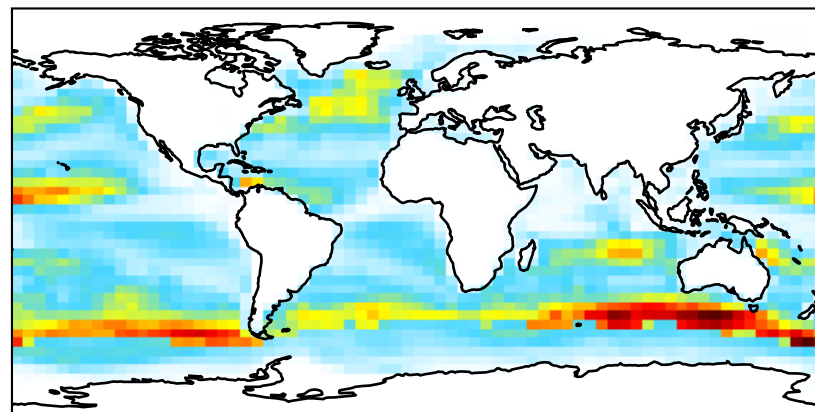


# EmisSALC\_Natural (Apr2019)

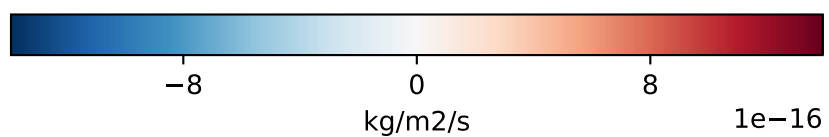
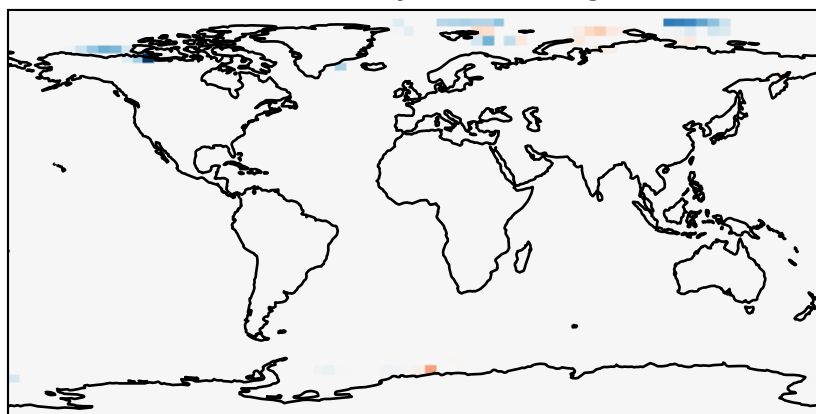
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



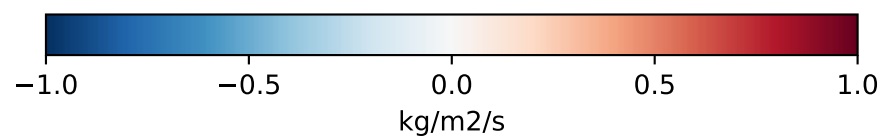
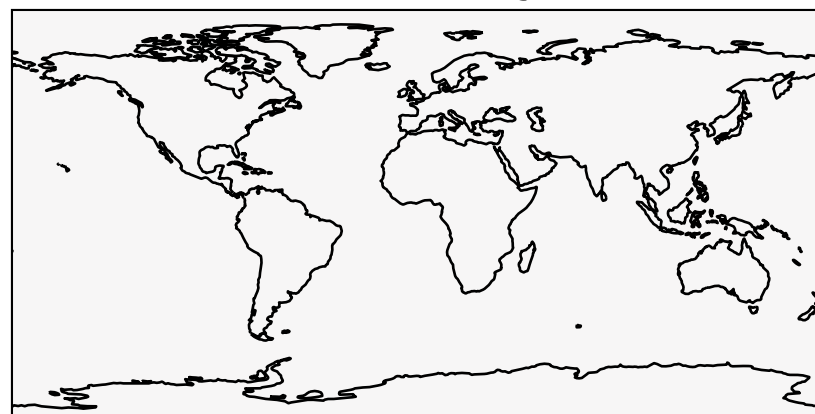
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



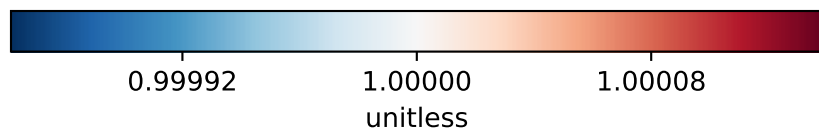
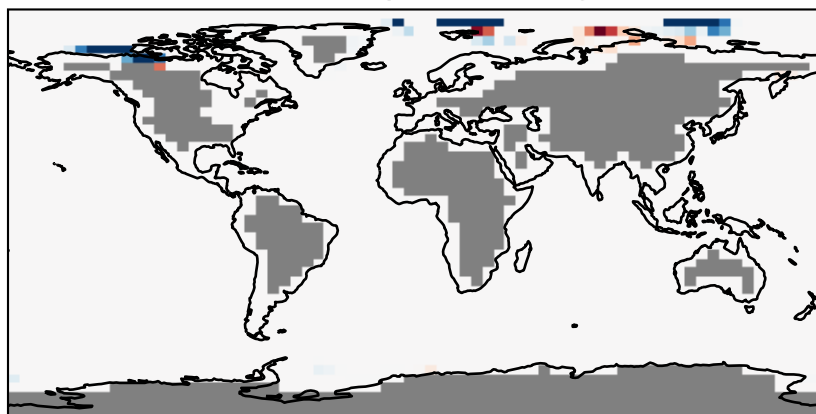
Difference  
Dev - Ref, Dynamic Range



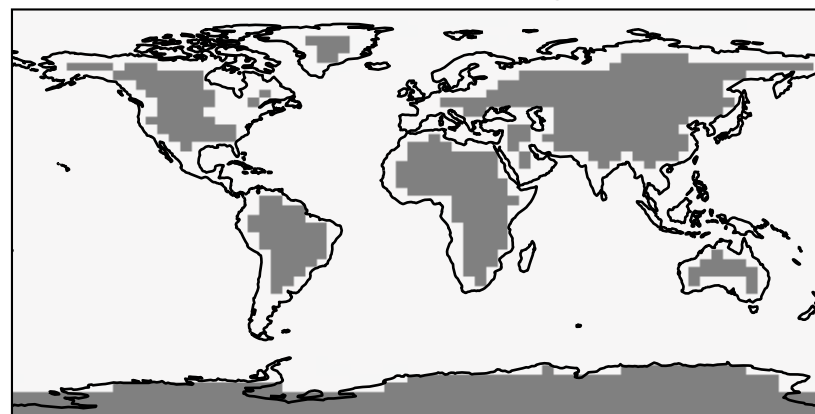
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

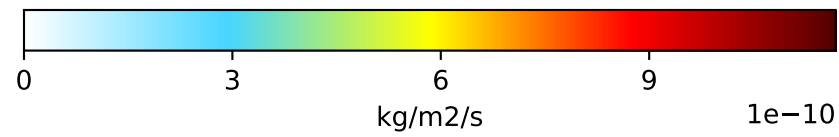
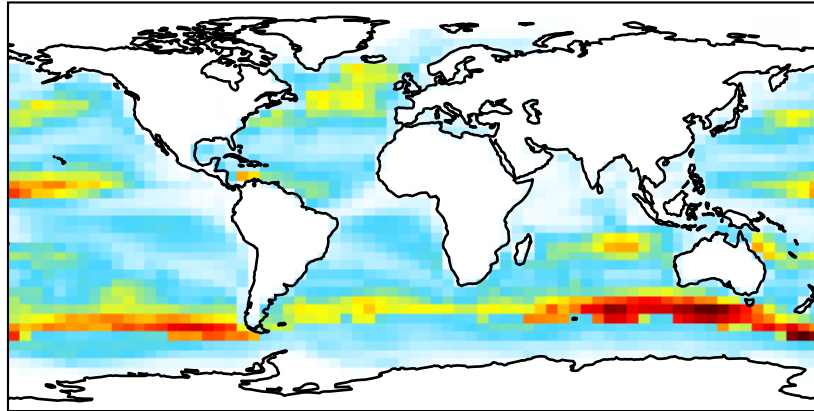


Ratio  
Dev/Ref, Fixed Range

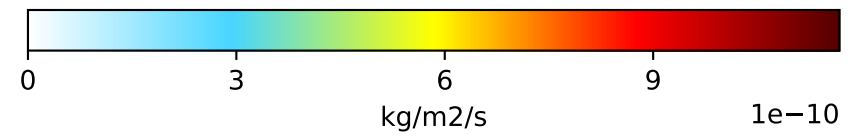
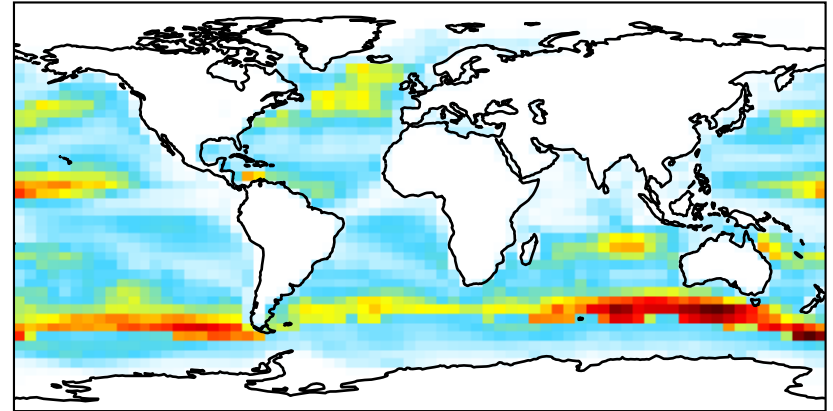


# EmisSALCAL\_Natural (Apr2019)

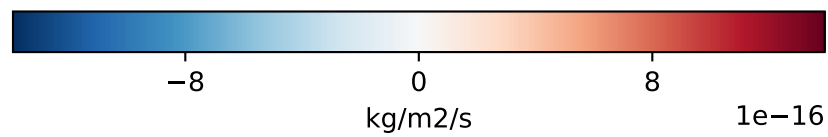
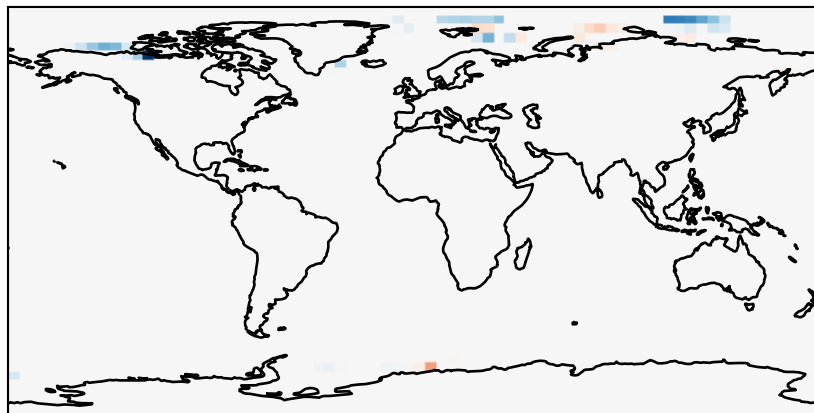
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



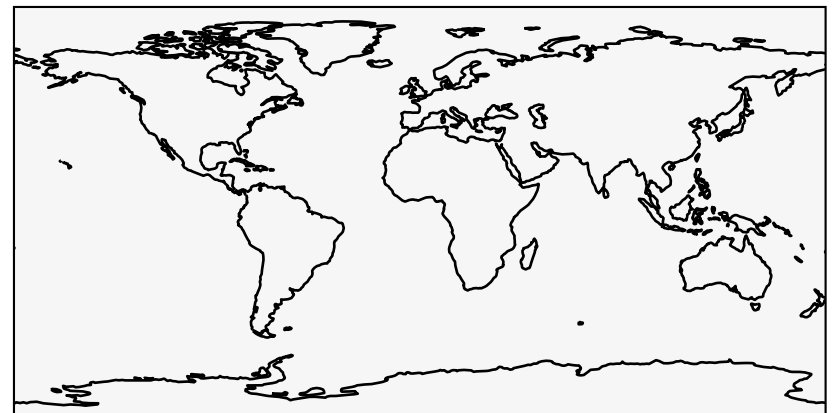
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



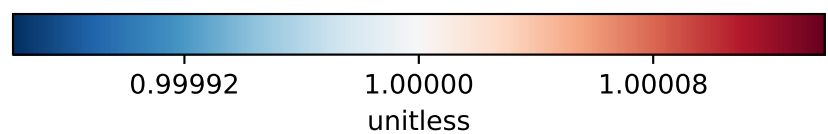
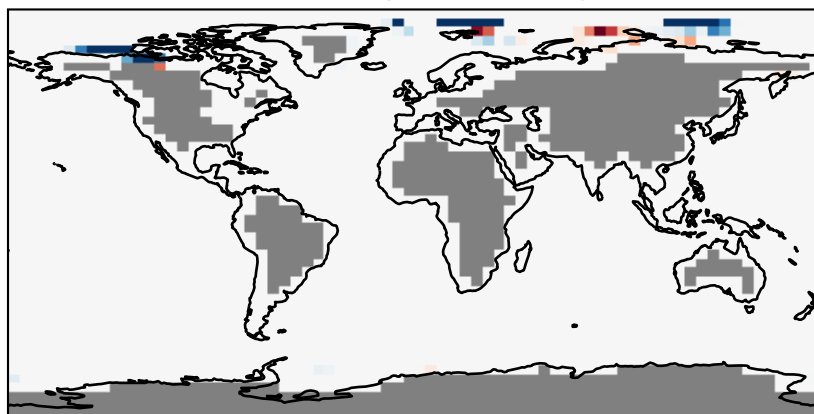
Difference  
Dev - Ref, Dynamic Range



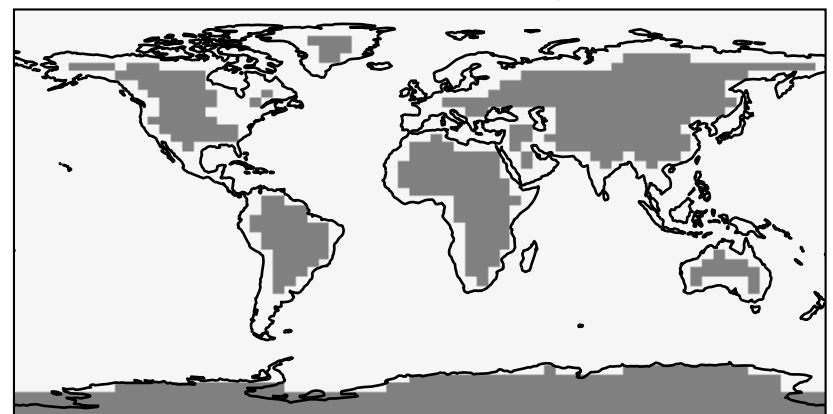
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

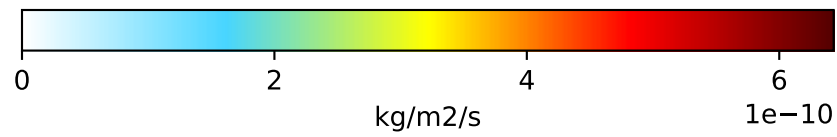
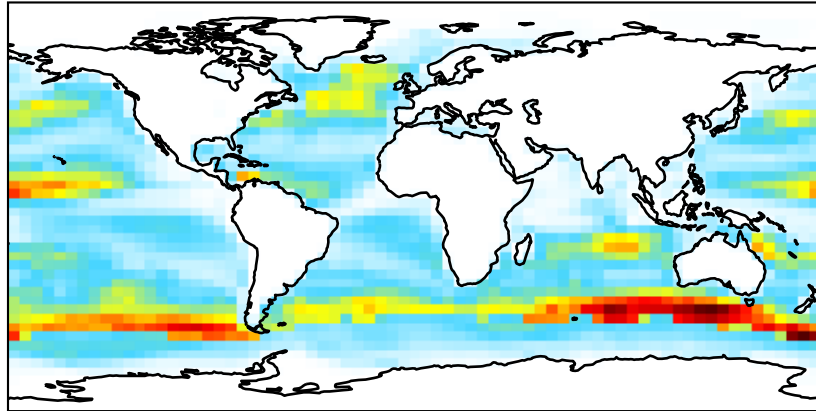


Ratio  
Dev/Ref, Fixed Range

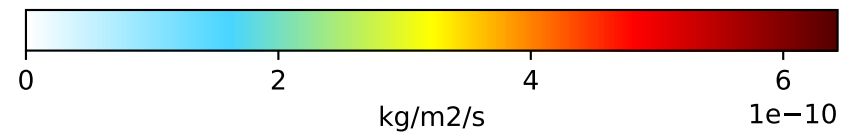
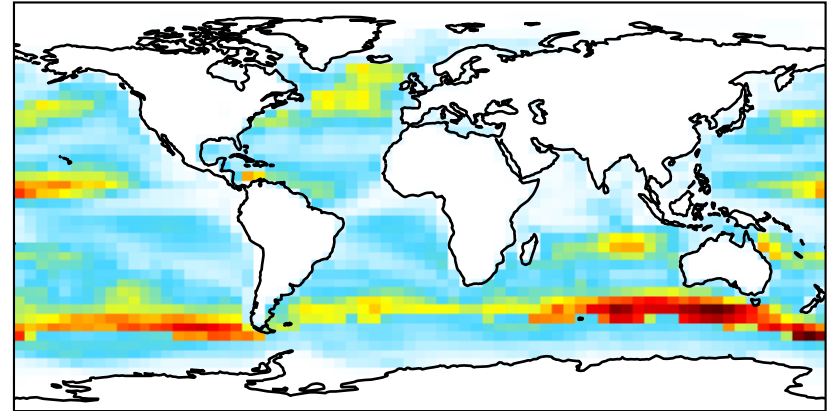


# EmisSALCCL\_Natural (Apr2019)

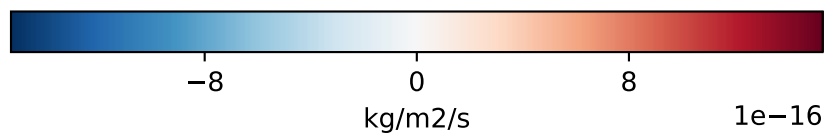
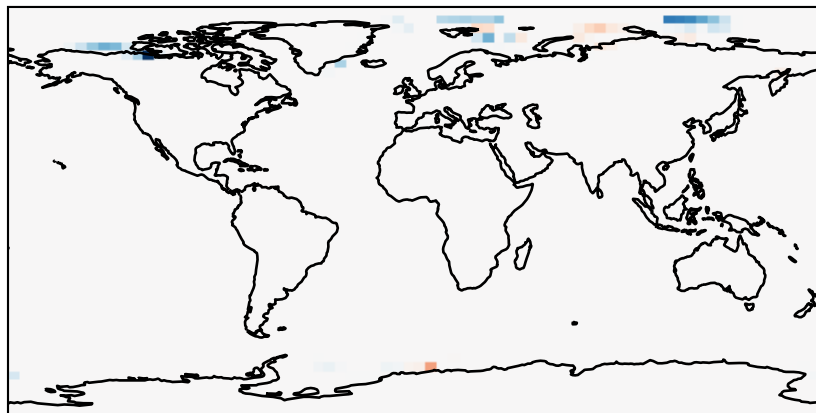
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



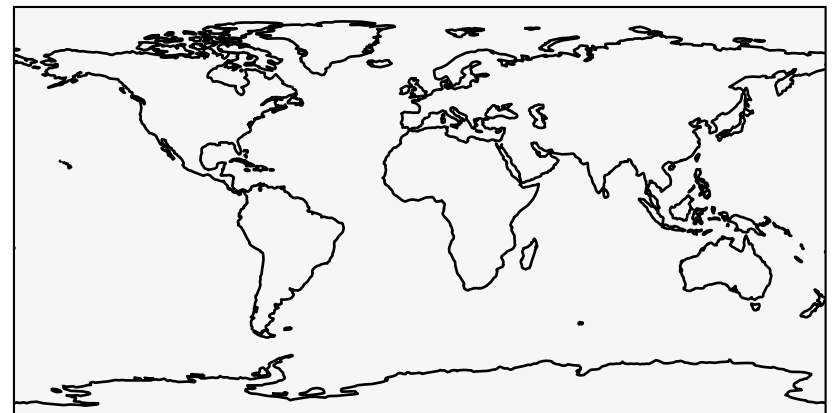
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



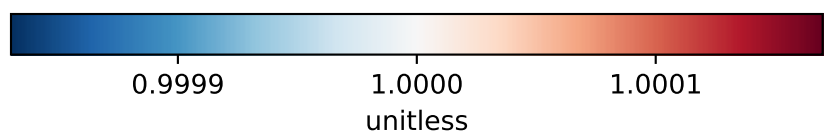
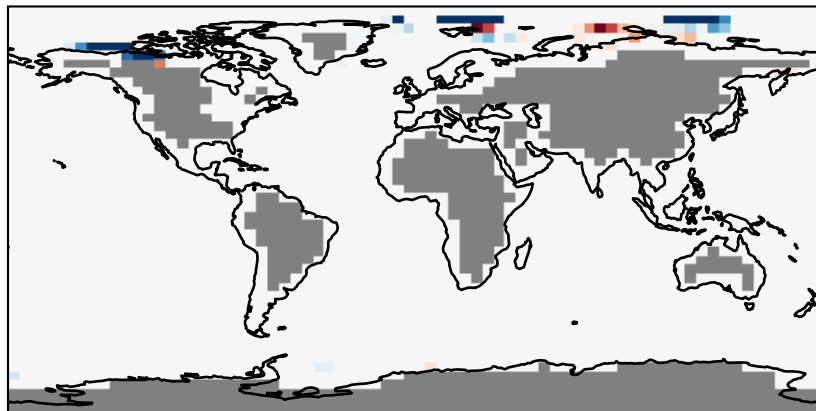
Difference  
Dev - Ref, Dynamic Range



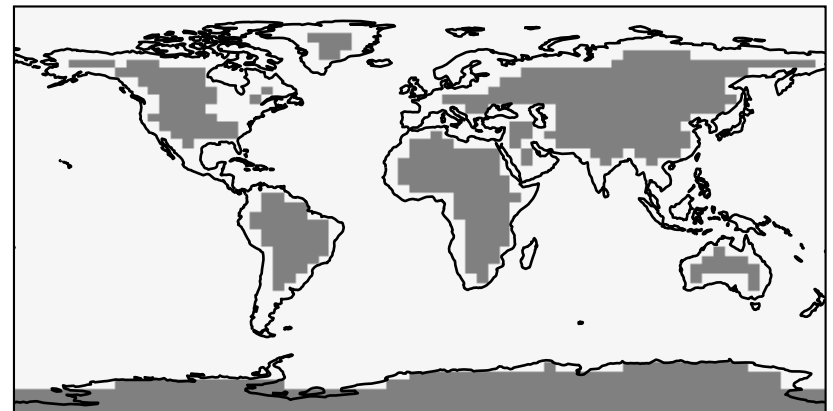
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

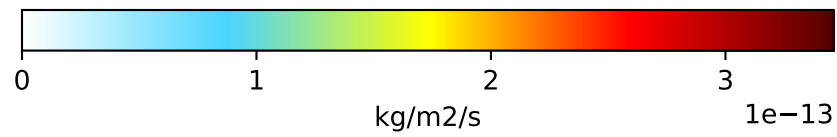
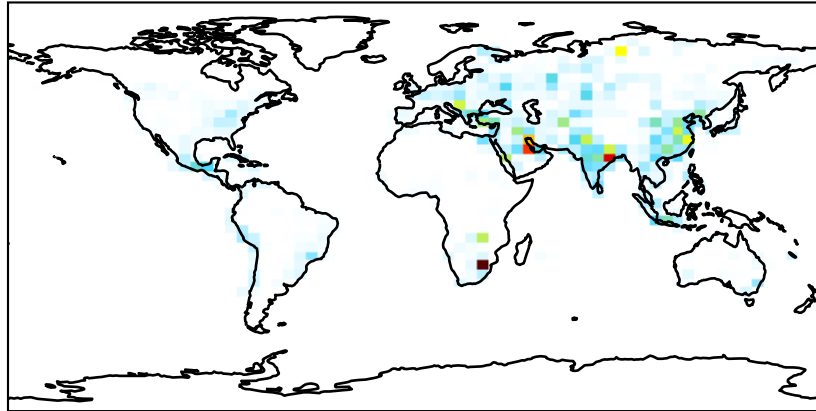


Ratio  
Dev/Ref, Fixed Range

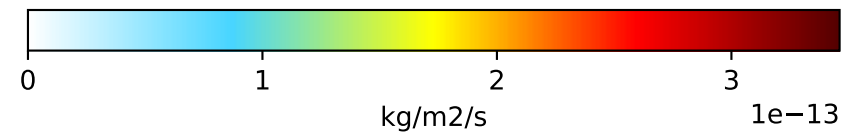
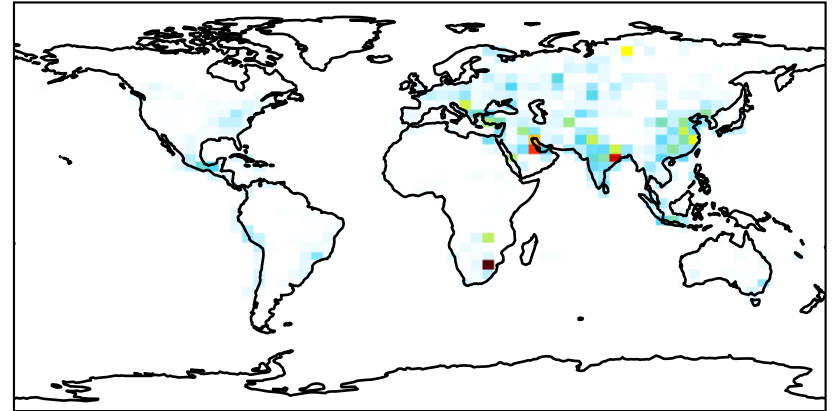


# EmispFe\_Anthro (Apr2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



Difference  
Dev - Ref, Dynamic Range



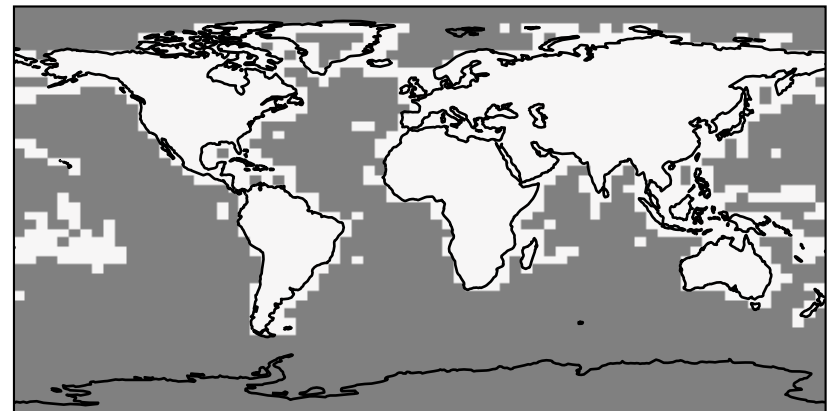
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

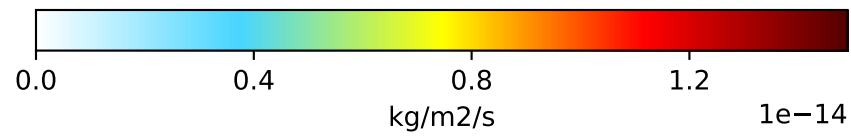
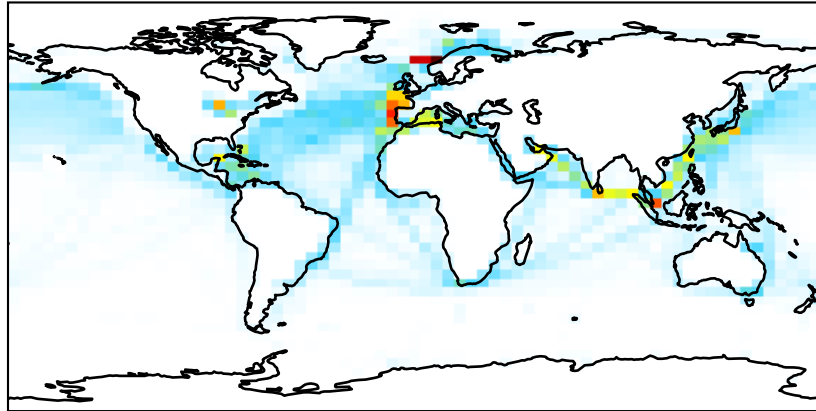


Ratio  
Dev/Ref, Fixed Range

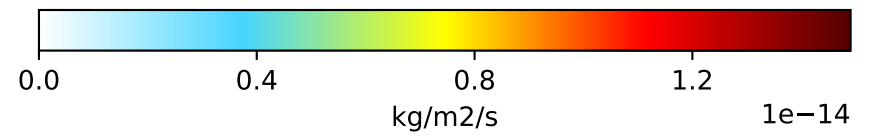
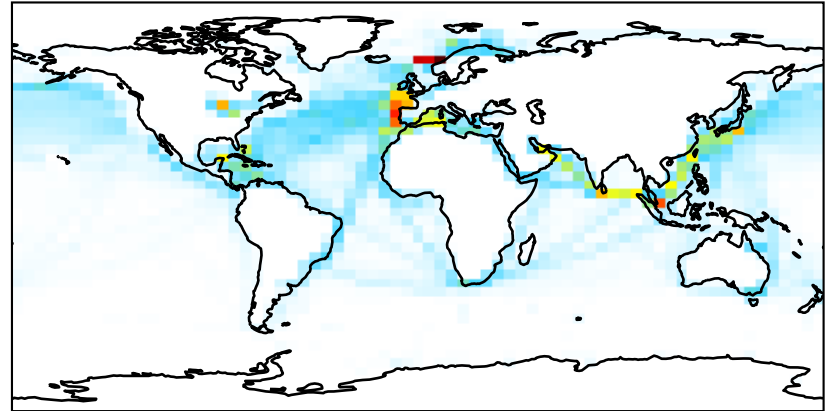


# EmispFe\_Ship (Apr2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



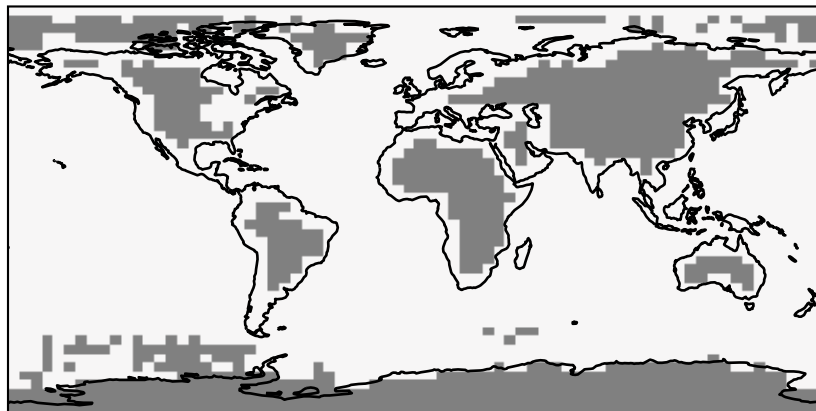
Difference  
Dev - Ref, Dynamic Range



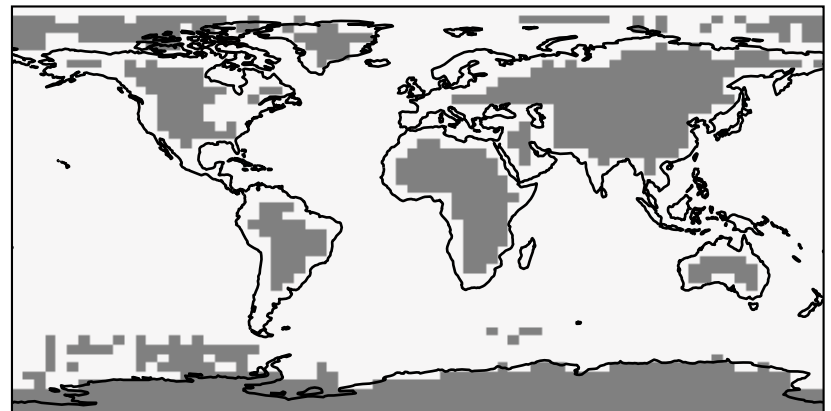
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

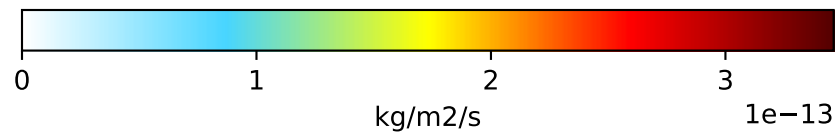
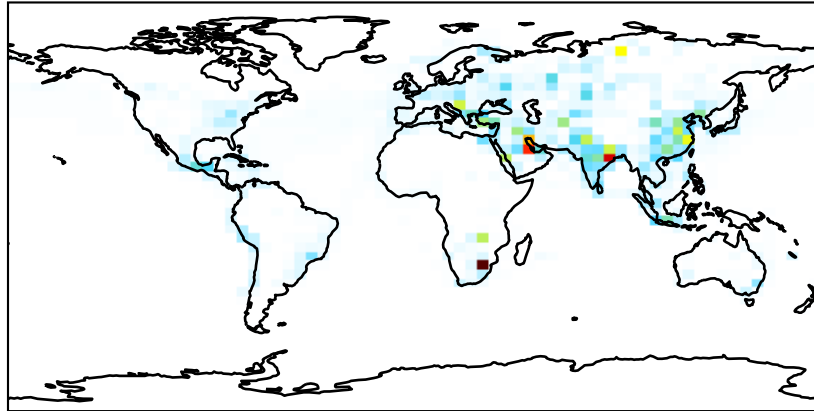


Ratio  
Dev/Ref, Fixed Range

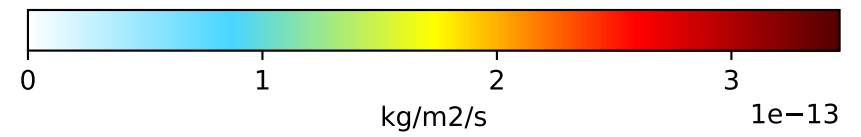
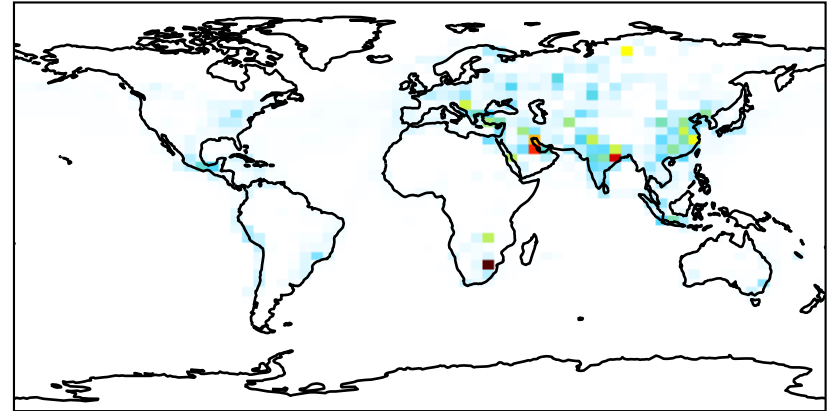


# EmispFe\_Total (Apr2019)

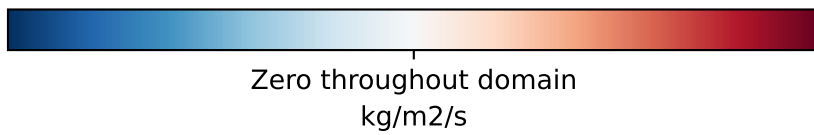
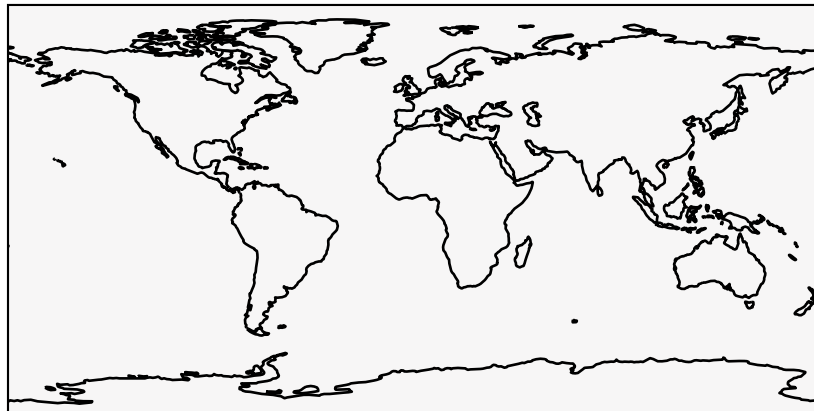
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



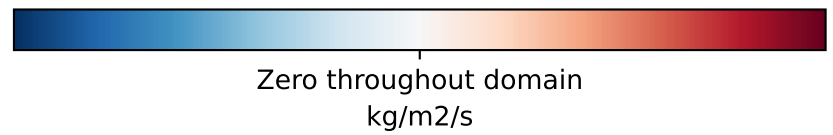
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



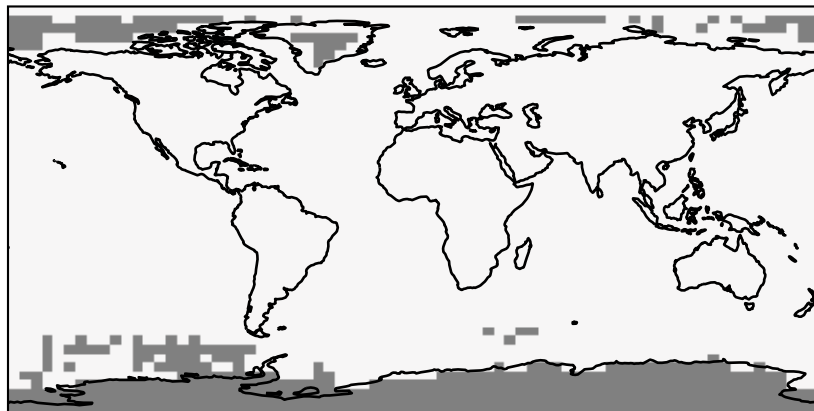
Difference  
Dev - Ref, Dynamic Range



Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

