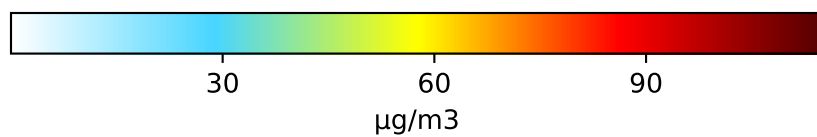
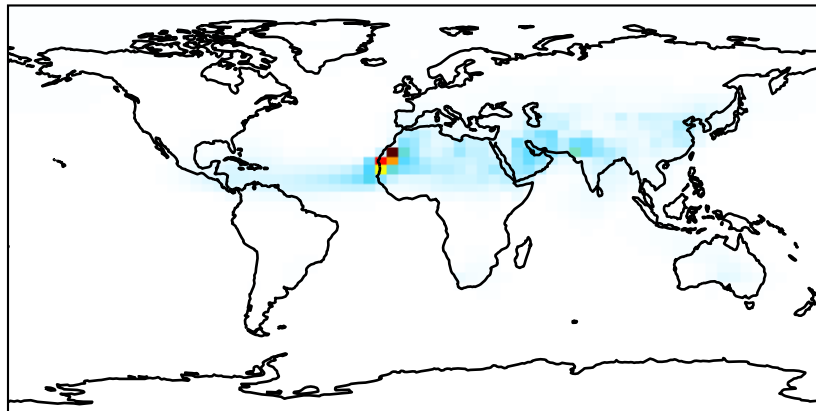
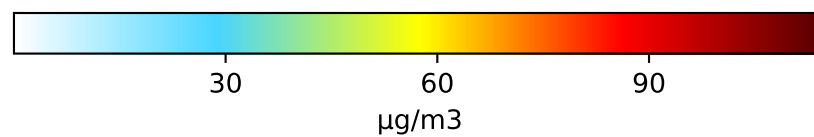
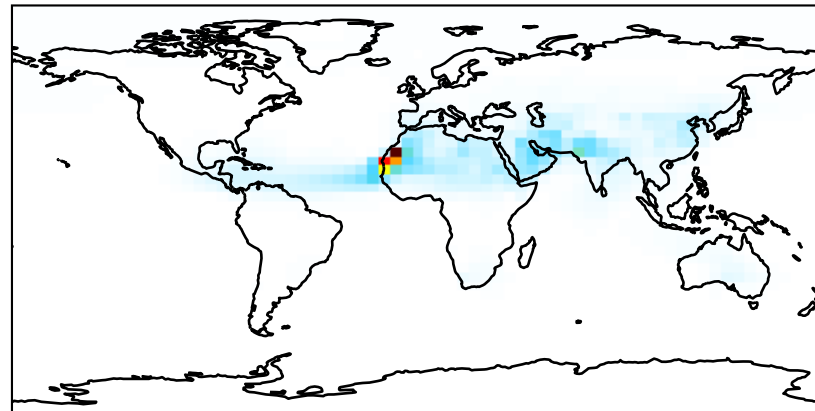


SpeciesConc_DST1 (Jul2019)

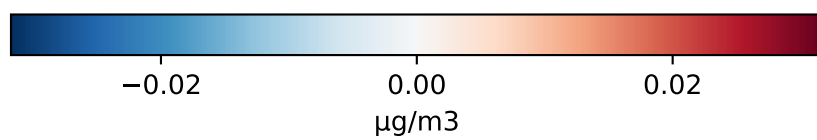
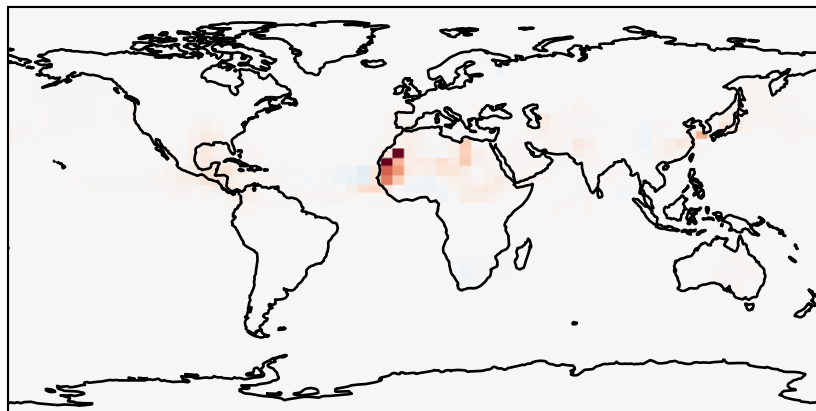
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



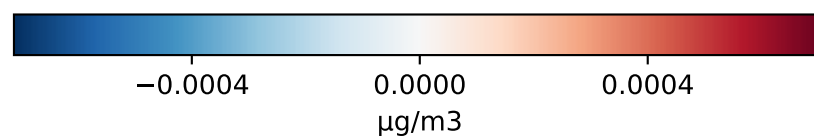
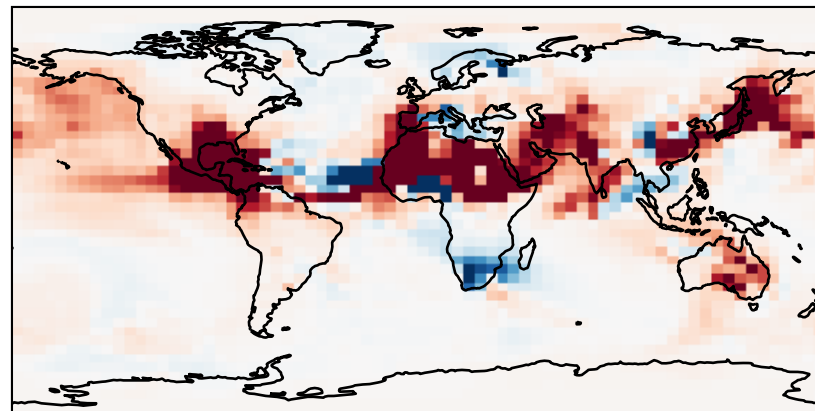
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



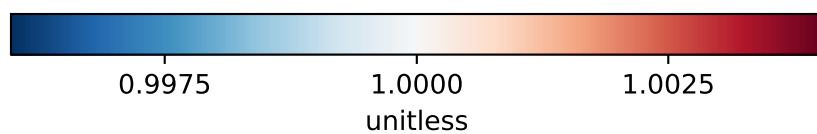
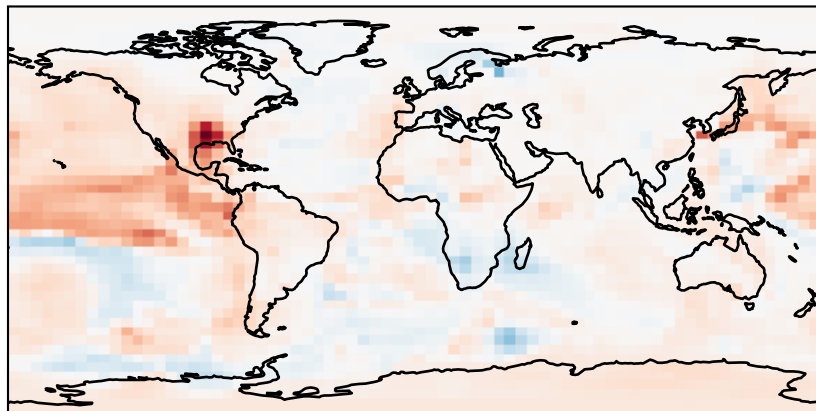
Difference
Dev - Ref, Dynamic Range



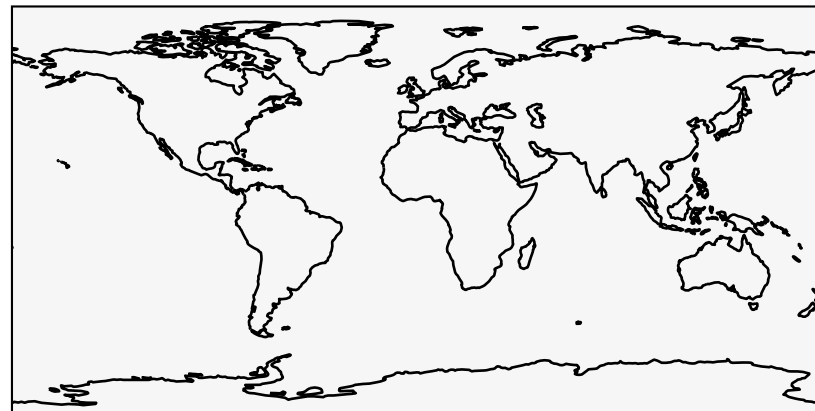
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

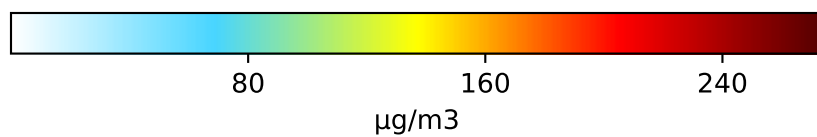
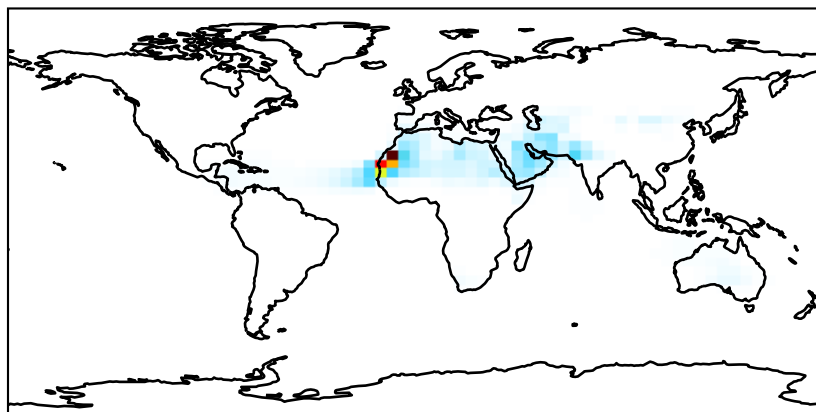


Ratio
Dev/Ref, Fixed Range

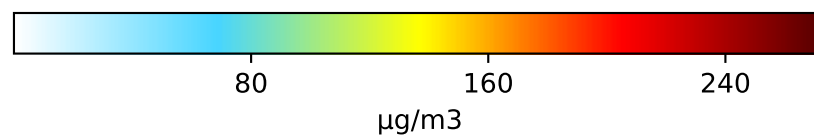
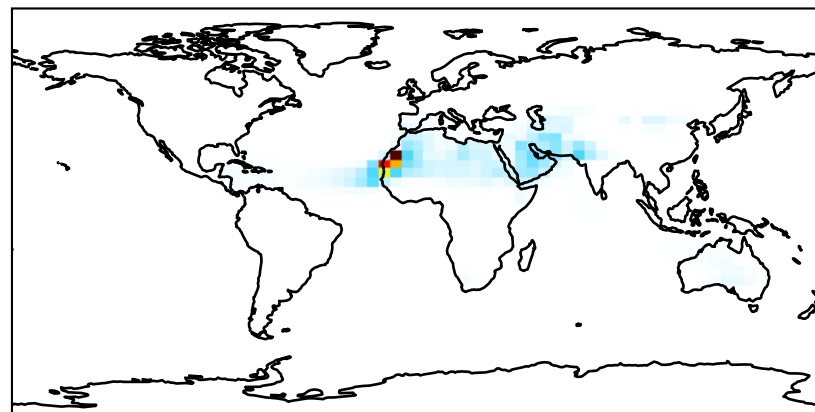


SpeciesConc_DST2 (Jul2019)

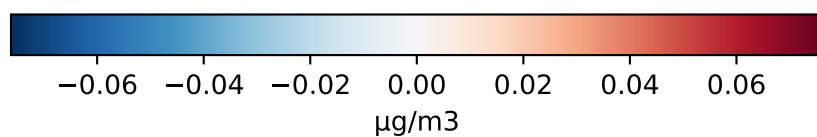
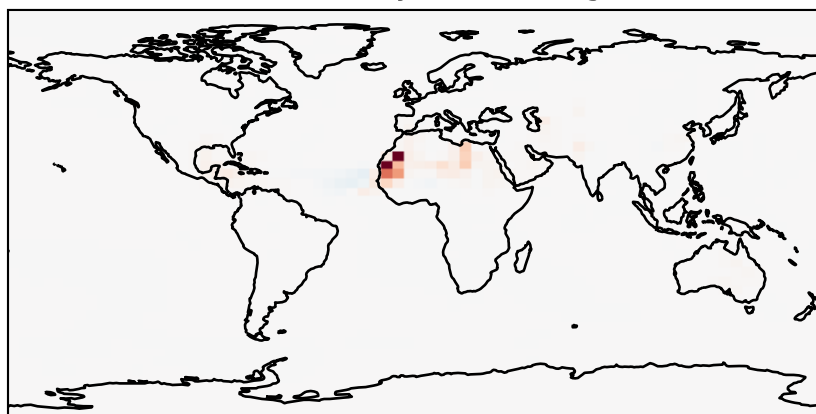
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



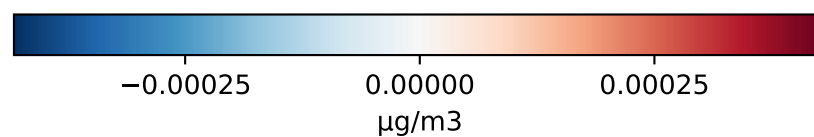
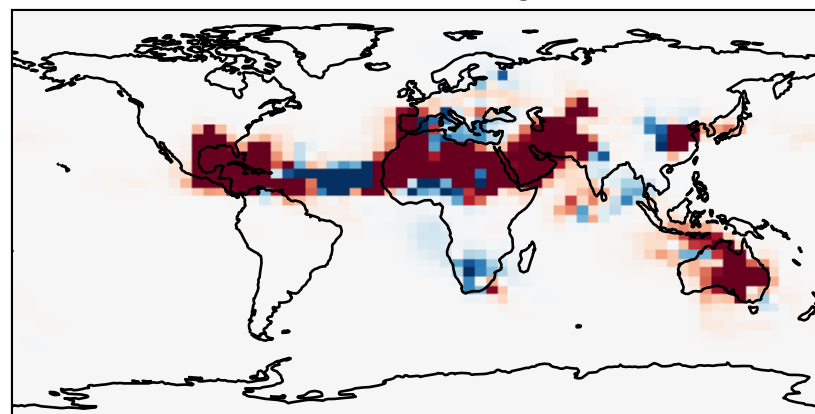
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



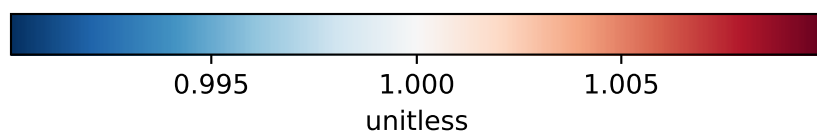
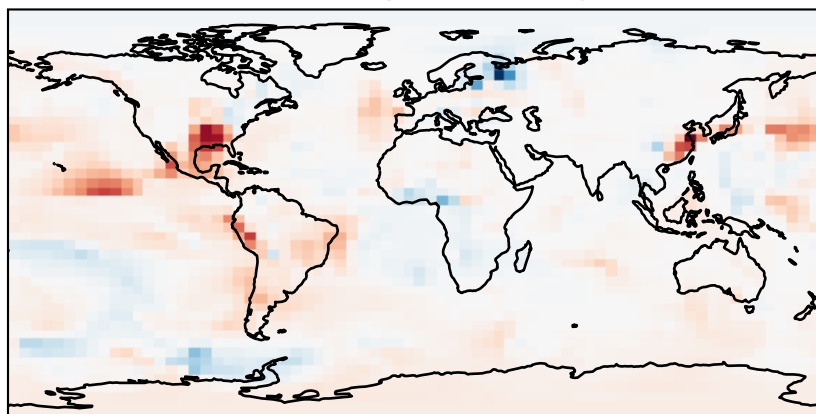
Difference
Dev - Ref, Dynamic Range



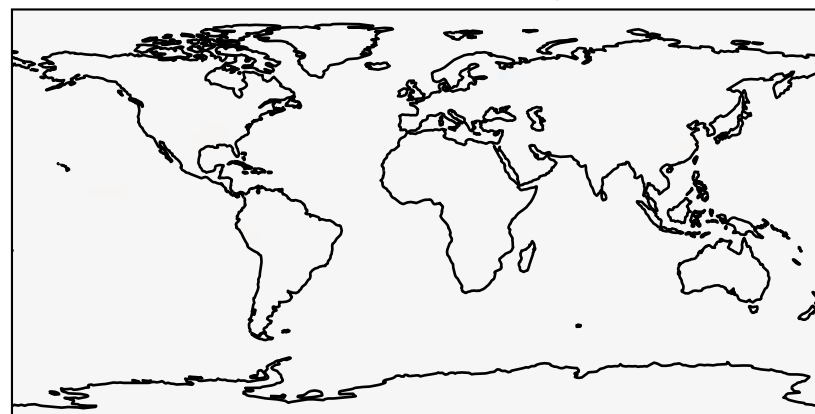
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

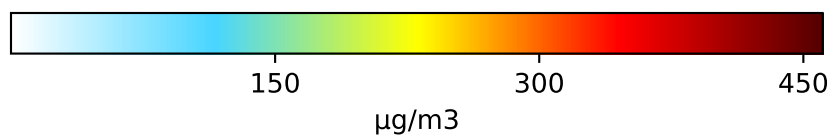
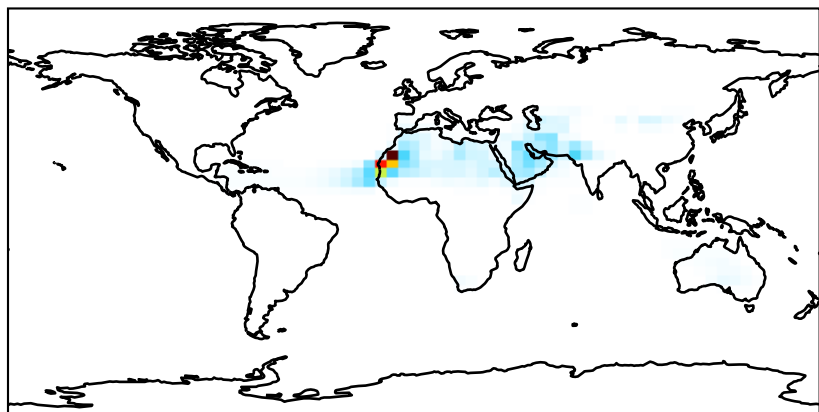


Ratio
Dev/Ref, Fixed Range

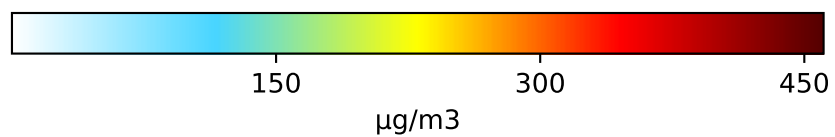
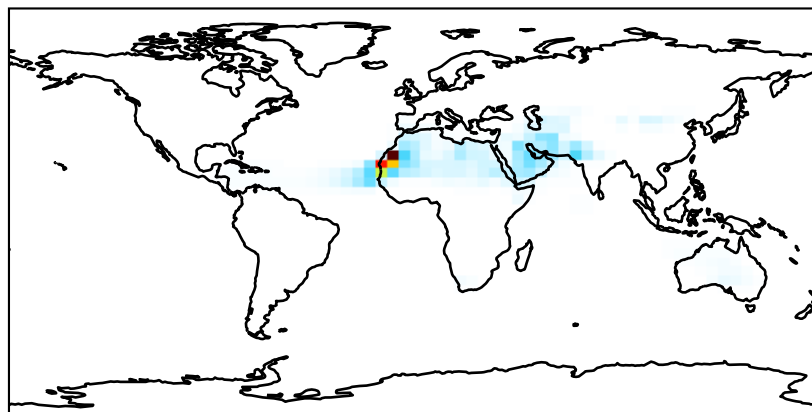


SpeciesConc_DST3 (Jul2019)

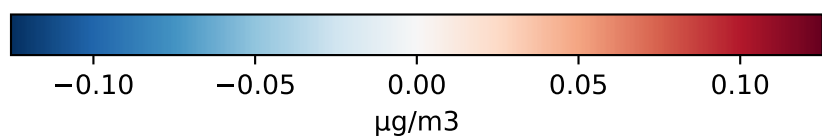
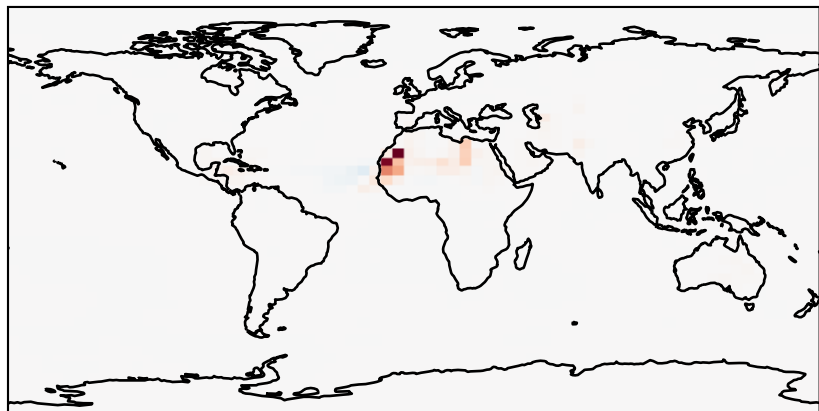
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



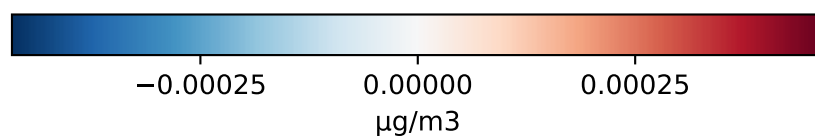
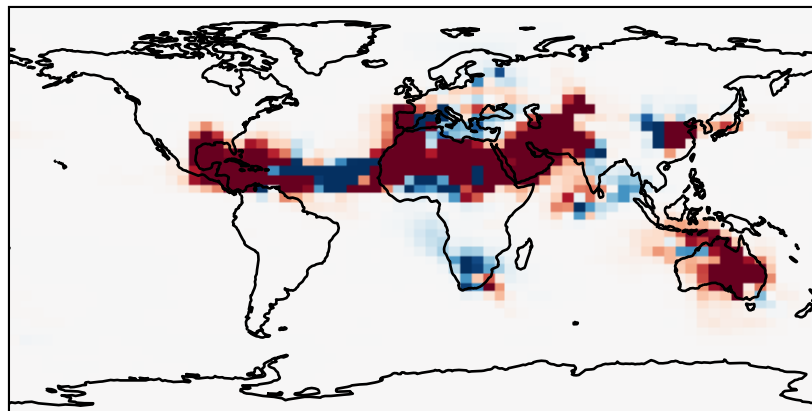
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



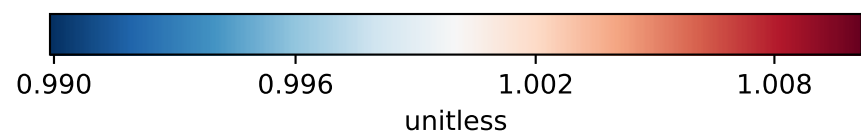
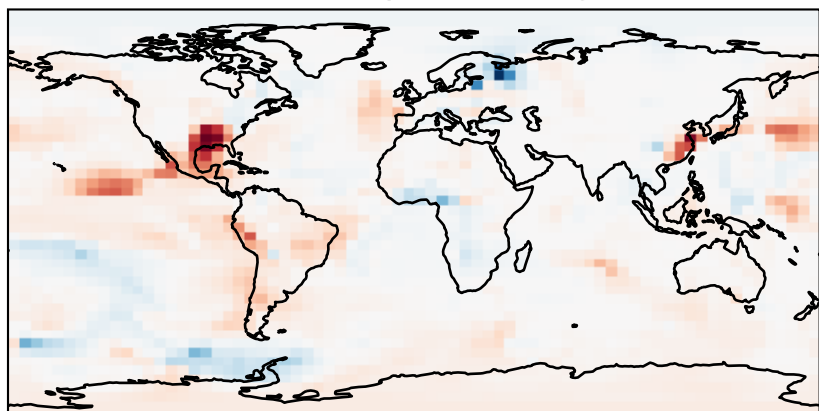
Difference
Dev - Ref, Dynamic Range



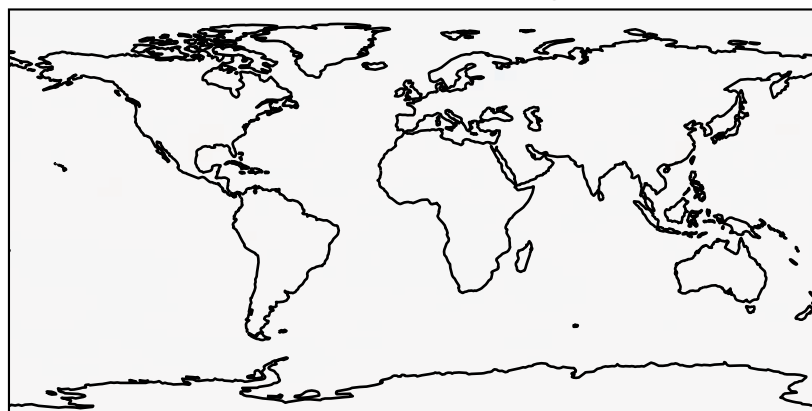
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

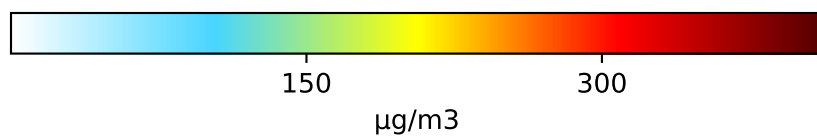
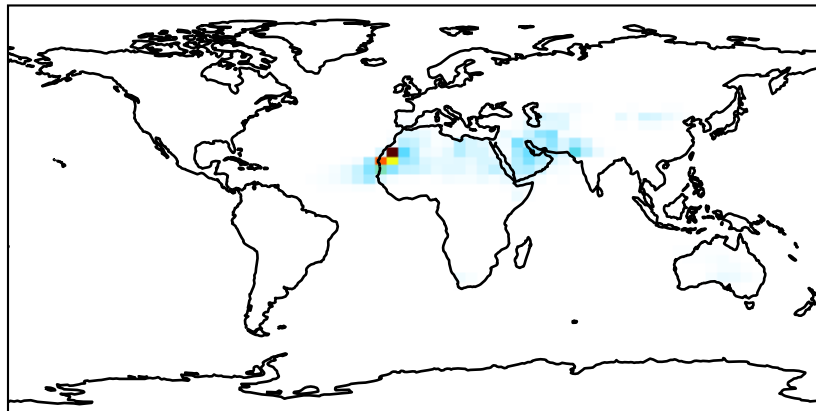


Ratio
Dev/Ref, Fixed Range

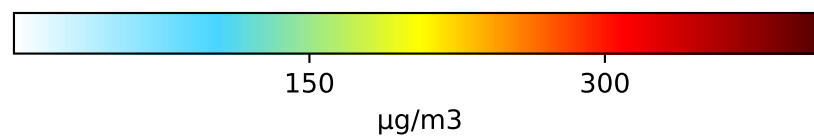
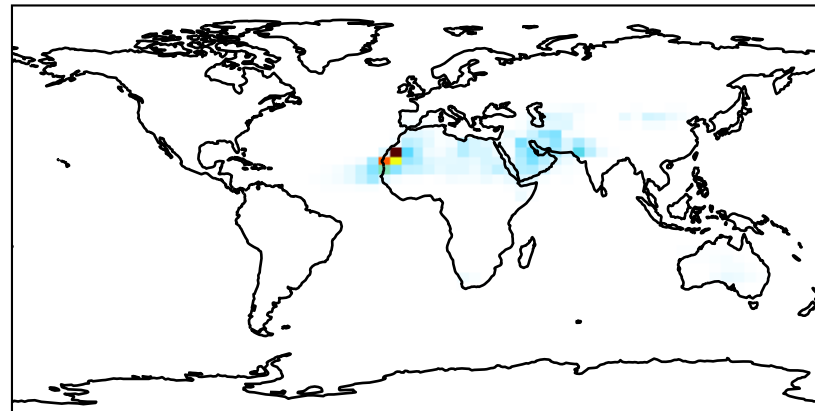


SpeciesConc_DST4 (Jul2019)

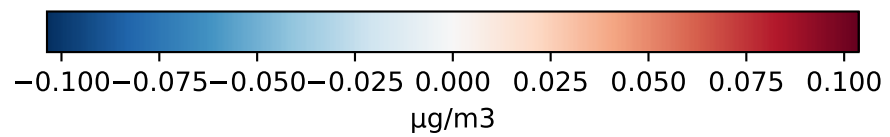
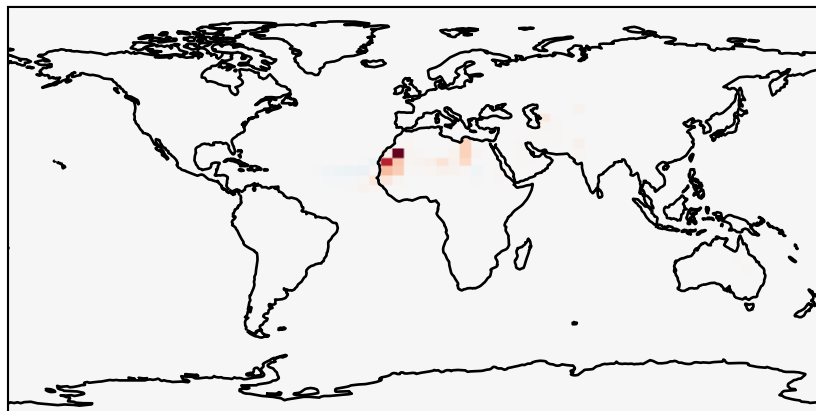
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



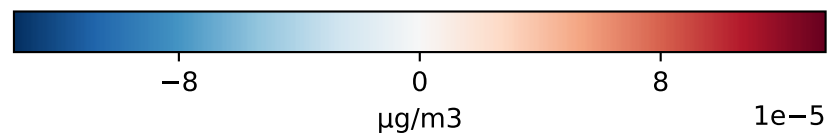
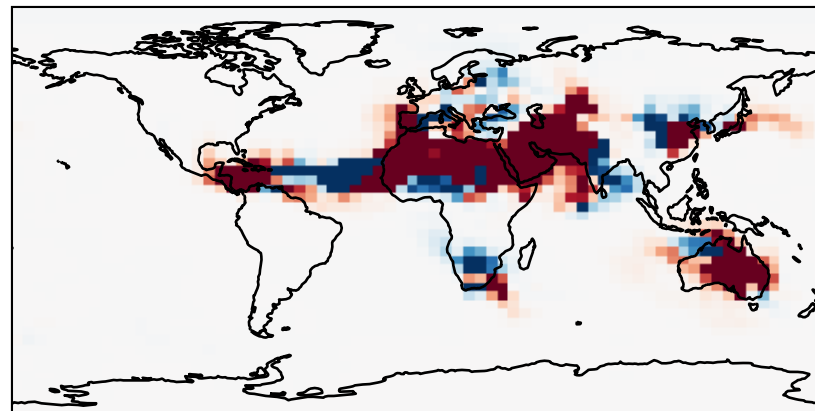
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



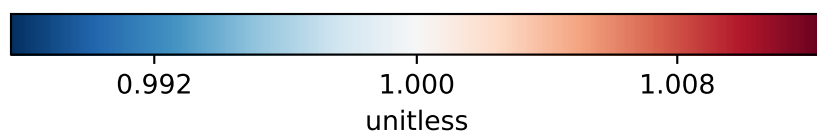
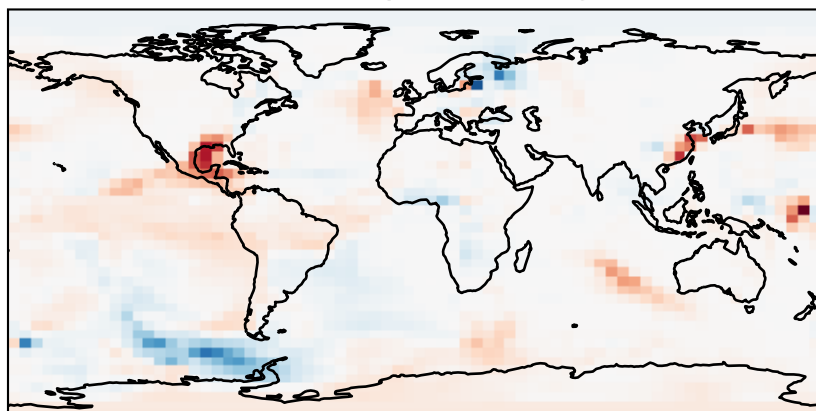
Difference
Dev - Ref, Dynamic Range



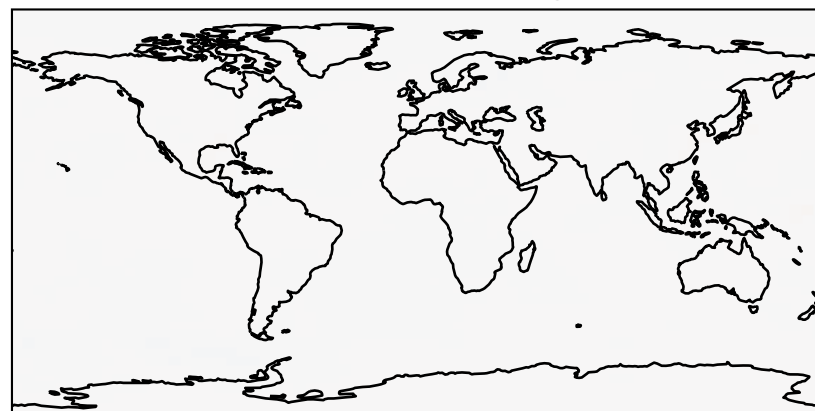
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

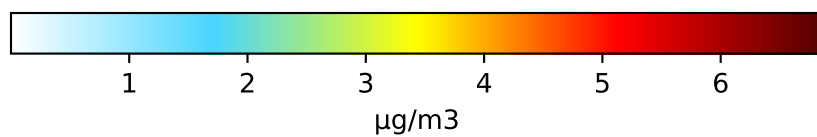
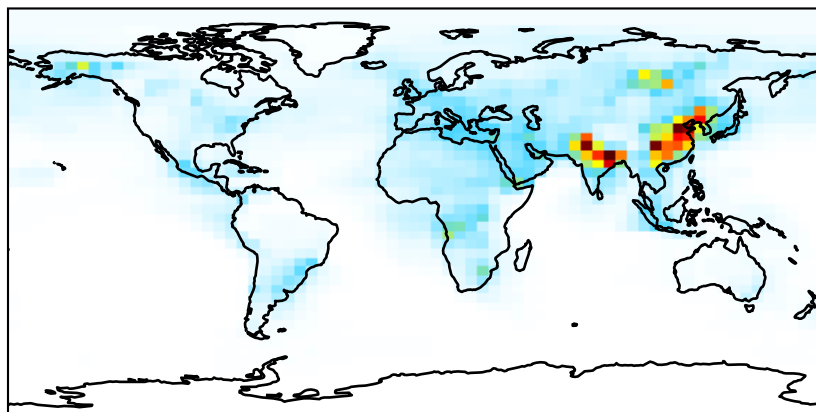


Ratio
Dev/Ref, Fixed Range

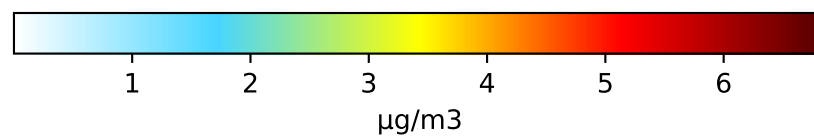
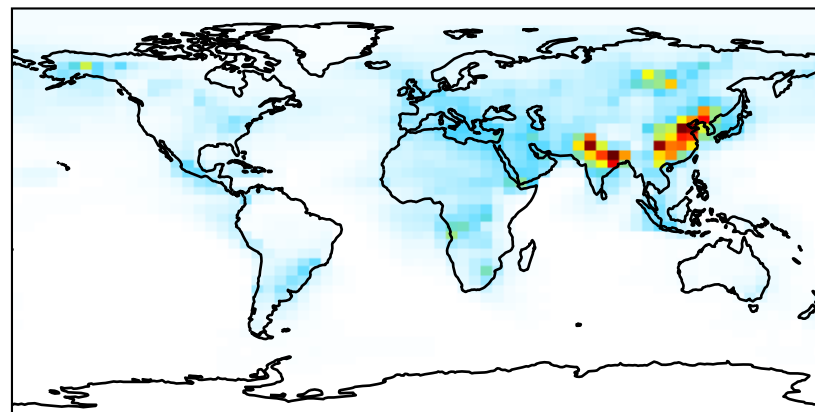


SpeciesConc_NH4 (Jul2019)

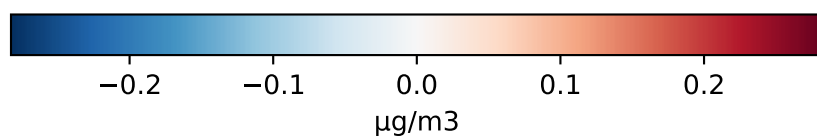
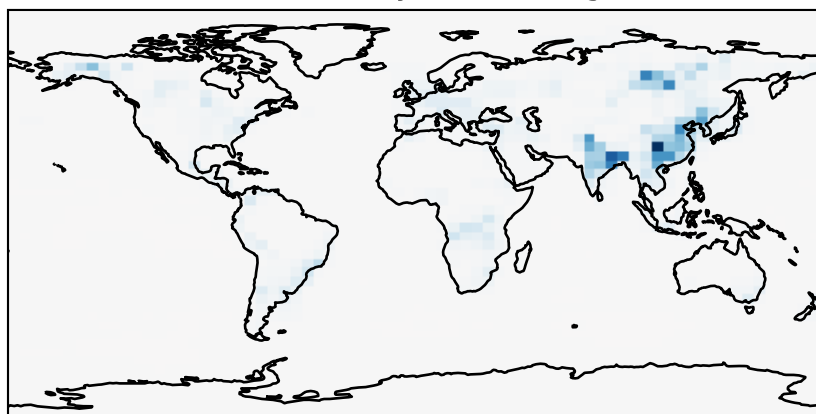
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



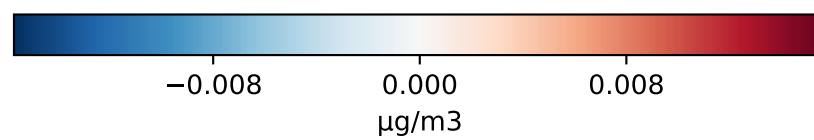
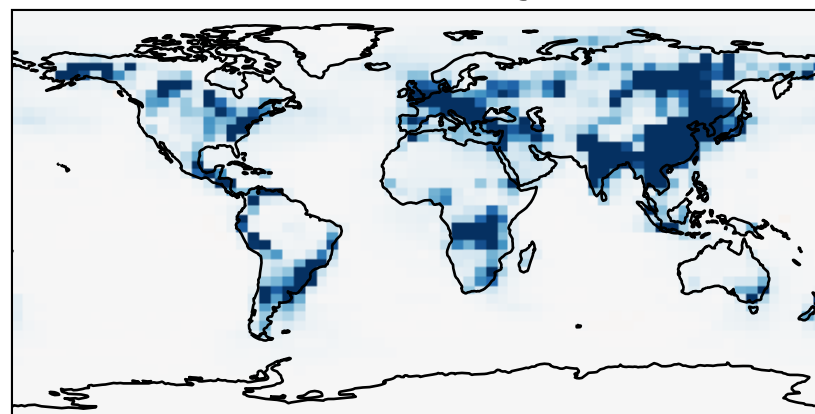
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



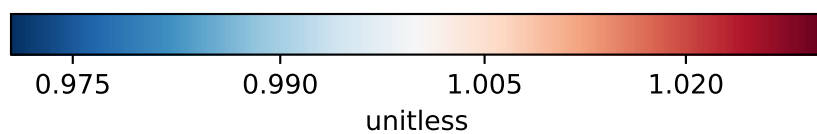
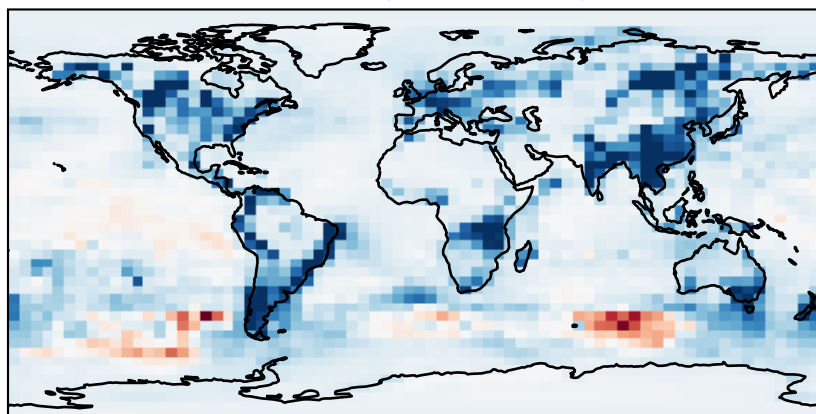
Difference
Dev - Ref, Dynamic Range



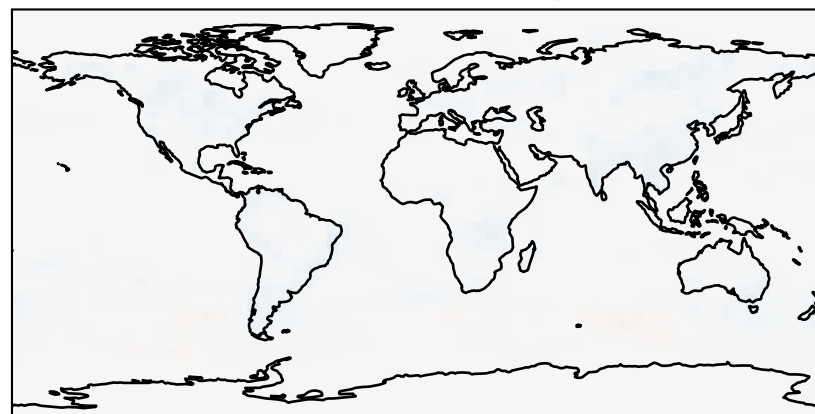
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

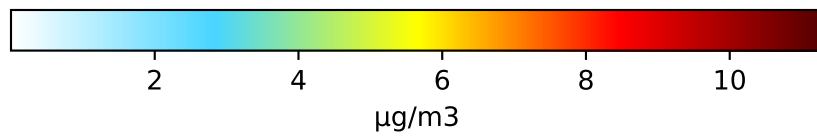
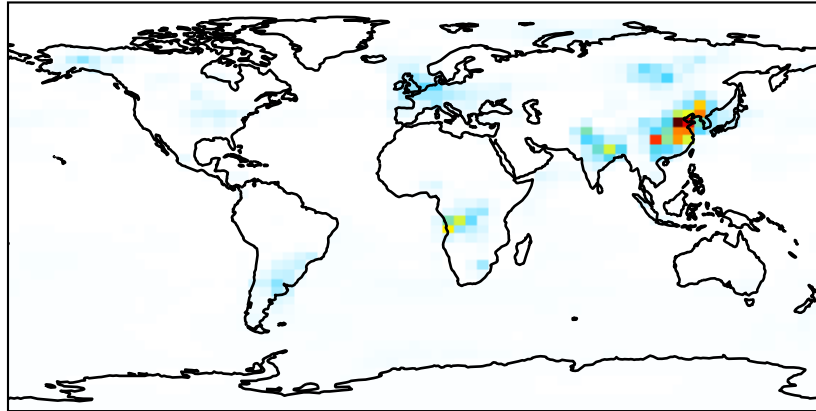


Ratio
Dev/Ref, Fixed Range

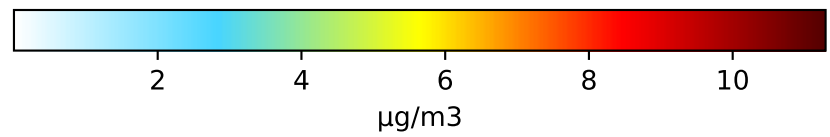
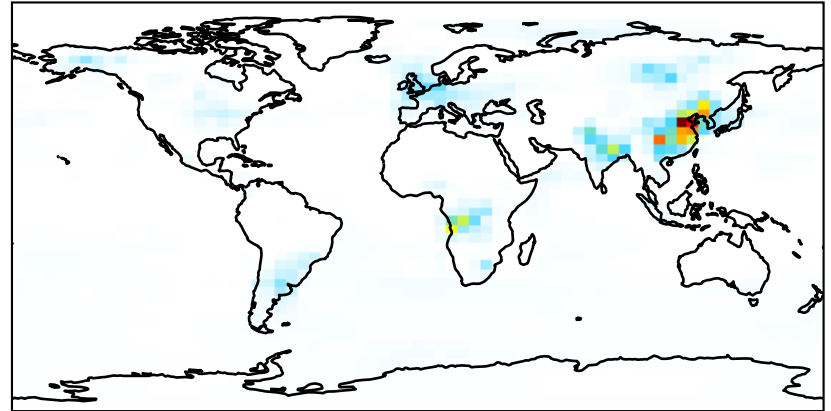


SpeciesConc_NIT (Jul2019)

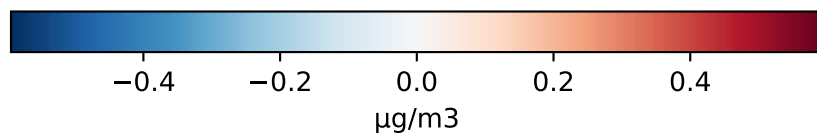
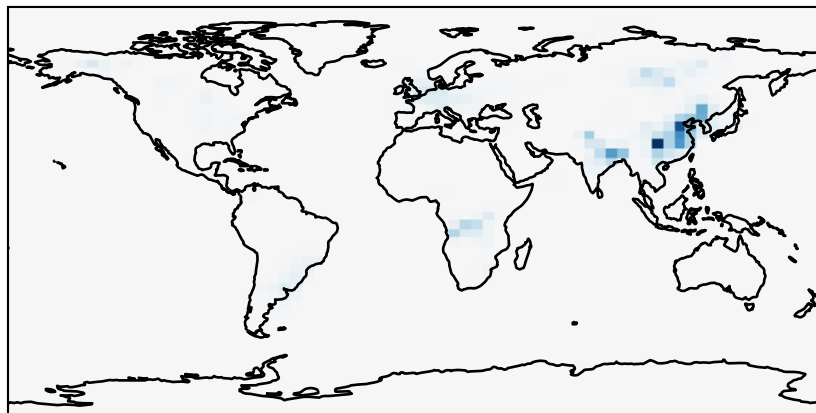
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



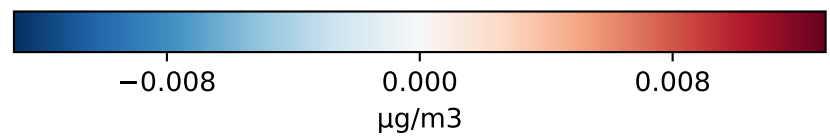
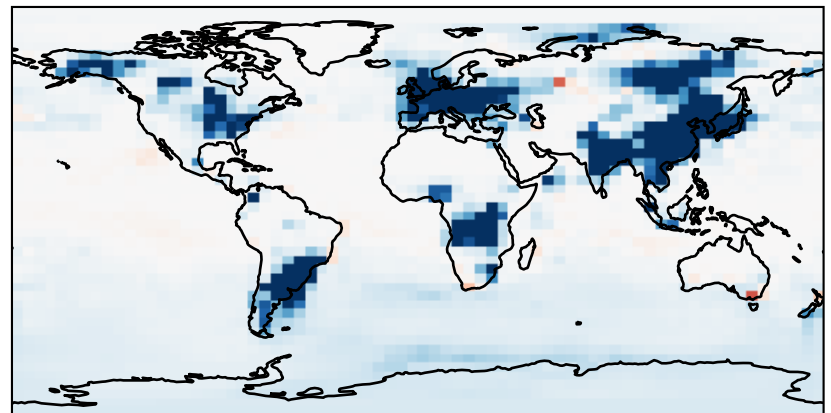
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



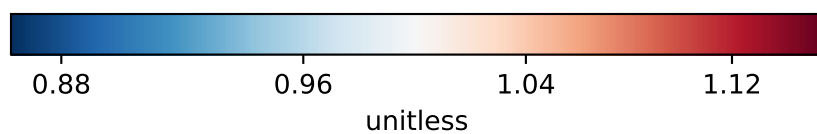
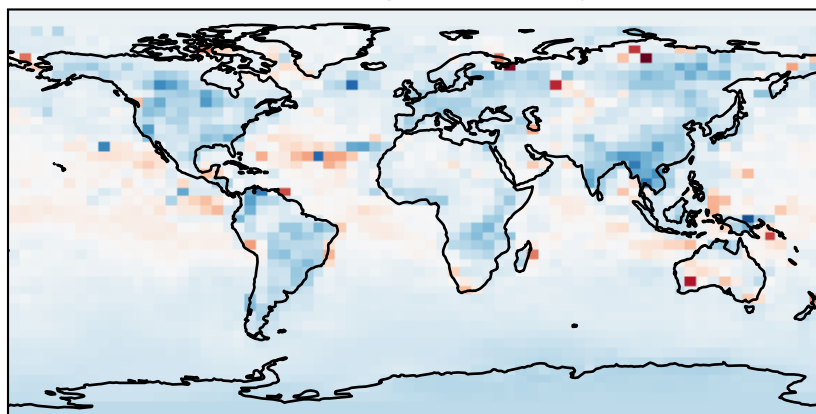
Difference
Dev - Ref, Dynamic Range



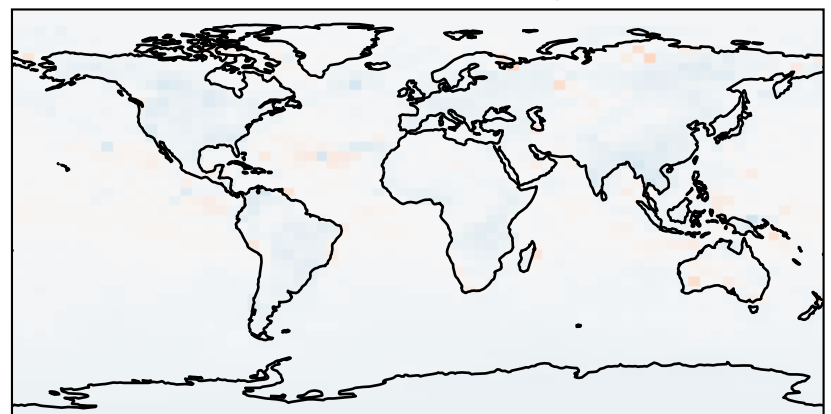
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

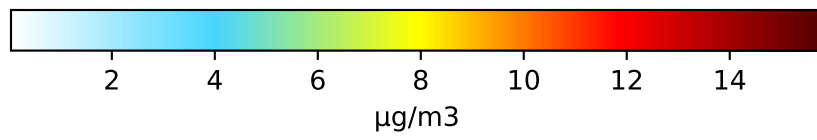
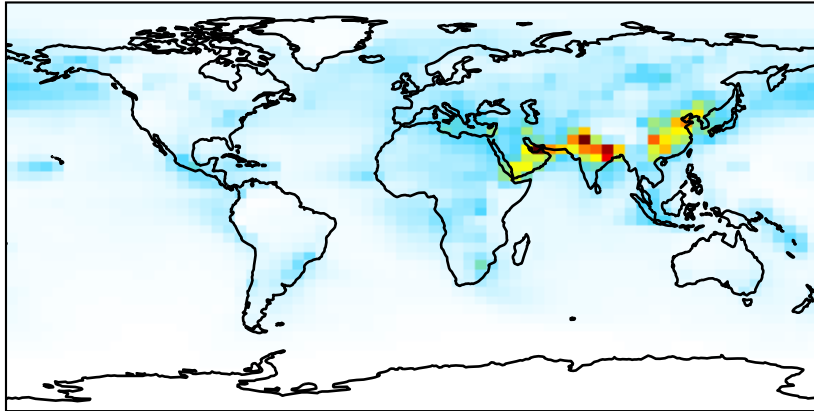


Ratio
Dev/Ref, Fixed Range

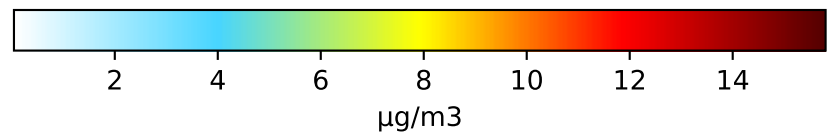
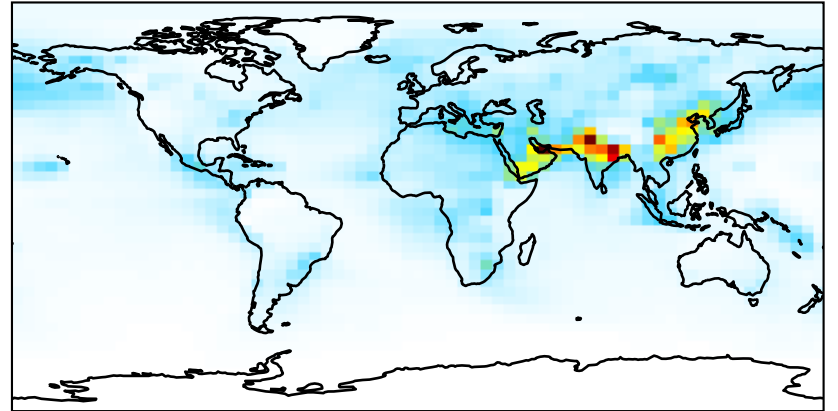


SpeciesConc_SO4 (Jul2019)

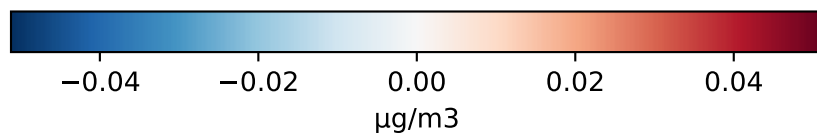
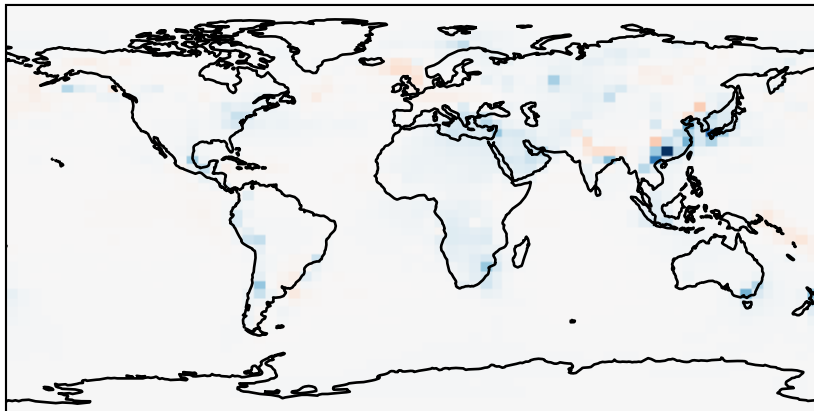
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



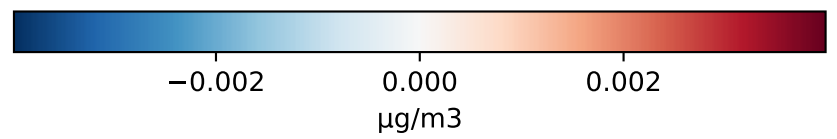
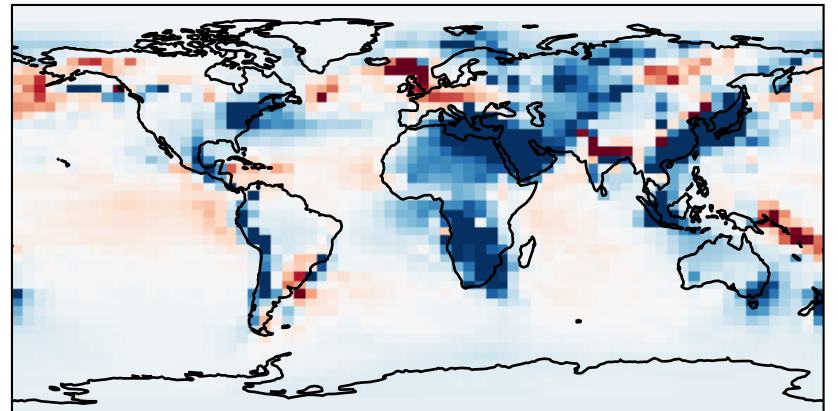
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



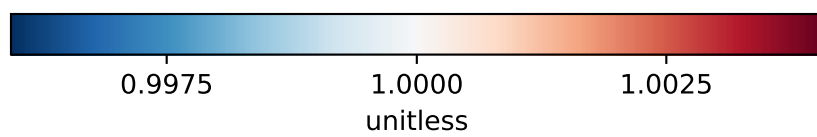
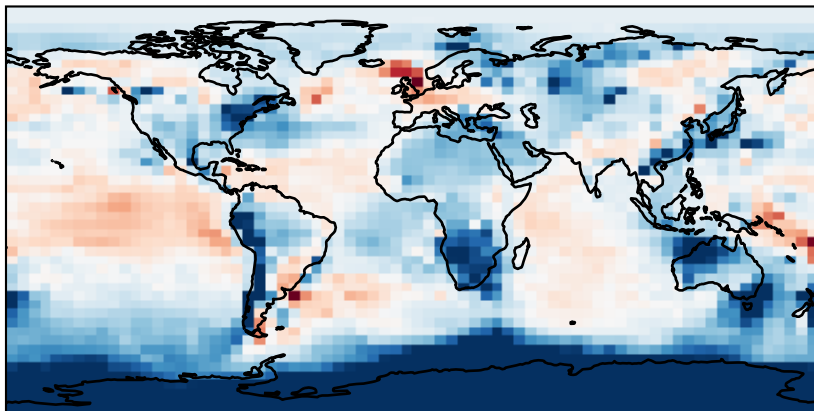
Difference
Dev - Ref, Dynamic Range



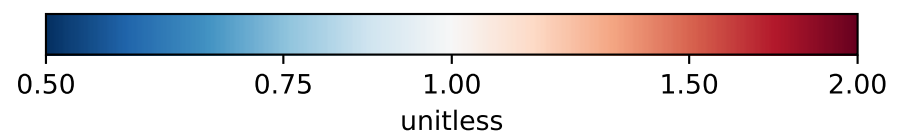
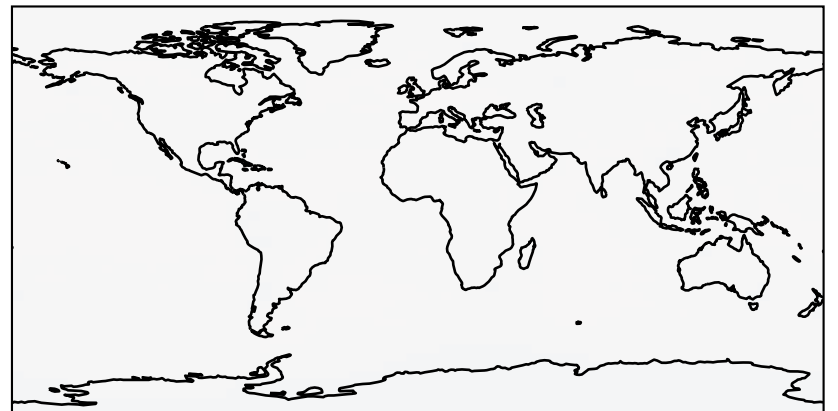
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

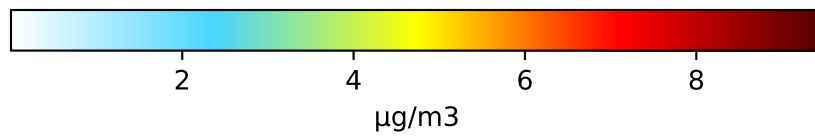
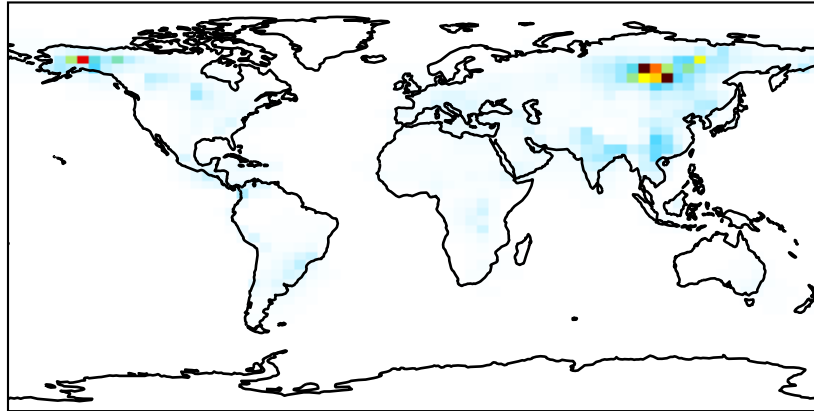


Ratio
Dev/Ref, Fixed Range

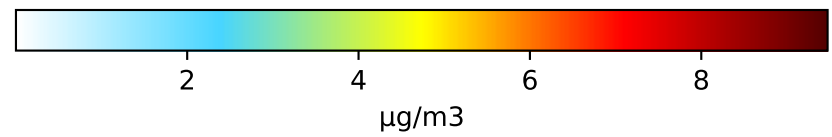
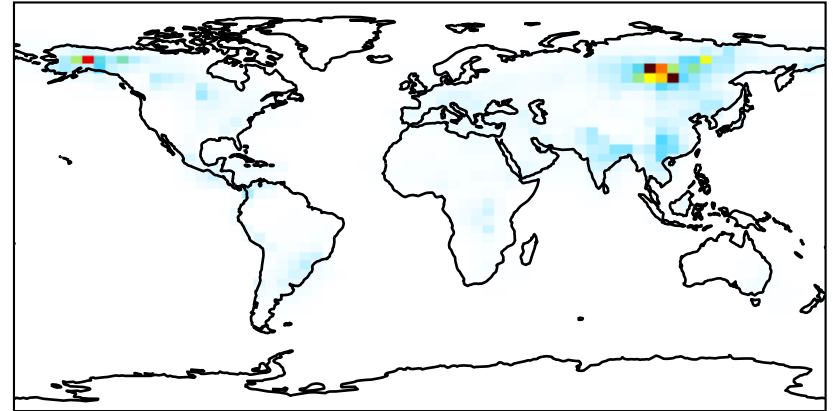


SpeciesConc_HMS (Jul2019)

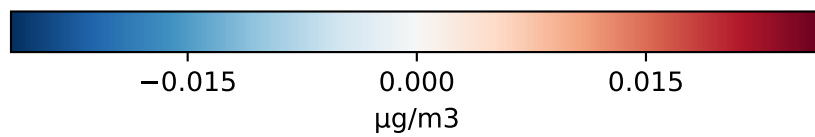
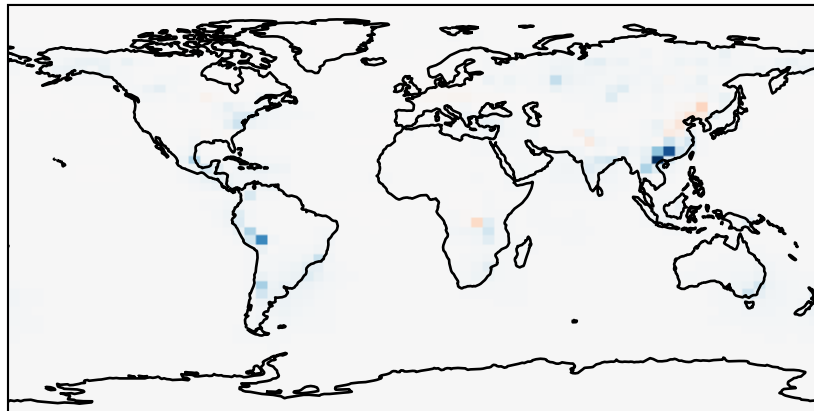
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



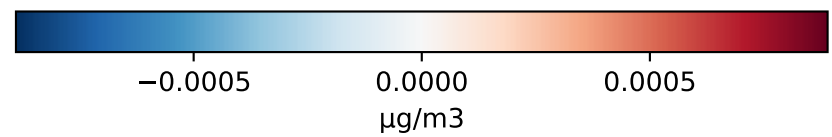
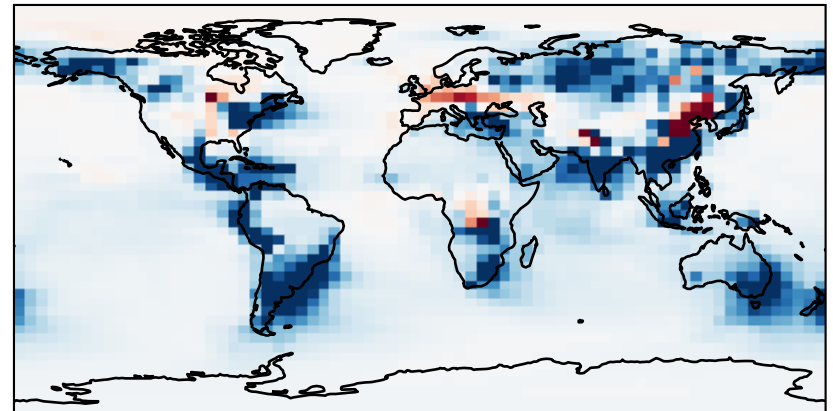
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



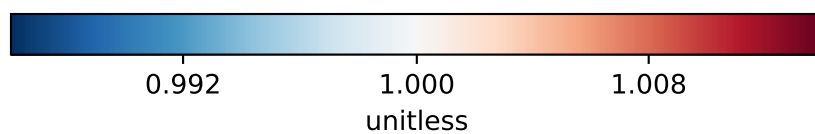
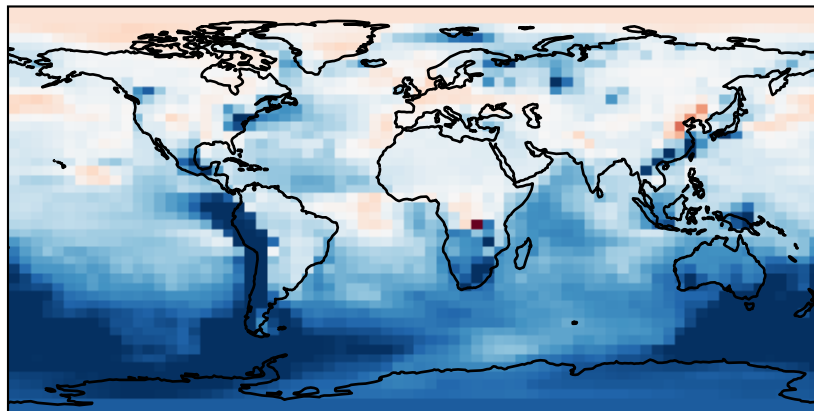
Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

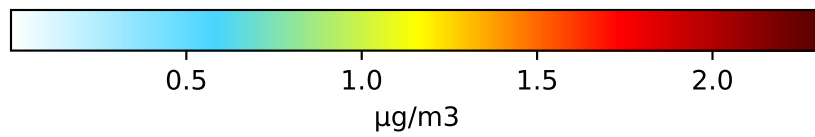
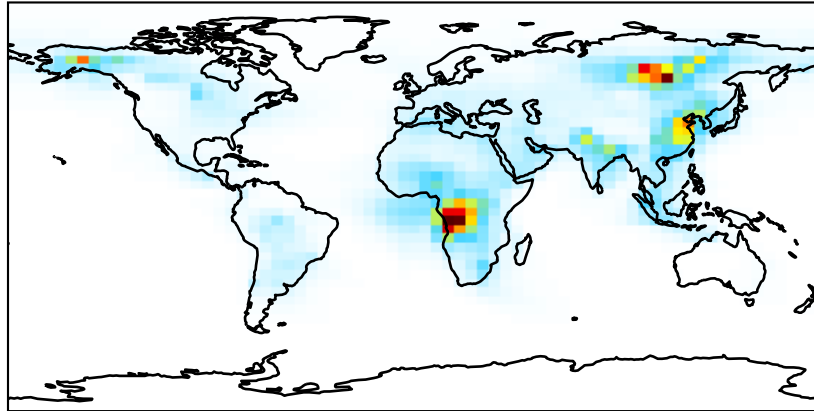


Ratio
Dev/Ref, Fixed Range

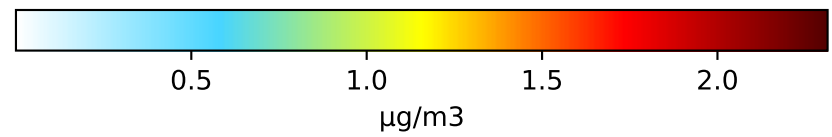
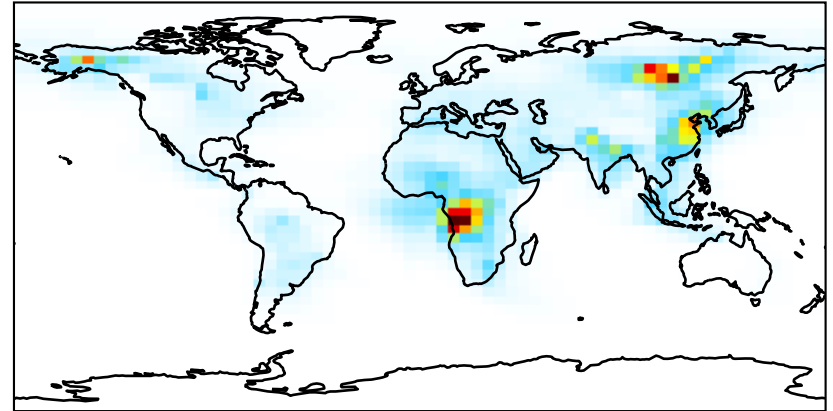


SpeciesConc_BCPI (Jul2019)

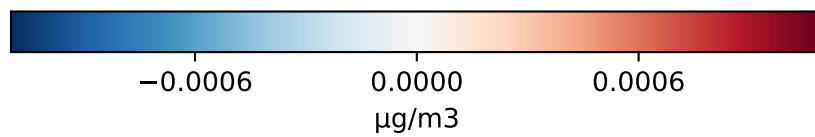
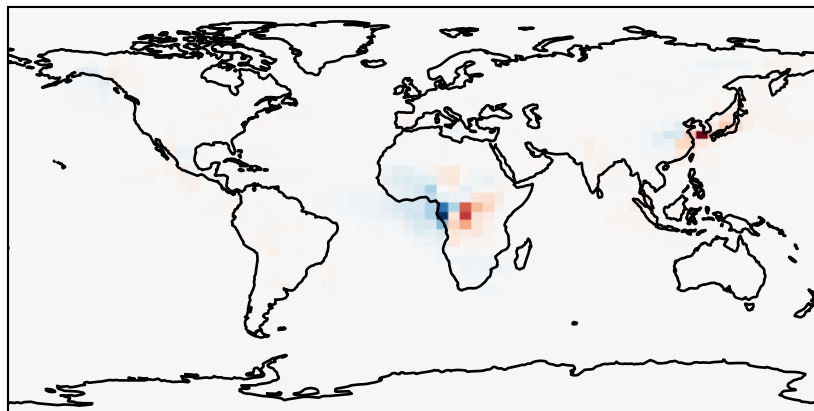
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



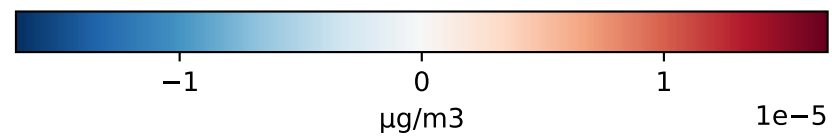
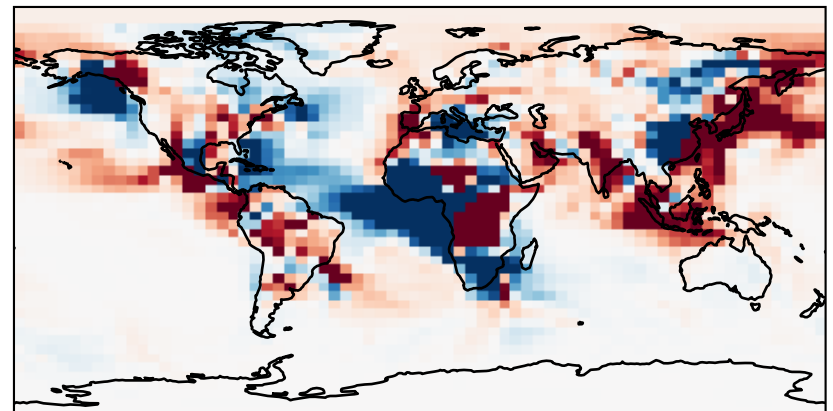
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



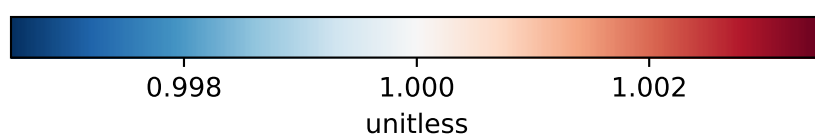
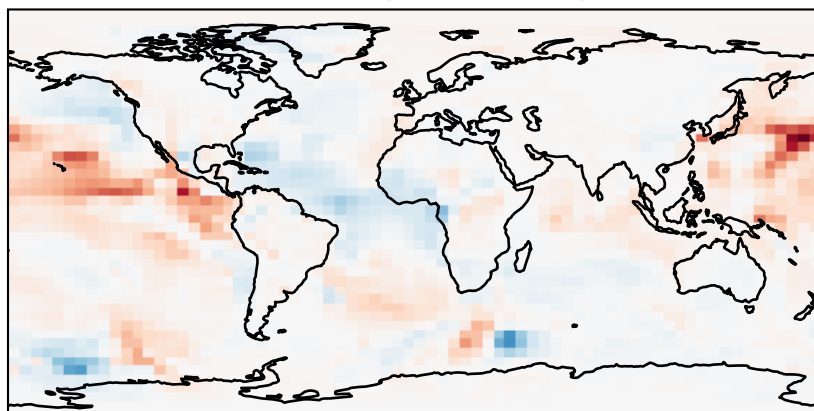
Difference
Dev - Ref, Dynamic Range



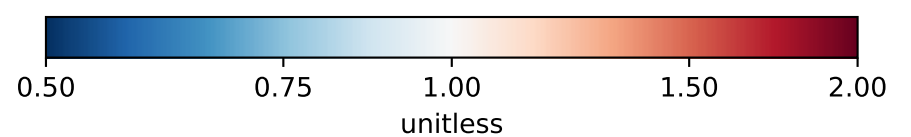
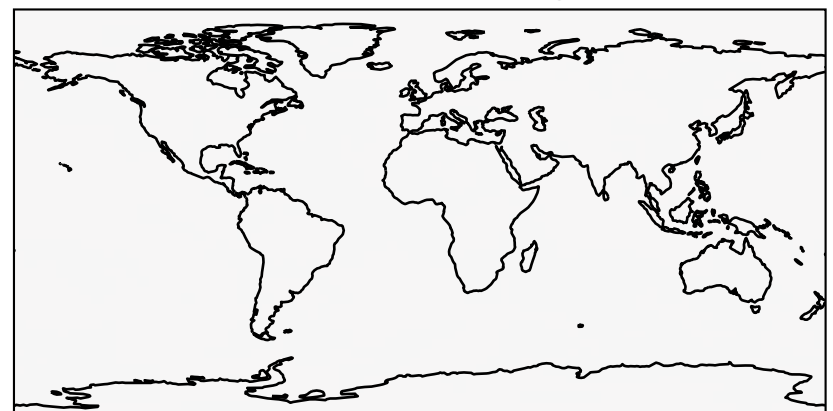
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

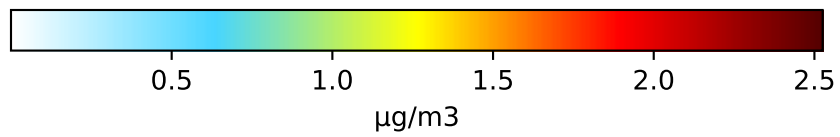
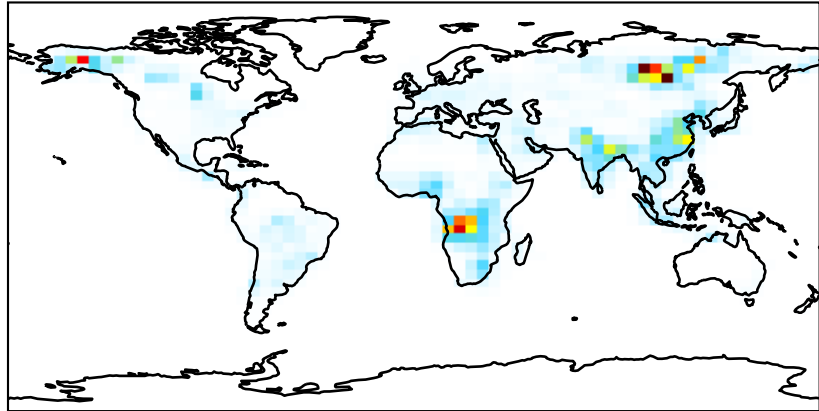


Ratio
Dev/Ref, Fixed Range

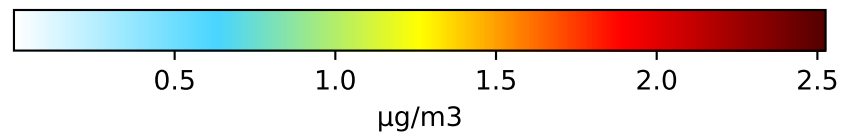
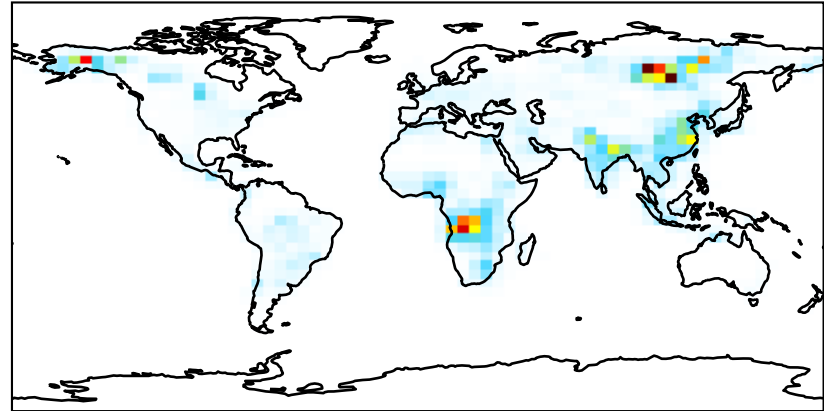


SpeciesConc_BCPO (Jul2019)

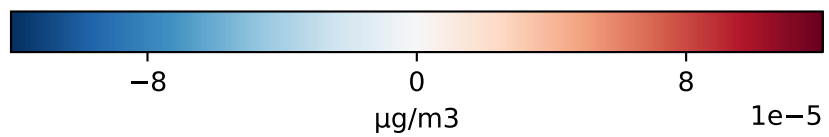
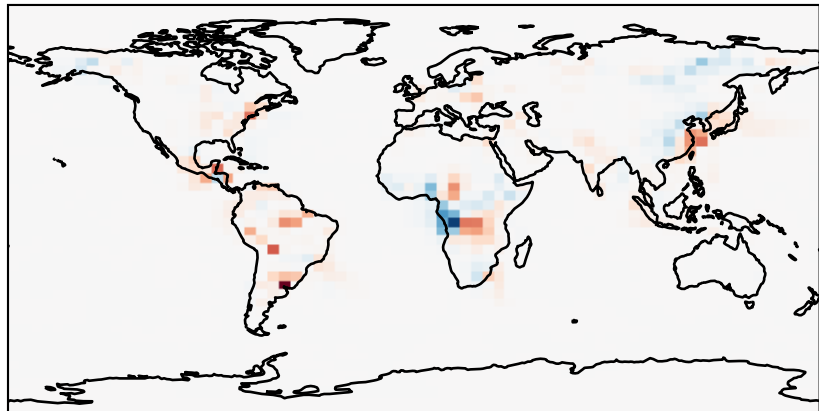
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



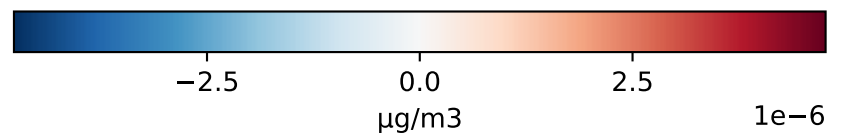
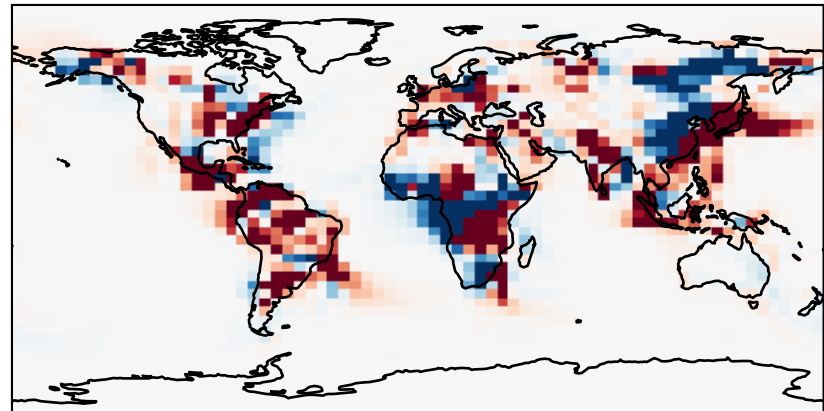
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



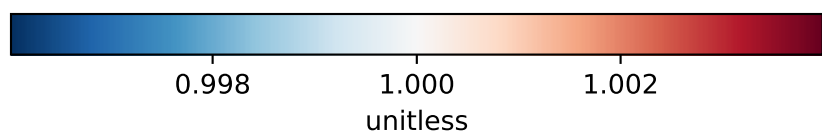
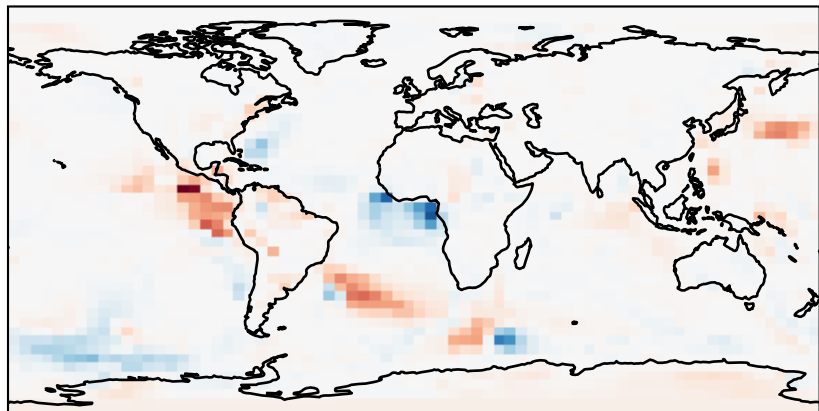
Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

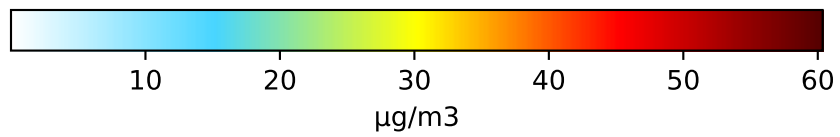
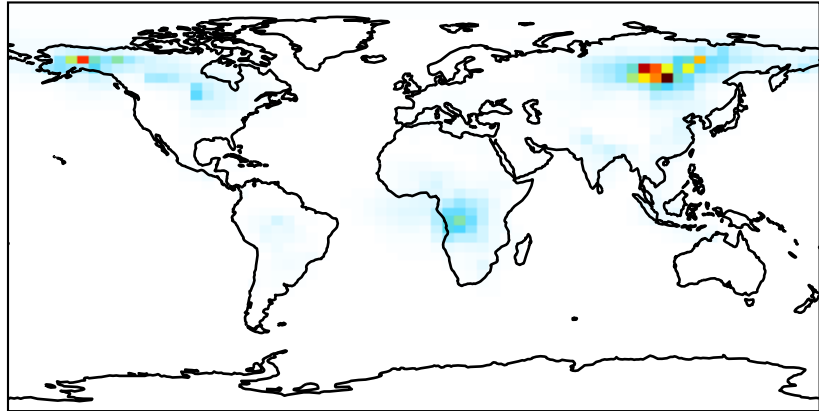


Ratio
Dev/Ref, Fixed Range

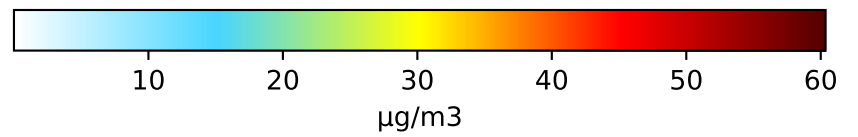
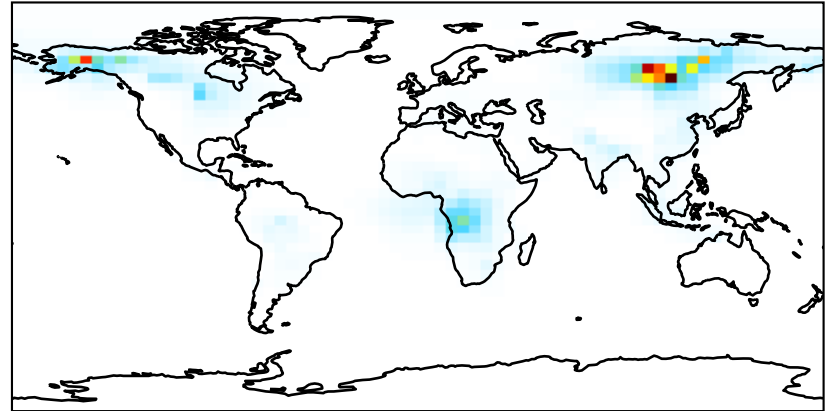


SpeciesConc_OCPI (Jul2019)

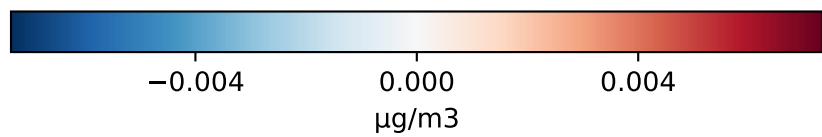
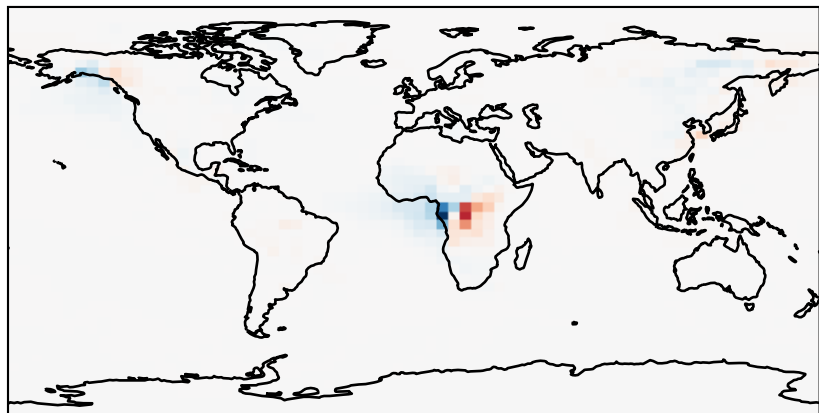
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



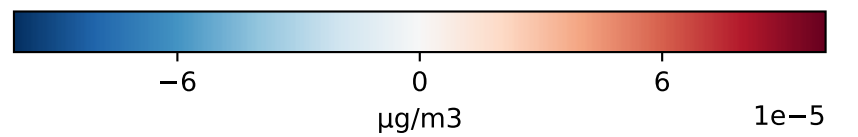
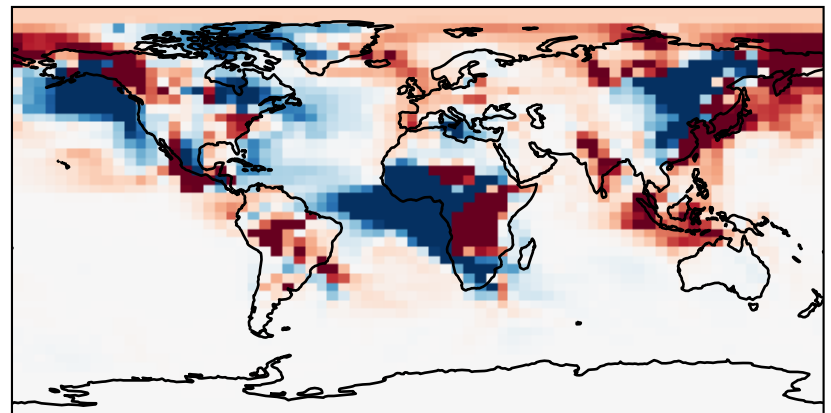
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



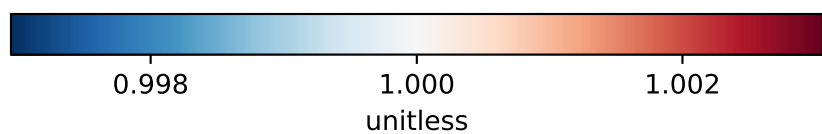
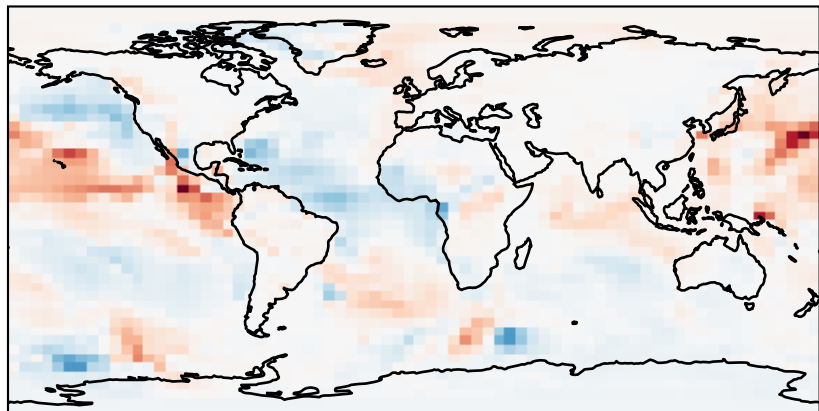
Difference
Dev - Ref, Dynamic Range



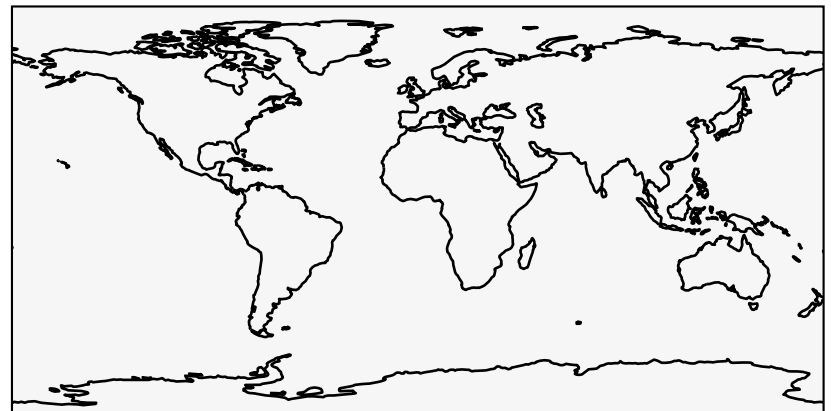
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

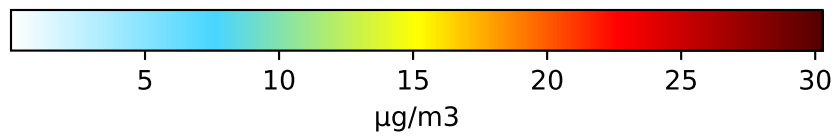
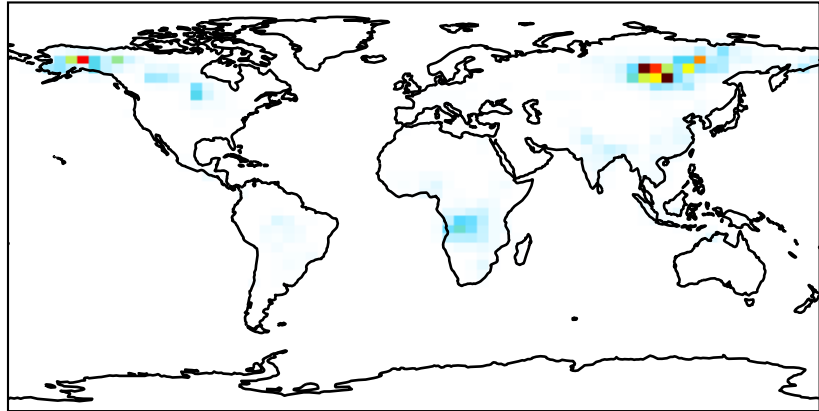


Ratio
Dev/Ref, Fixed Range

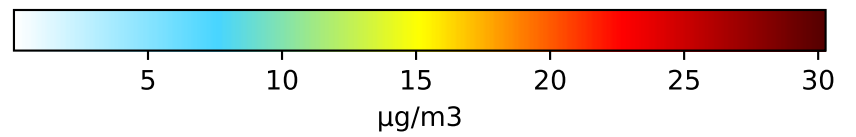
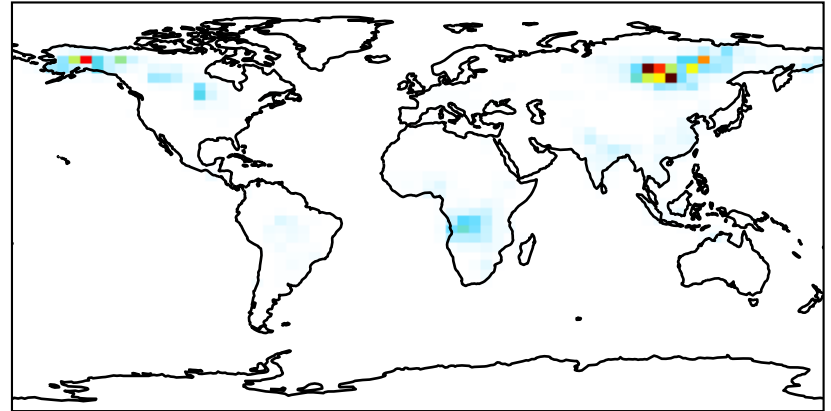


SpeciesConc_OCPO (Jul2019)

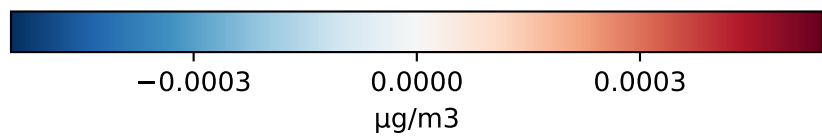
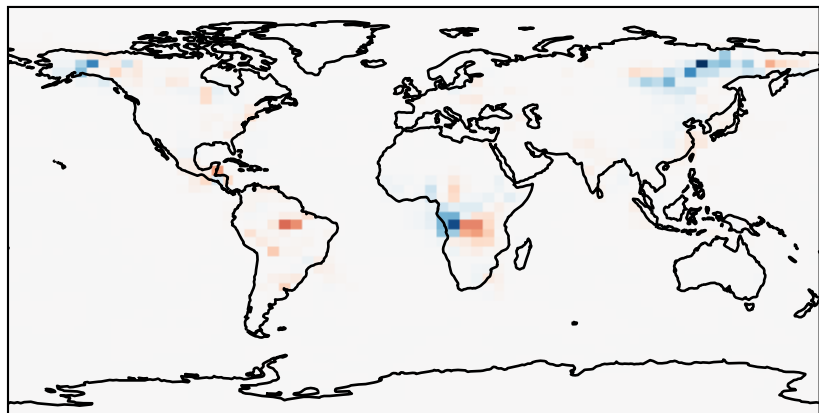
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



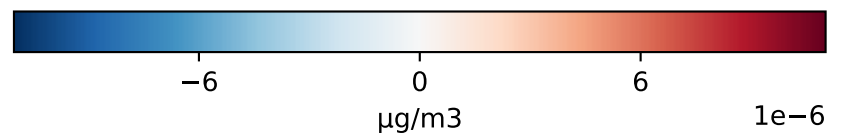
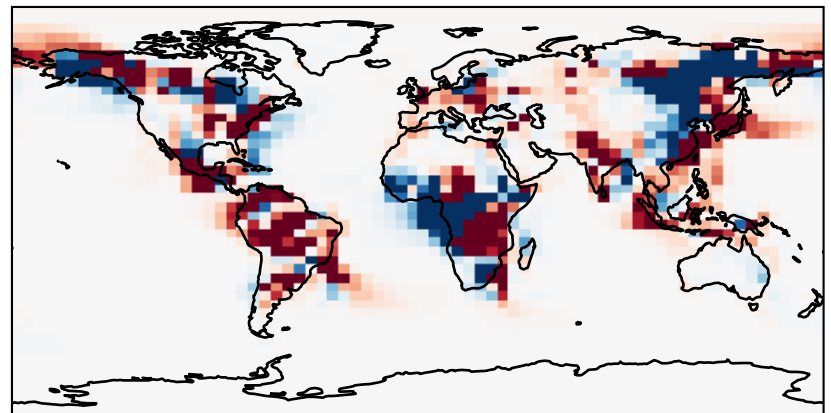
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



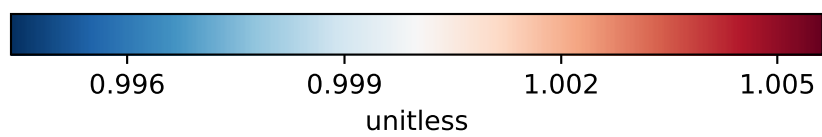
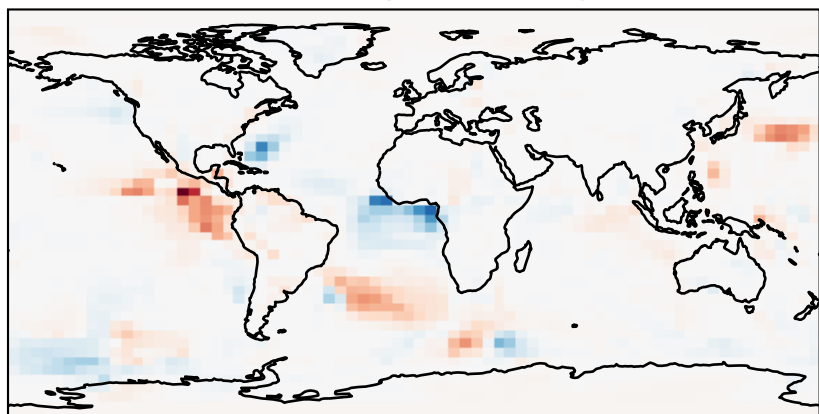
Difference
Dev - Ref, Dynamic Range



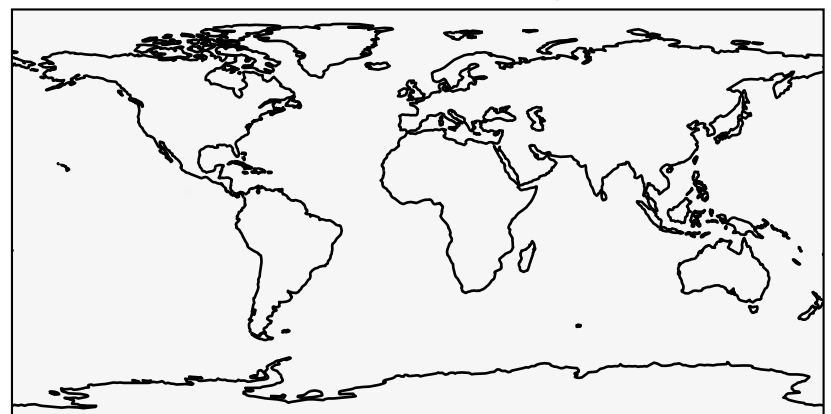
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

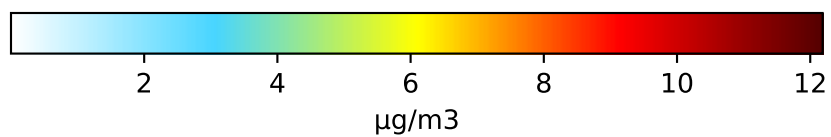
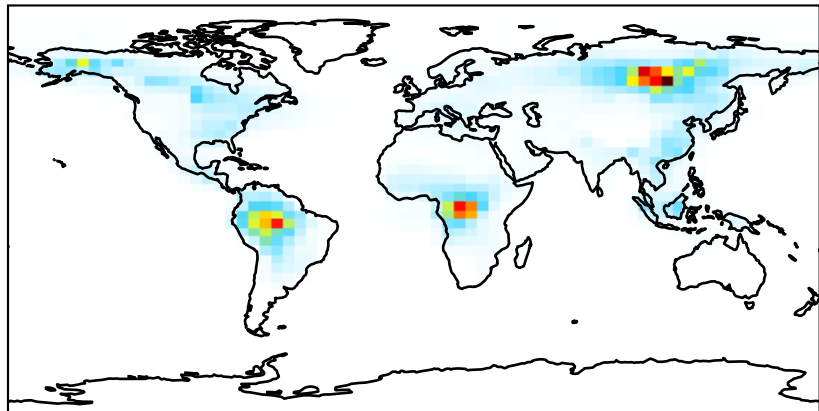


Ratio
Dev/Ref, Fixed Range

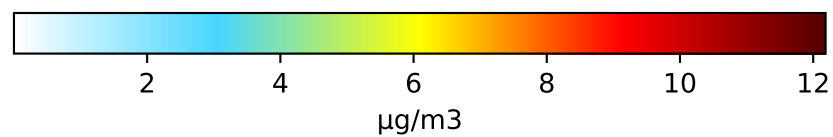
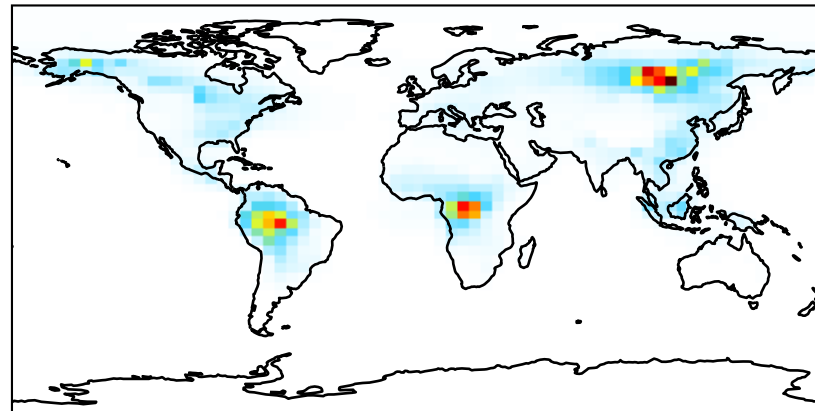


SpeciesConc_Complex_SOA (Jul2019)

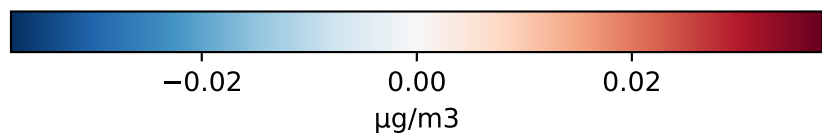
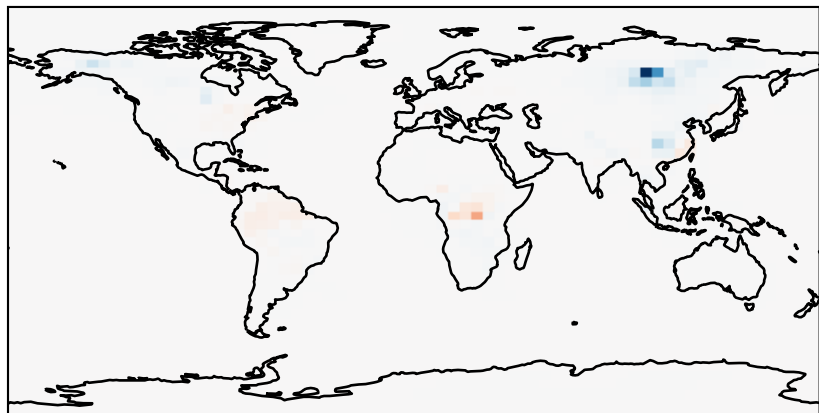
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



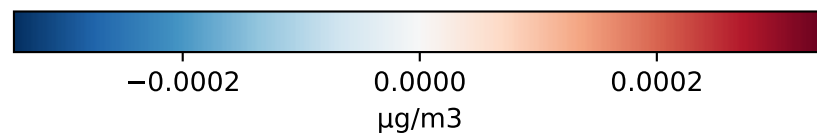
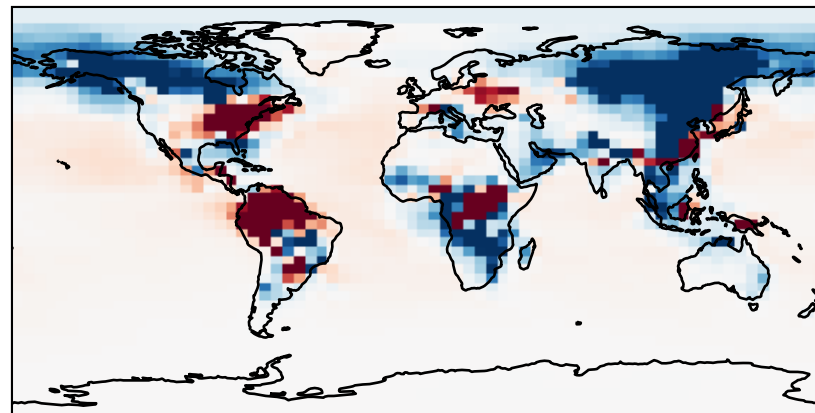
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



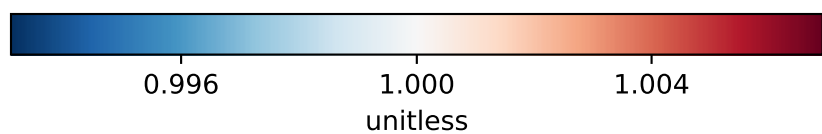
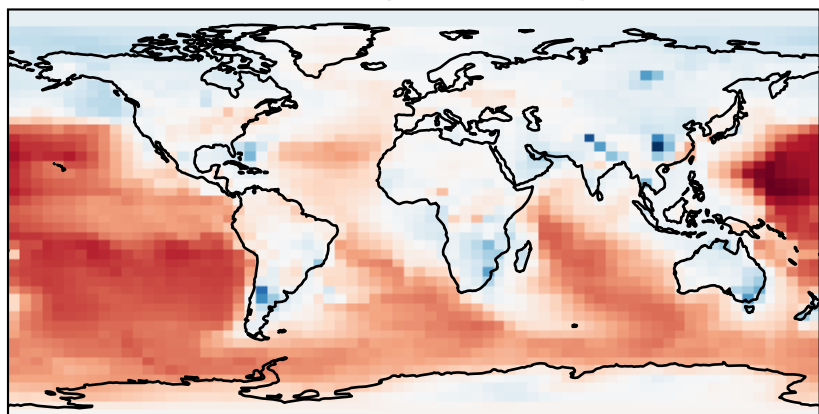
Difference
Dev - Ref, Dynamic Range



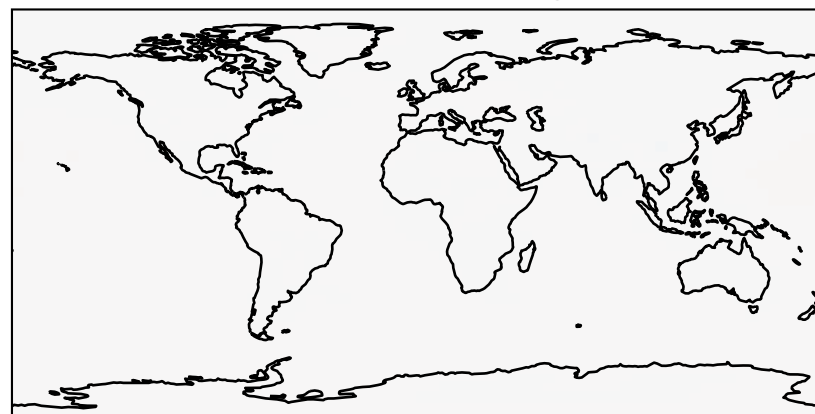
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

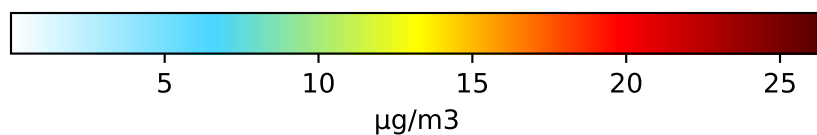
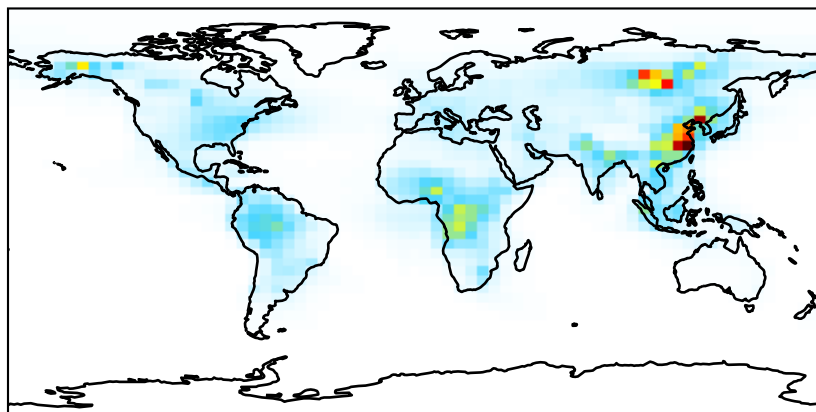


Ratio
Dev/Ref, Fixed Range

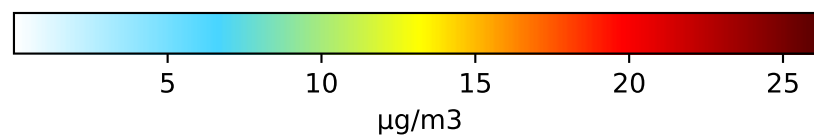
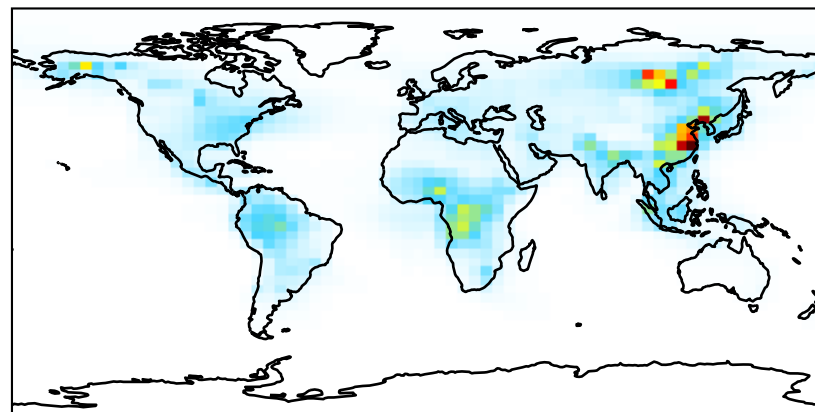


SpeciesConc_Simple_SOA (Jul2019)

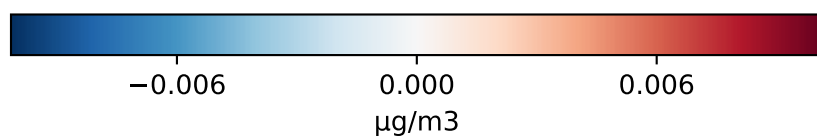
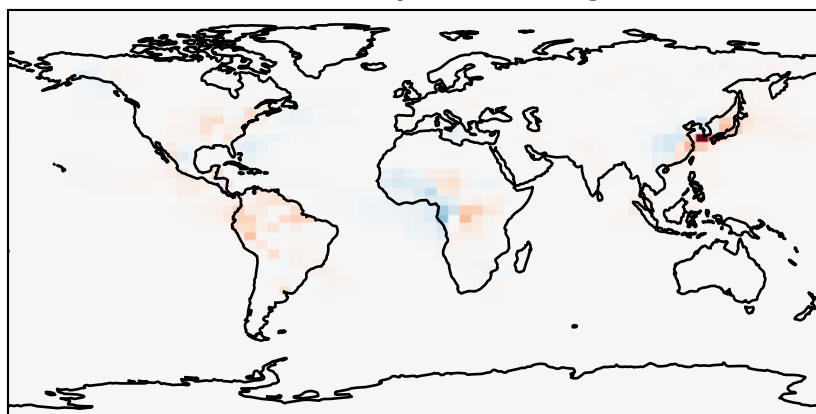
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



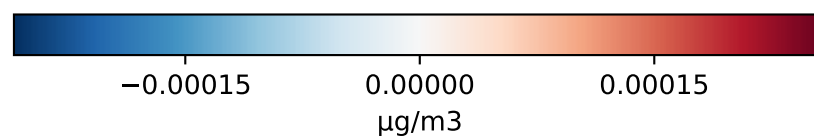
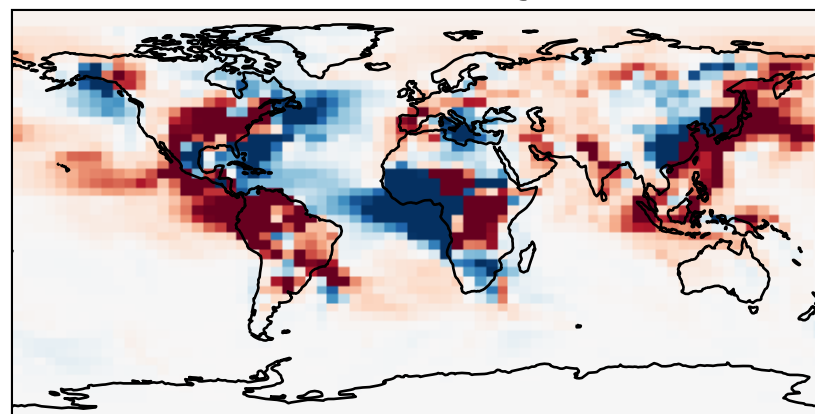
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



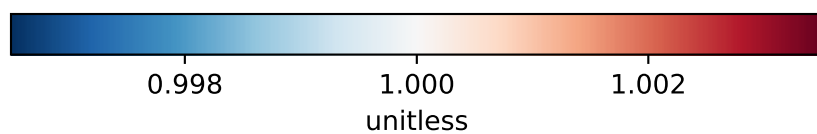
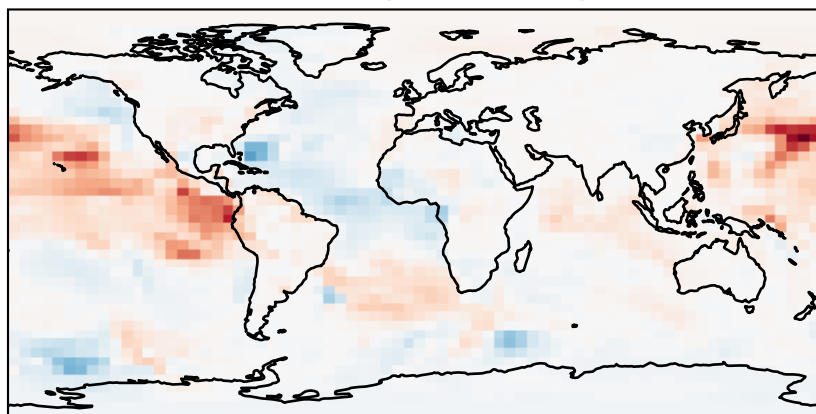
Difference
Dev - Ref, Dynamic Range



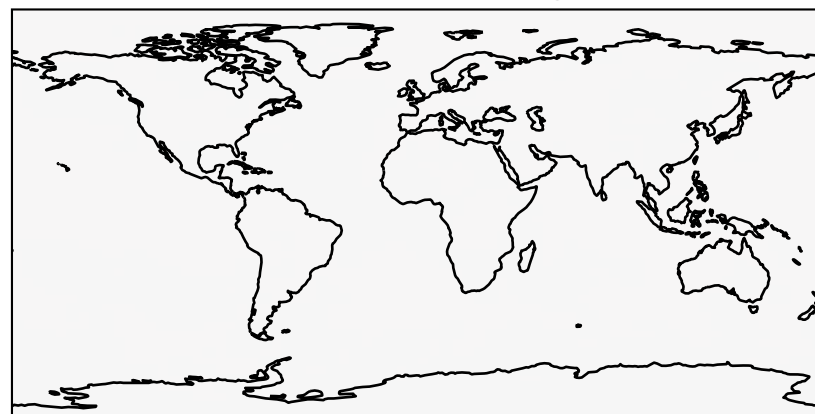
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

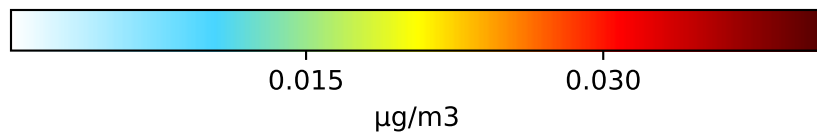
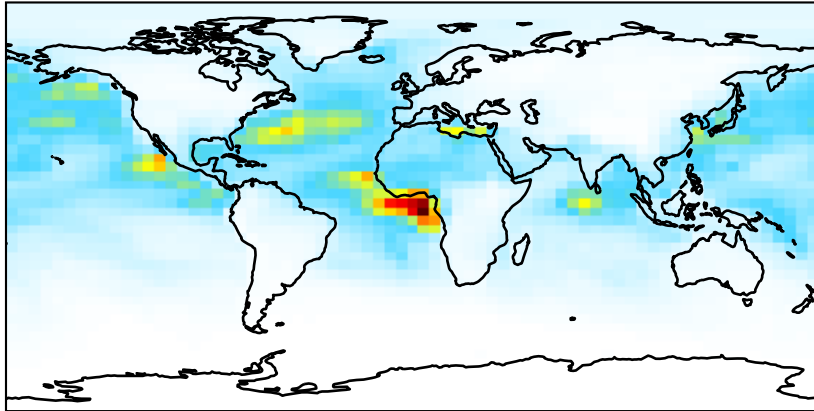


Ratio
Dev/Ref, Fixed Range

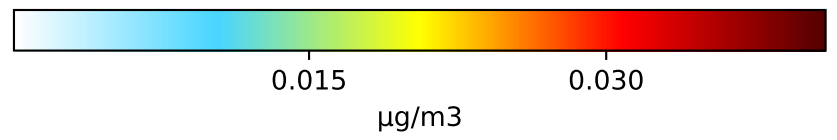
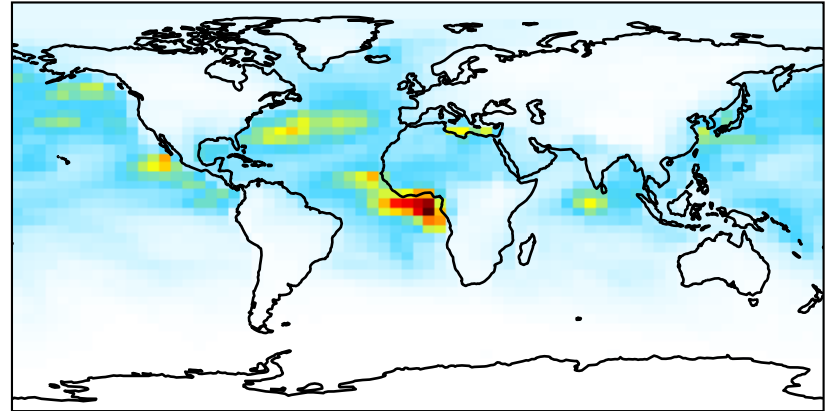


SpeciesConc_AERI (Jul2019)

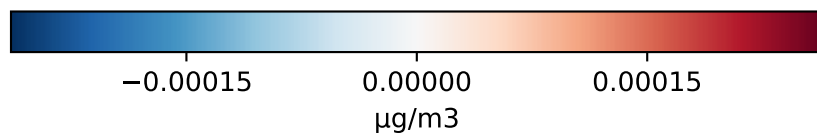
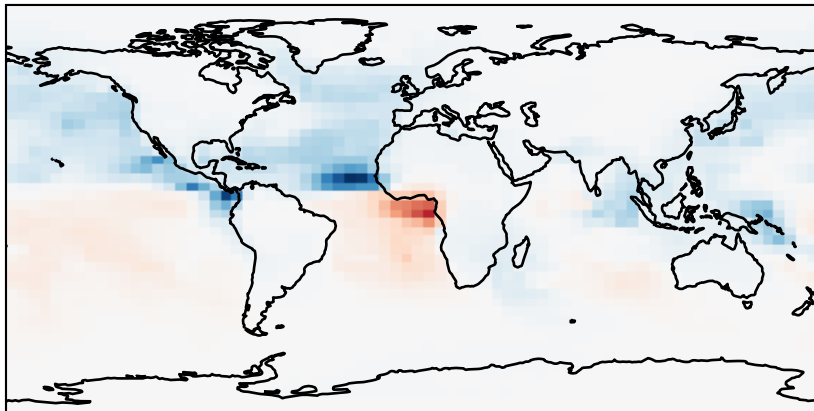
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



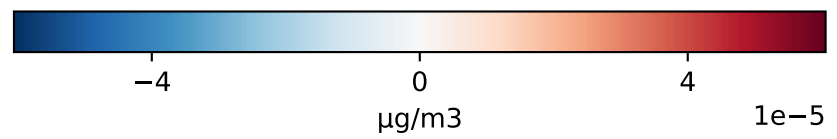
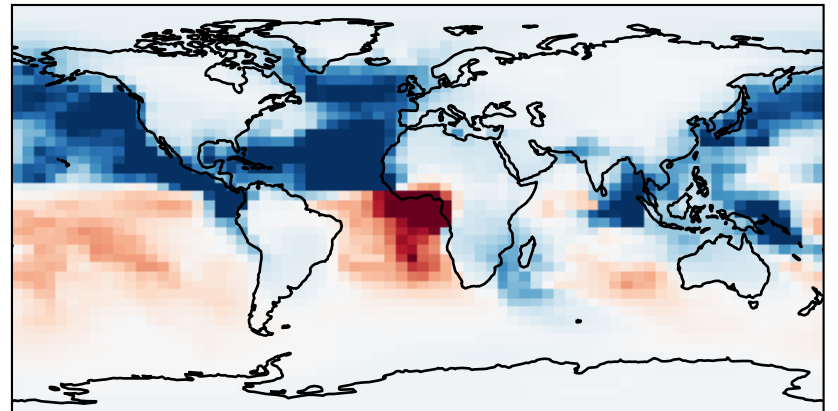
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



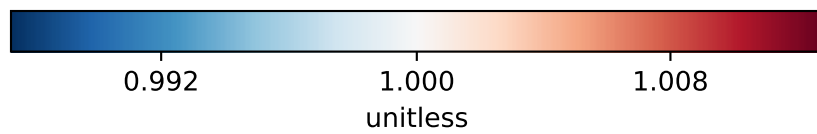
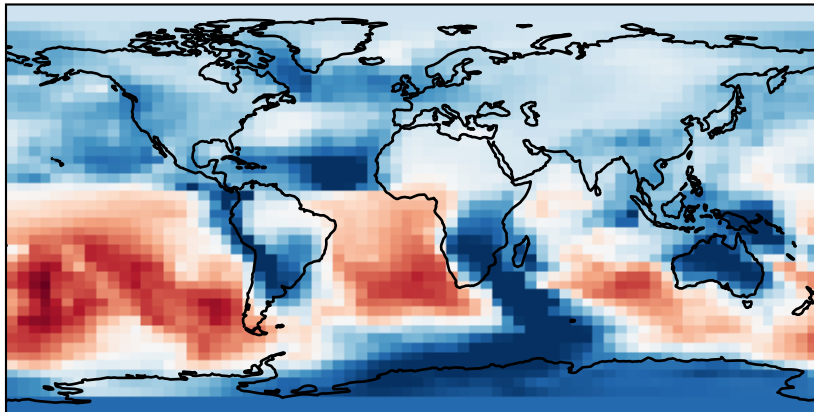
Difference
Dev - Ref, Dynamic Range



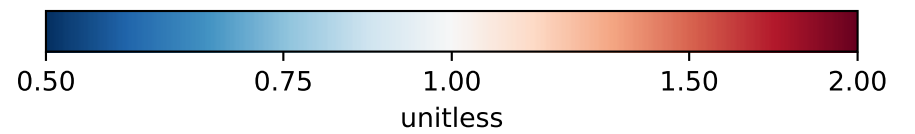
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

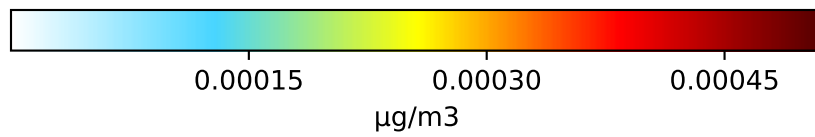
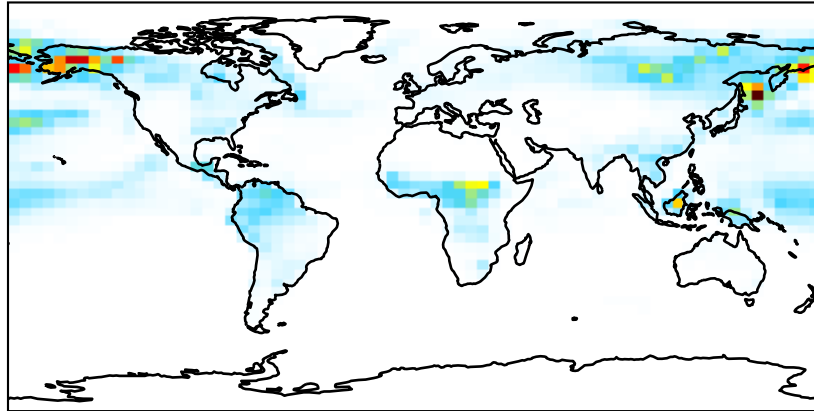


Ratio
Dev/Ref, Fixed Range

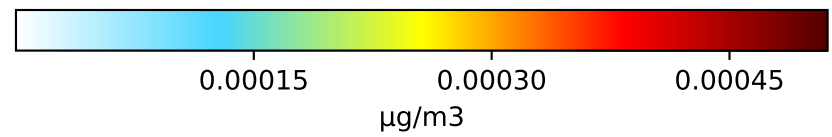
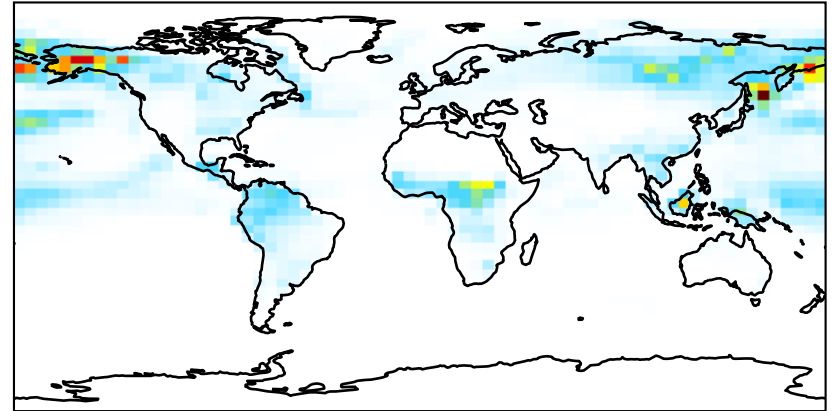


SpeciesConc_BrSALA (Jul2019)

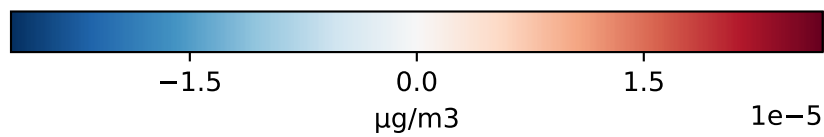
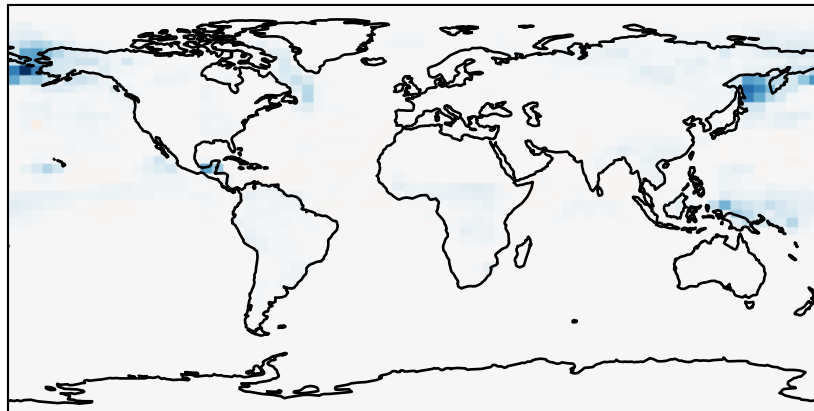
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



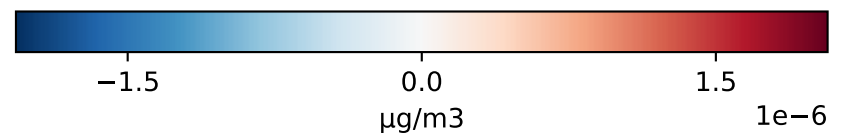
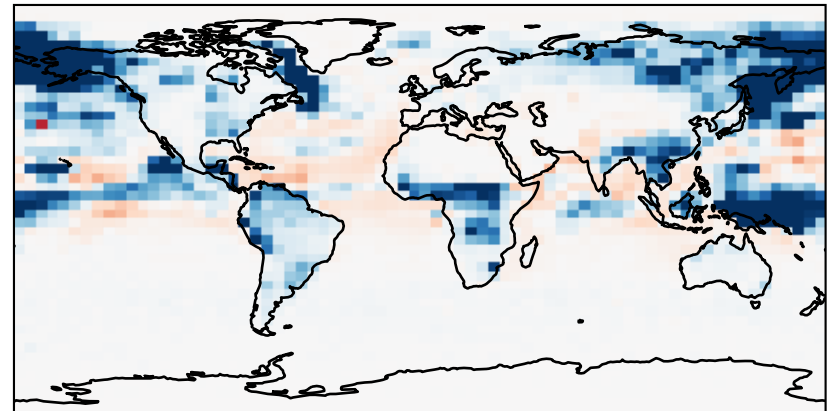
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



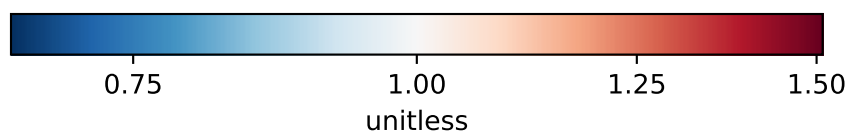
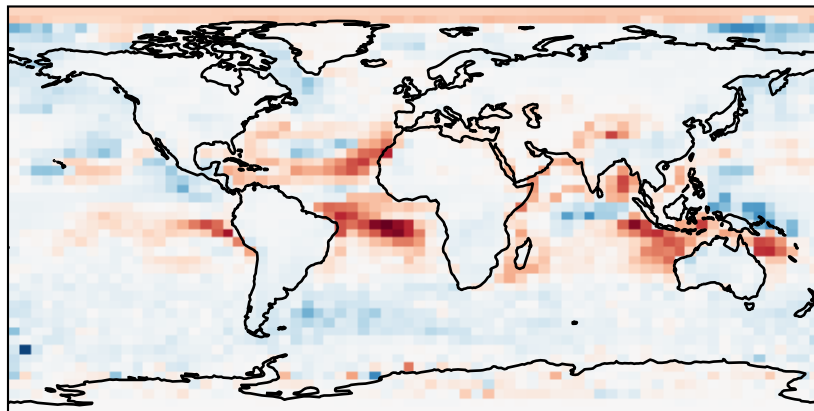
Difference
Dev - Ref, Dynamic Range



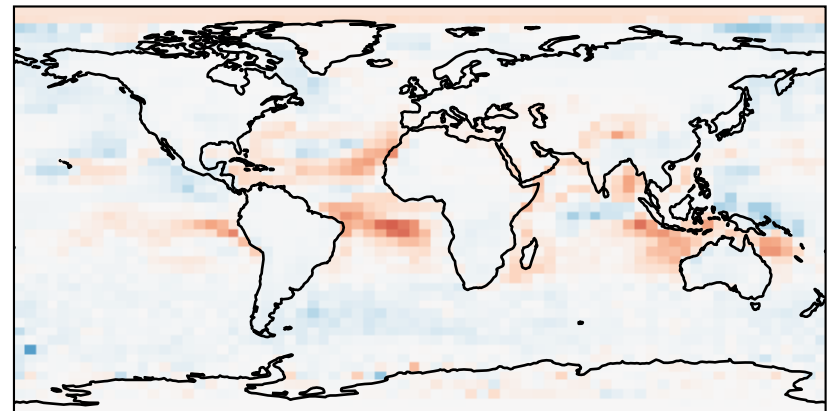
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

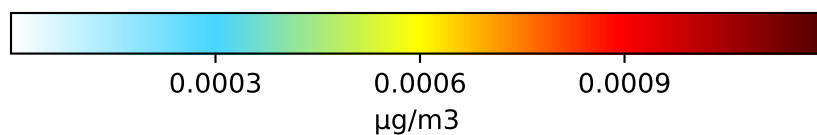
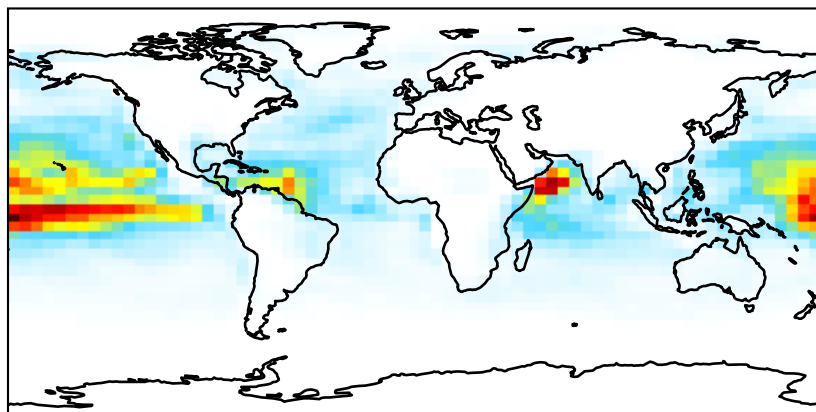


Ratio
Dev/Ref, Fixed Range

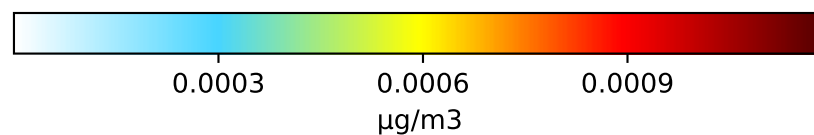
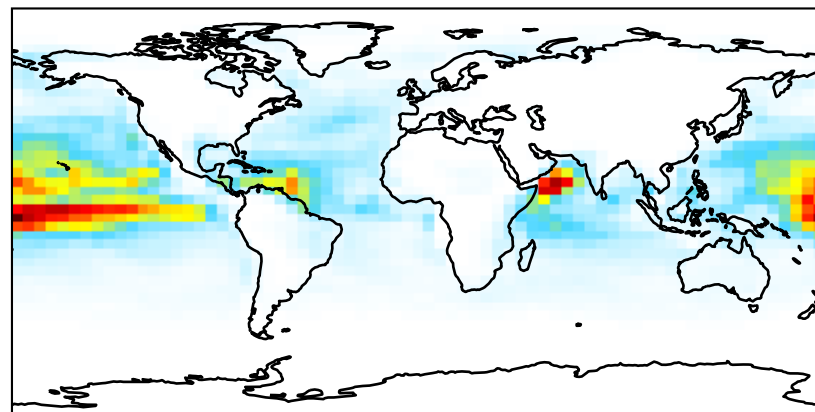


SpeciesConc_BrSALC (Jul2019)

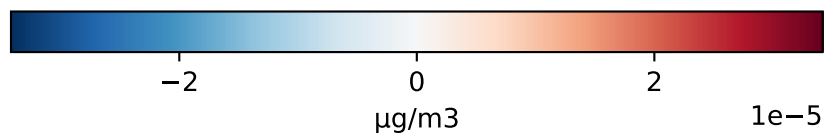
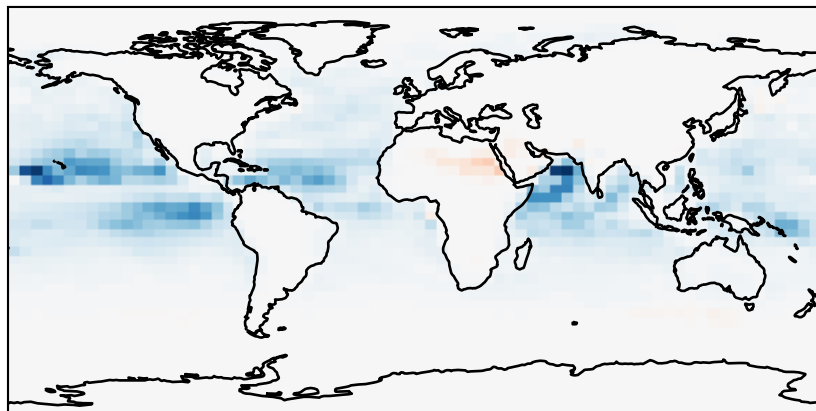
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



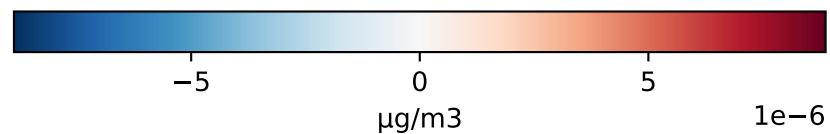
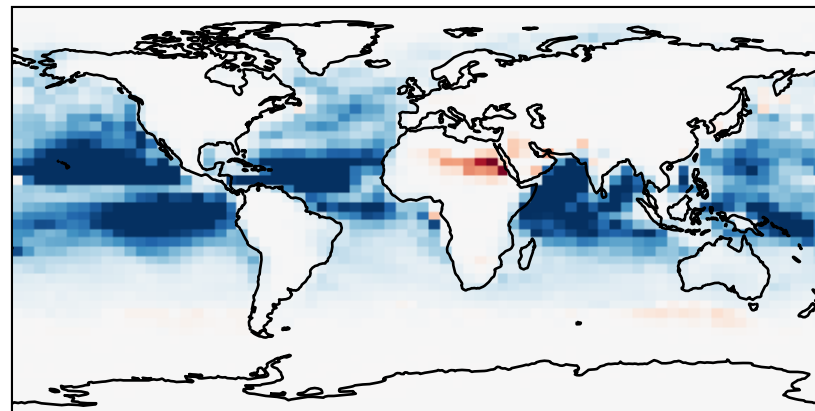
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



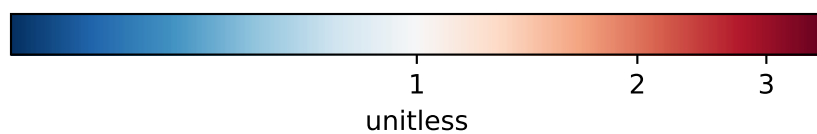
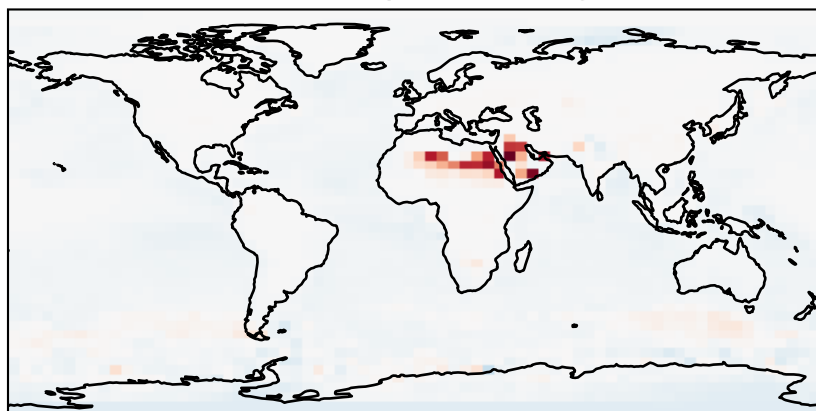
Difference
Dev - Ref, Dynamic Range



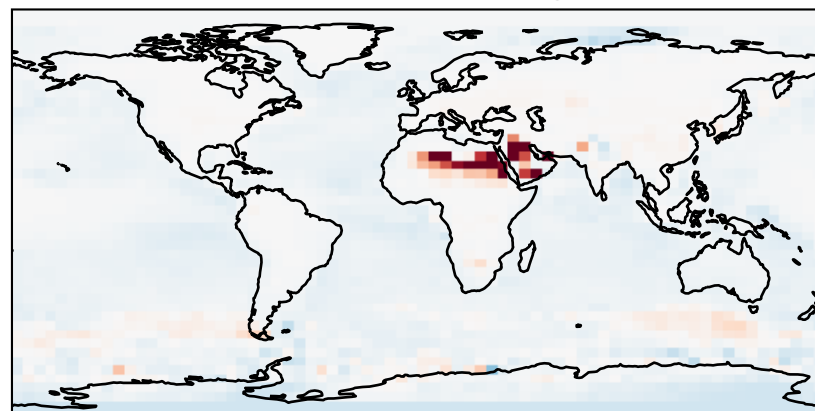
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

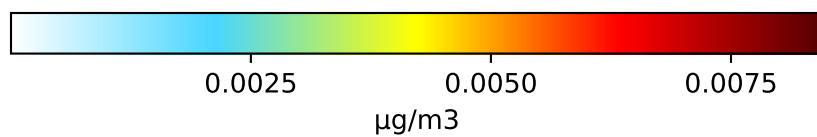
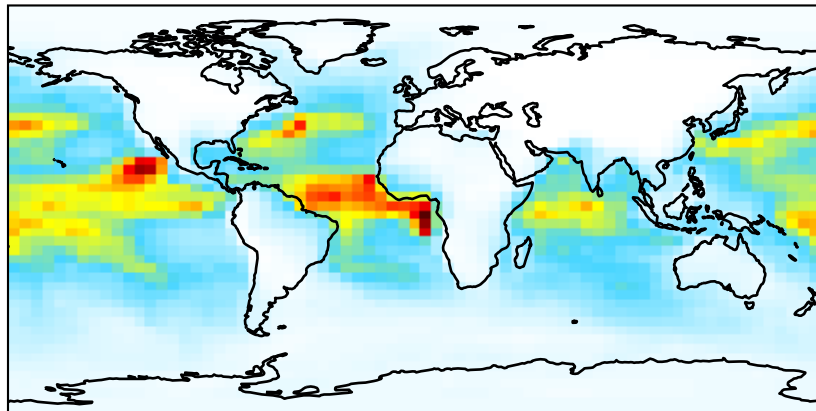


Ratio
Dev/Ref, Fixed Range

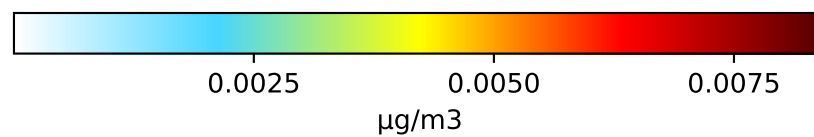
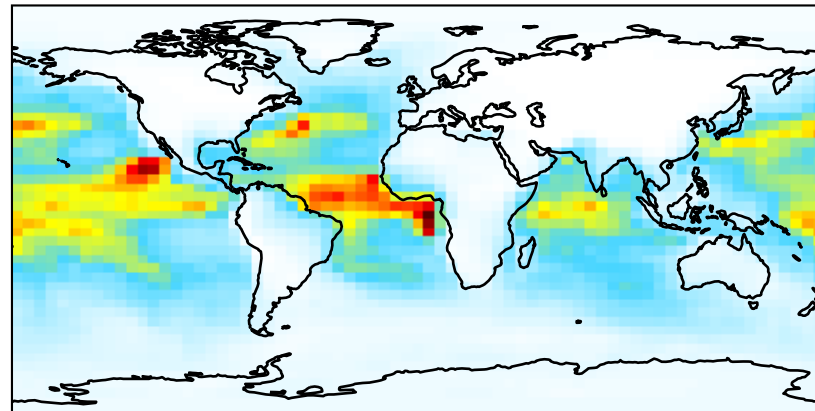


SpeciesConc_ISALA (Jul2019)

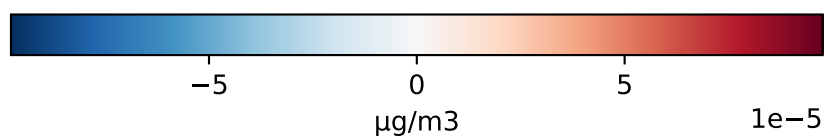
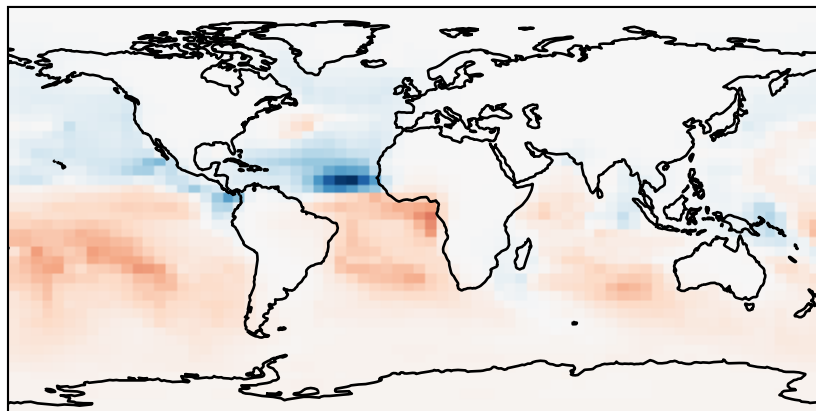
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



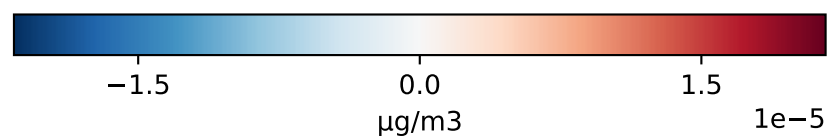
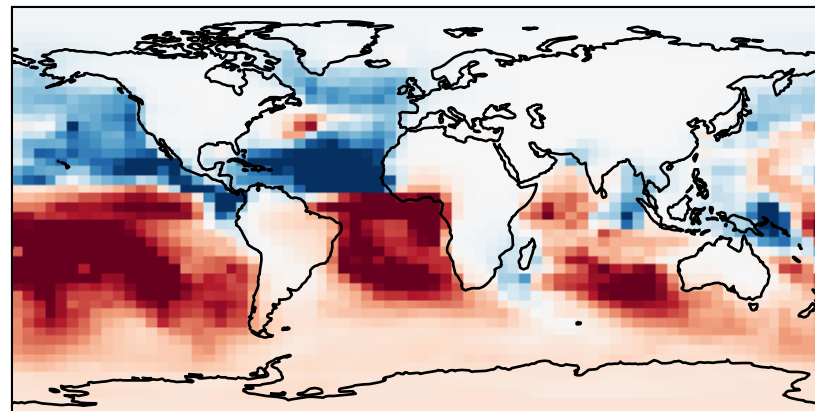
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



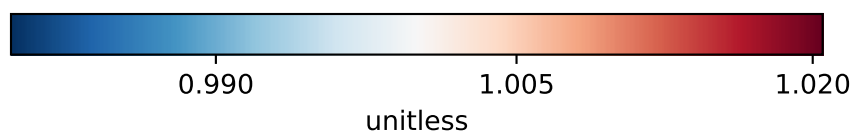
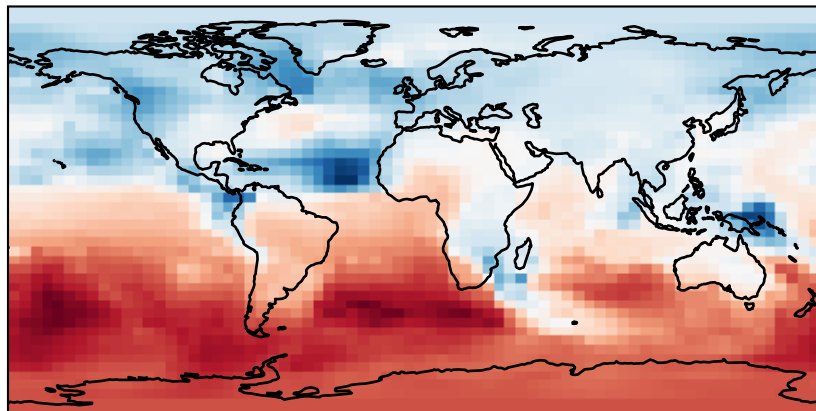
Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

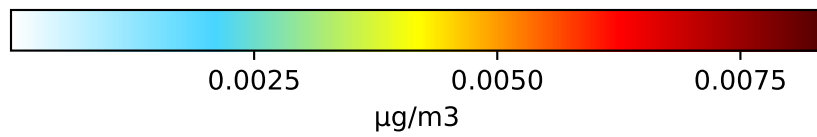
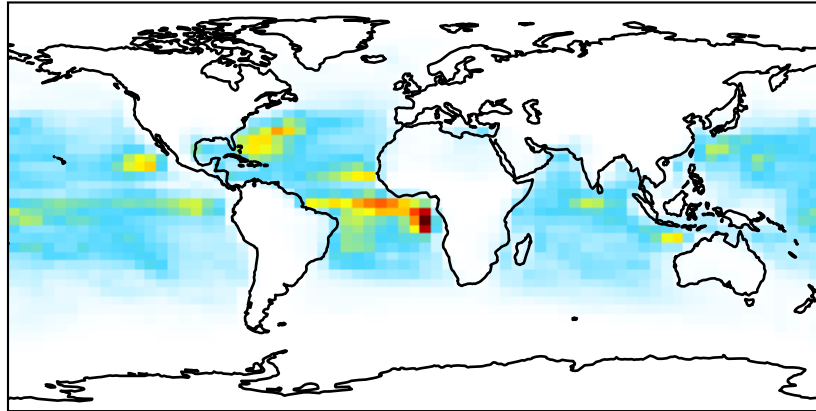


Ratio
Dev/Ref, Fixed Range

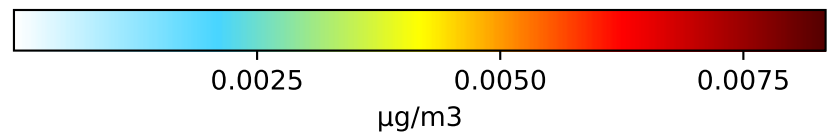
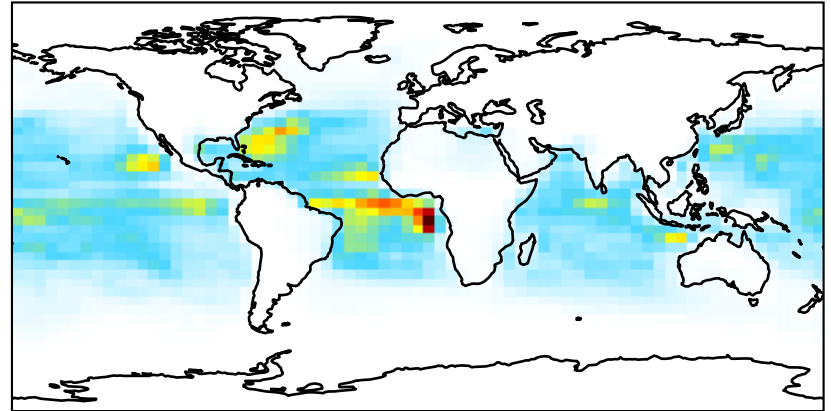


SpeciesConc_ISALC (Jul2019)

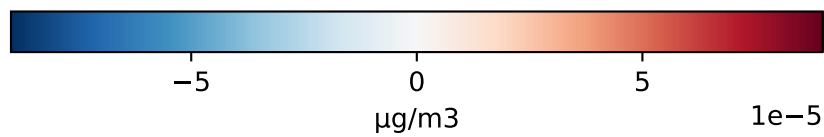
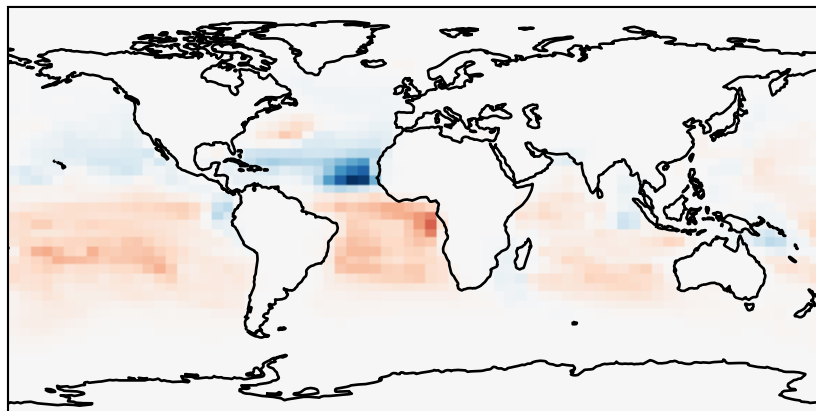
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



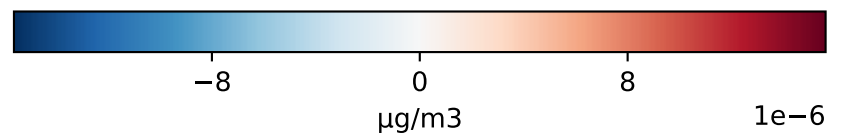
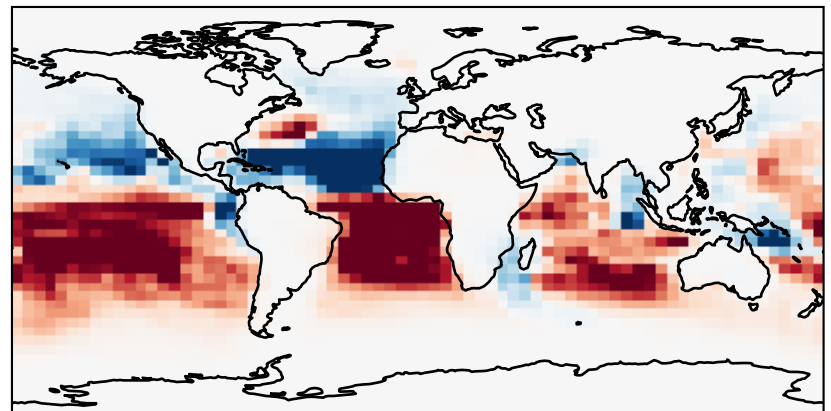
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



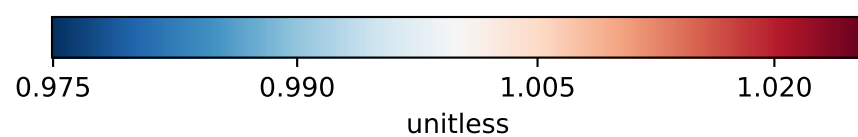
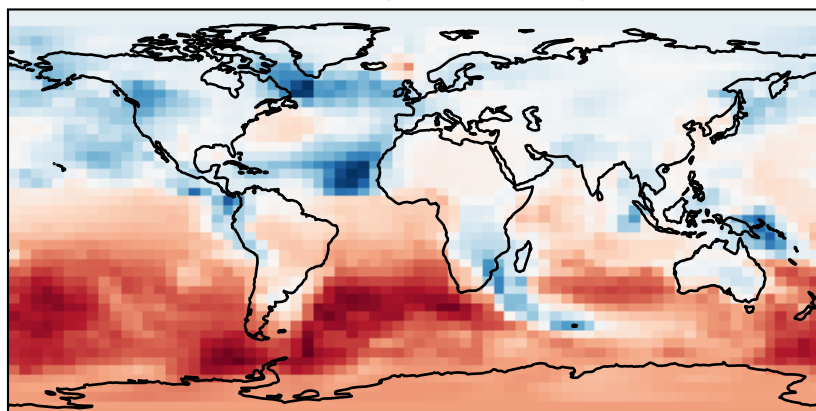
Difference
Dev - Ref, Dynamic Range



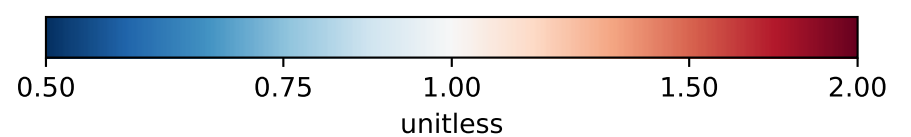
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

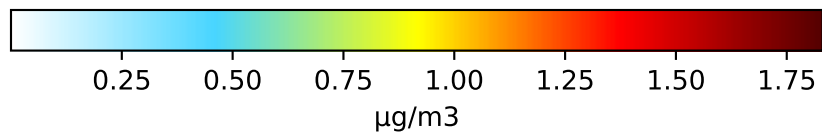
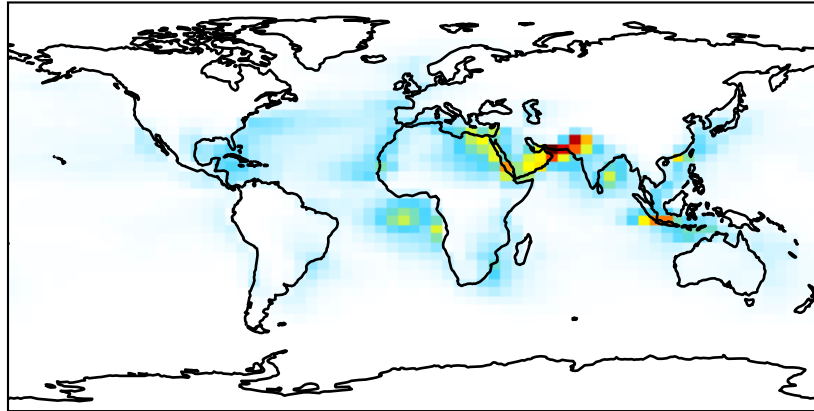


Ratio
Dev/Ref, Fixed Range

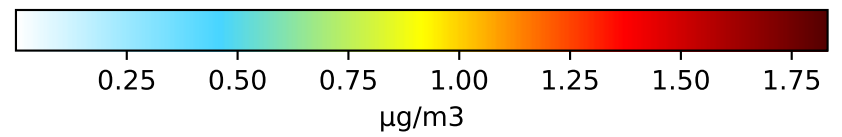
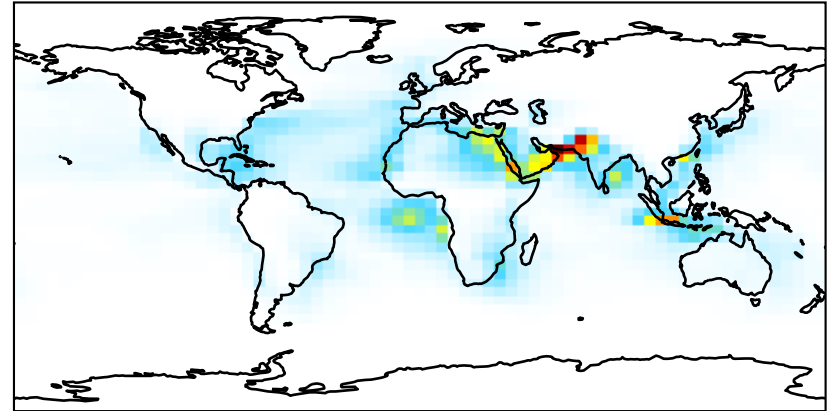


SpeciesConc_NITs (Jul2019)

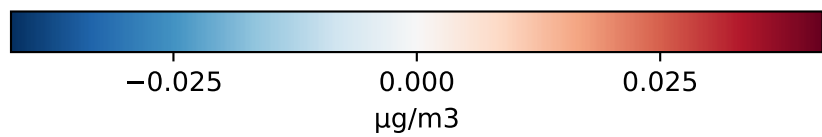
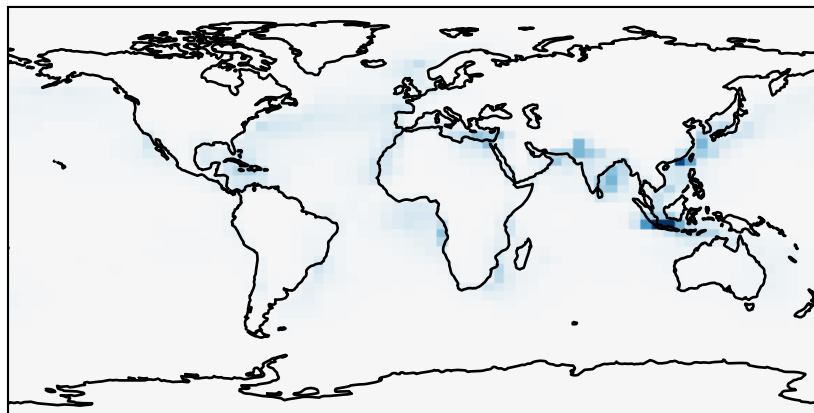
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



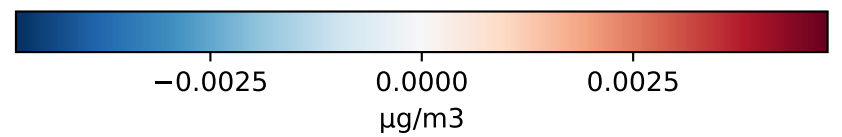
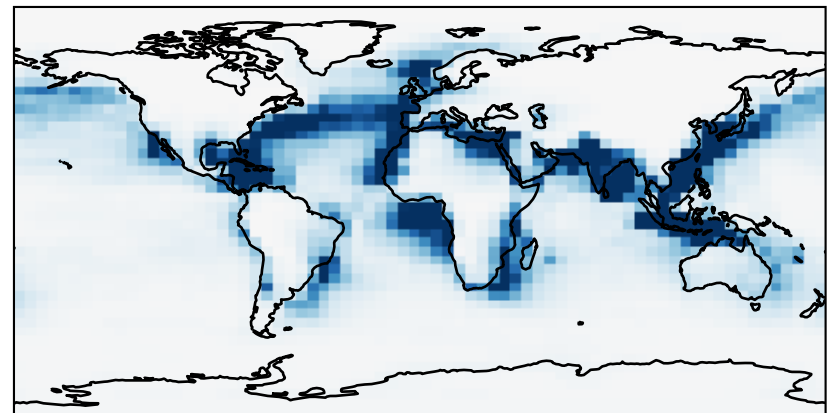
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



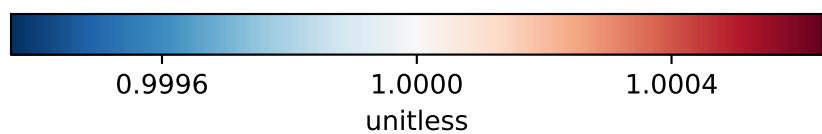
Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

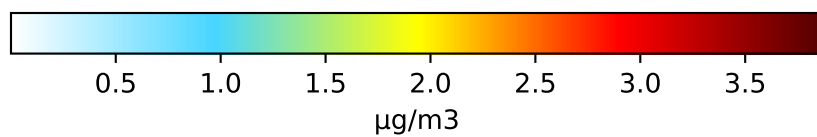
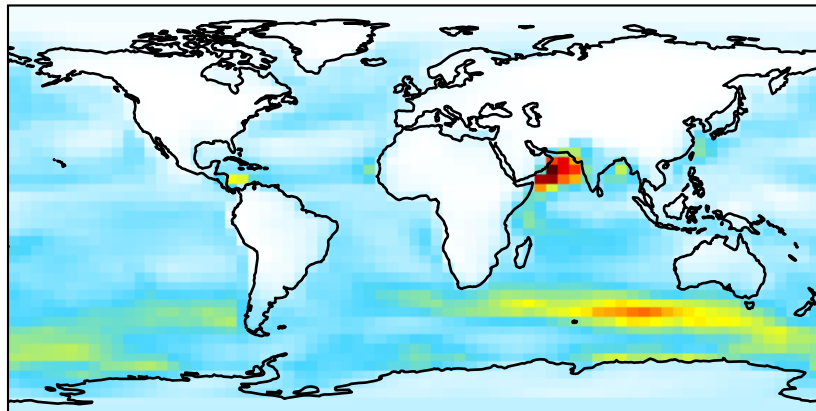


Ratio
Dev/Ref, Fixed Range

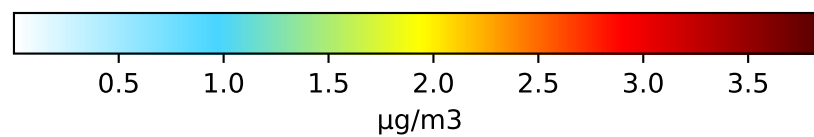
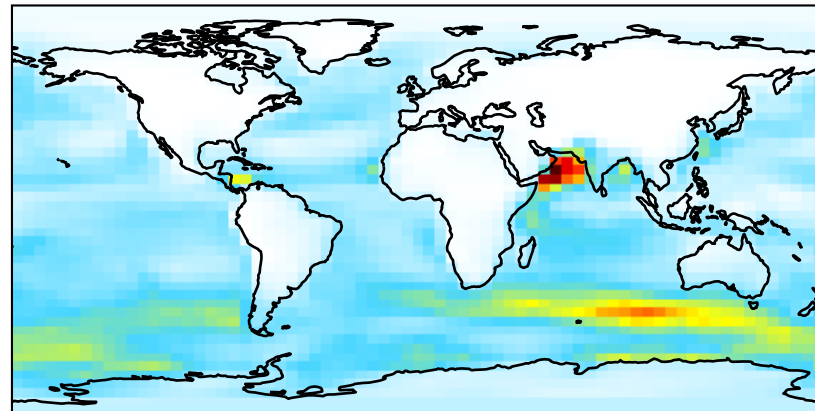


SpeciesConc_SALA (Jul2019)

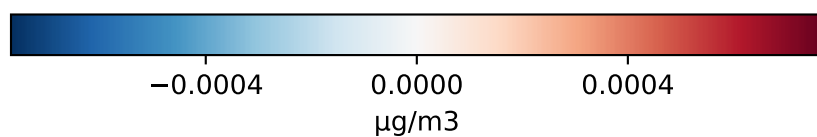
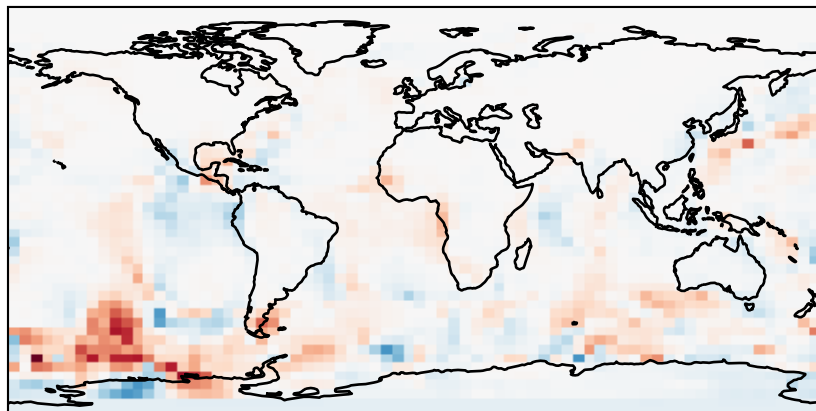
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



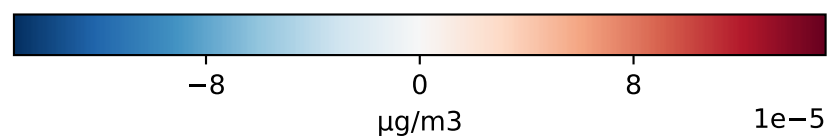
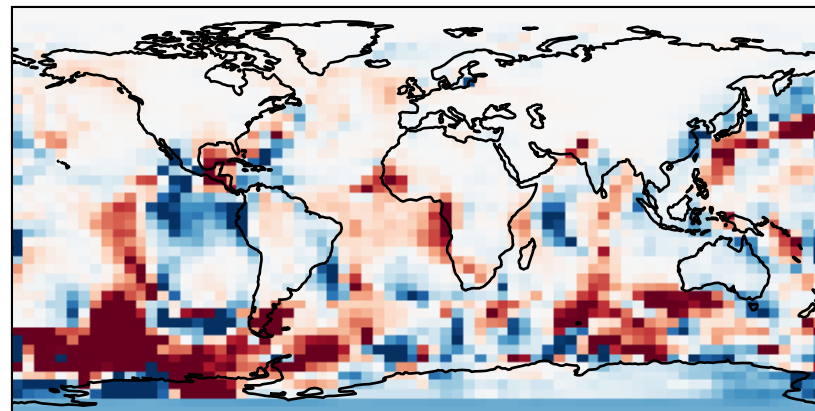
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



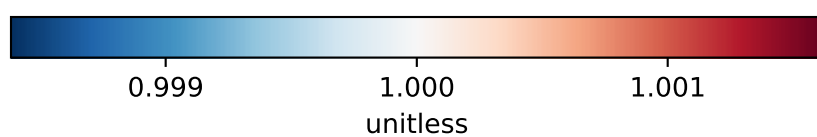
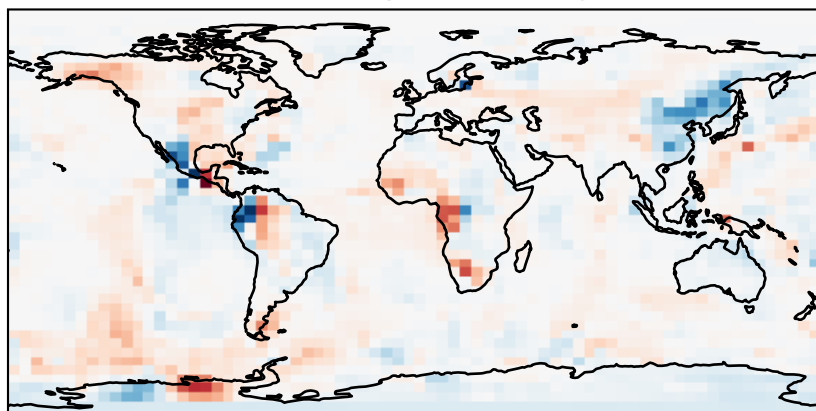
Difference
Dev - Ref, Dynamic Range



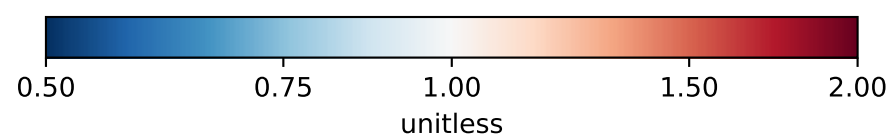
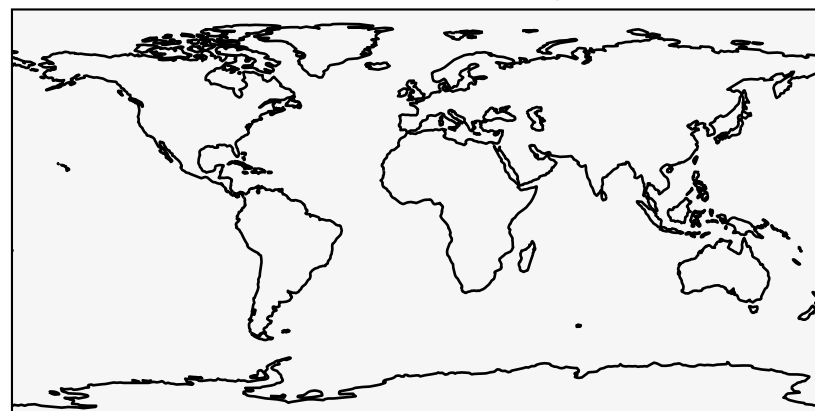
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

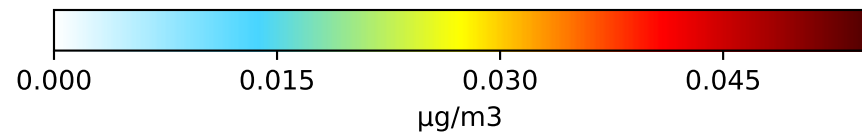
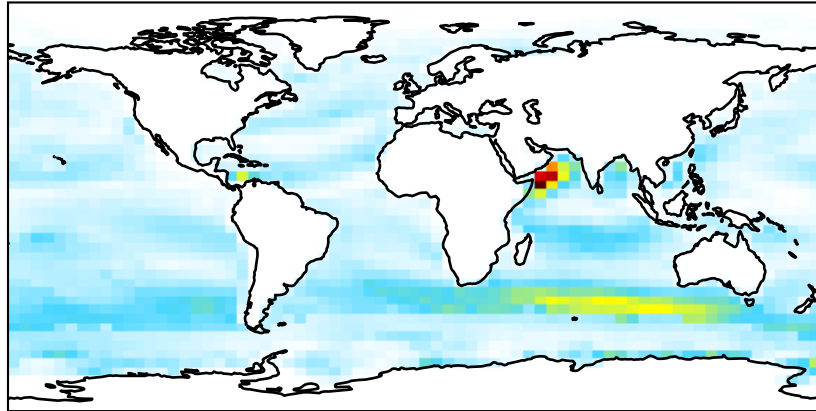


Ratio
Dev/Ref, Fixed Range

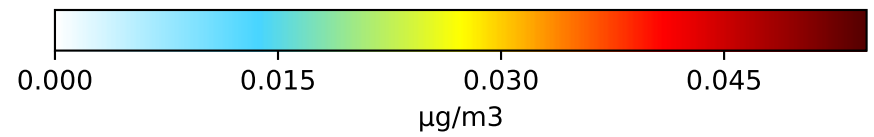
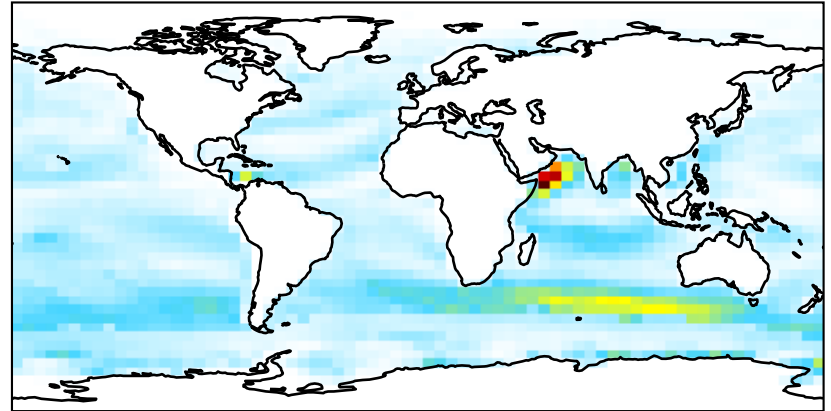


SpeciesConc_SALAAL (Jul2019)

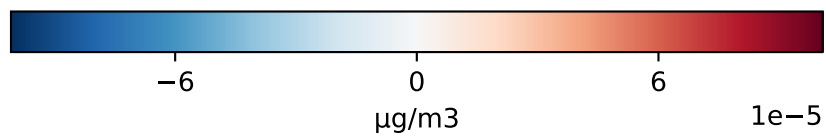
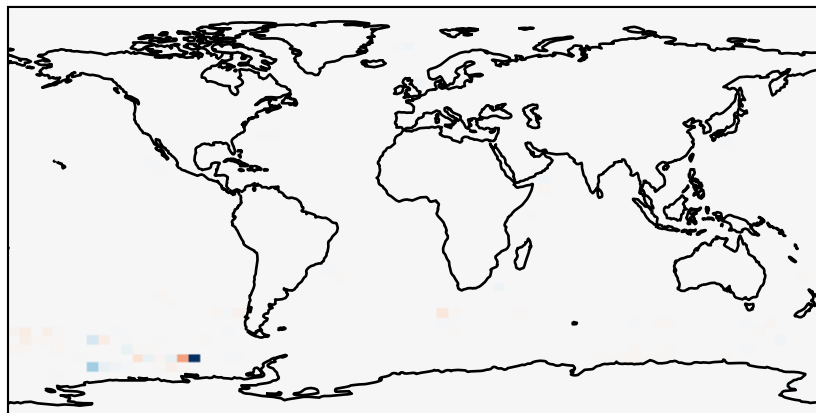
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



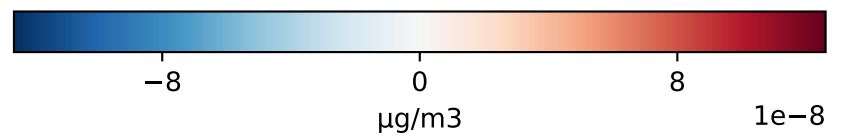
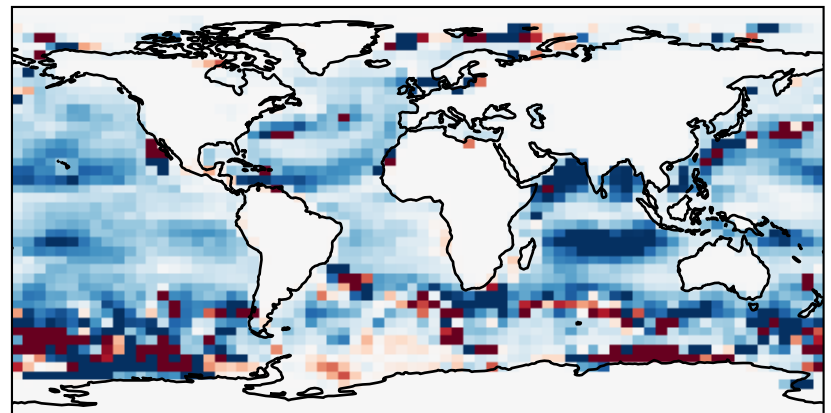
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



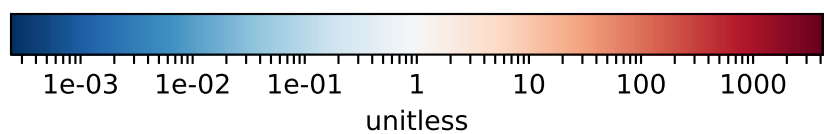
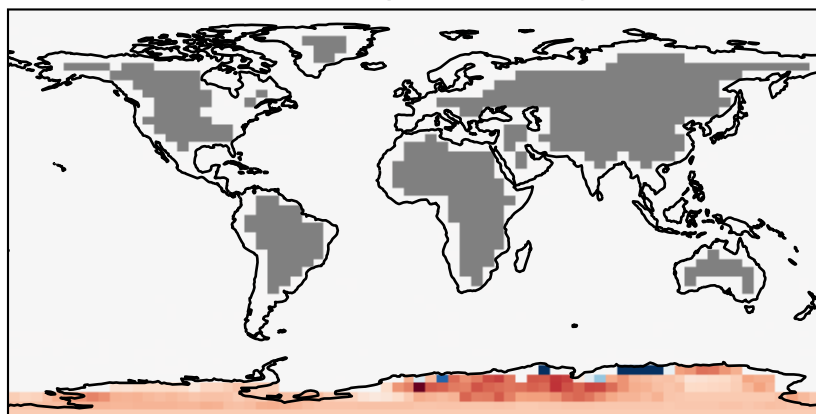
Difference
Dev - Ref, Dynamic Range



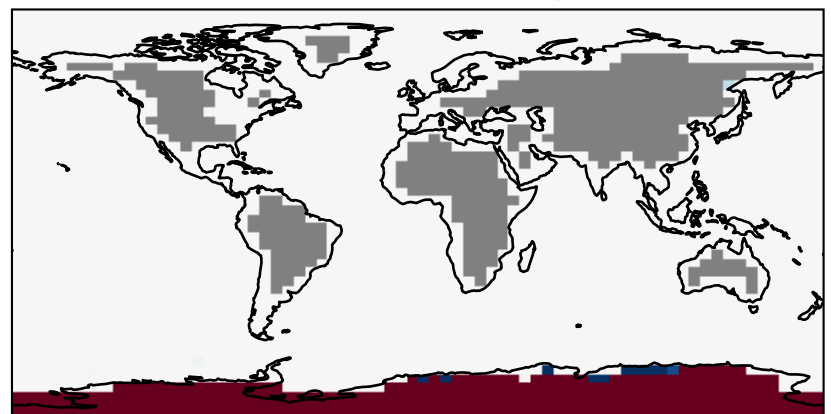
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

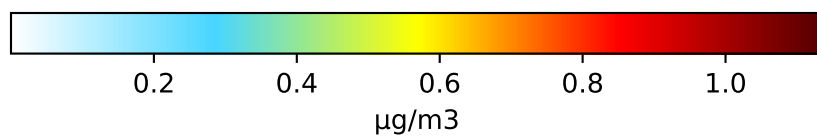
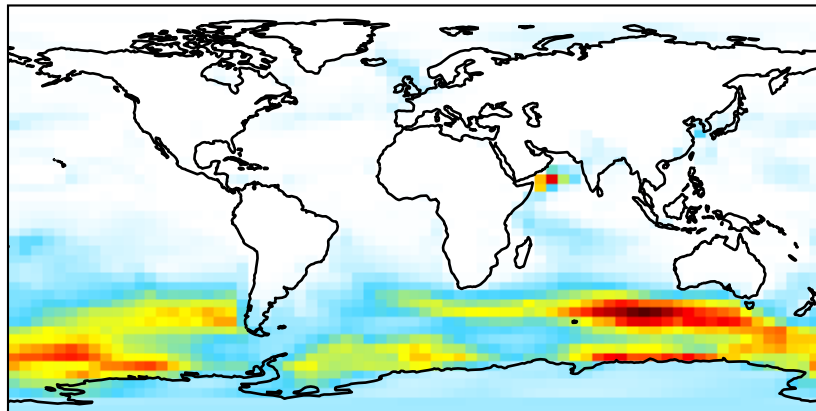


Ratio
Dev/Ref, Fixed Range

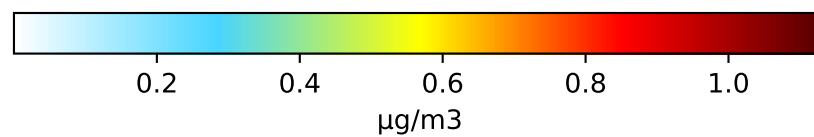
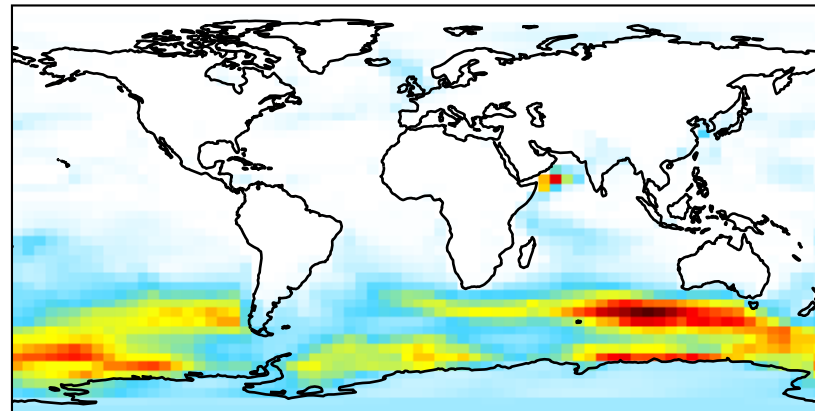


SpeciesConc_SALACL (Jul2019)

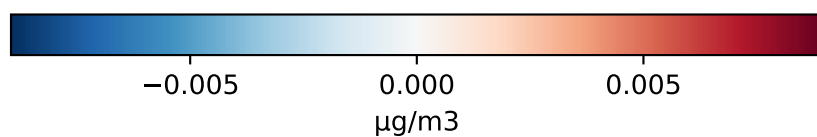
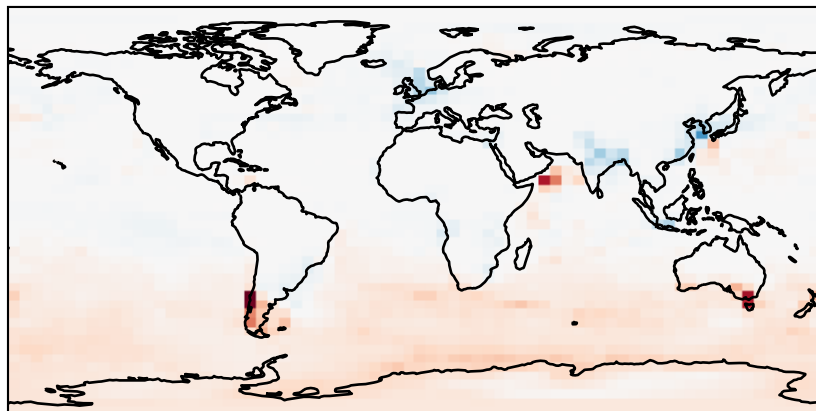
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



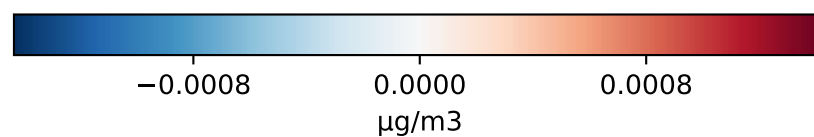
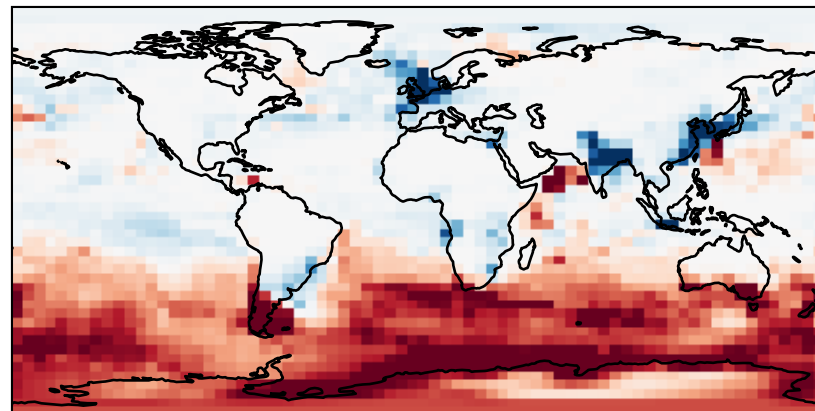
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



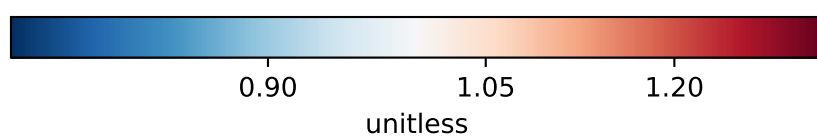
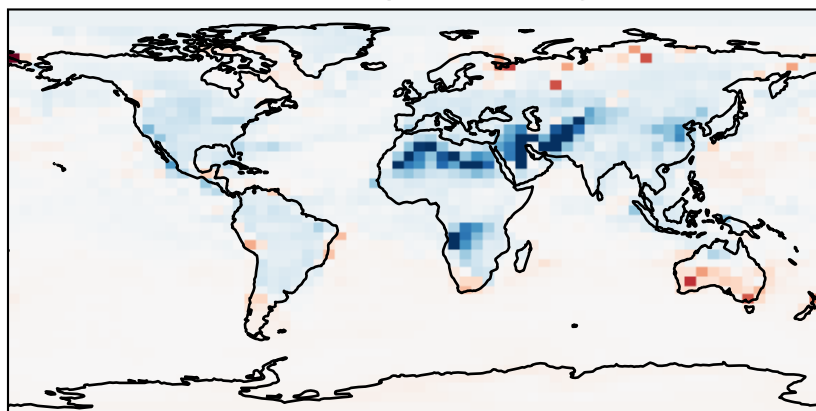
Difference
Dev - Ref, Dynamic Range



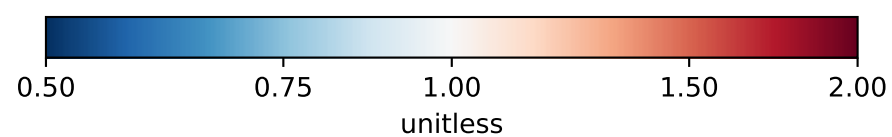
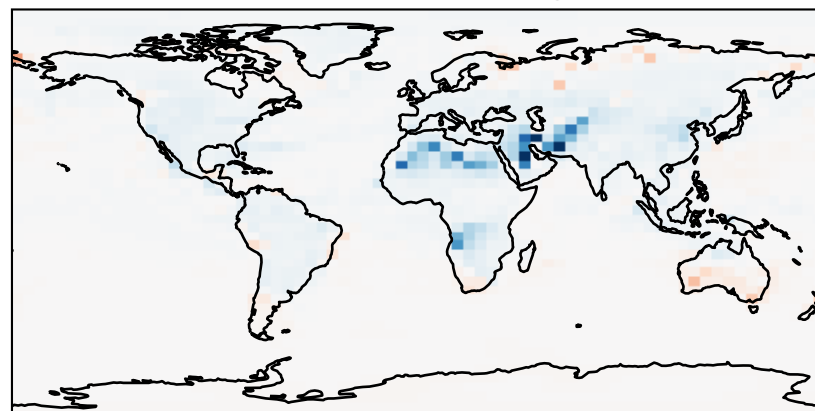
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

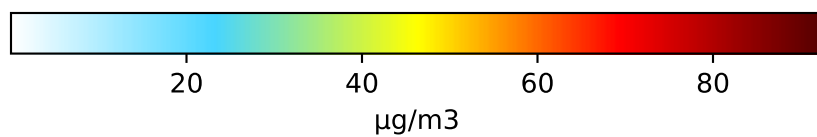
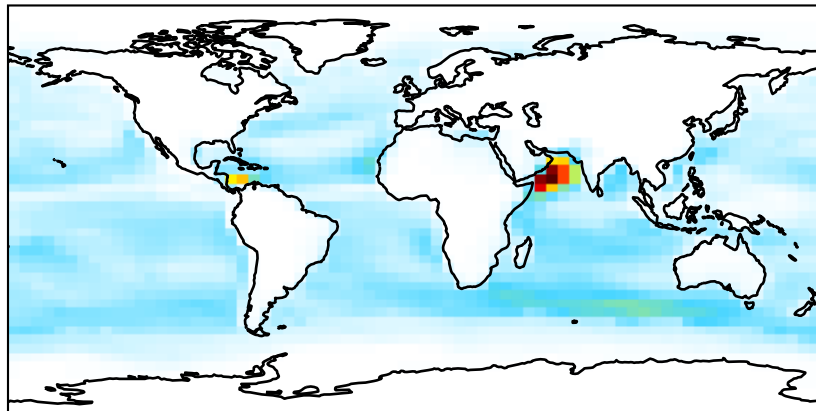


Ratio
Dev/Ref, Fixed Range

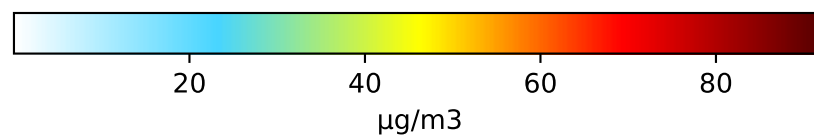
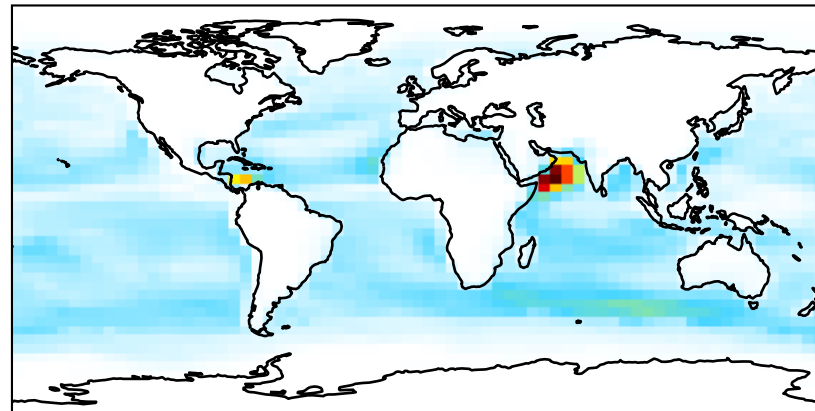


SpeciesConc_SALC (Jul2019)

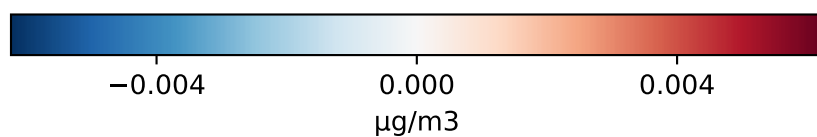
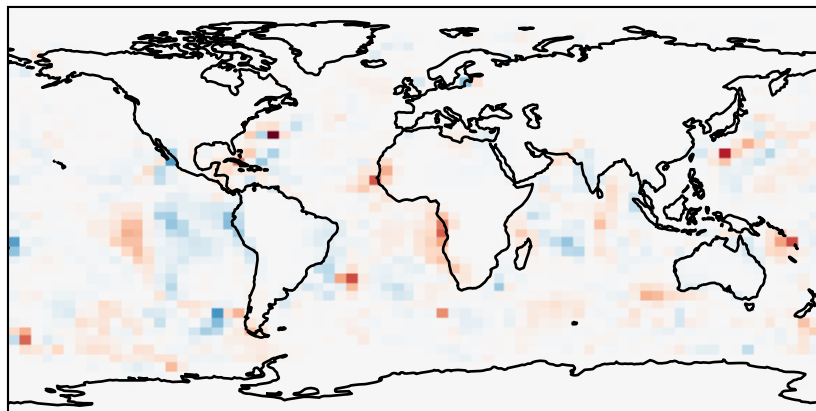
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



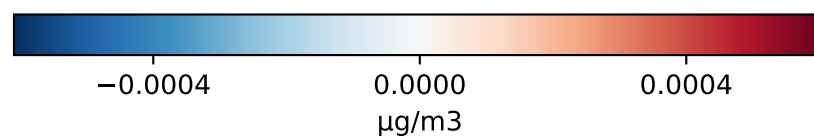
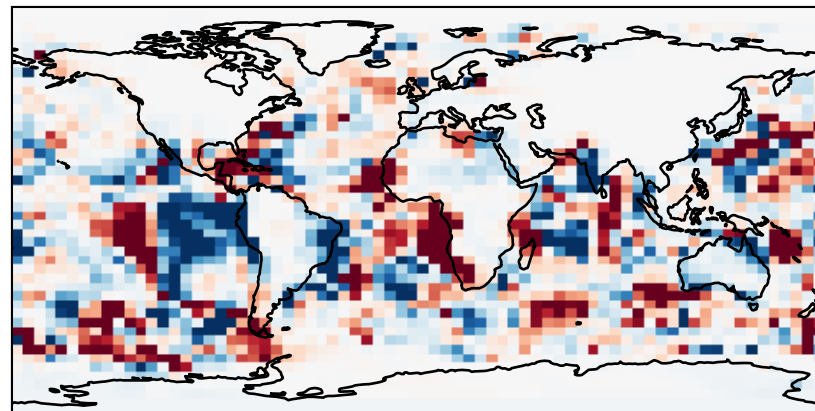
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



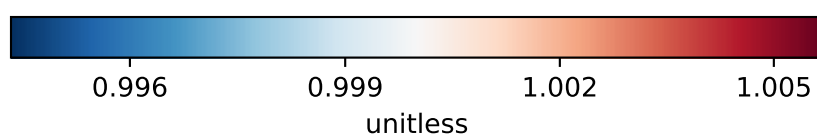
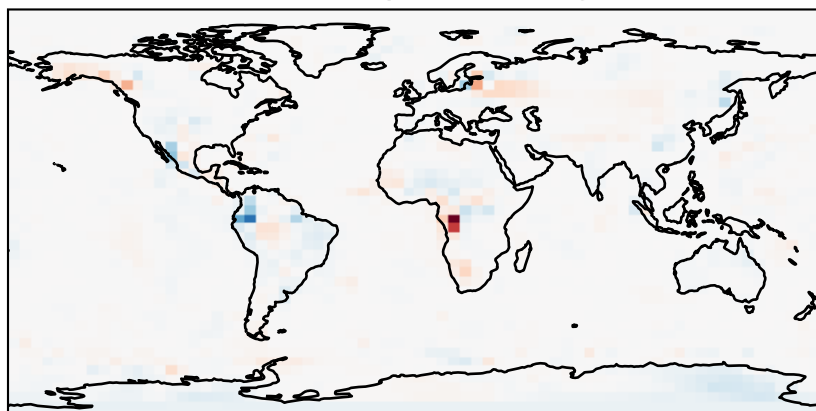
Difference
Dev - Ref, Dynamic Range



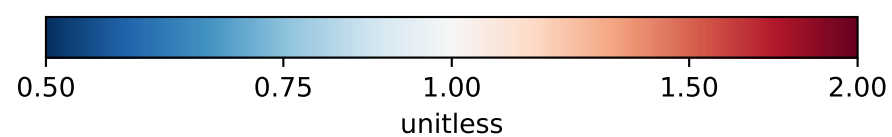
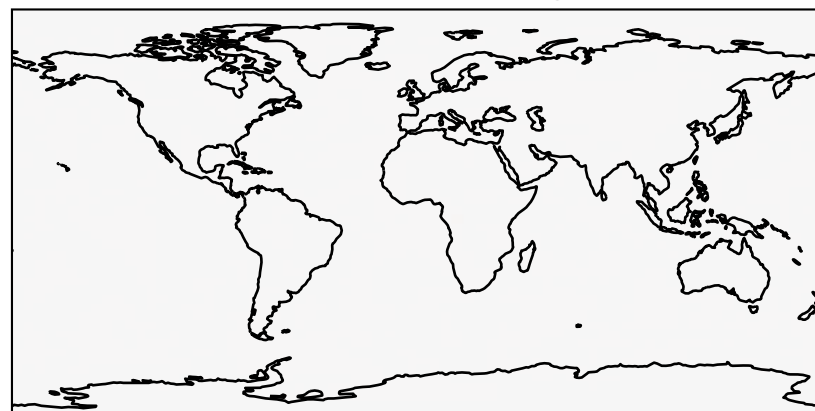
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

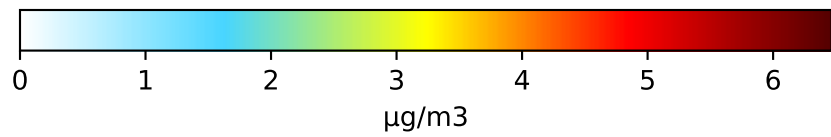
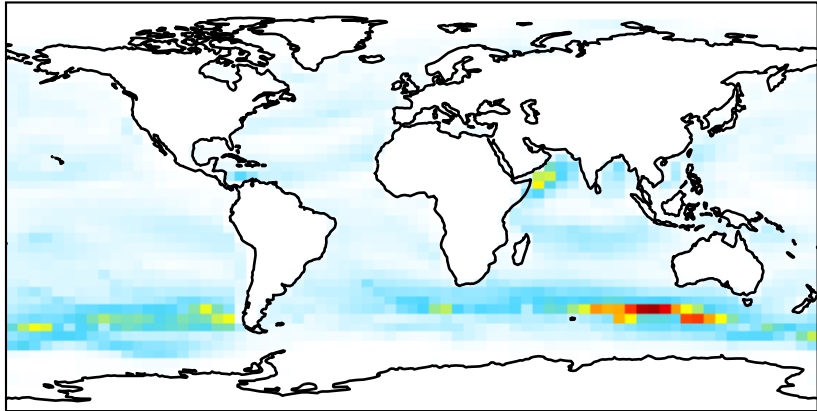


Ratio
Dev/Ref, Fixed Range

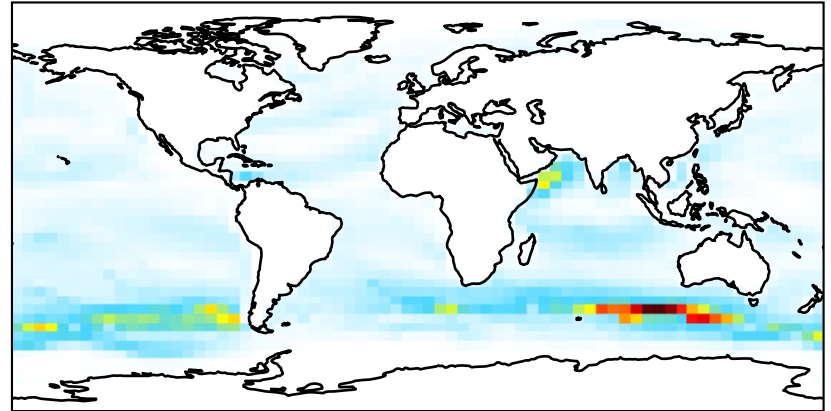


SpeciesConc_SALCAL (Jul2019)

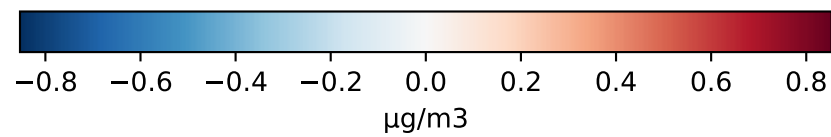
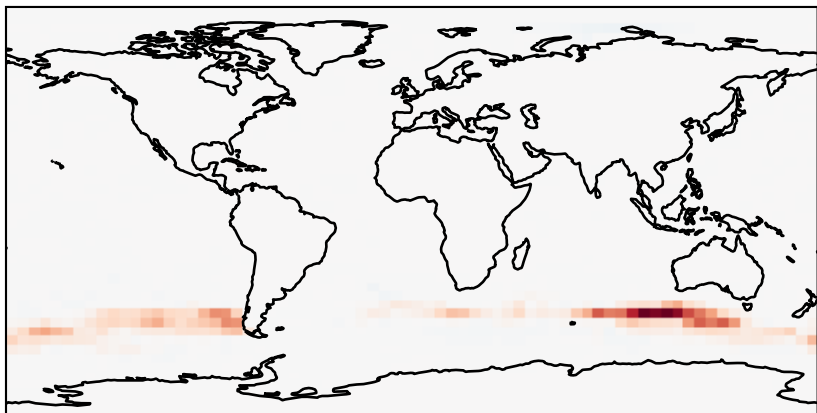
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



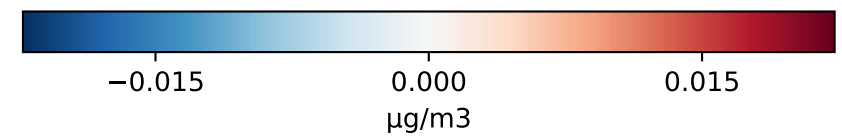
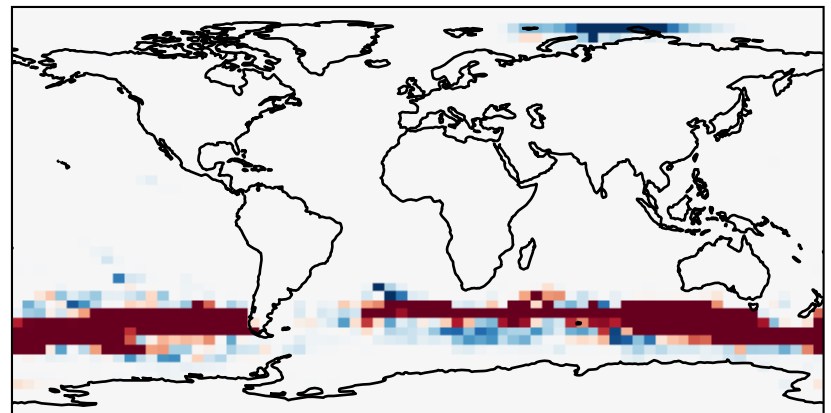
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



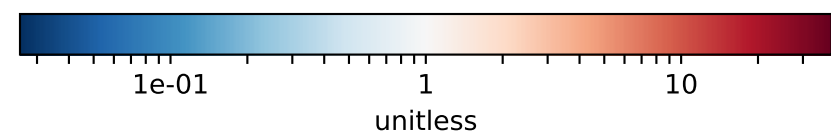
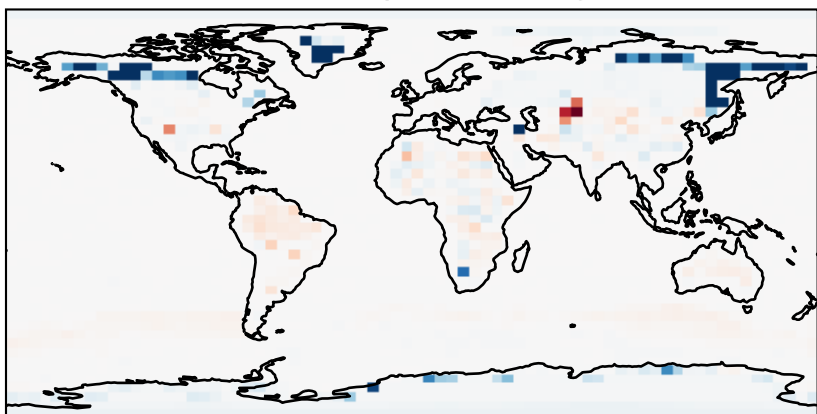
Difference
Dev - Ref, Dynamic Range



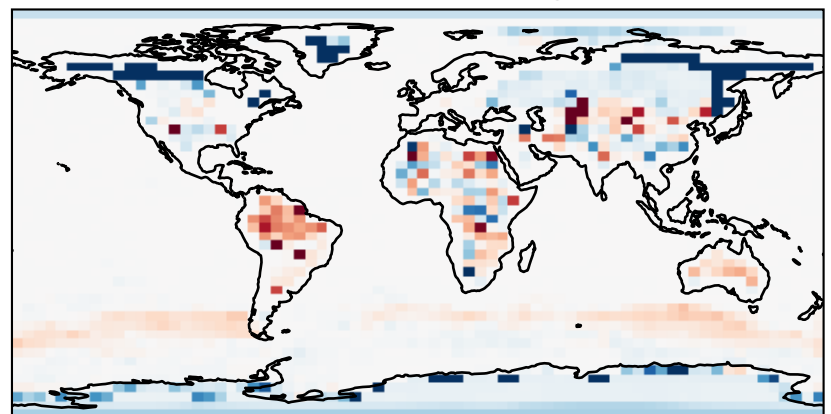
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

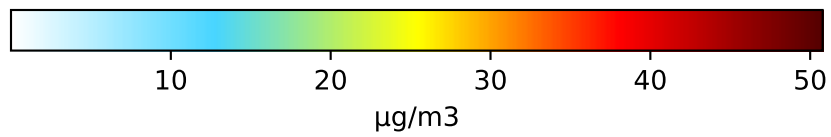
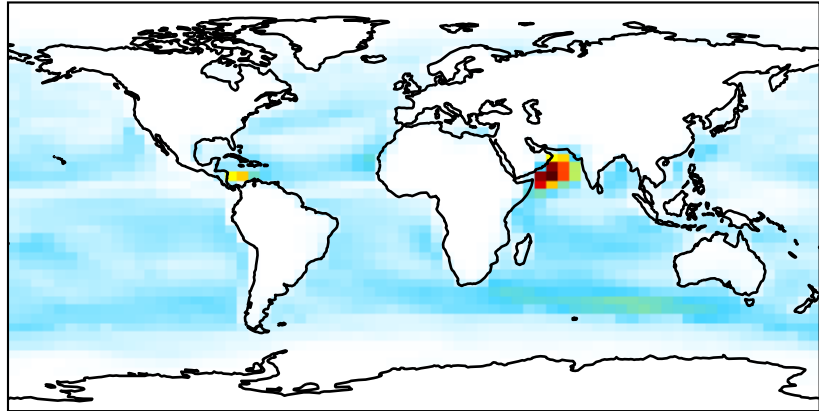


Ratio
Dev/Ref, Fixed Range

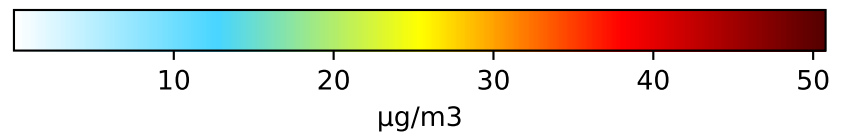
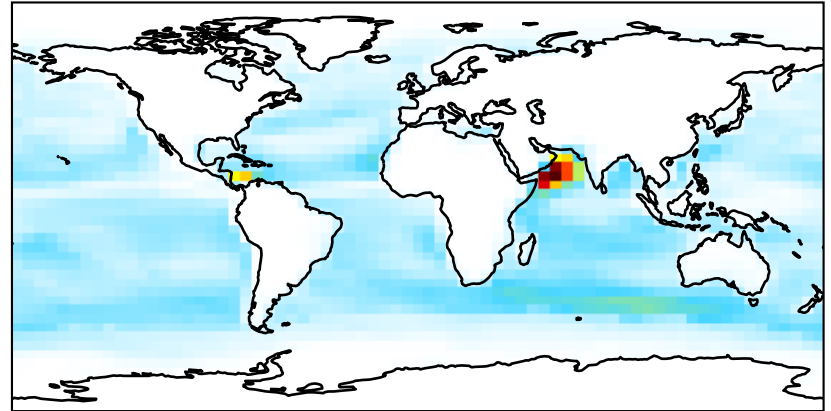


SpeciesConc_SALCCL (Jul2019)

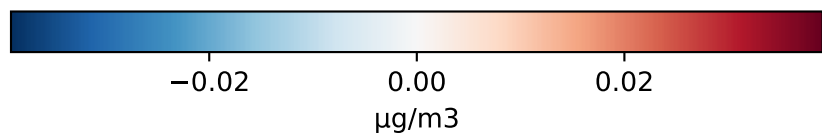
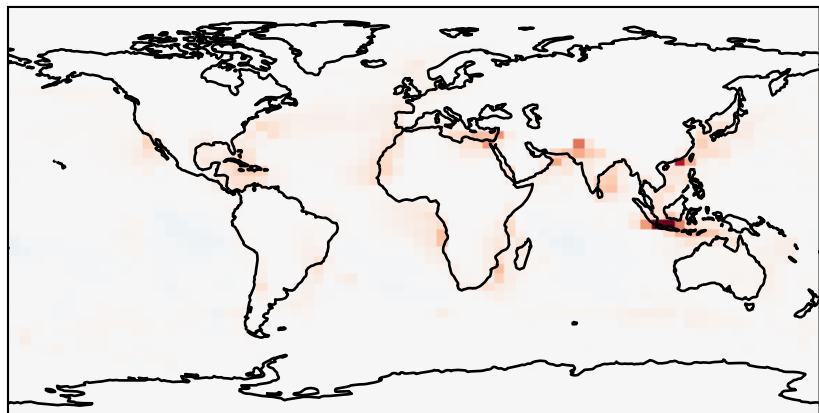
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



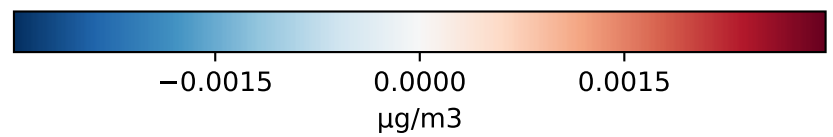
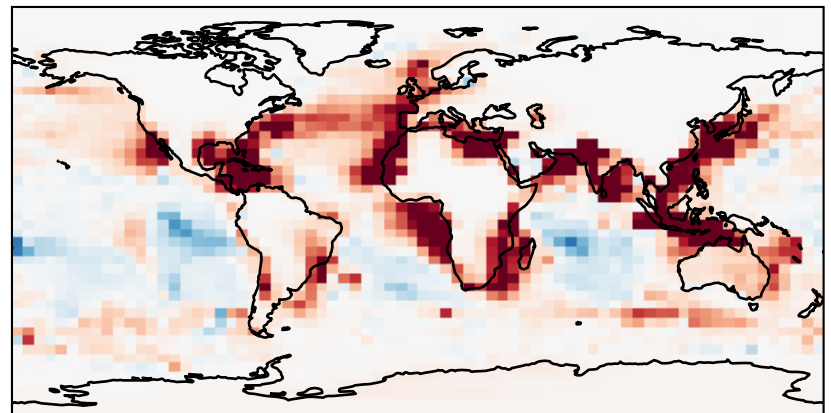
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



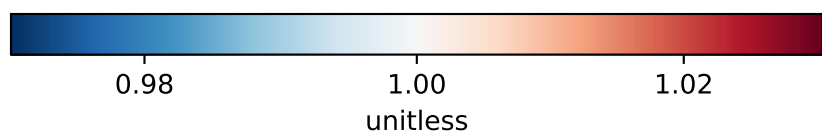
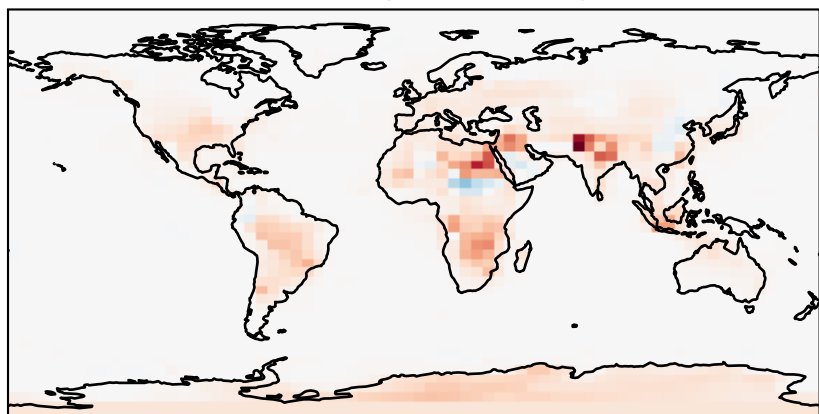
Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

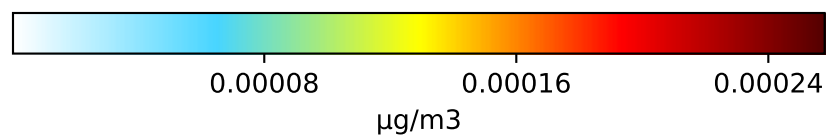
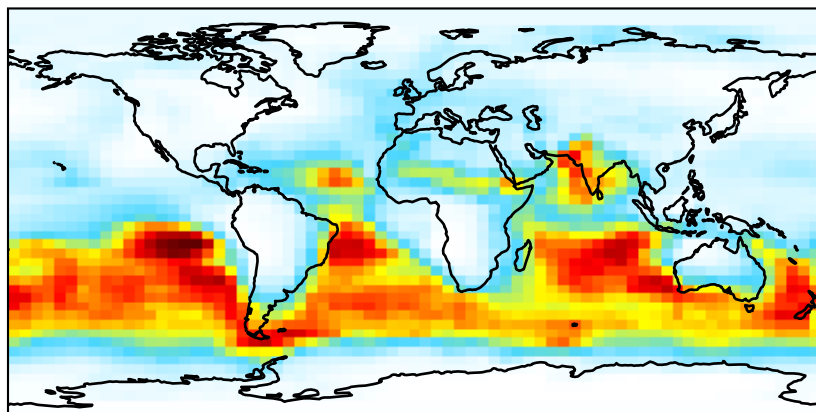


Ratio
Dev/Ref, Fixed Range

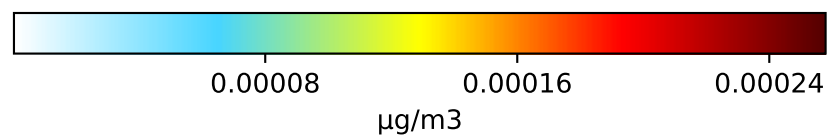
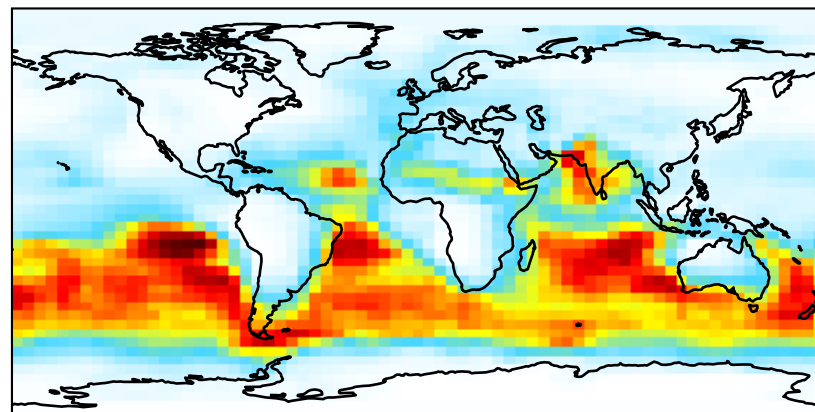


SpeciesConc_SO4s (Jul2019)

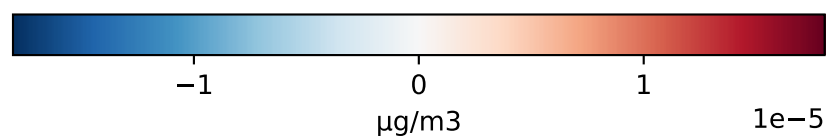
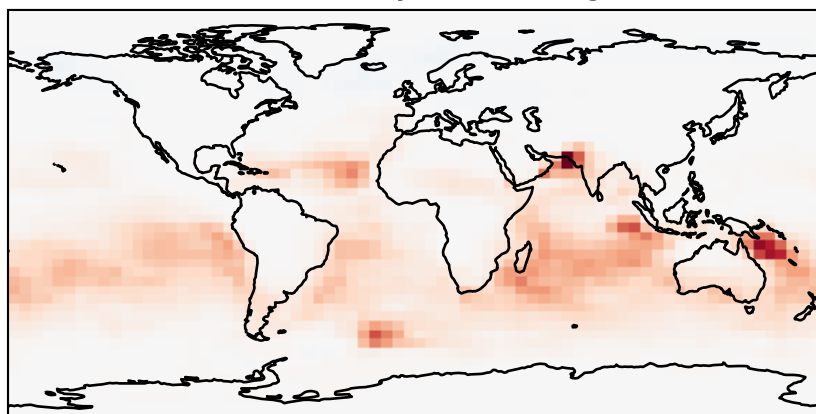
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



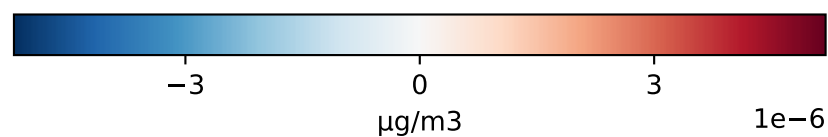
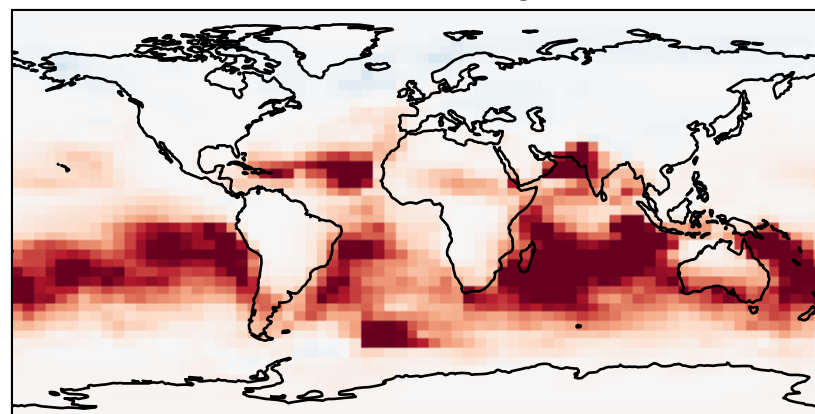
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



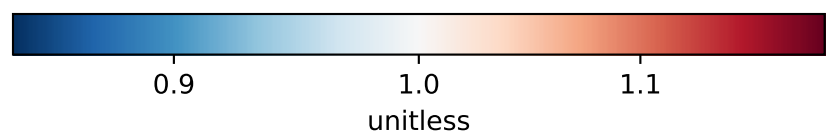
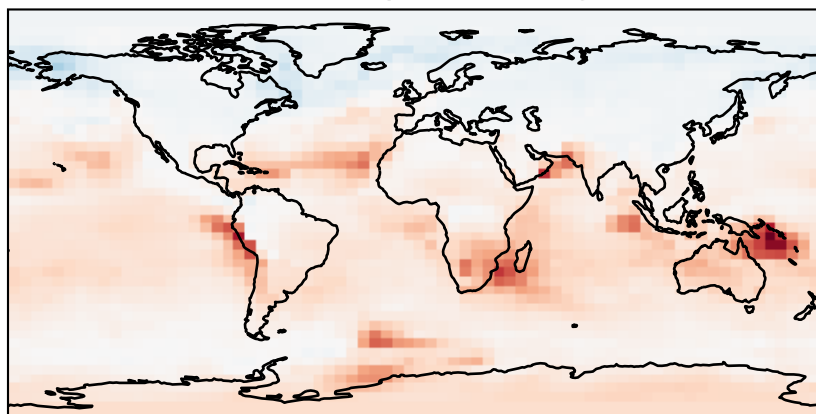
Difference
Dev - Ref, Dynamic Range



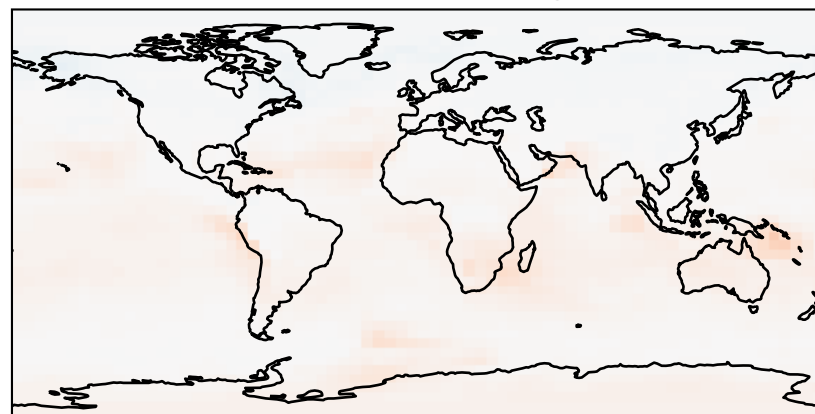
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

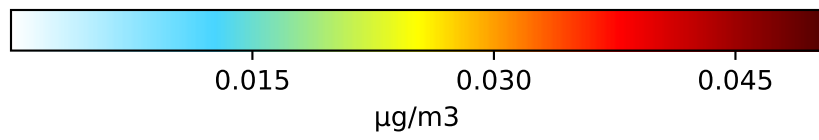
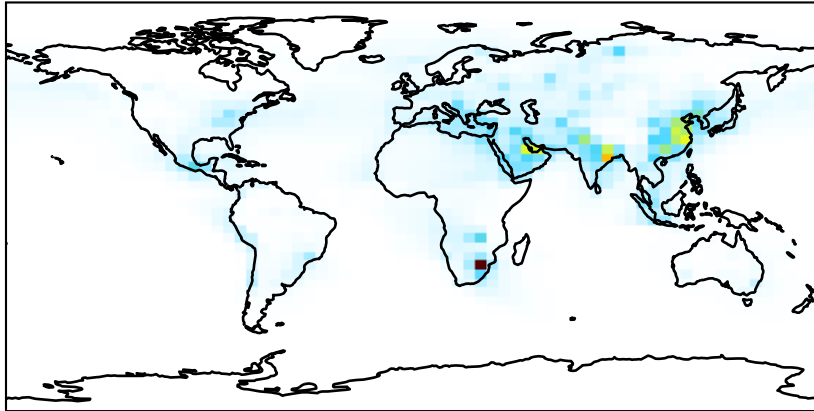


Ratio
Dev/Ref, Fixed Range

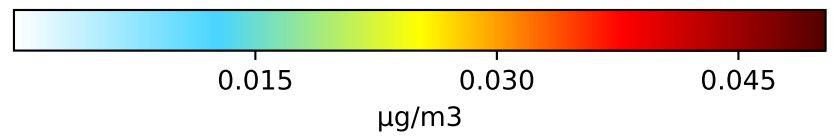
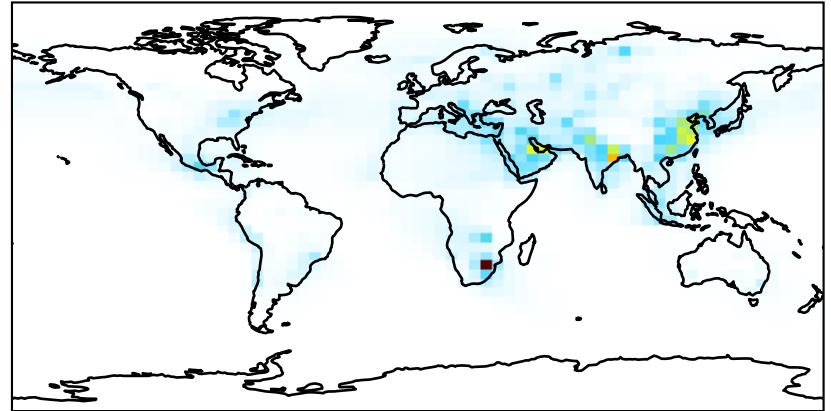


SpeciesConc_pFe (Jul2019)

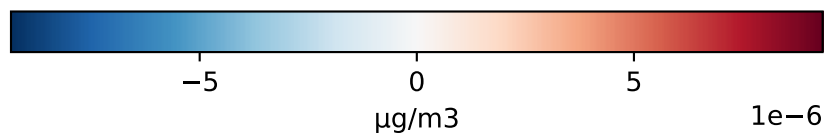
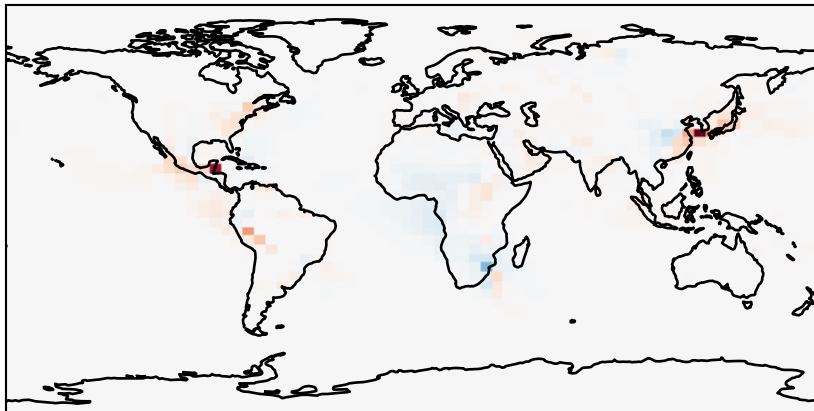
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



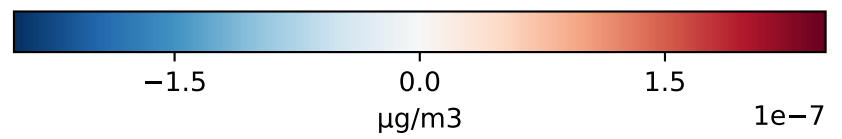
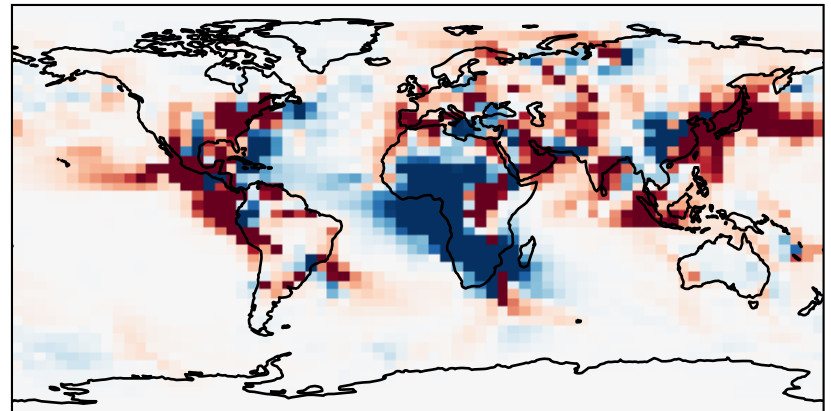
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



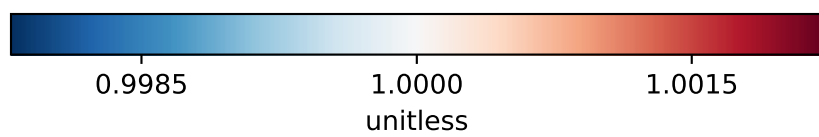
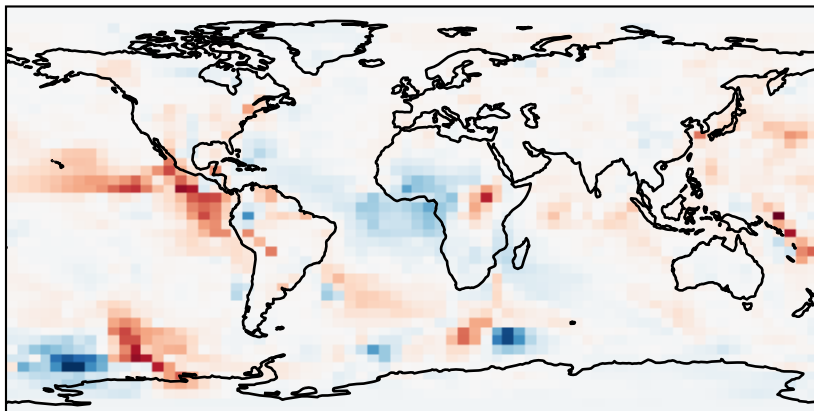
Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range



Ratio
Dev/Ref, Fixed Range

