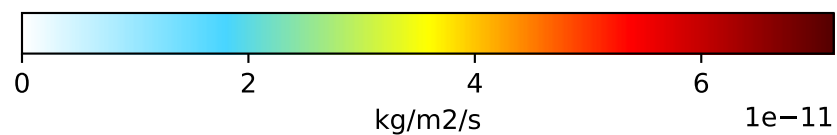
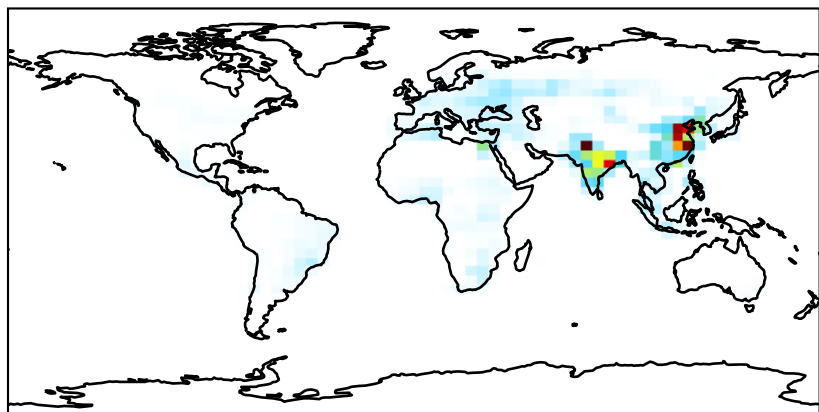
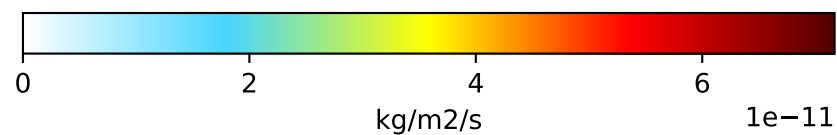
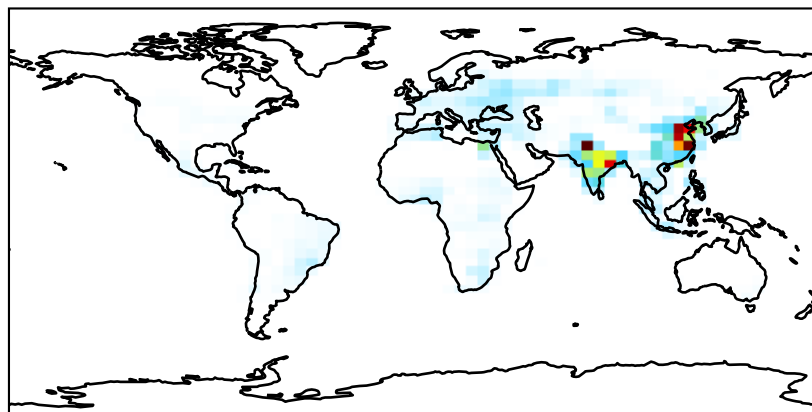


EmisDST1_Anthro (Oct2019)

GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



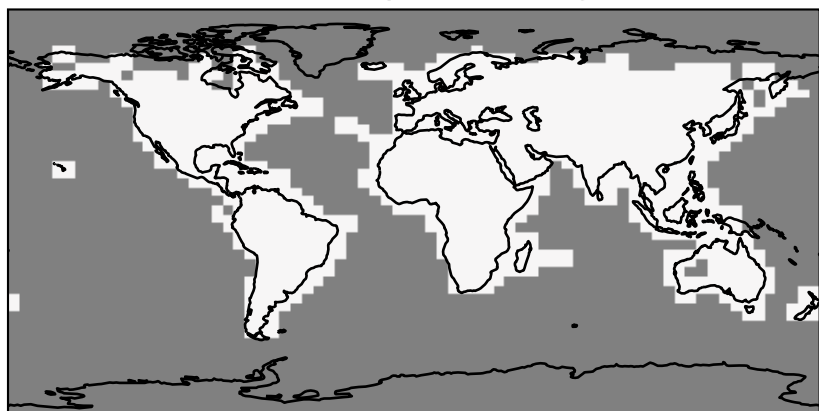
Difference
Dev - Ref, Dynamic Range



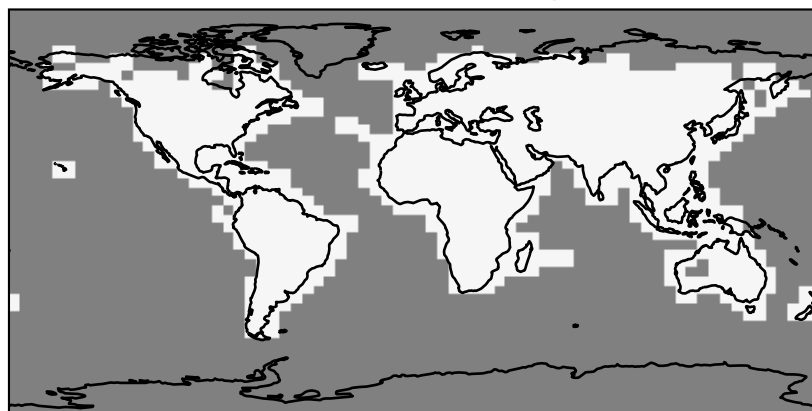
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

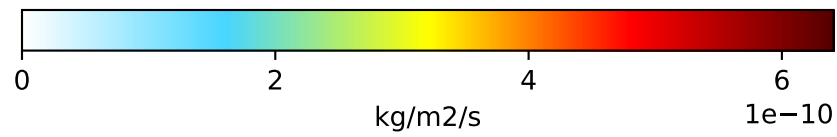
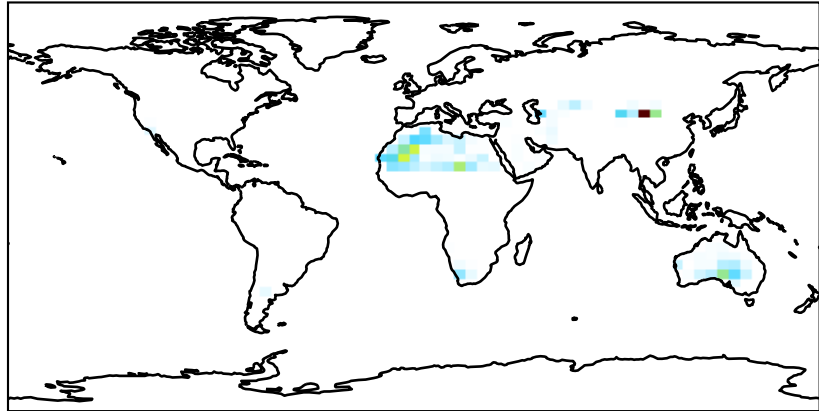


Ratio
Dev/Ref, Fixed Range

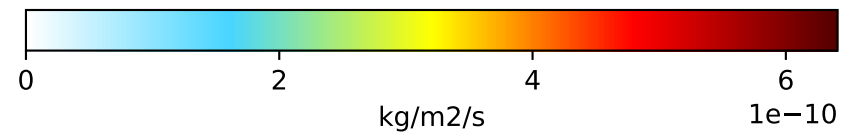
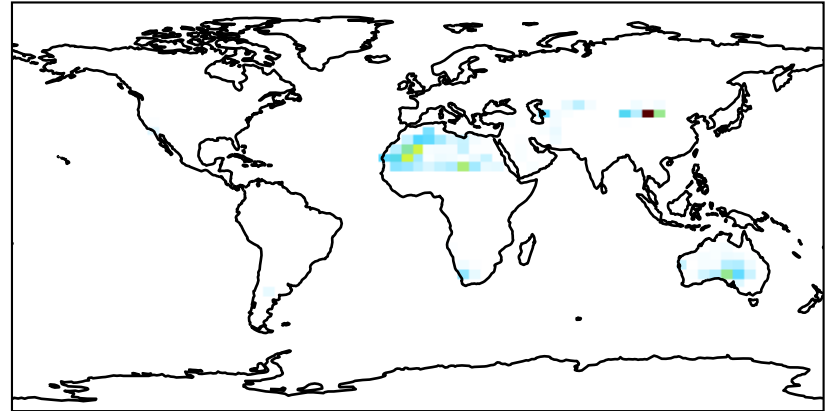


EmisDST1_Natural (Oct2019)

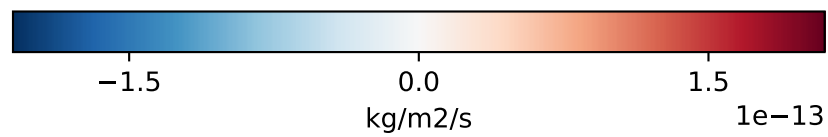
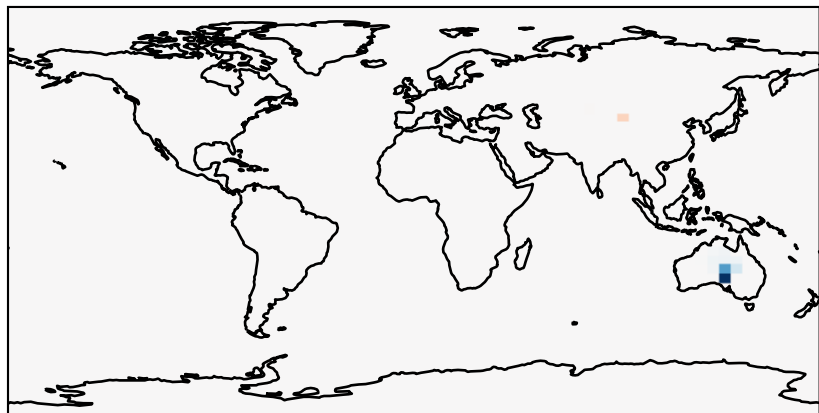
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



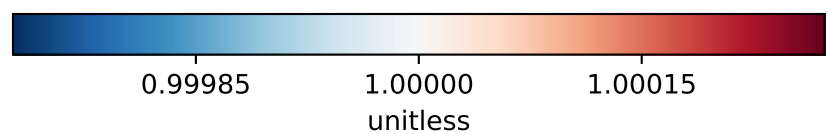
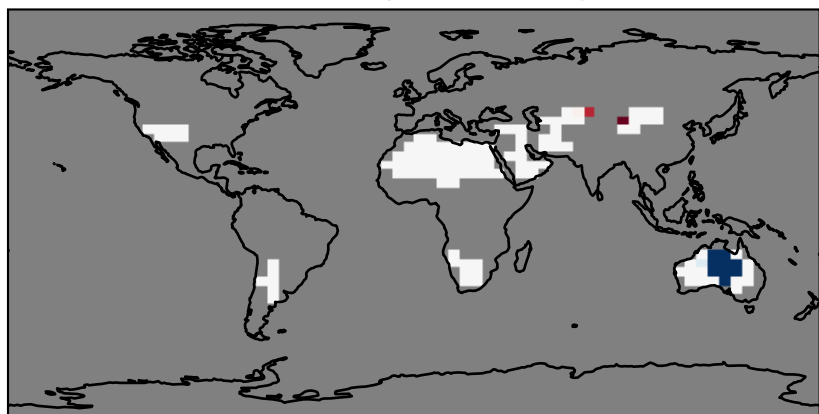
Difference
Dev - Ref, Dynamic Range



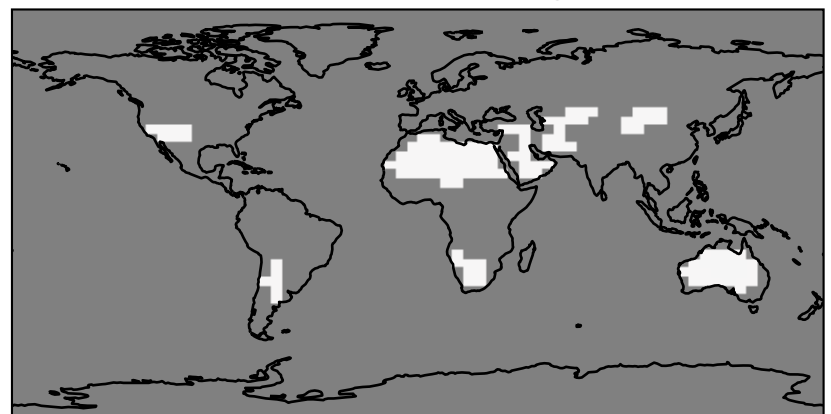
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

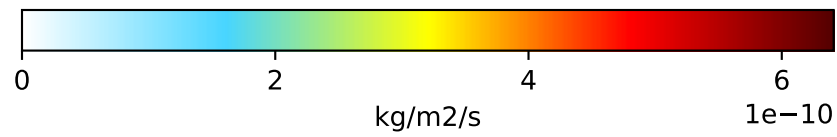
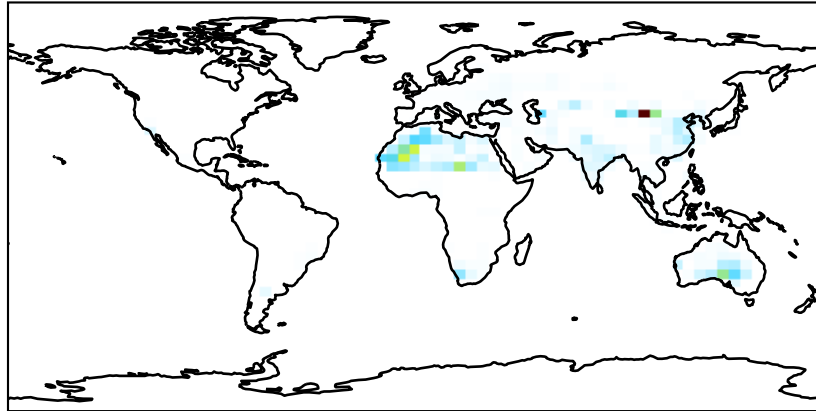


Ratio
Dev/Ref, Fixed Range

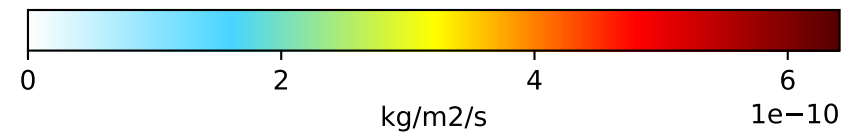
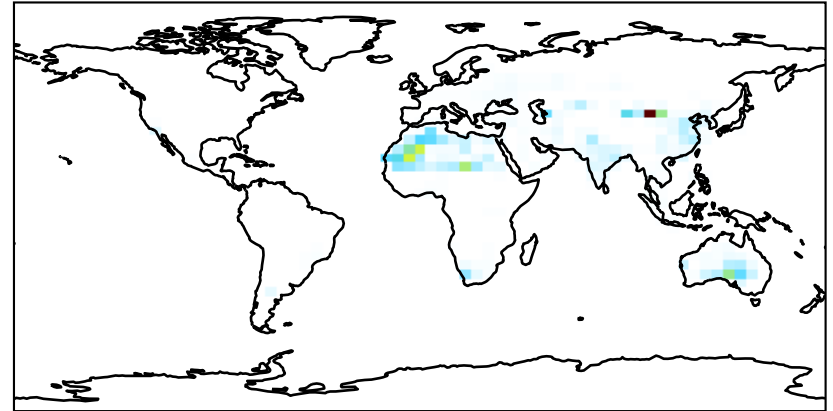


EmisDST1_Total (Oct2019)

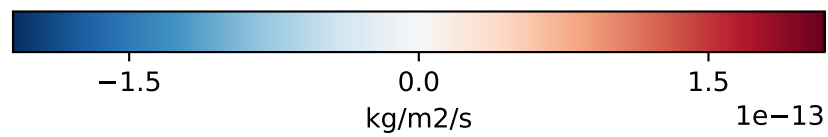
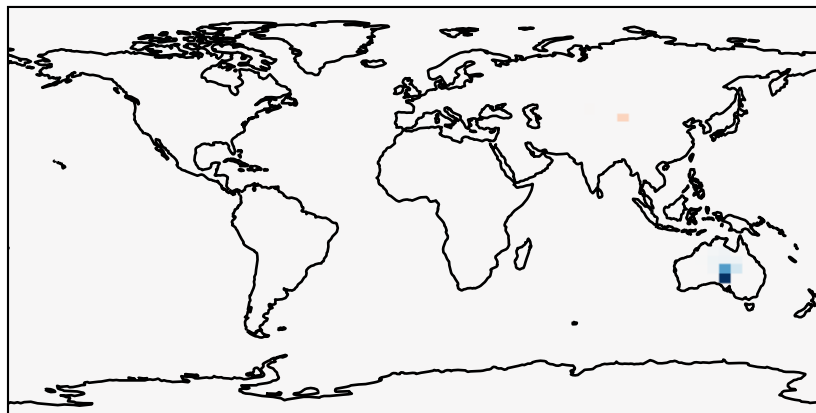
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



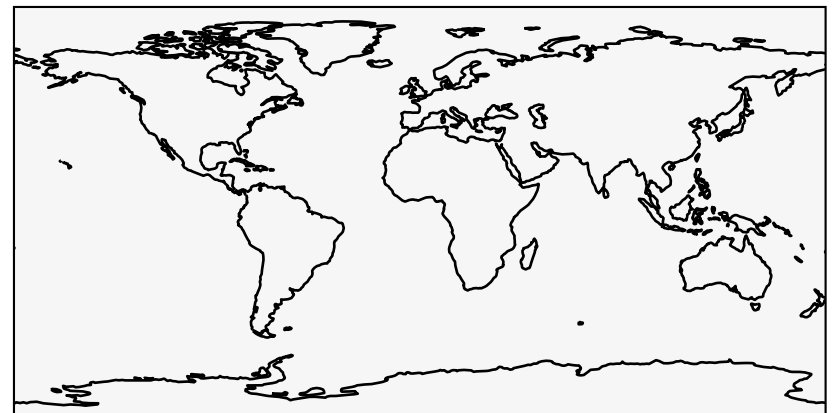
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



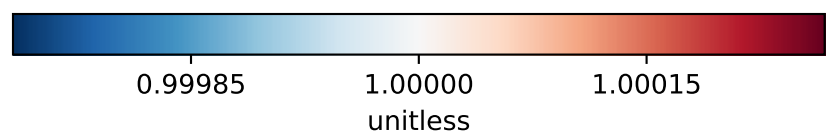
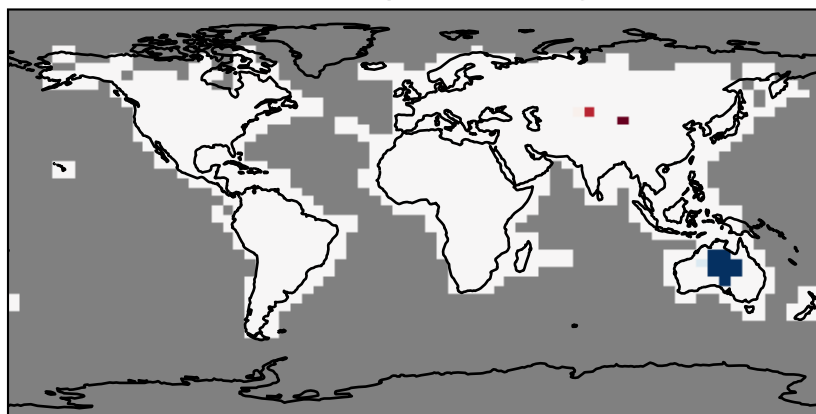
Difference
Dev - Ref, Dynamic Range



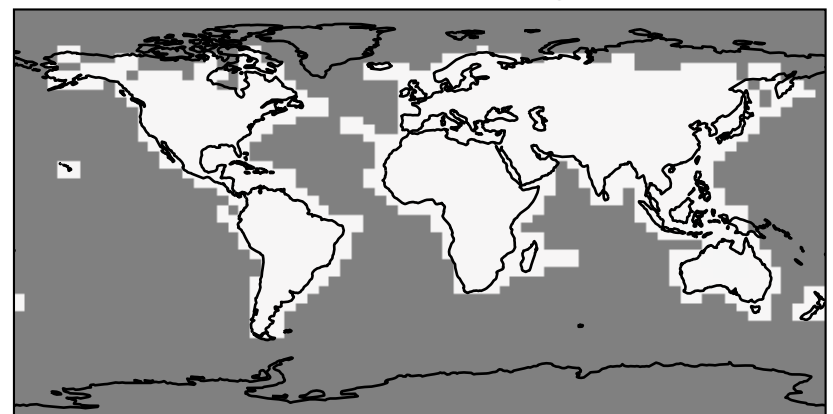
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

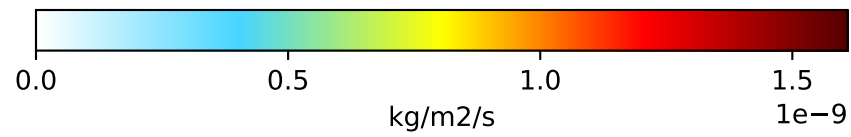
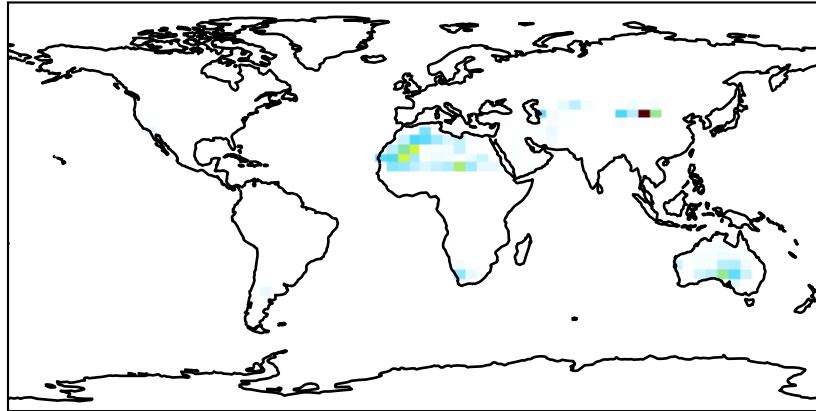


Ratio
Dev/Ref, Fixed Range

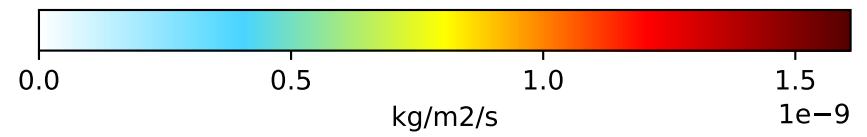
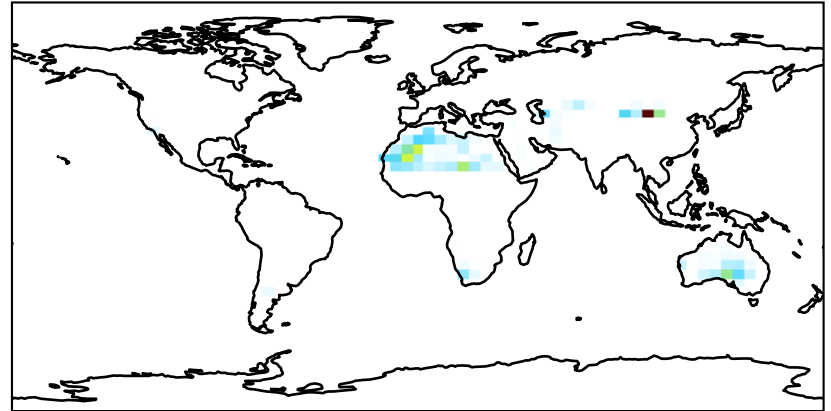


EmisDST2_Natural (Oct2019)

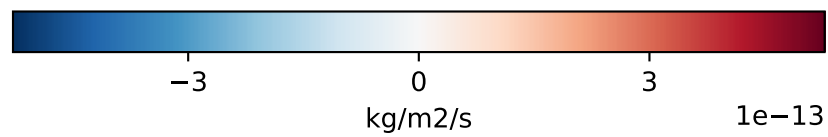
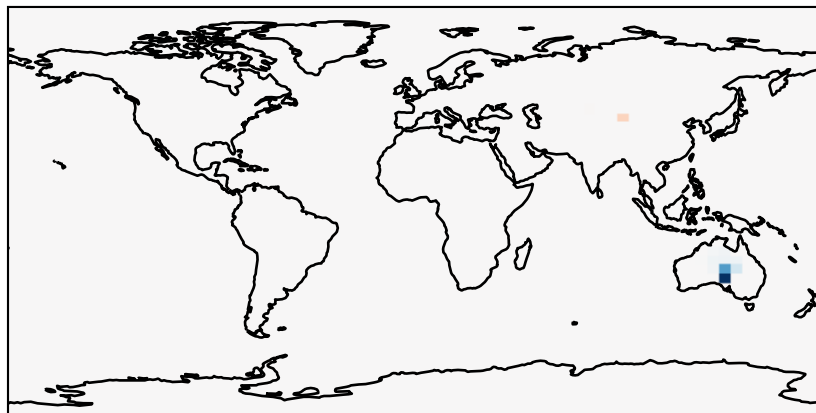
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



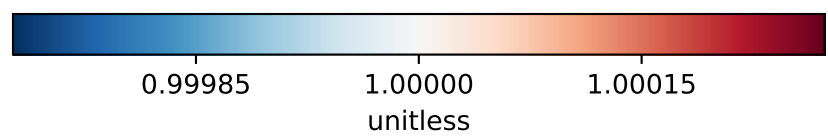
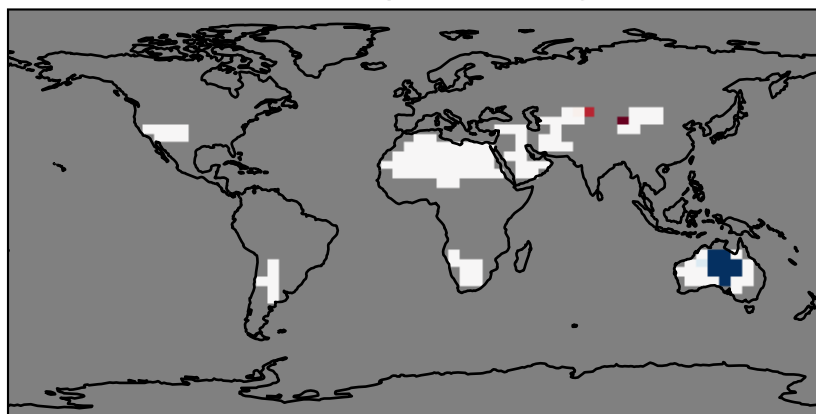
Difference
Dev - Ref, Dynamic Range



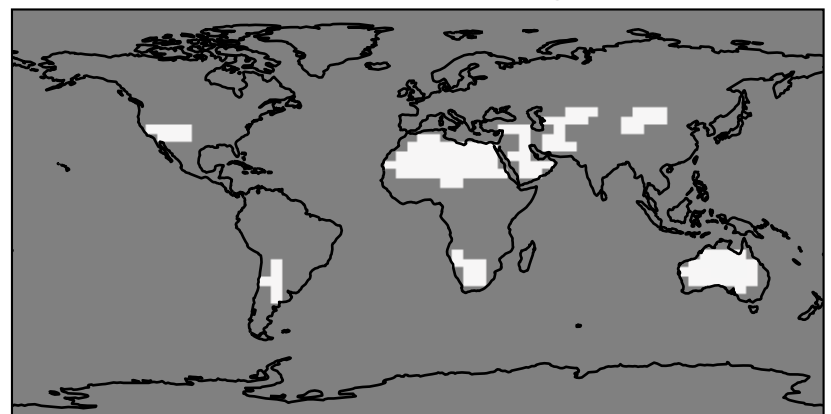
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

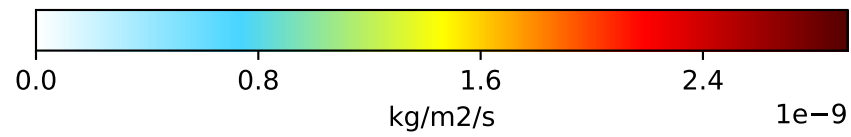
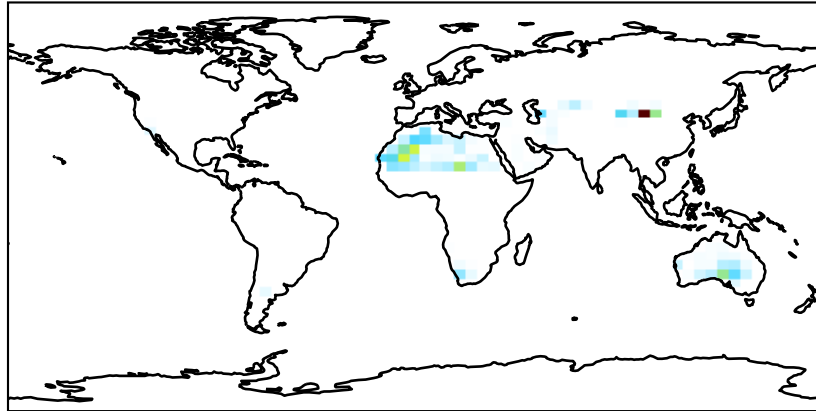


Ratio
Dev/Ref, Fixed Range

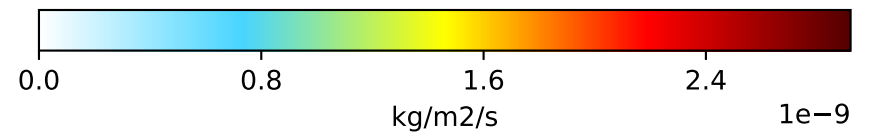
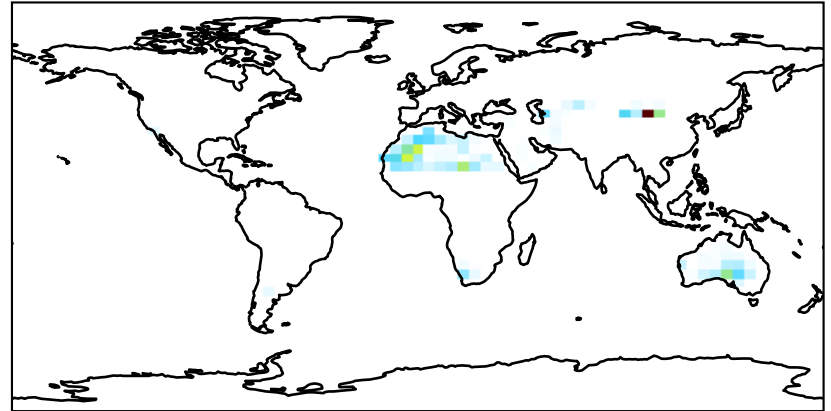


EmisDST3_Natural (Oct2019)

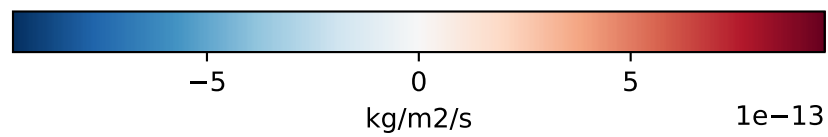
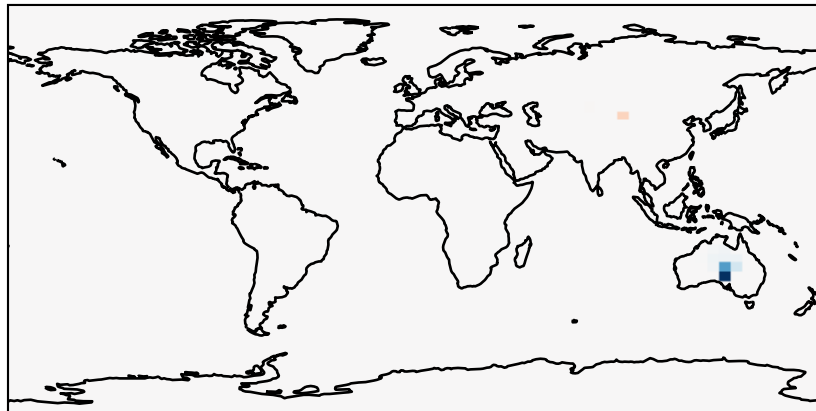
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



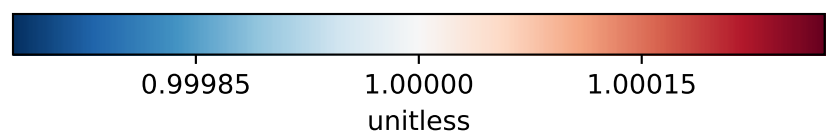
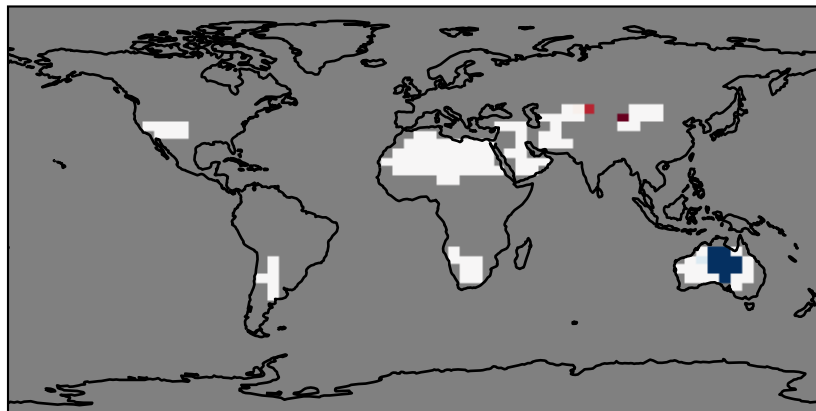
Difference
Dev - Ref, Dynamic Range



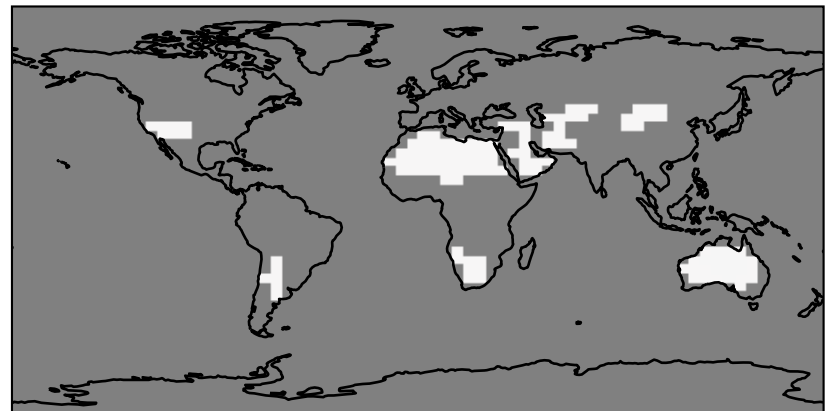
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

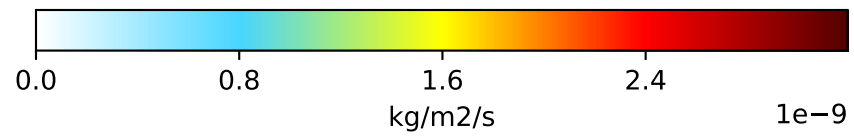
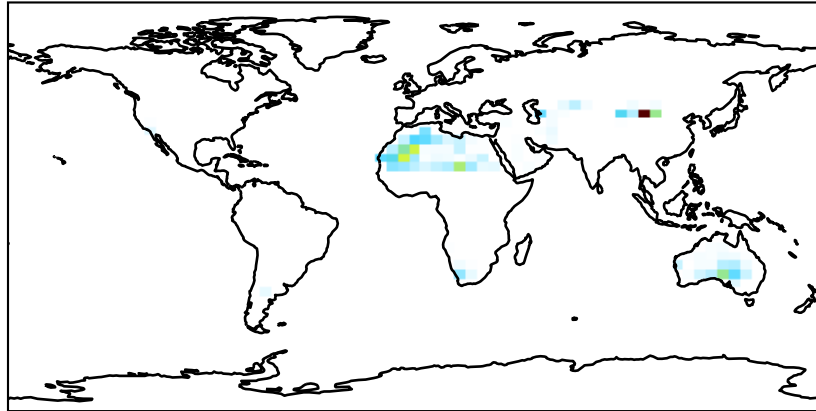


Ratio
Dev/Ref, Fixed Range

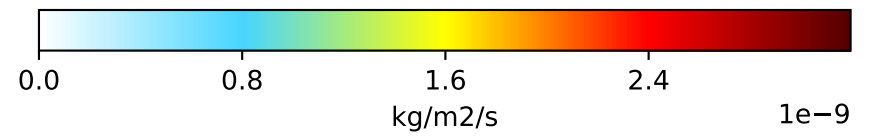
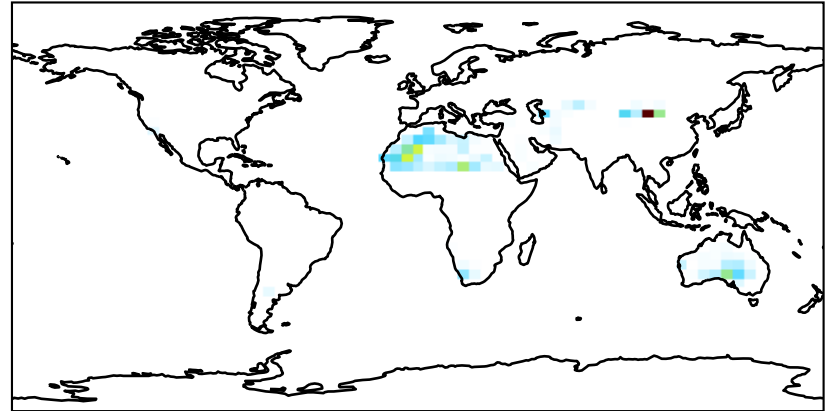


EmisDST4_Natural (Oct2019)

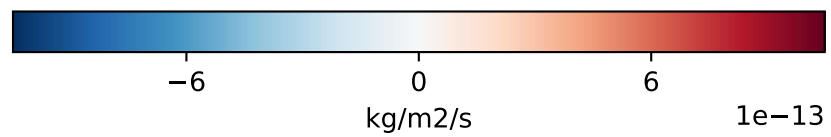
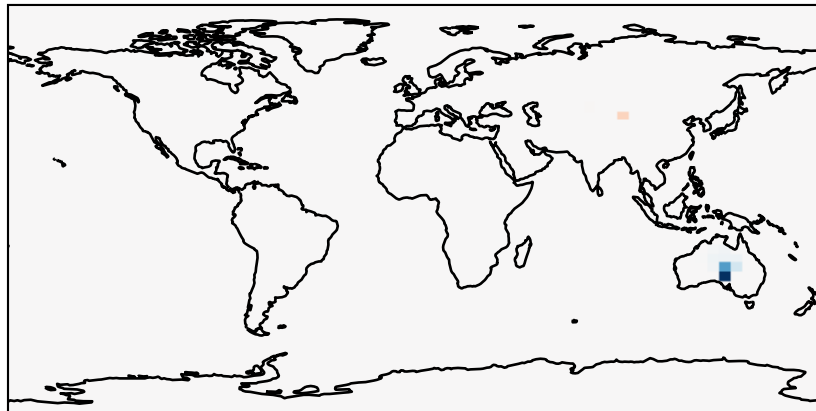
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



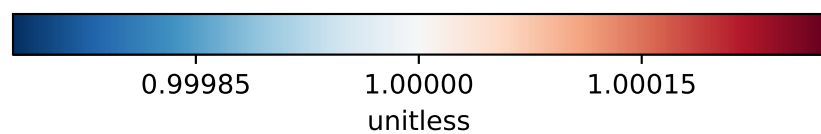
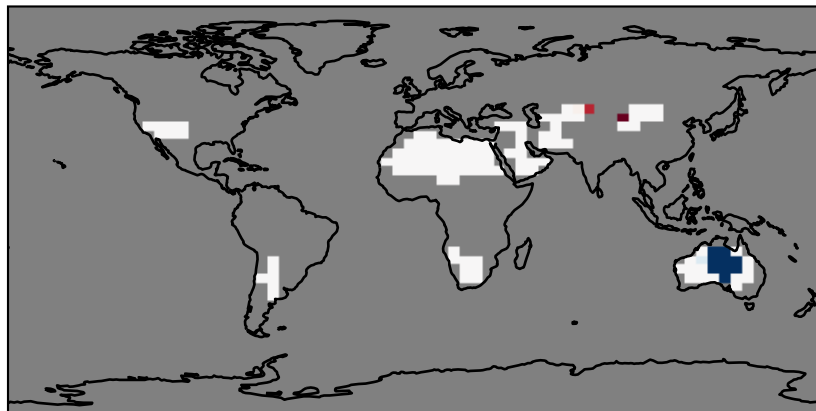
Difference
Dev - Ref, Dynamic Range



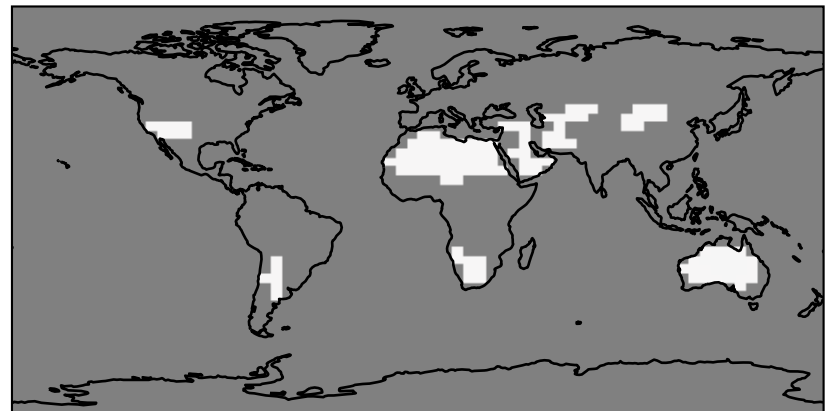
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

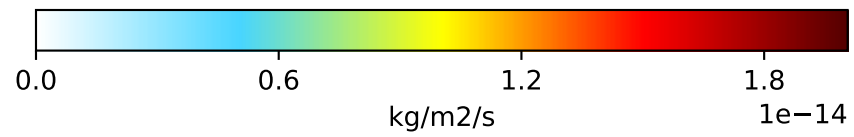
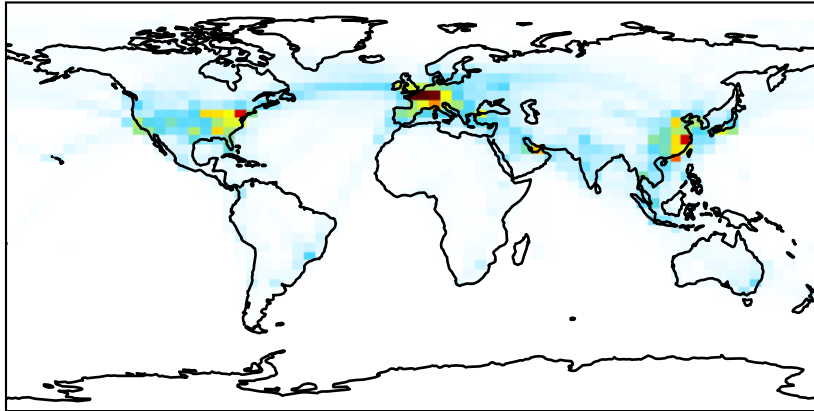


Ratio
Dev/Ref, Fixed Range

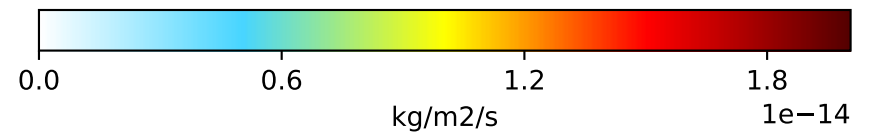
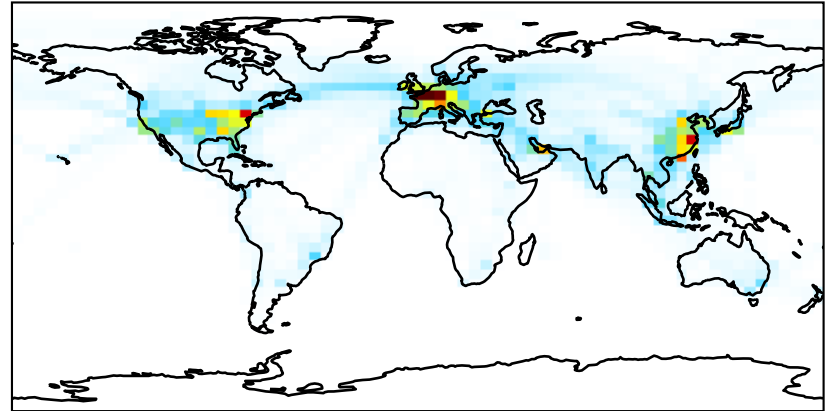


EmisSO4_Aircraft (Oct2019)

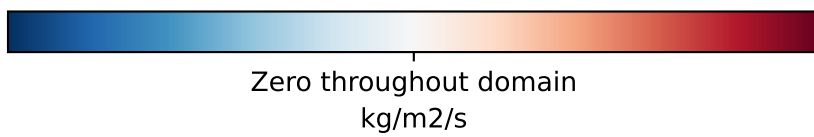
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



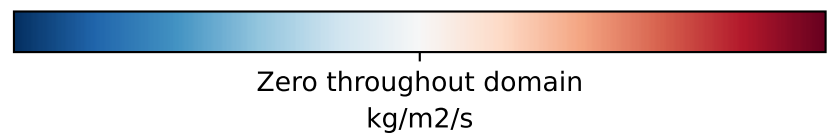
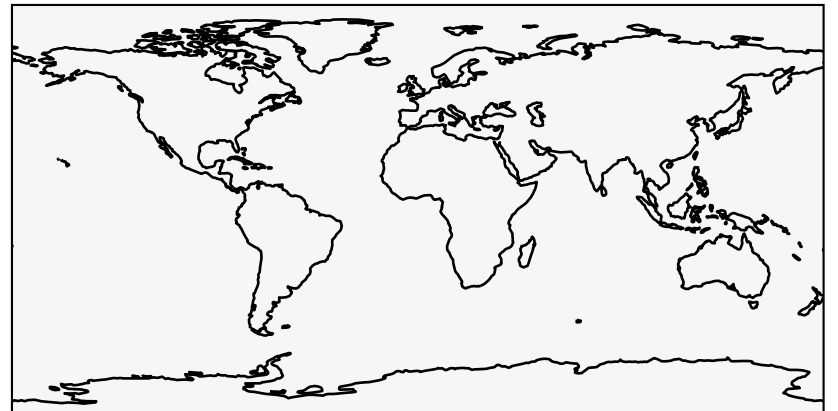
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



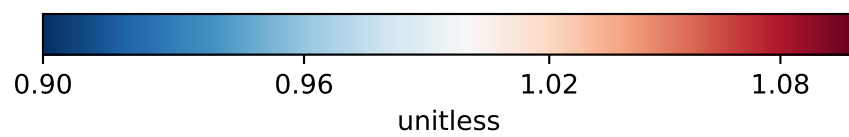
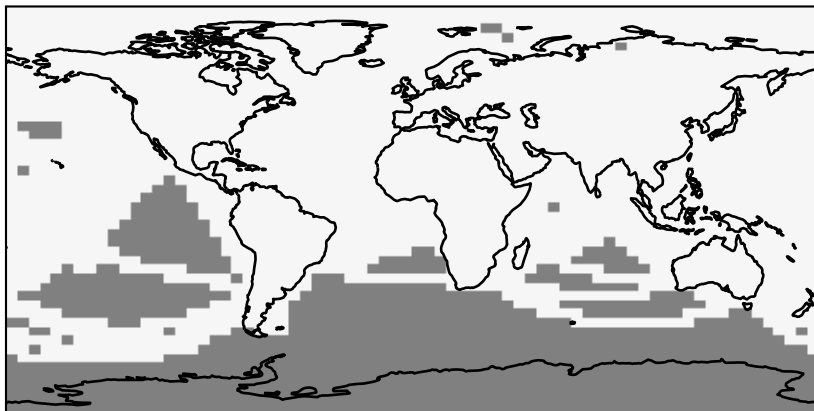
Difference
Dev - Ref, Dynamic Range



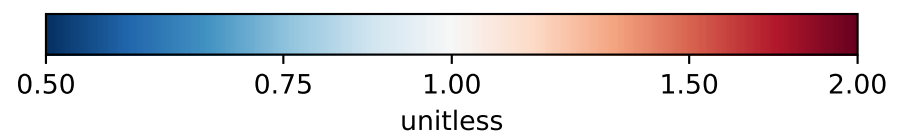
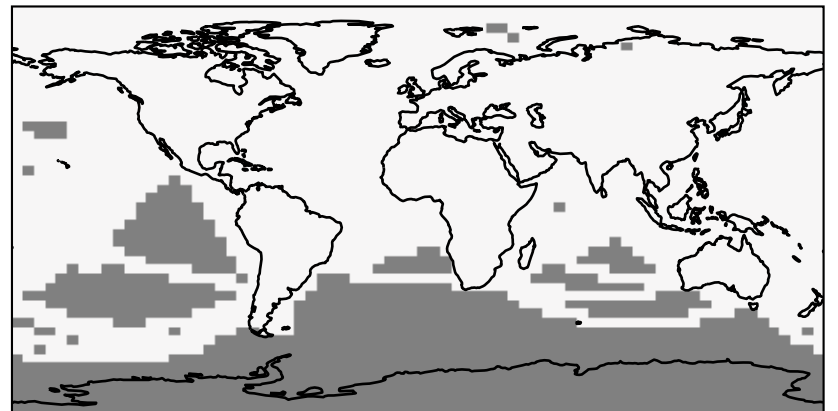
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

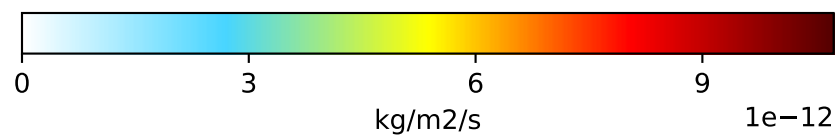
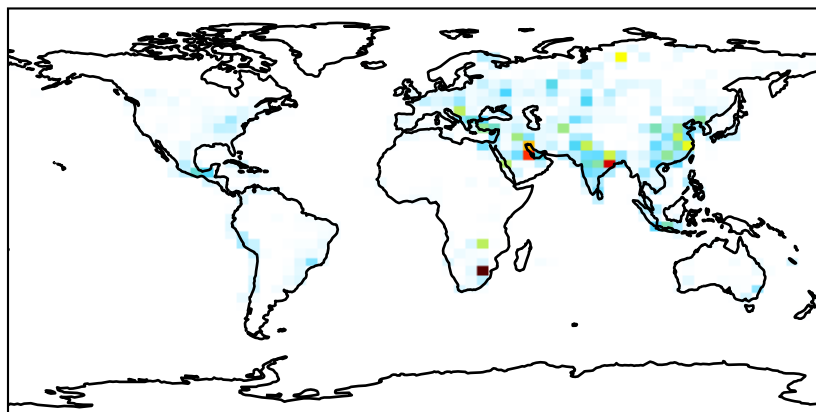


Ratio
Dev/Ref, Fixed Range

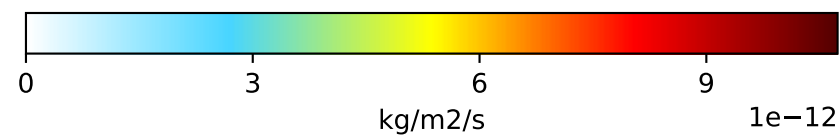
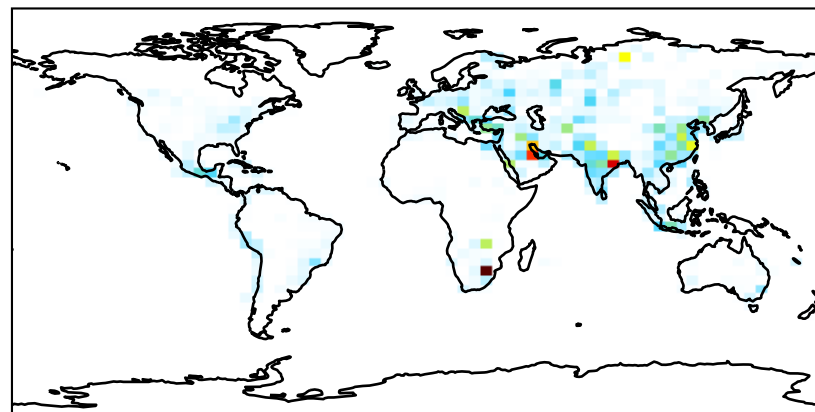


EmisSO4_Anthro (Oct2019)

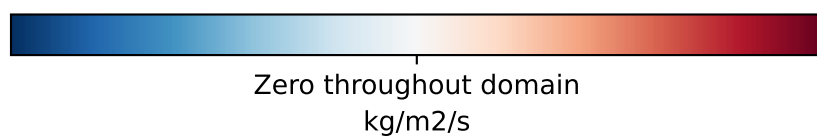
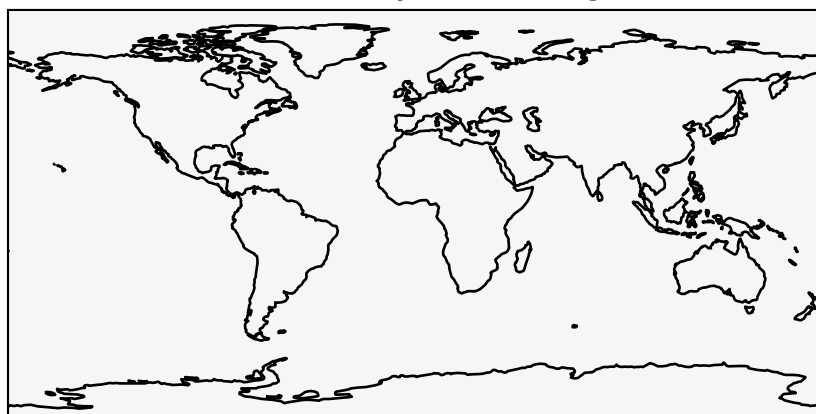
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



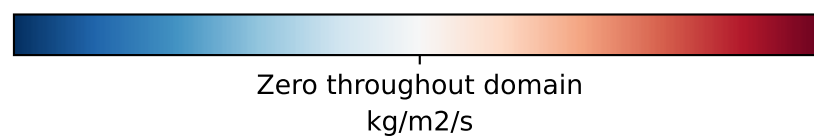
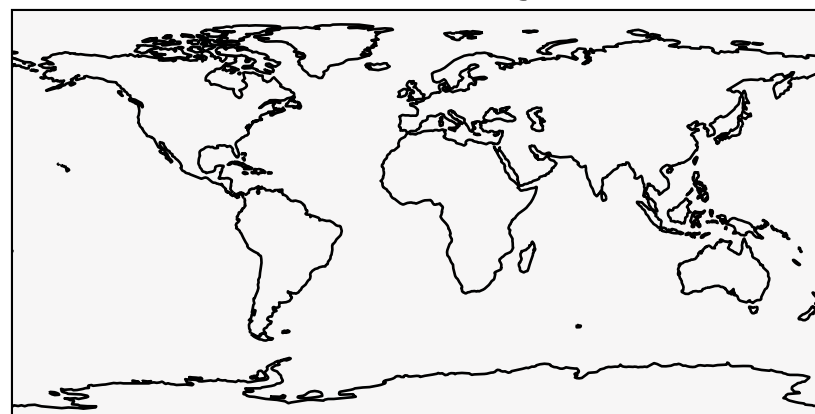
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



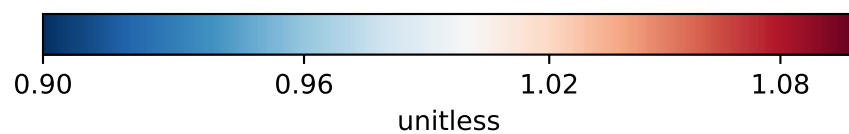
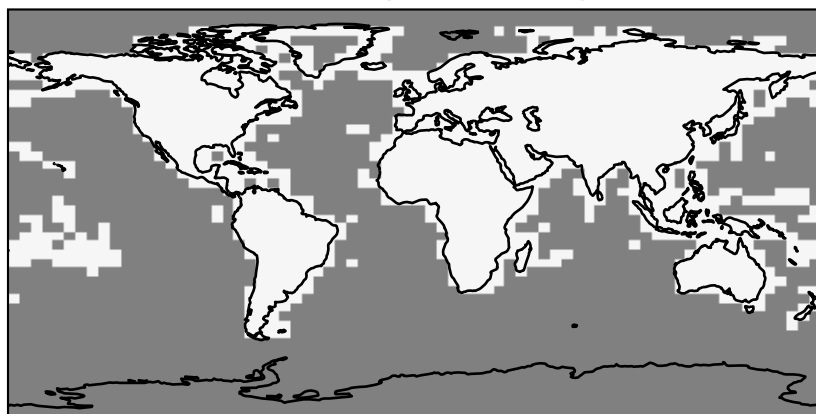
Difference
Dev - Ref, Dynamic Range



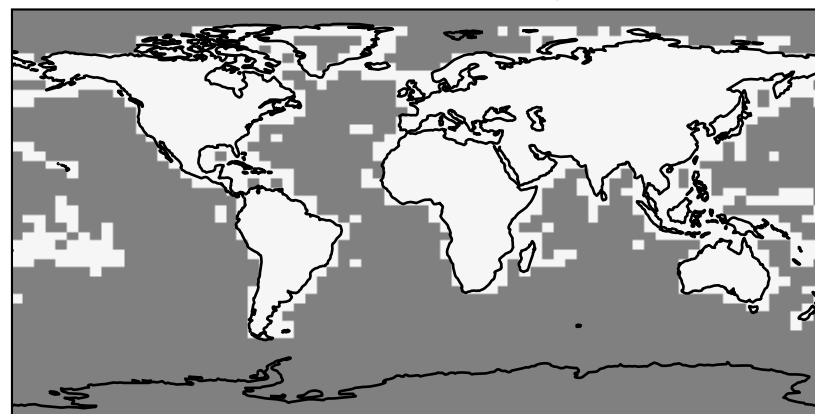
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

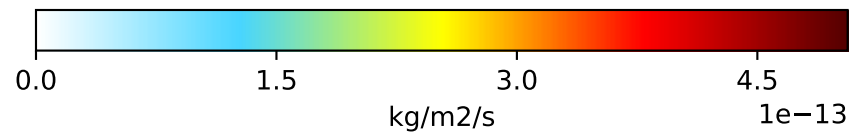
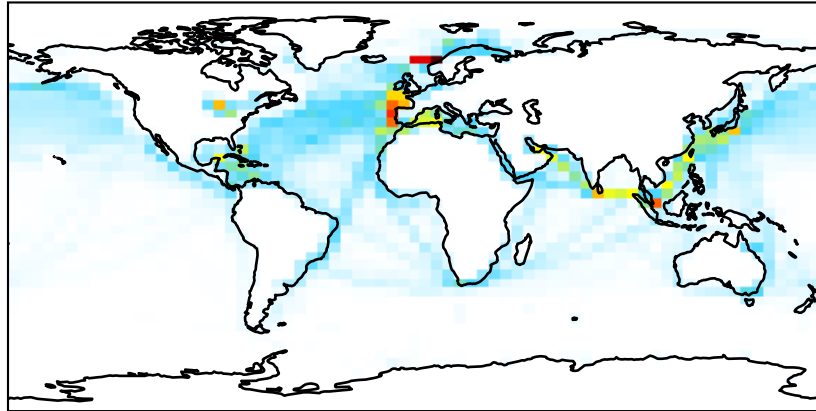


Ratio
Dev/Ref, Fixed Range

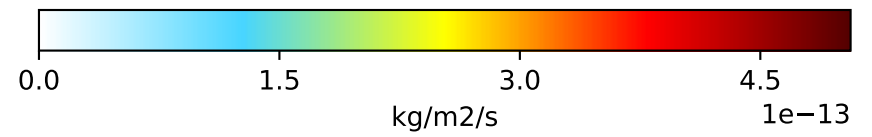
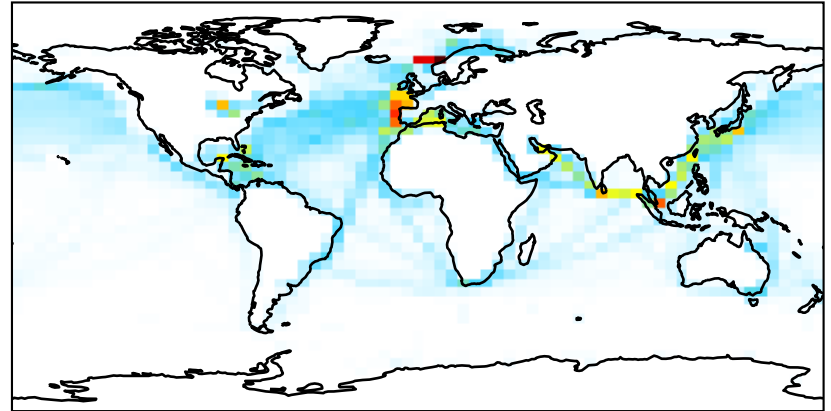


EmisSO4_Ship (Oct2019)

GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

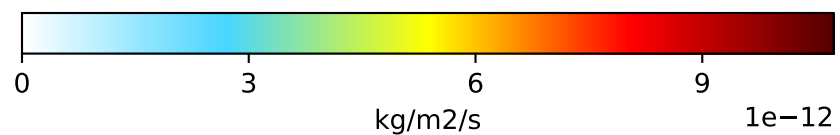
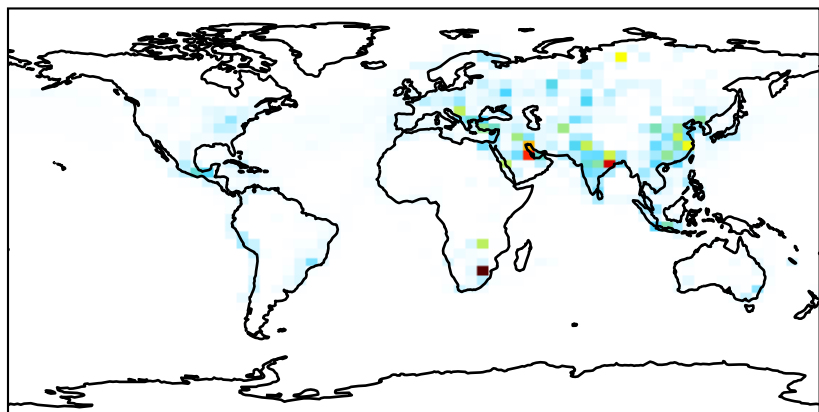


Ratio
Dev/Ref, Fixed Range

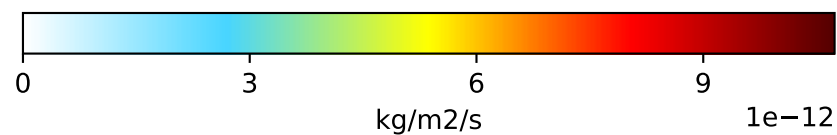
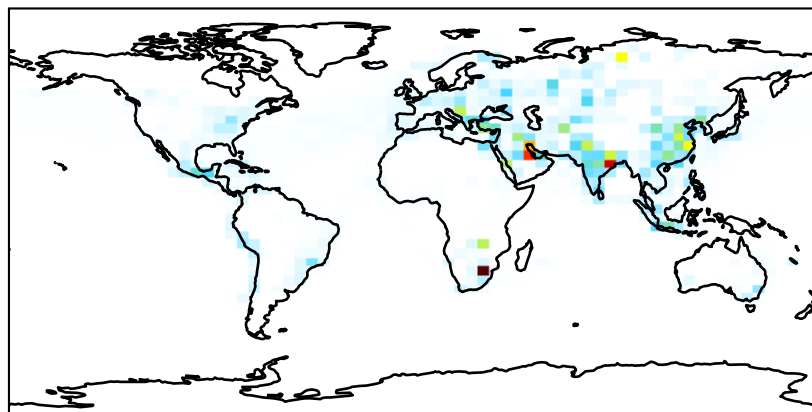


EmisSO4_Total (Oct2019)

GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



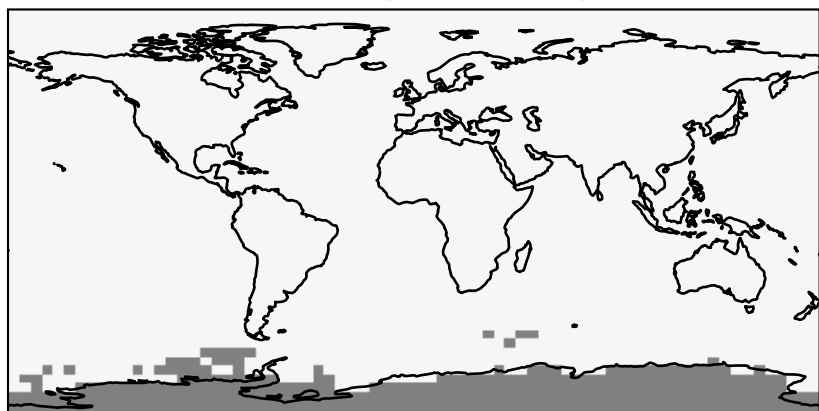
Difference
Dev - Ref, Dynamic Range



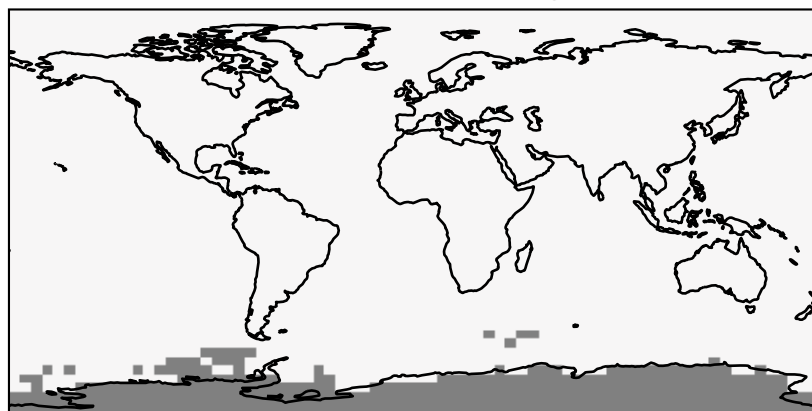
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

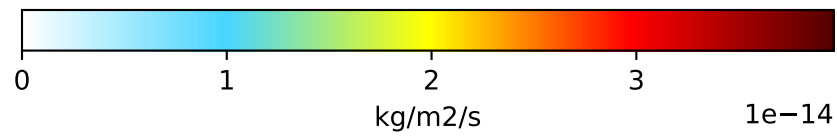
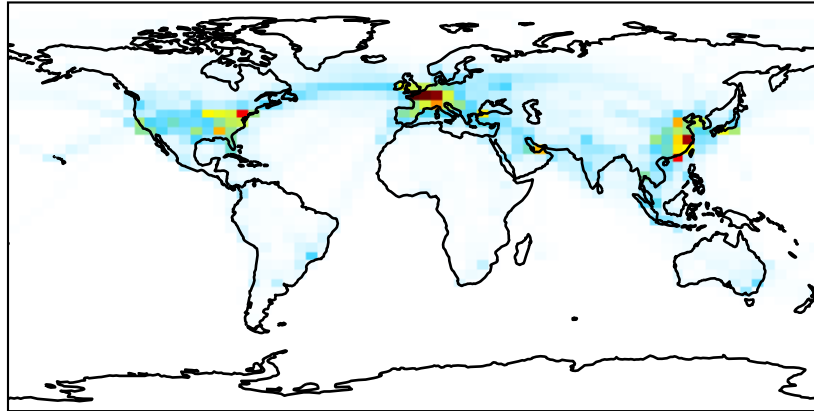


Ratio
Dev/Ref, Fixed Range

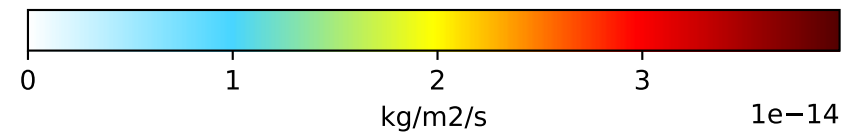
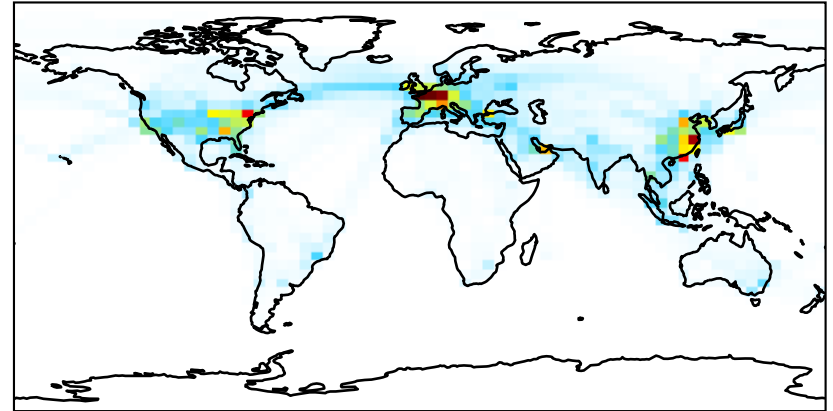


EmisBCPI_Aircraft (Oct2019)

GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



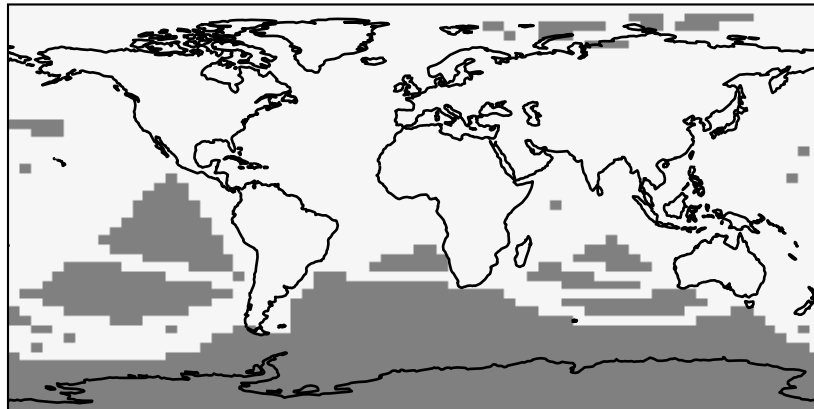
Difference
Dev - Ref, Dynamic Range



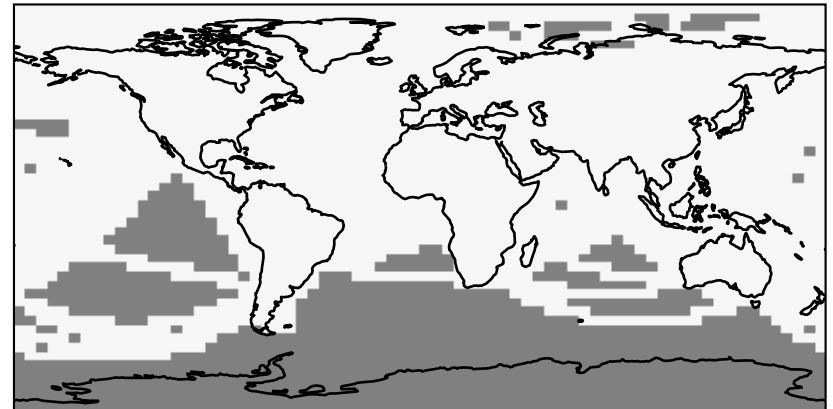
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

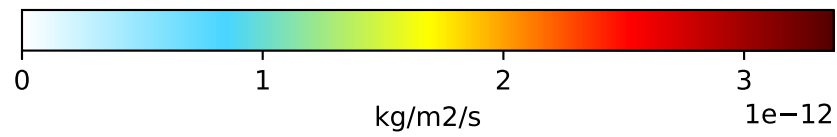
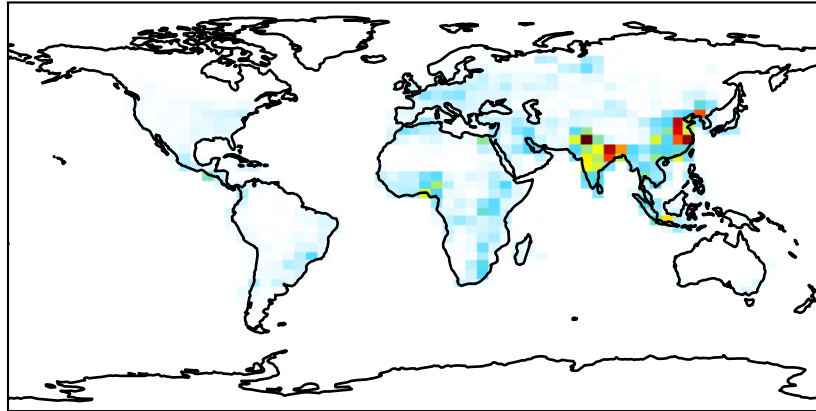


Ratio
Dev/Ref, Fixed Range

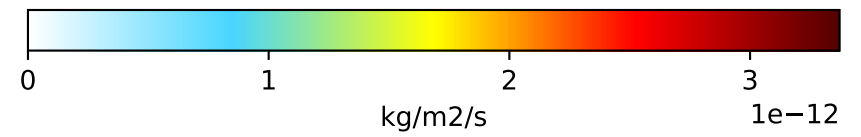
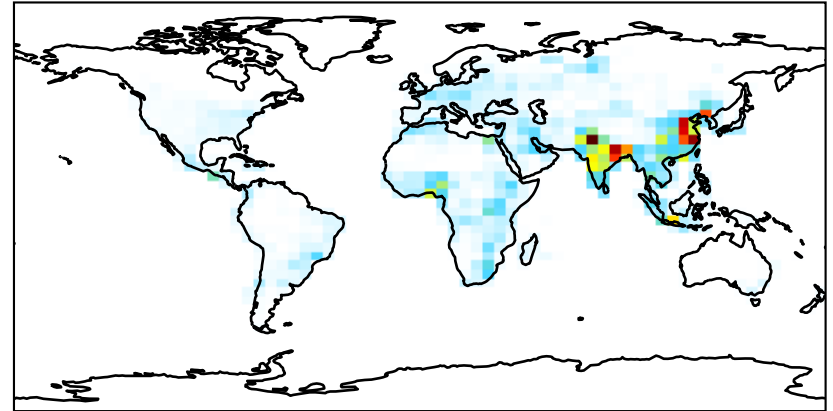


EmisBCPI_Anthro (Oct2019)

GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



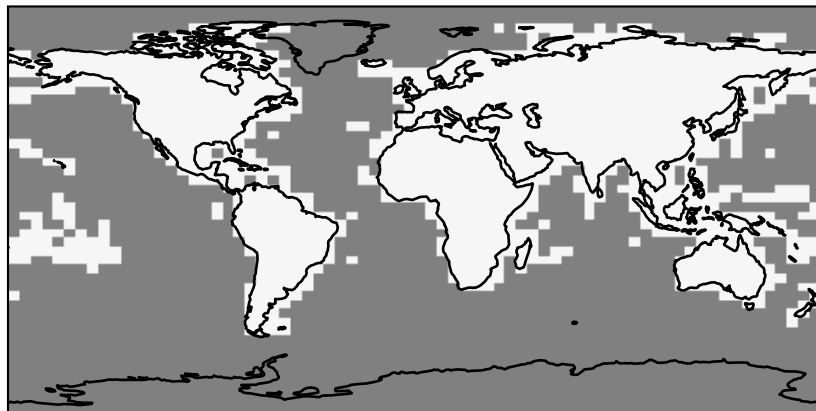
Difference
Dev - Ref, Dynamic Range



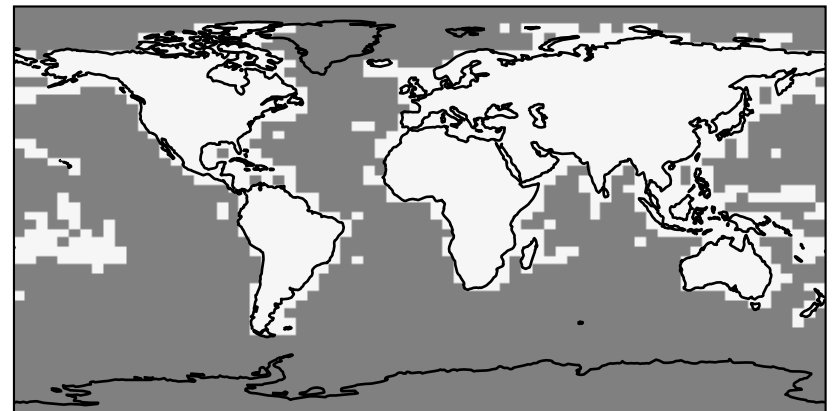
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

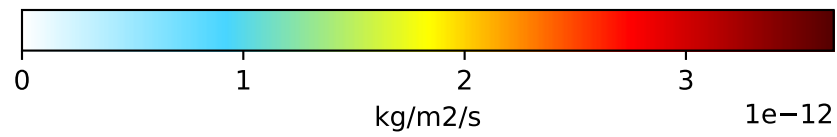
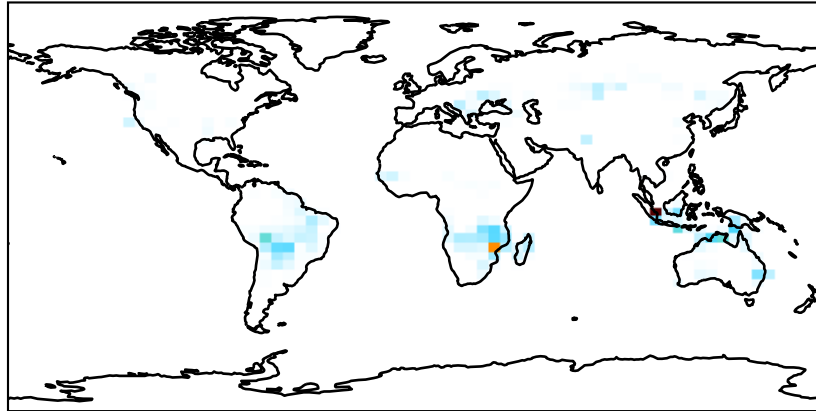


Ratio
Dev/Ref, Fixed Range

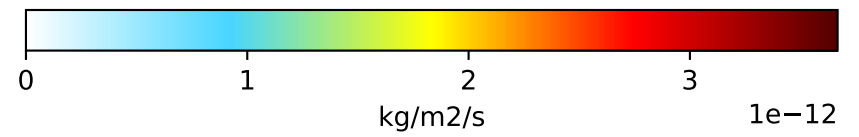
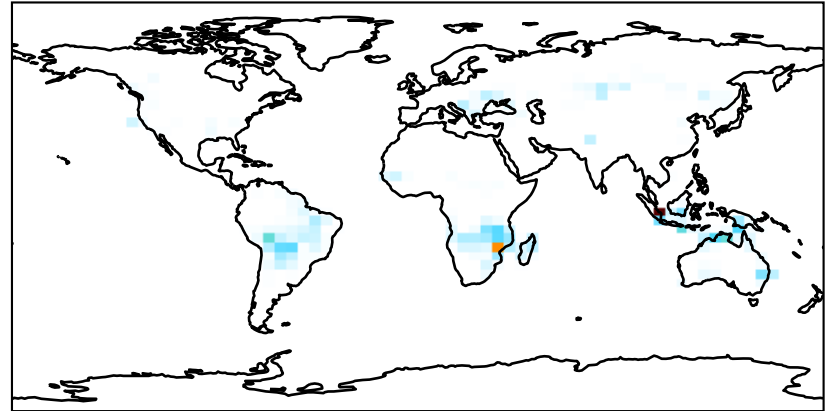


EmisBCPI_BioBurn (Oct2019)

GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



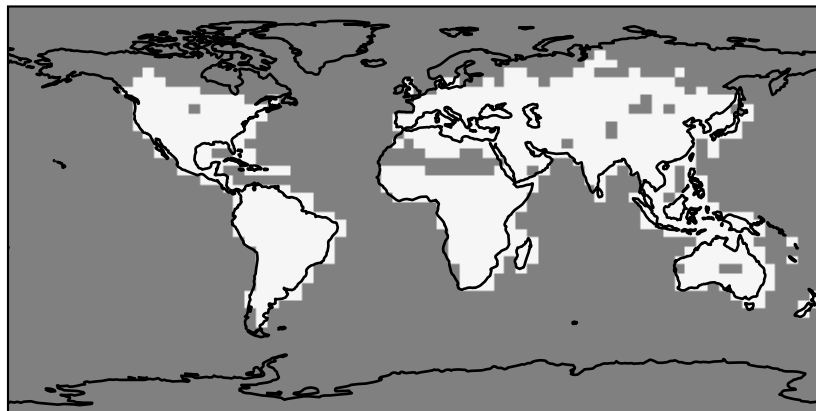
Difference
Dev - Ref, Dynamic Range



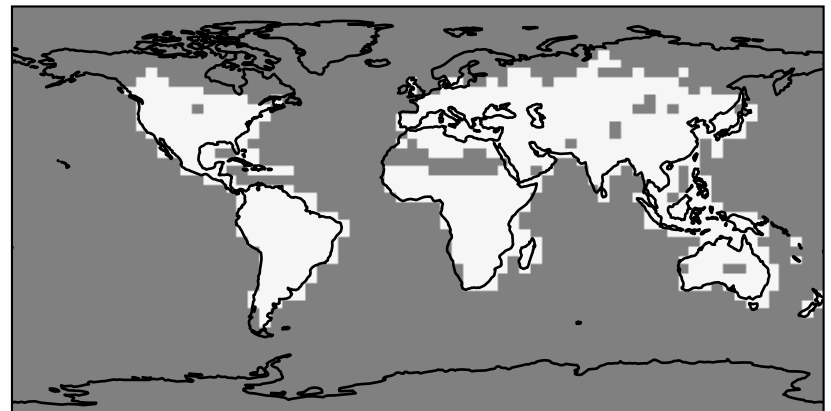
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

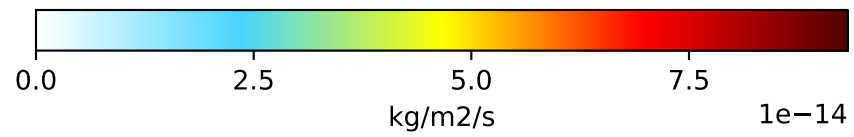
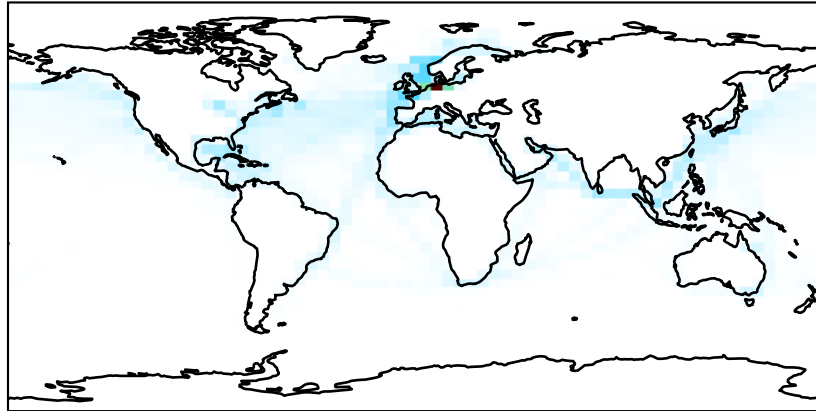


Ratio
Dev/Ref, Fixed Range

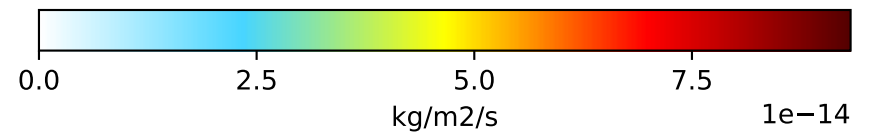
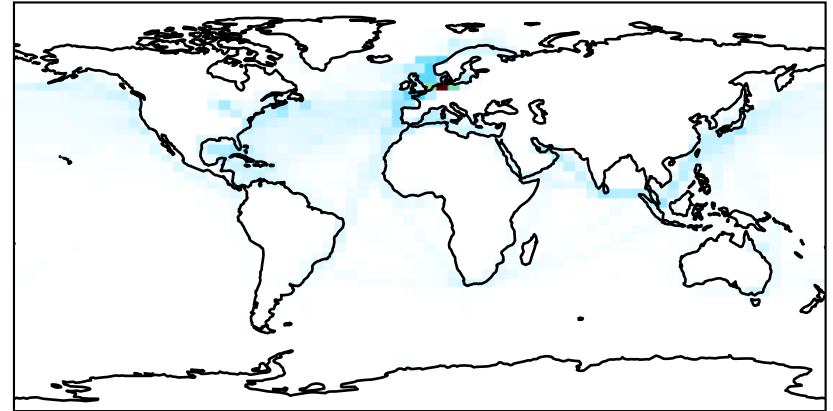


EmisBCPI_Ship (Oct2019)

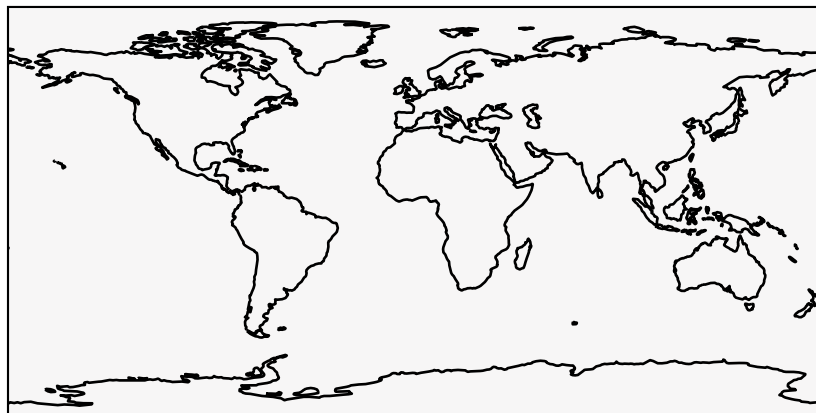
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



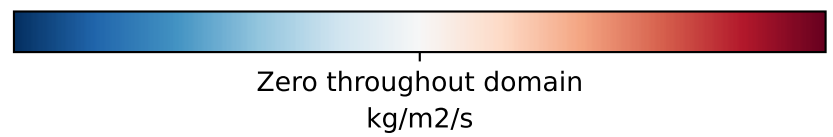
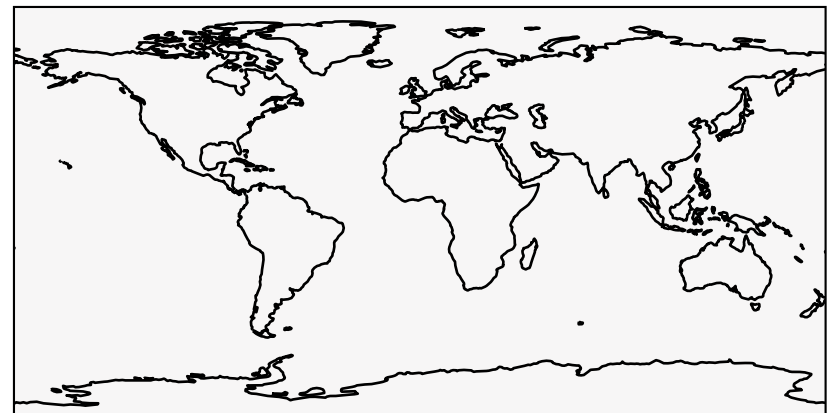
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



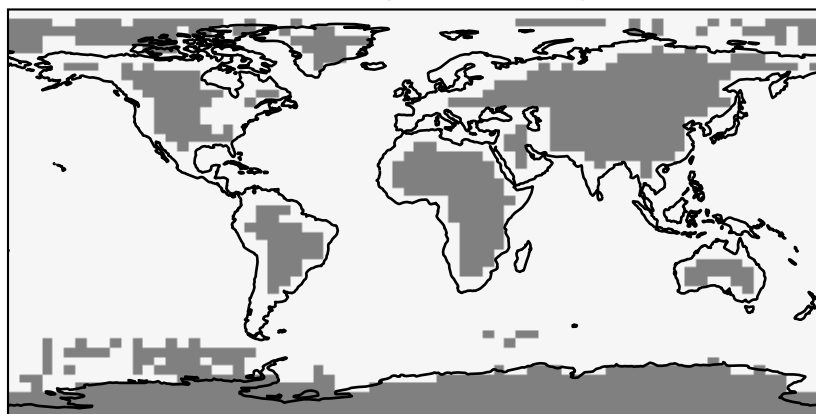
Difference
Dev - Ref, Dynamic Range



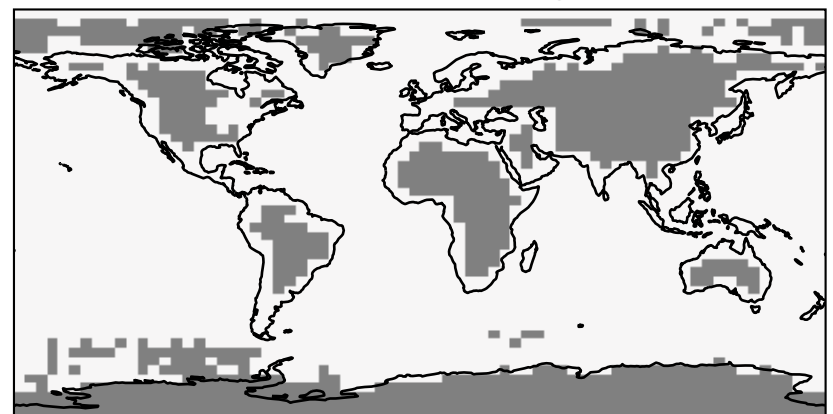
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

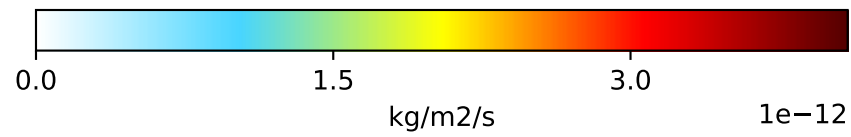
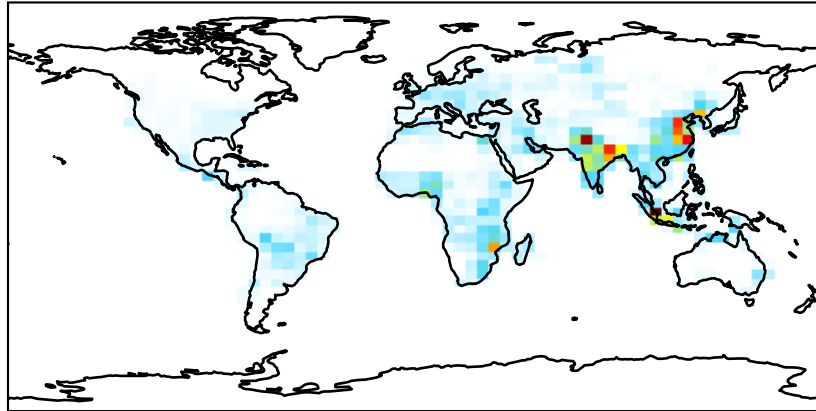


Ratio
Dev/Ref, Fixed Range

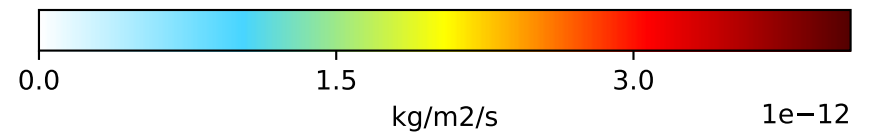
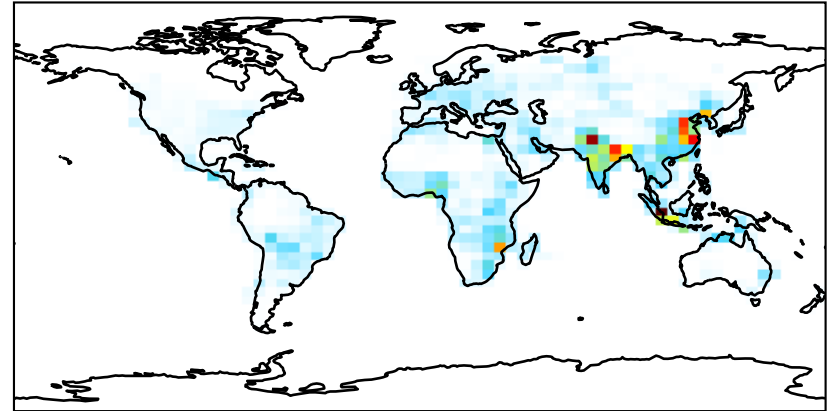


EmisBCPI_Total (Oct2019)

GCC_13.4.0-rc.4 (Ref)
4.0x5.0



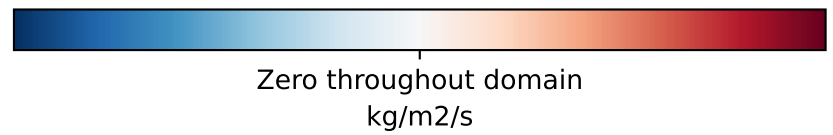
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



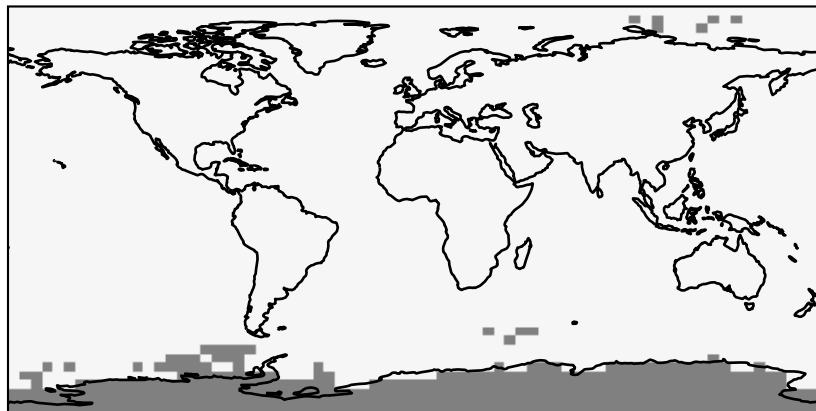
Difference
Dev - Ref, Dynamic Range



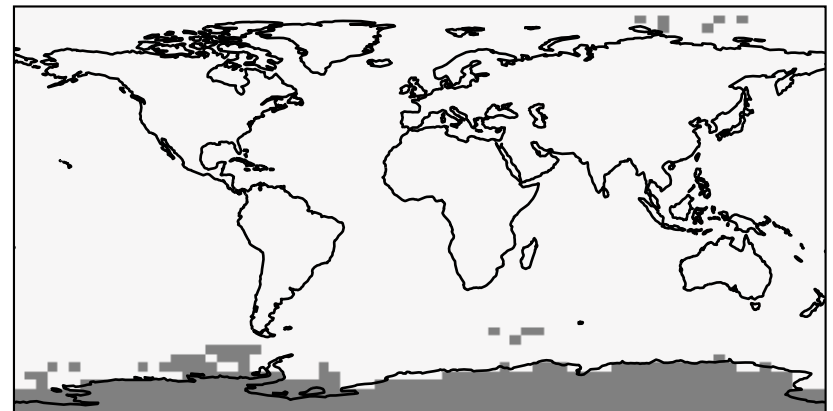
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

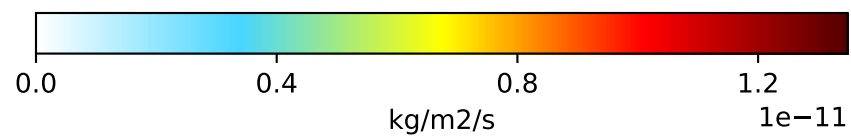
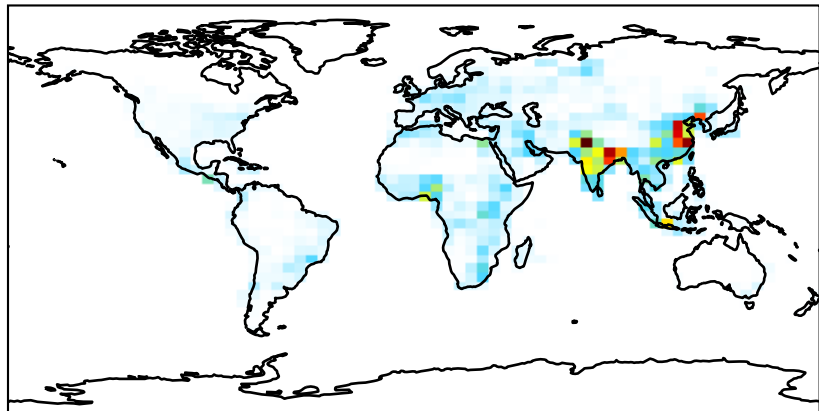


Ratio
Dev/Ref, Fixed Range

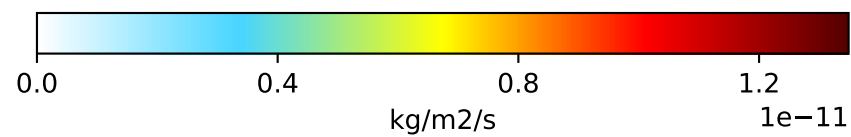
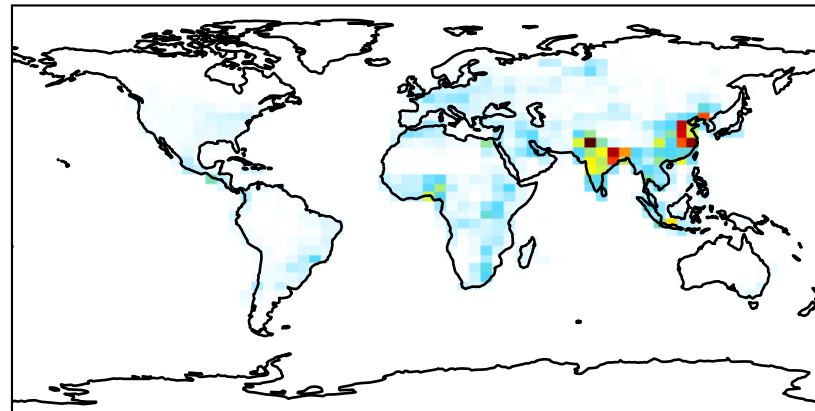


EmisBCPO_Anthro (Oct2019)

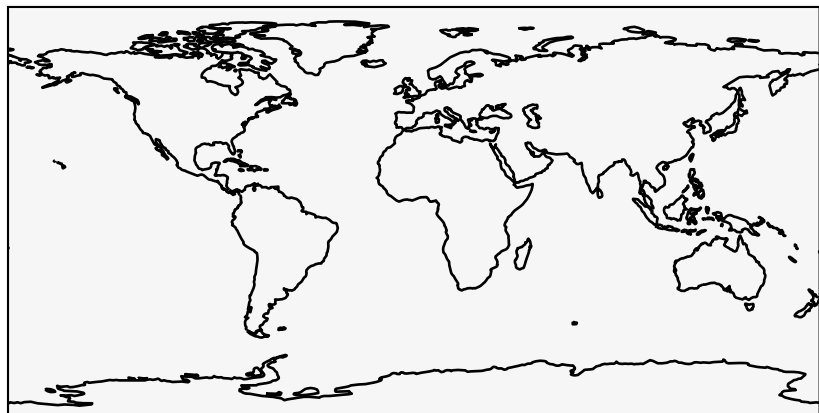
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



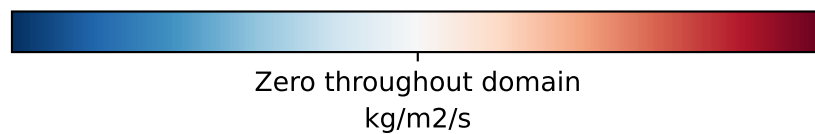
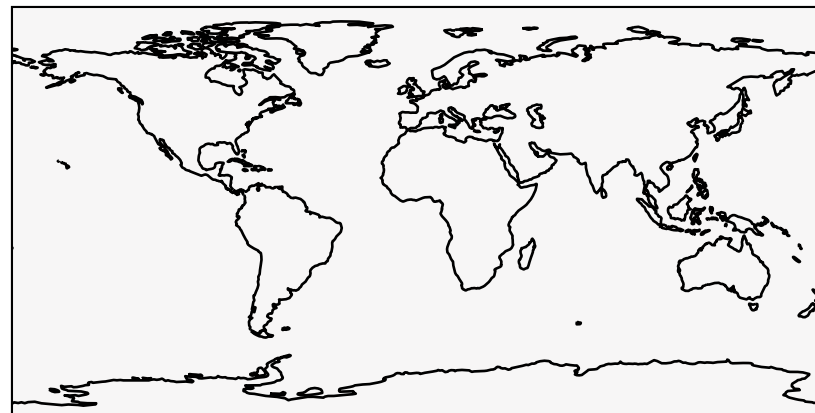
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



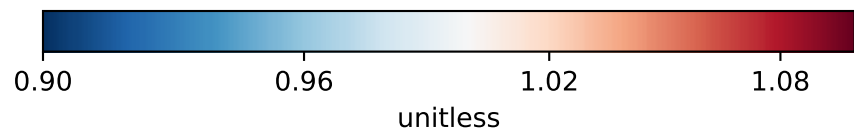
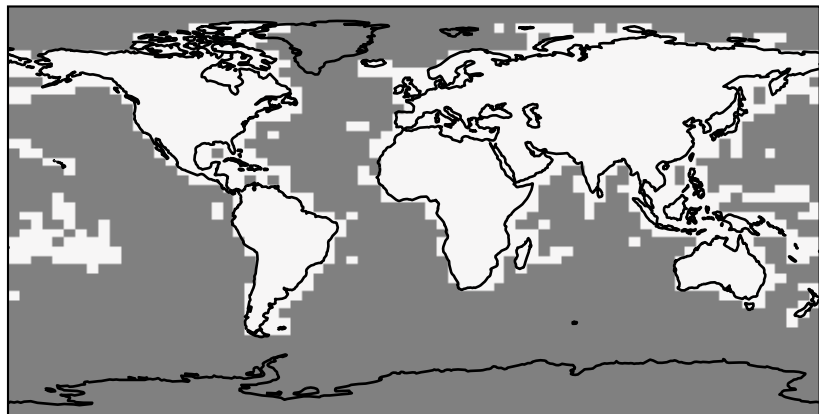
Difference
Dev - Ref, Dynamic Range



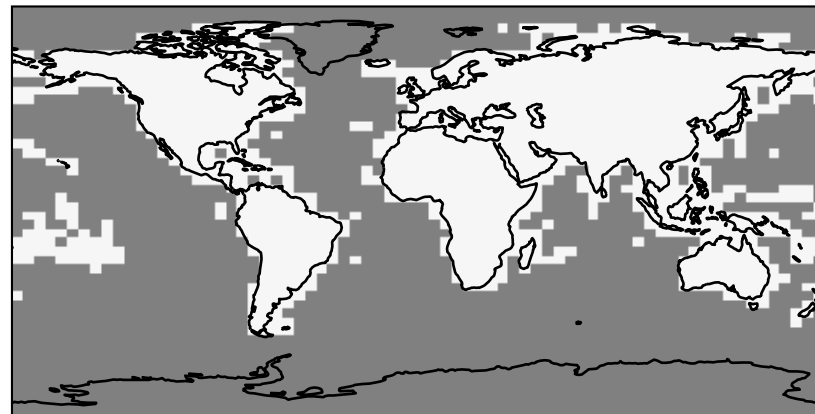
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

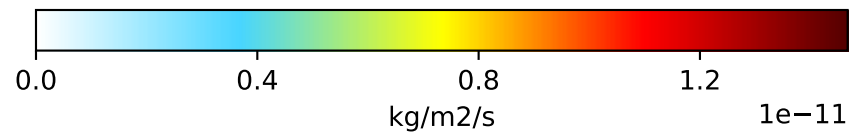
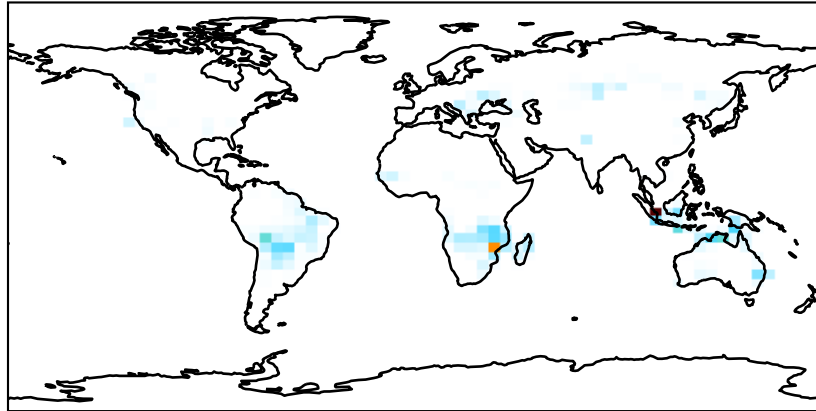


Ratio
Dev/Ref, Fixed Range

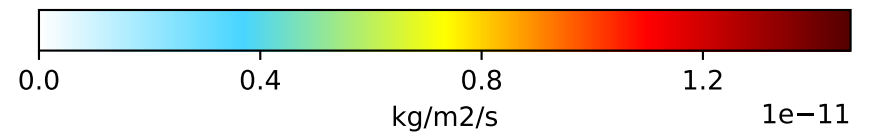
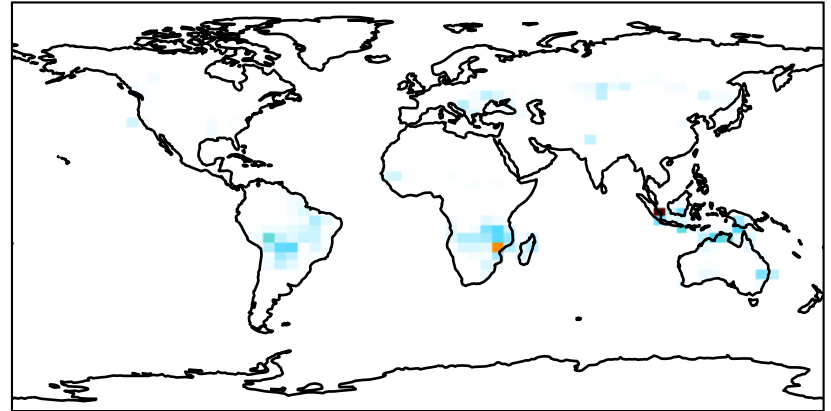


EmisBCPO_BioBurn (Oct2019)

GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



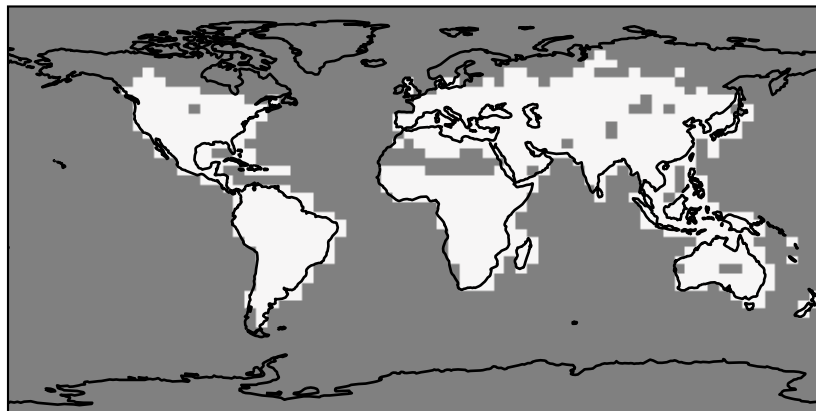
Difference
Dev - Ref, Dynamic Range



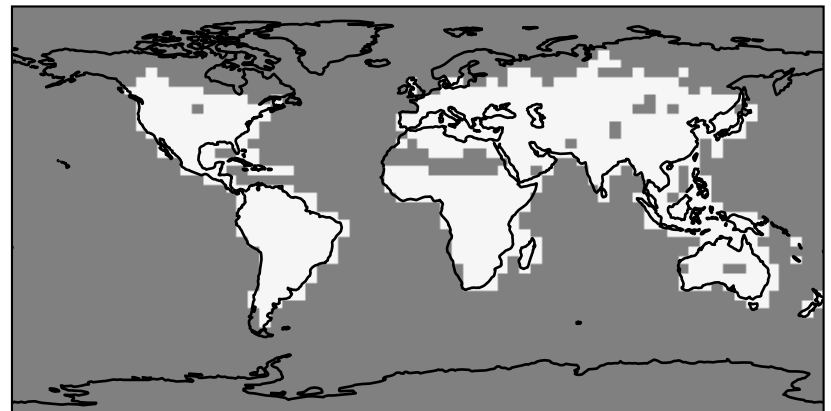
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

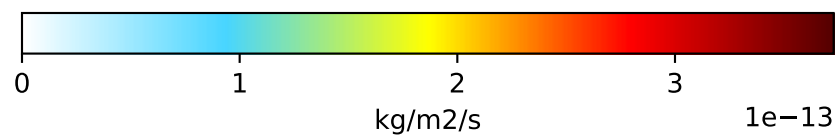
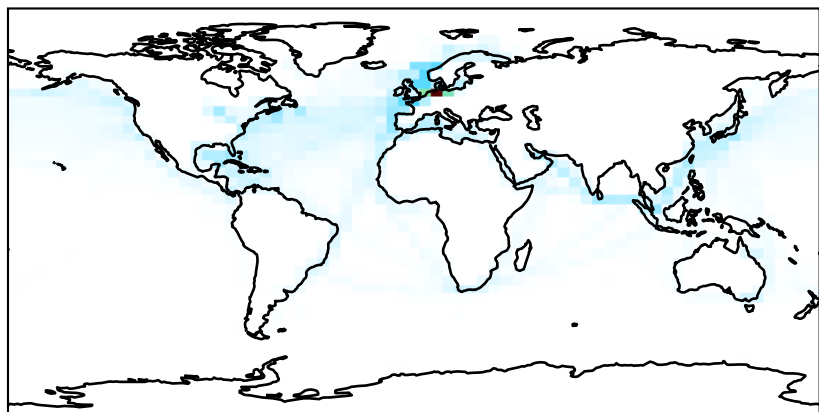


Ratio
Dev/Ref, Fixed Range

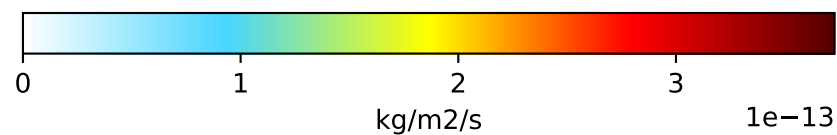
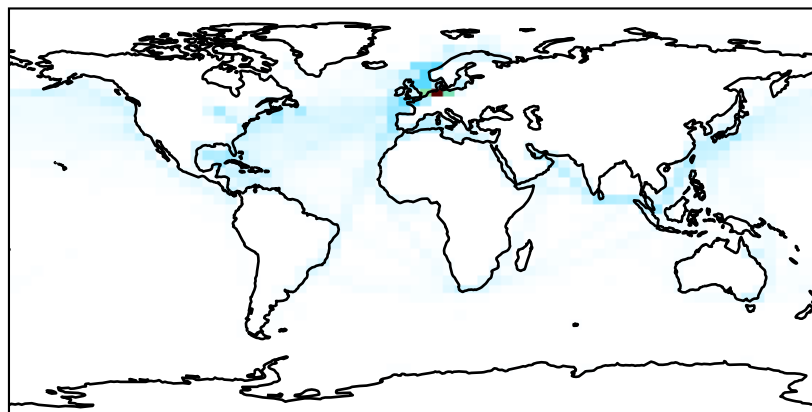


EmisBCPO_Ship (Oct2019)

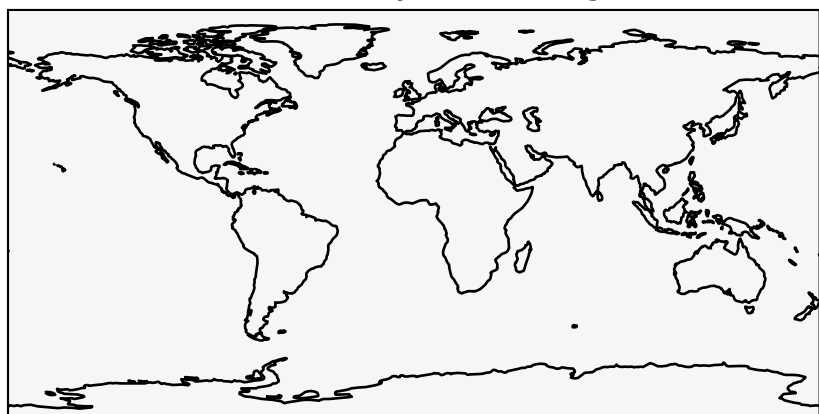
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



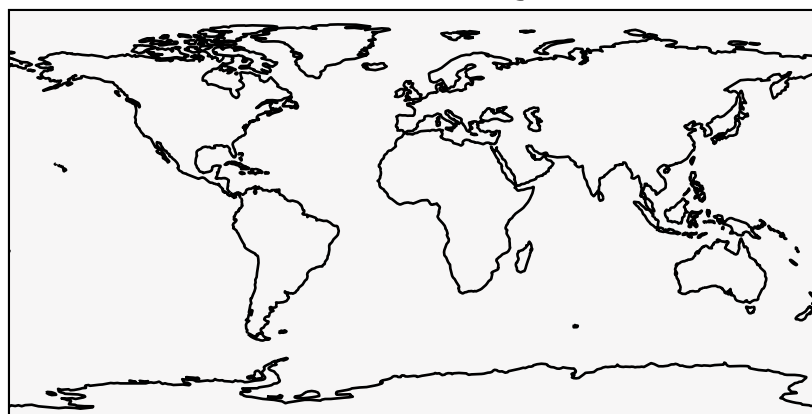
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



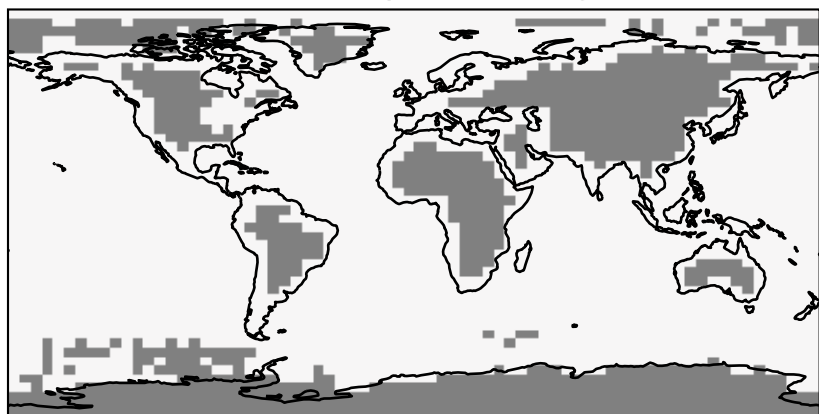
Difference
Dev - Ref, Dynamic Range



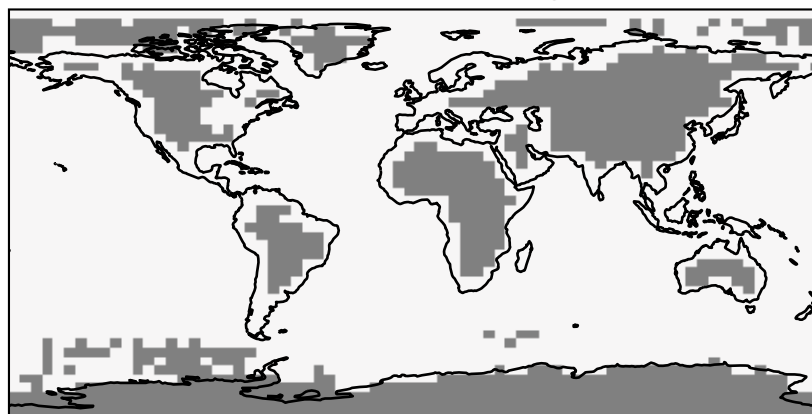
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

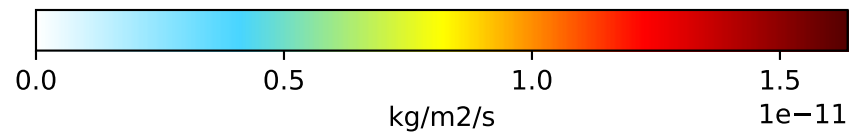
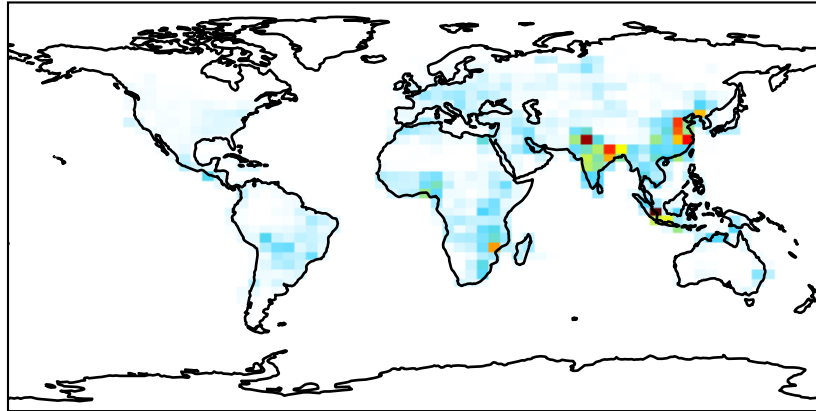


Ratio
Dev/Ref, Fixed Range

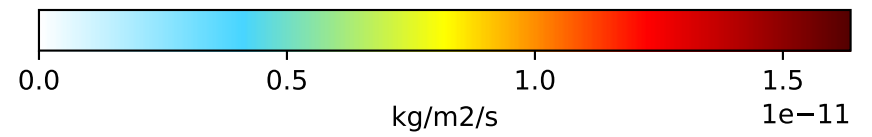
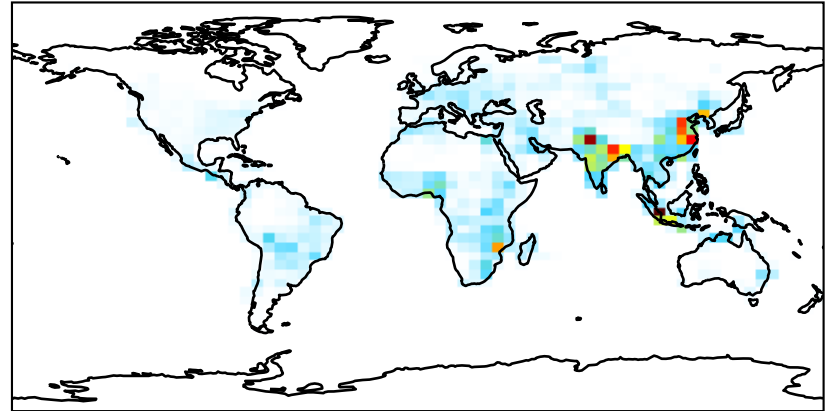


EmisBCPO_Total (Oct2019)

GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



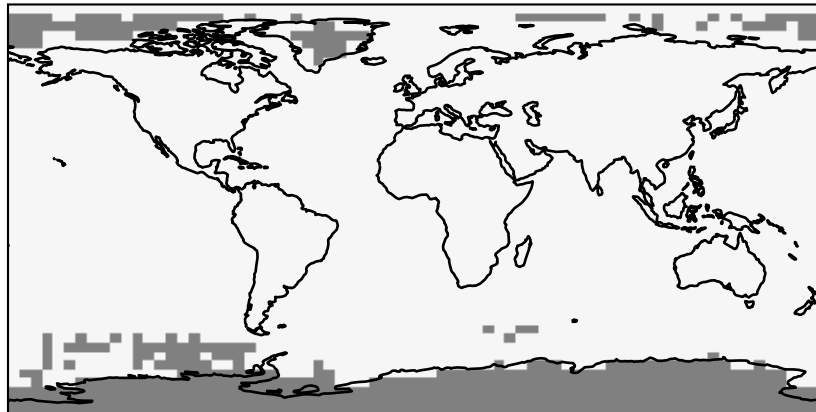
Difference
Dev - Ref, Dynamic Range



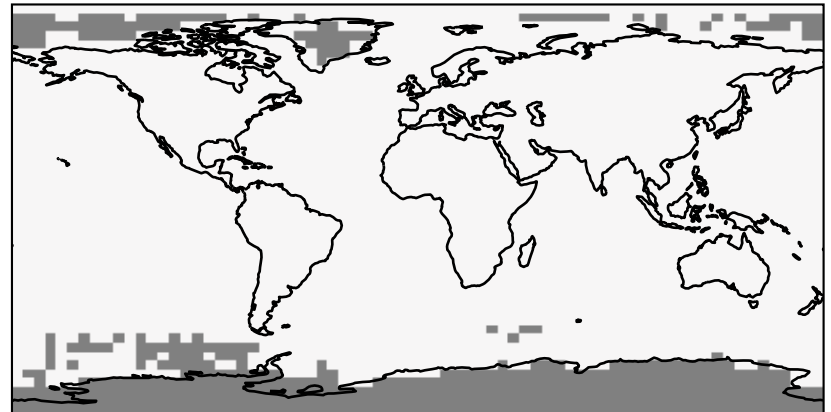
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

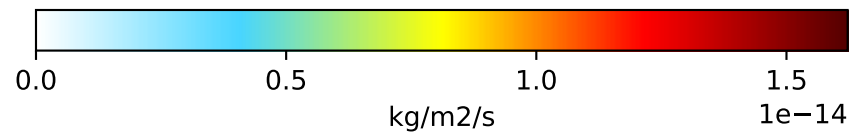
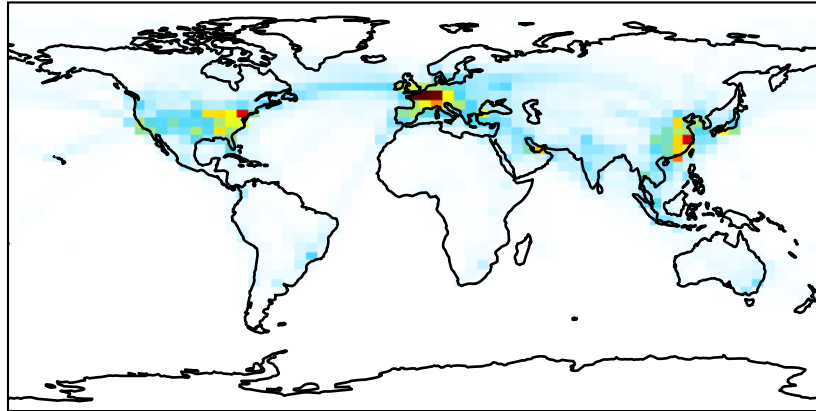


Ratio
Dev/Ref, Fixed Range

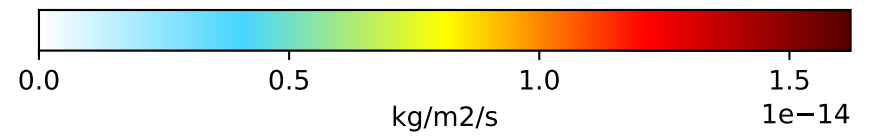
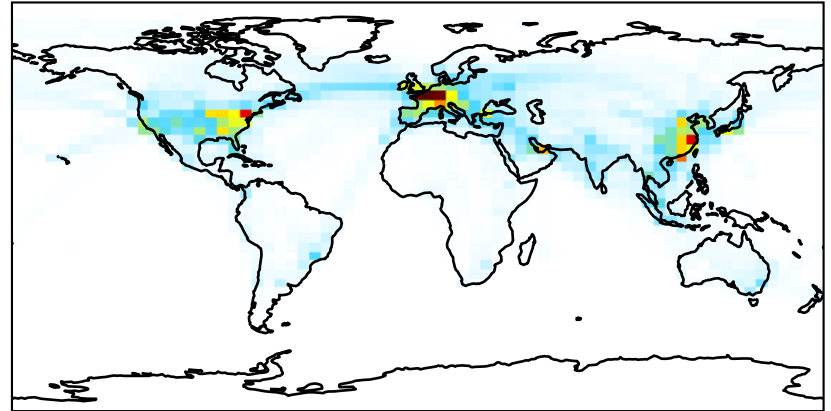


EmisOCPI_Aircraft (Oct2019)

GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



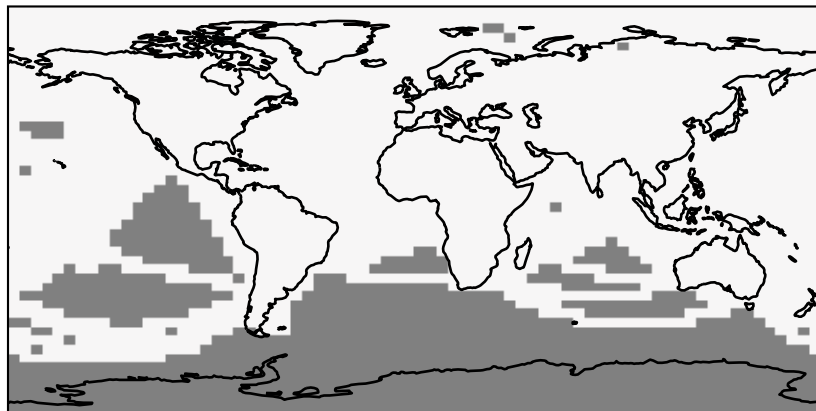
Difference
Dev - Ref, Dynamic Range



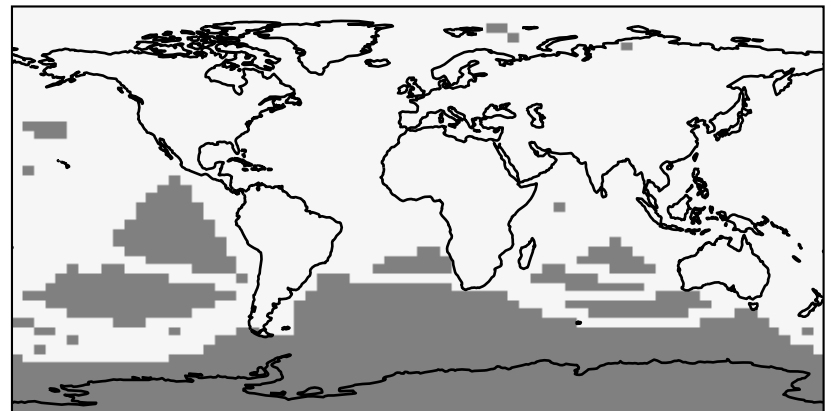
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

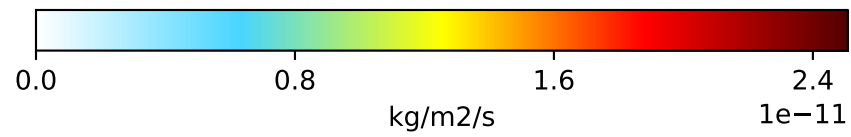
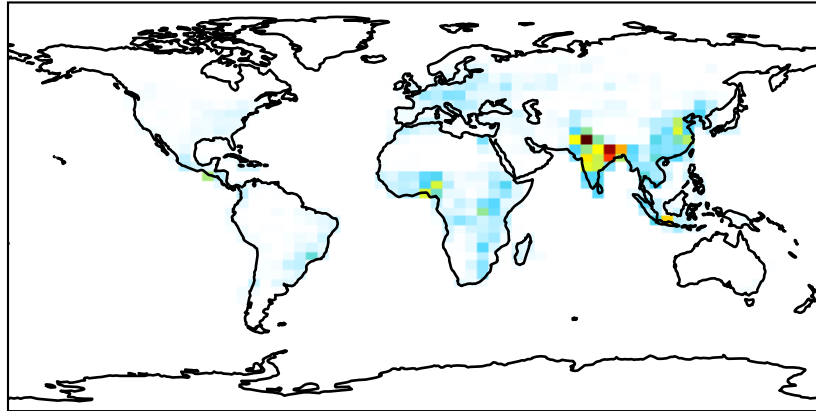


Ratio
Dev/Ref, Fixed Range

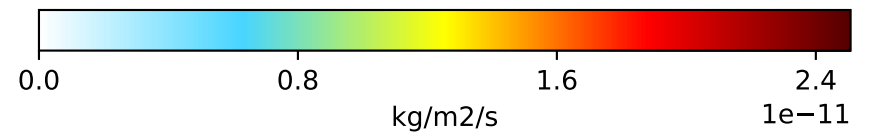
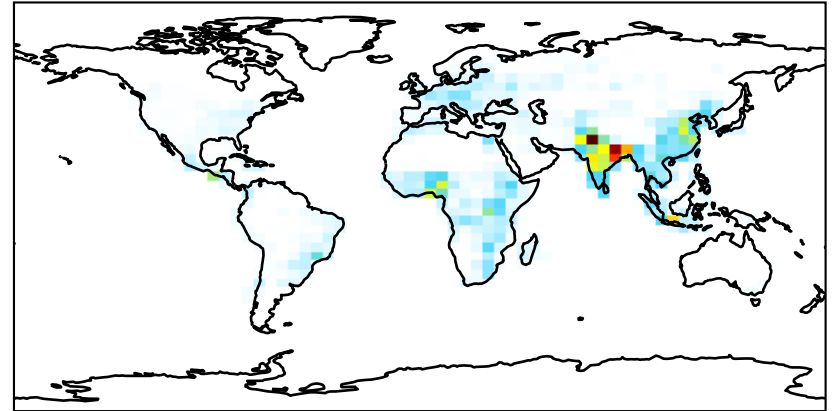


EmisOCPI_Anthro (Oct2019)

GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



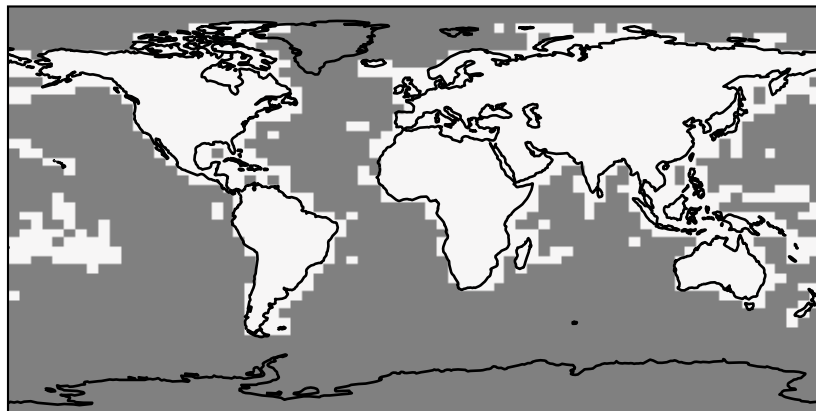
Difference
Dev - Ref, Dynamic Range



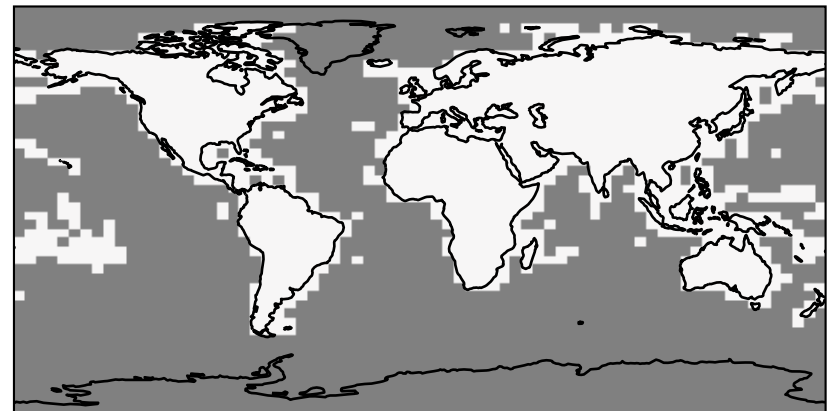
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

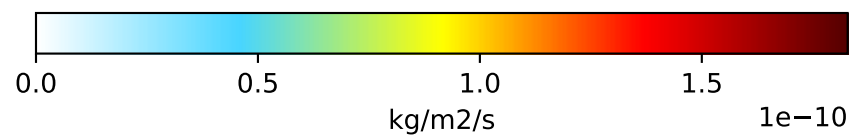
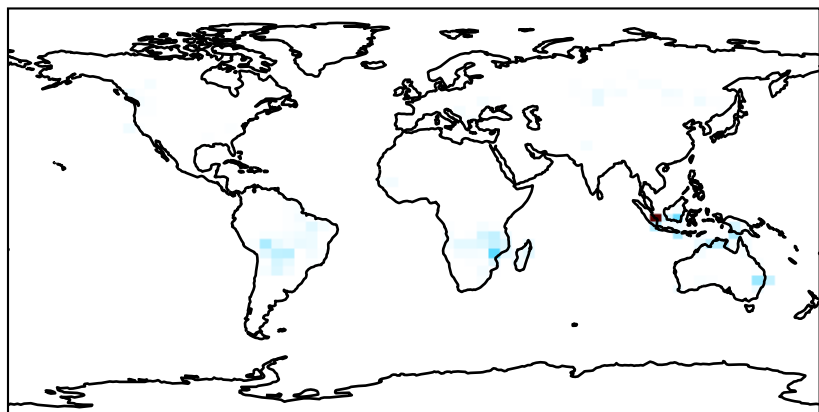


Ratio
Dev/Ref, Fixed Range

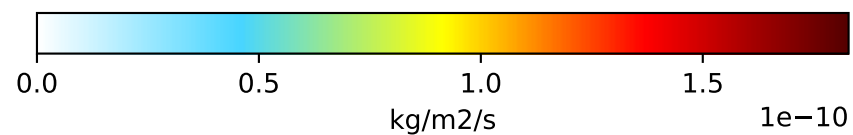
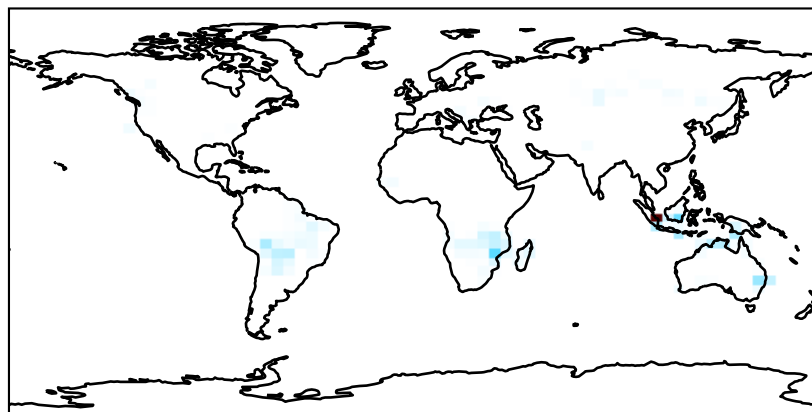


EmisOCPI_BioBurn (Oct2019)

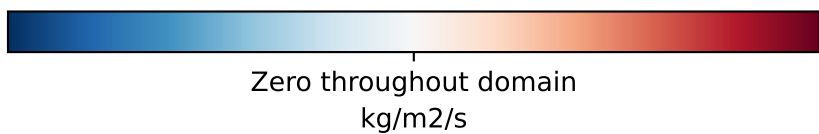
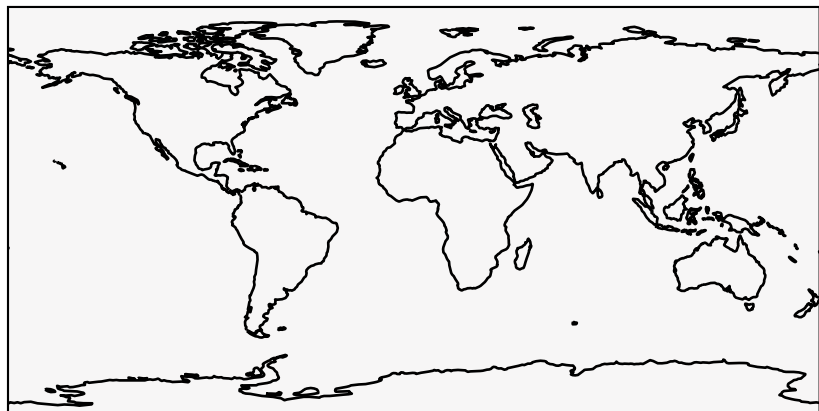
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



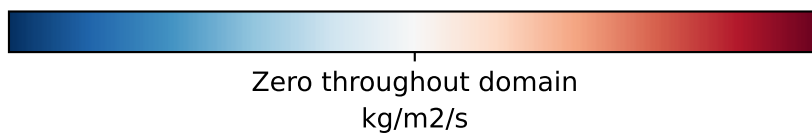
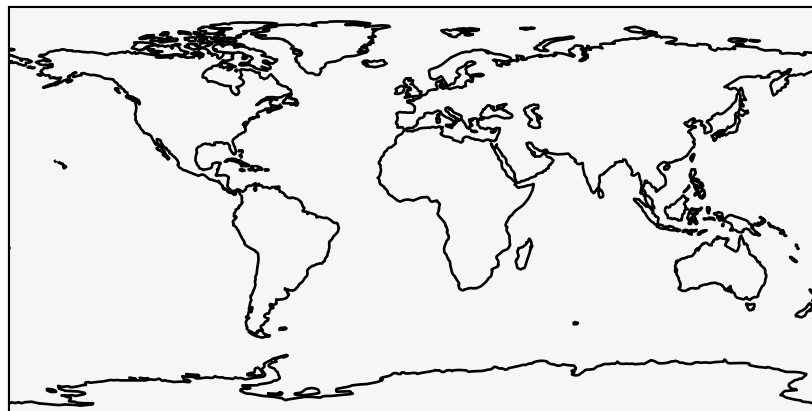
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



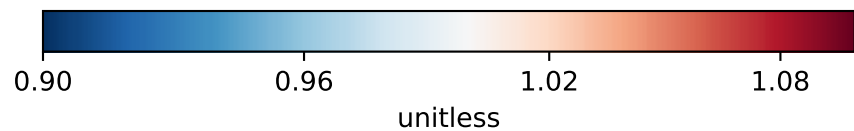
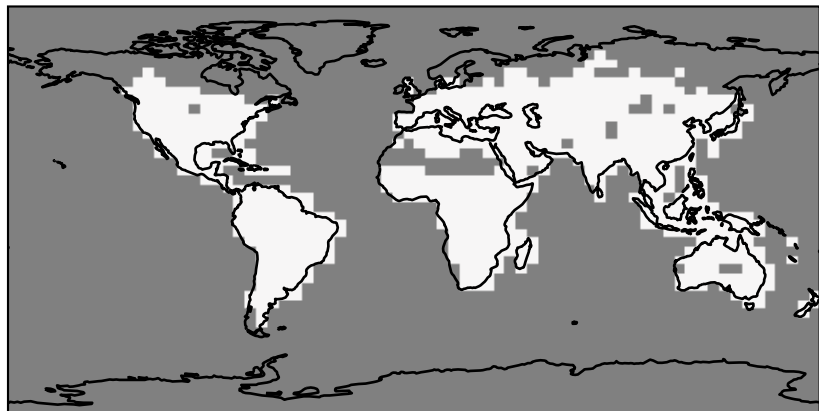
Difference
Dev - Ref, Dynamic Range



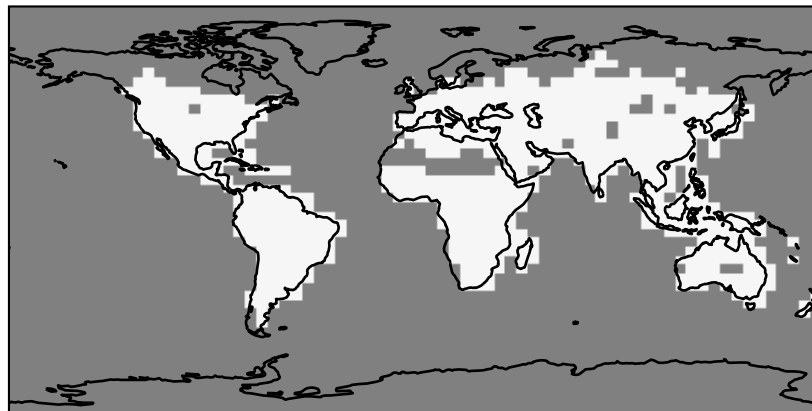
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

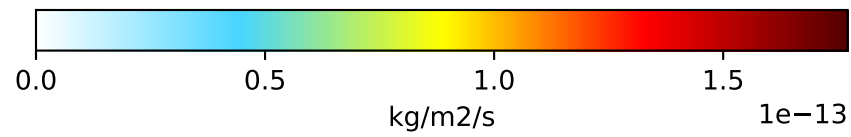
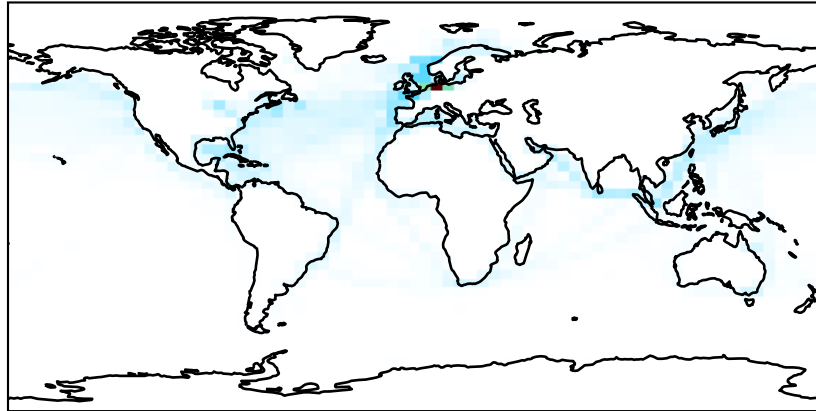


Ratio
Dev/Ref, Fixed Range

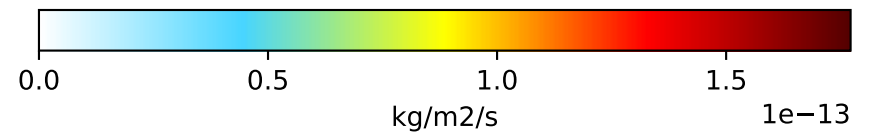
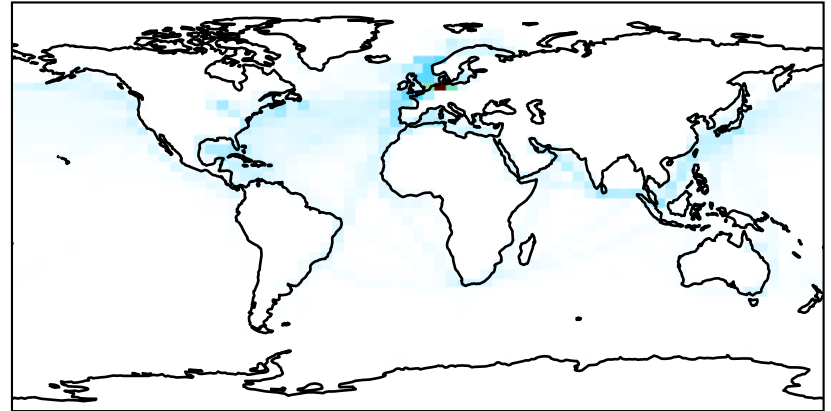


EmisOCPI_Ship (Oct2019)

GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



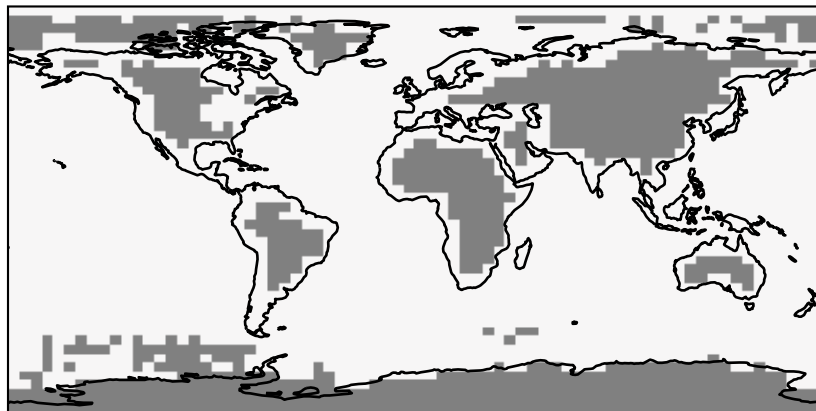
Difference
Dev - Ref, Dynamic Range



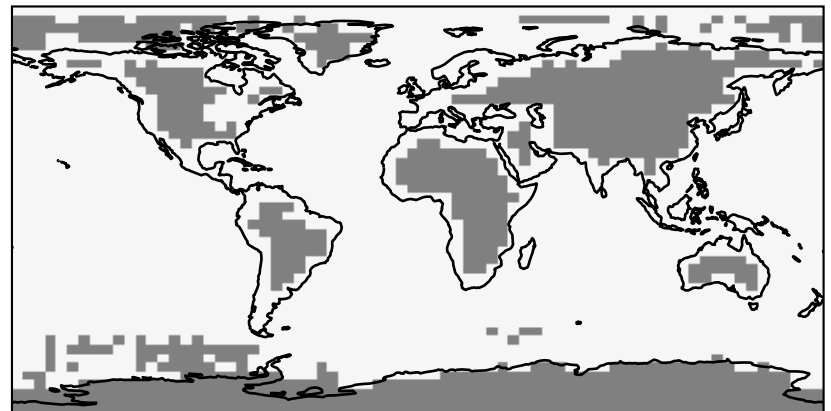
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

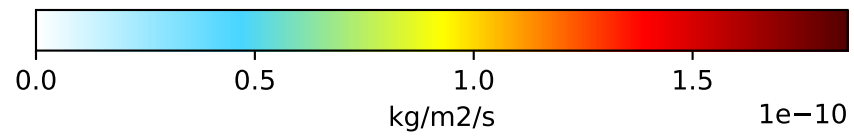
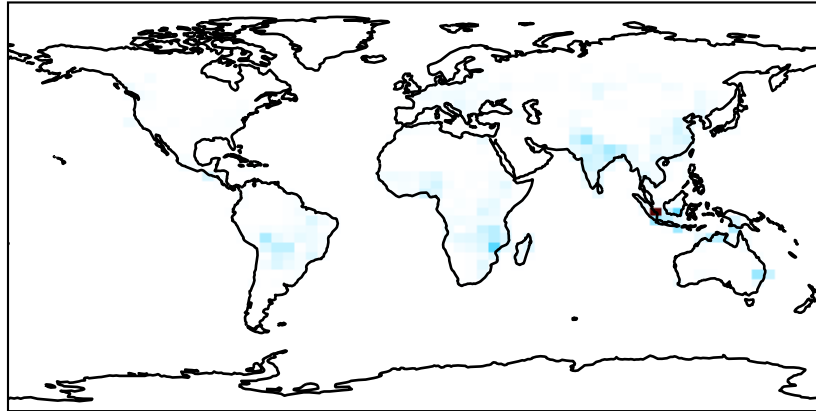


Ratio
Dev/Ref, Fixed Range

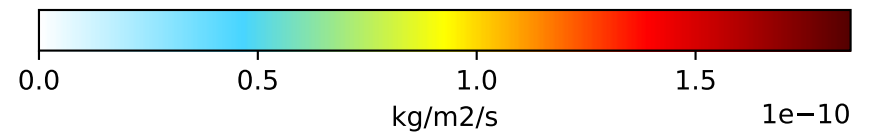
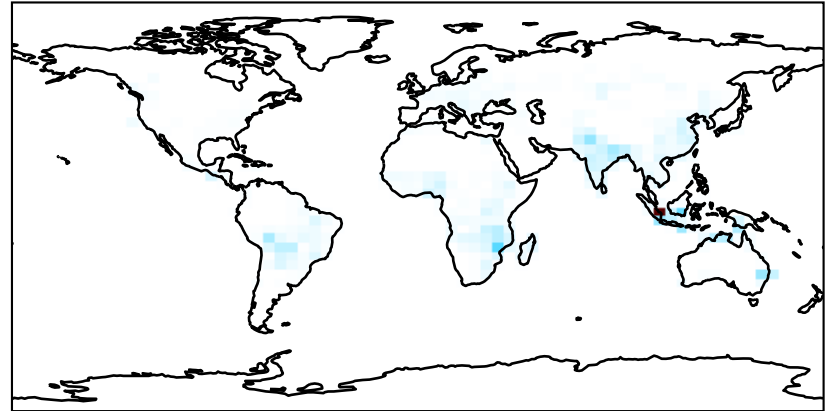


EmisOCPI_Total (Oct2019)

GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



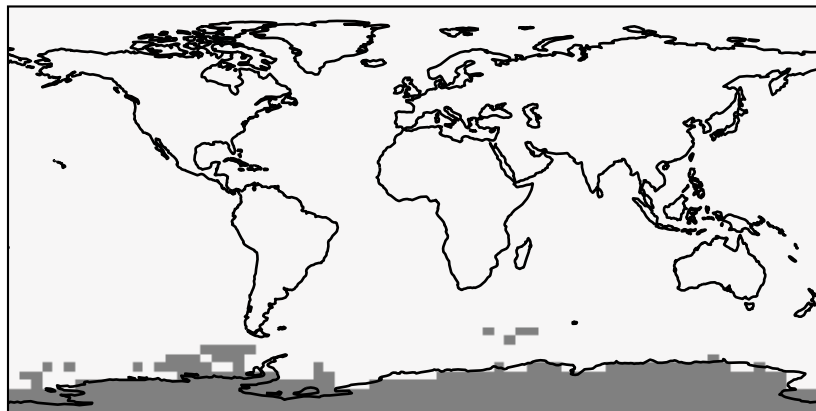
Difference
Dev - Ref, Dynamic Range



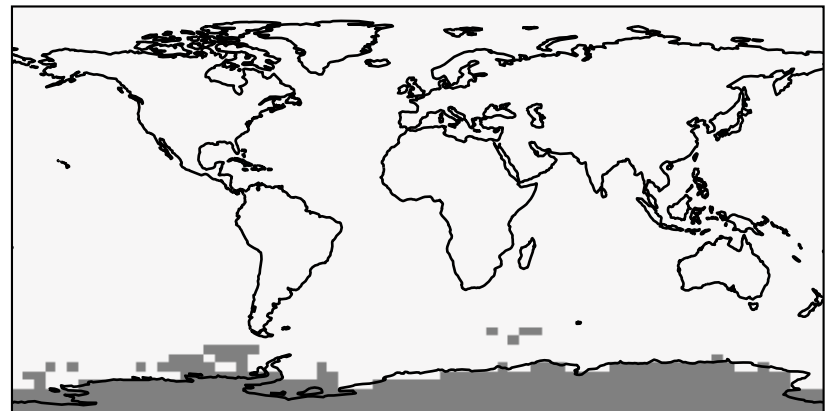
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

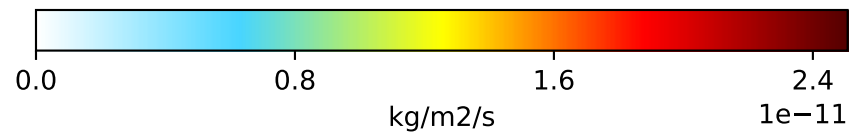
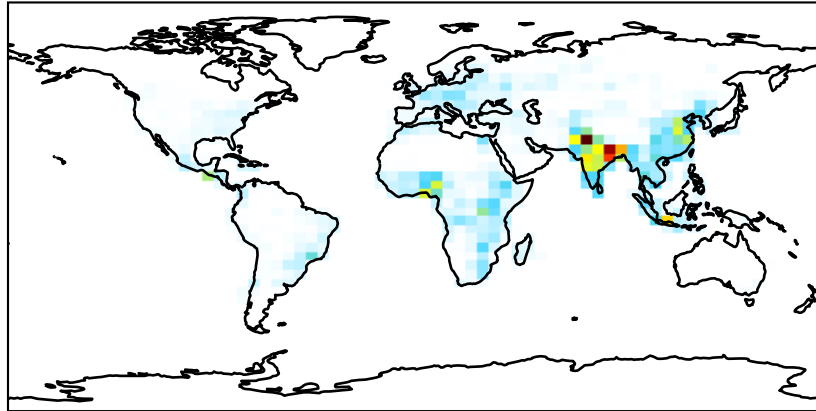


Ratio
Dev/Ref, Fixed Range

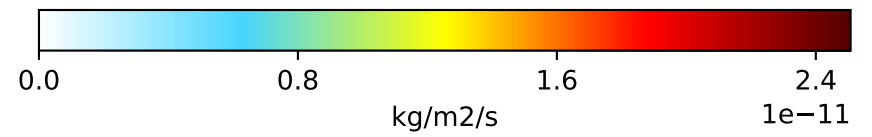
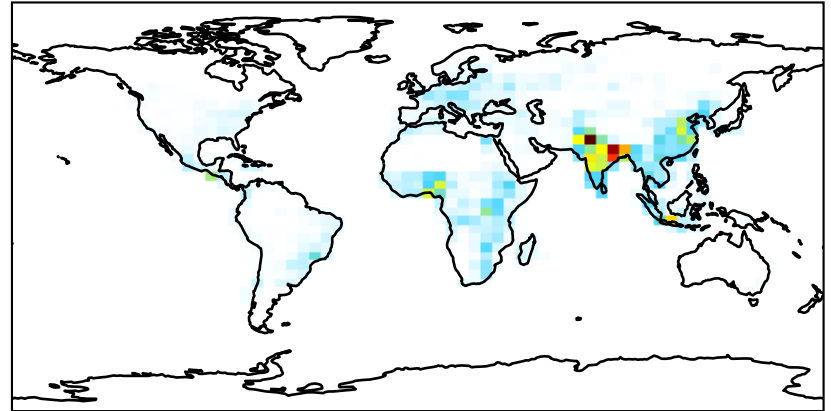


EmisOCPO_Anthro (Oct2019)

GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



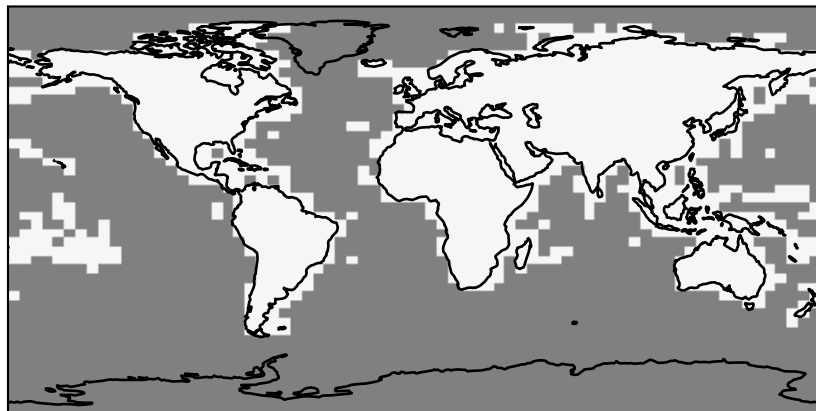
Difference
Dev - Ref, Dynamic Range



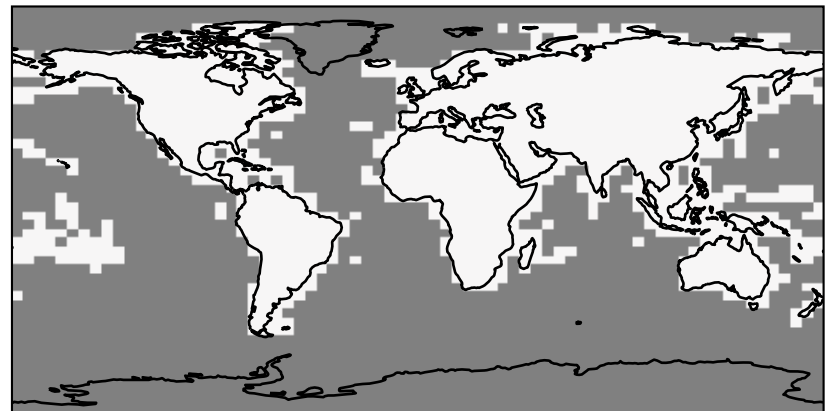
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

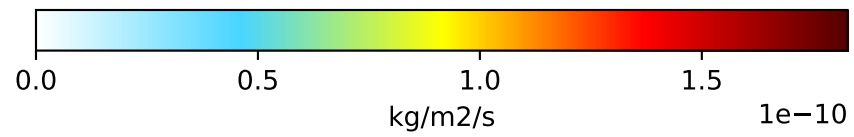
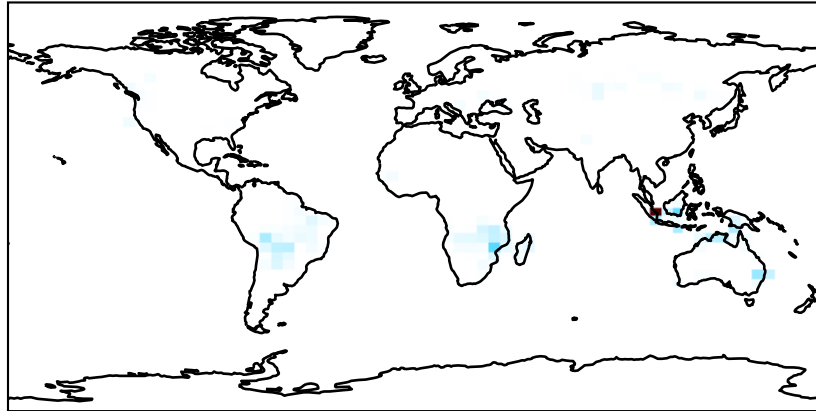


Ratio
Dev/Ref, Fixed Range

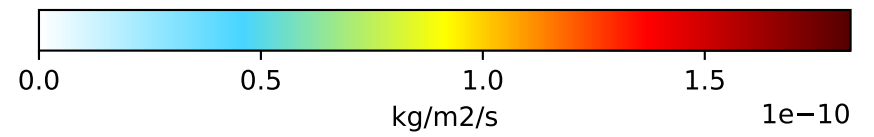
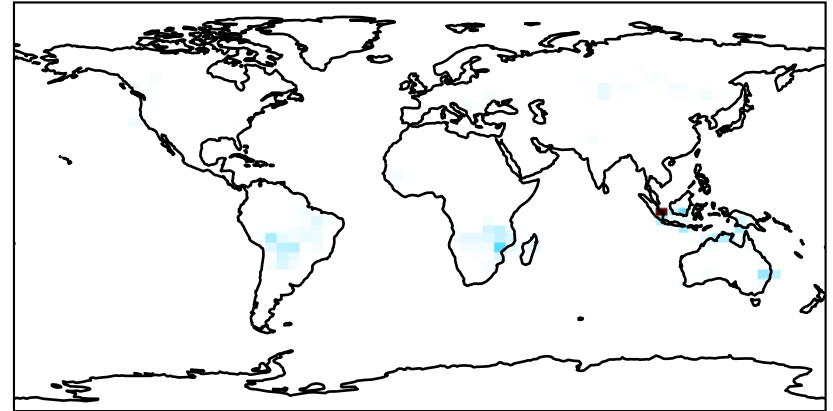


EmisOCPO_BioBurn (Oct2019)

GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



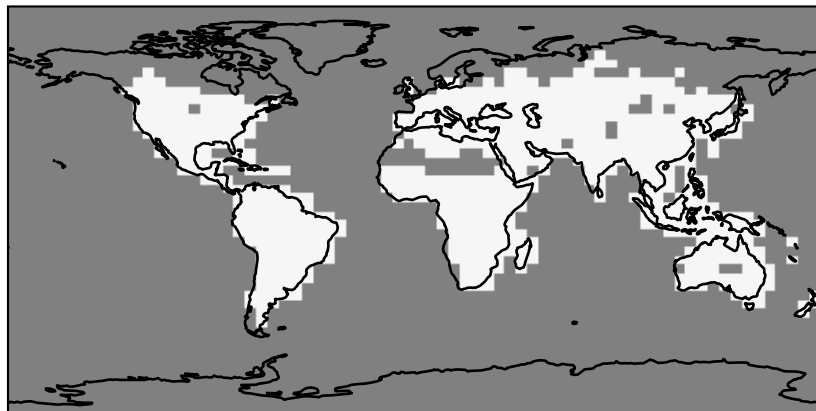
Difference
Dev - Ref, Dynamic Range



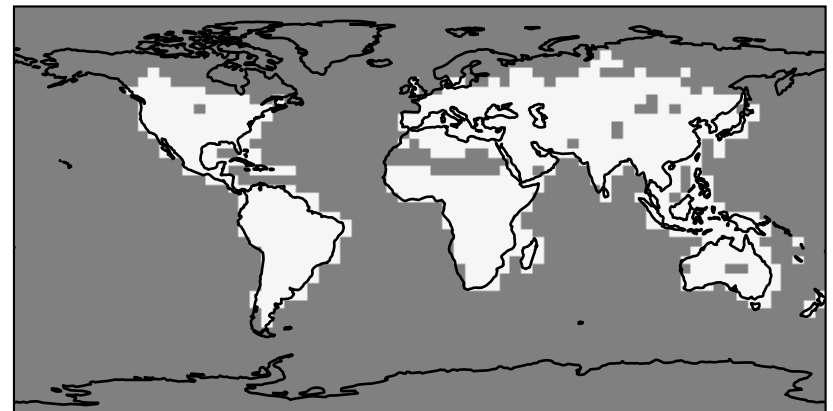
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

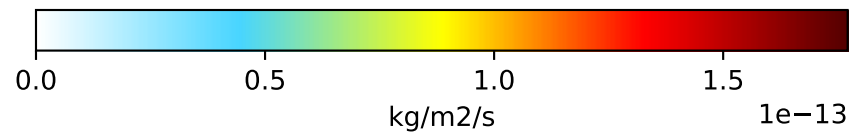
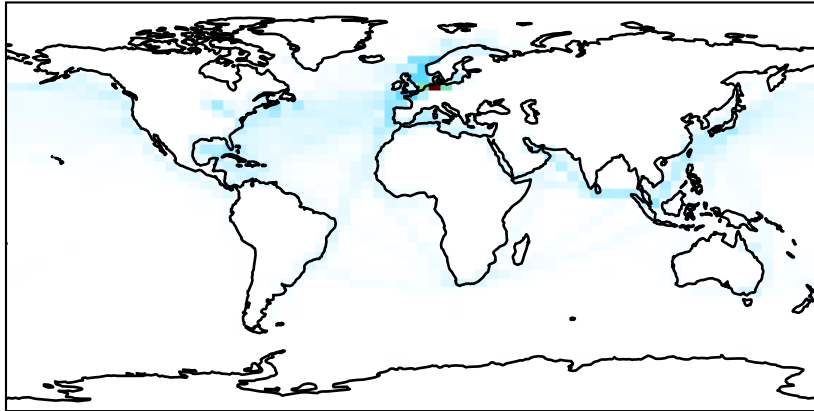


Ratio
Dev/Ref, Fixed Range

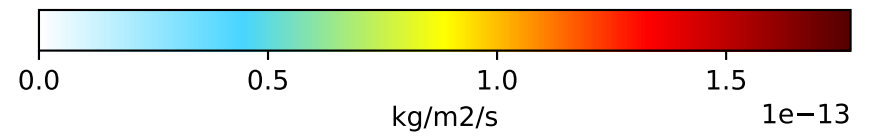
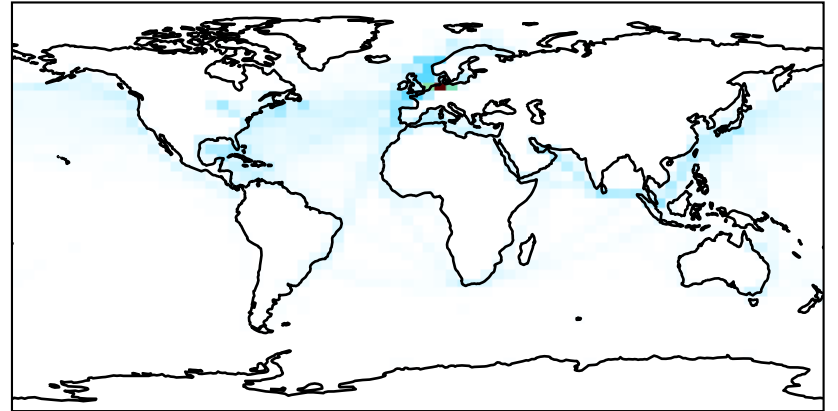


EmisOCPO_Ship (Oct2019)

GCC_13.4.0-rc.4 (Ref)
4.0x5.0



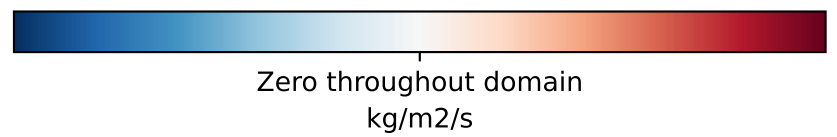
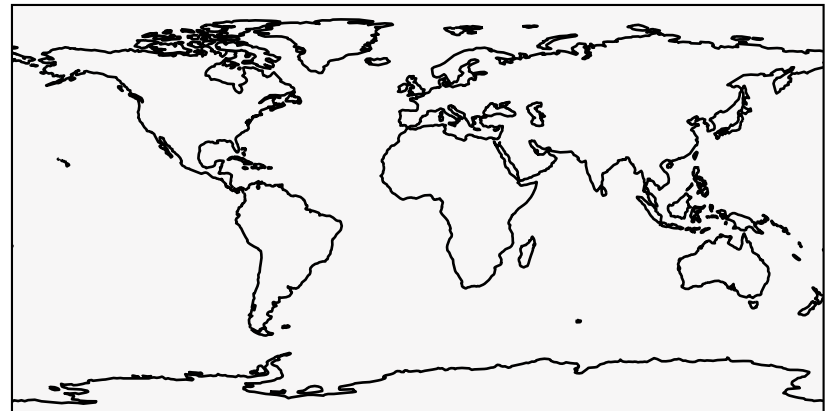
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



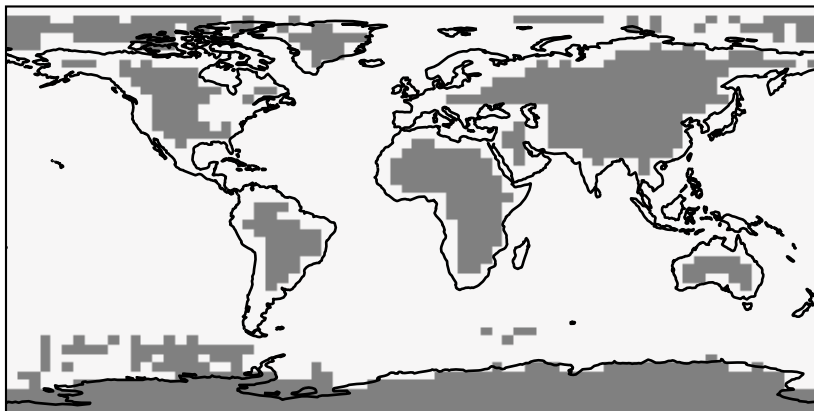
Difference
Dev - Ref, Dynamic Range



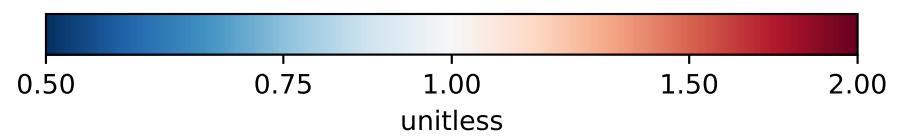
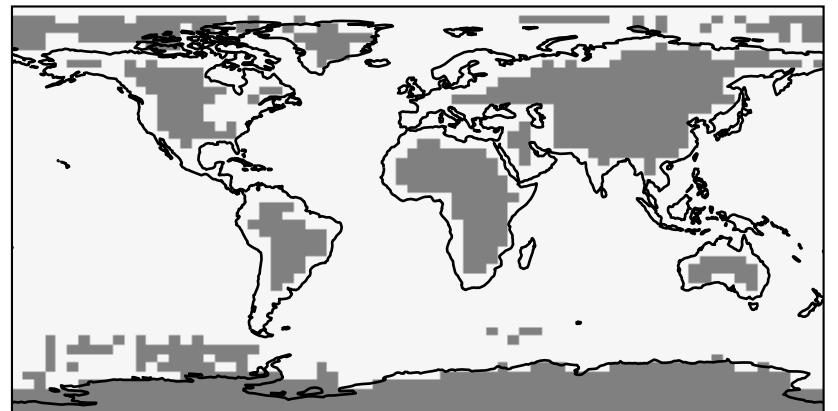
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

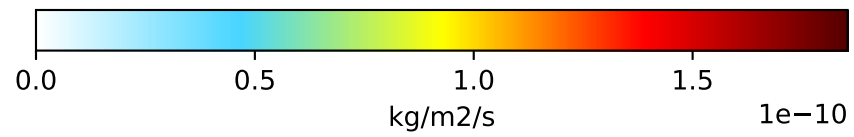
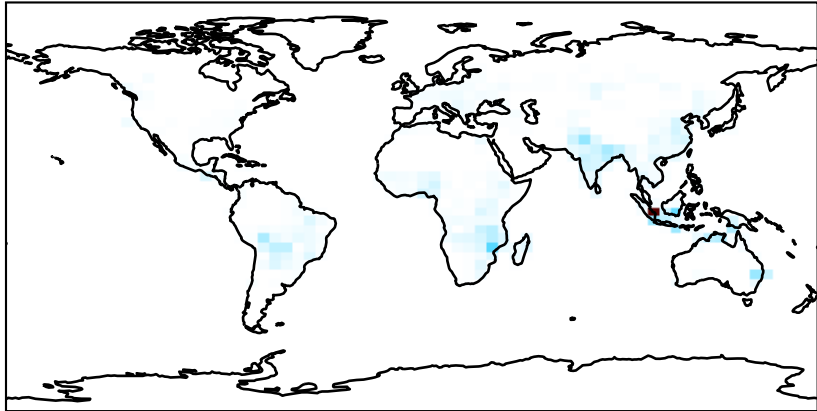


Ratio
Dev/Ref, Fixed Range

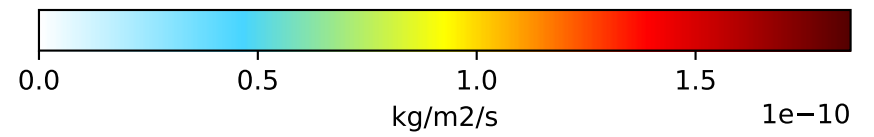
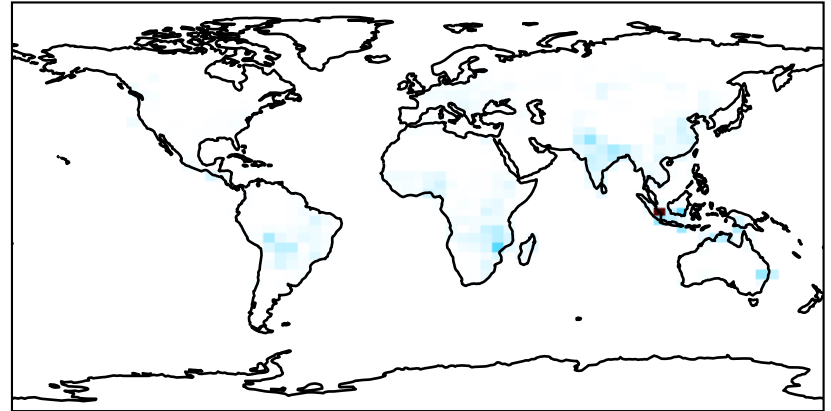


EmisOCPO_Total (Oct2019)

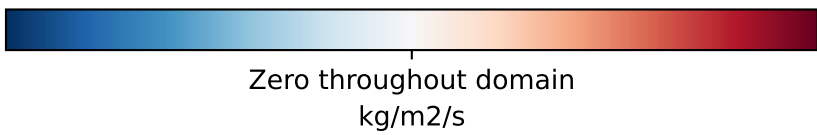
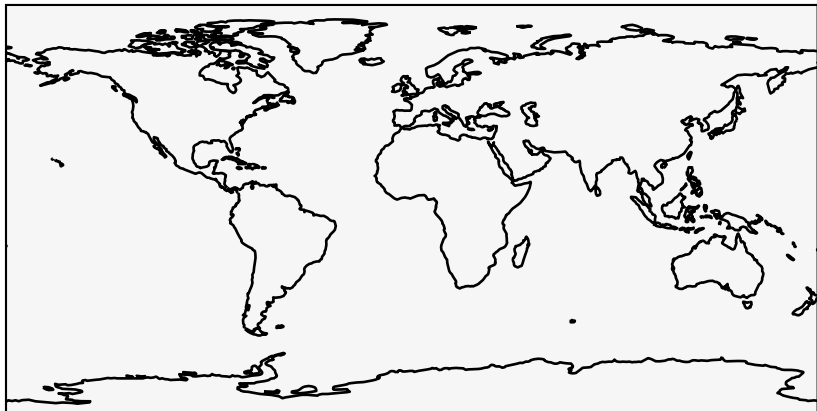
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



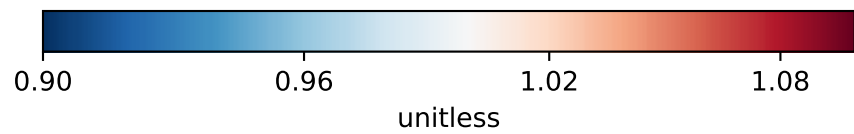
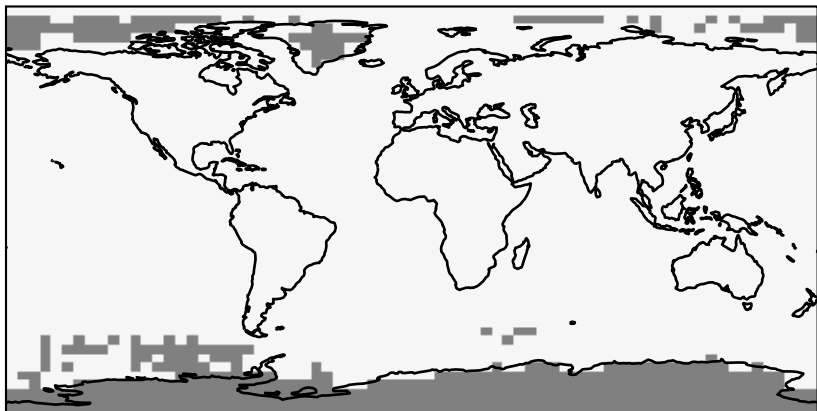
Difference
Dev - Ref, Dynamic Range



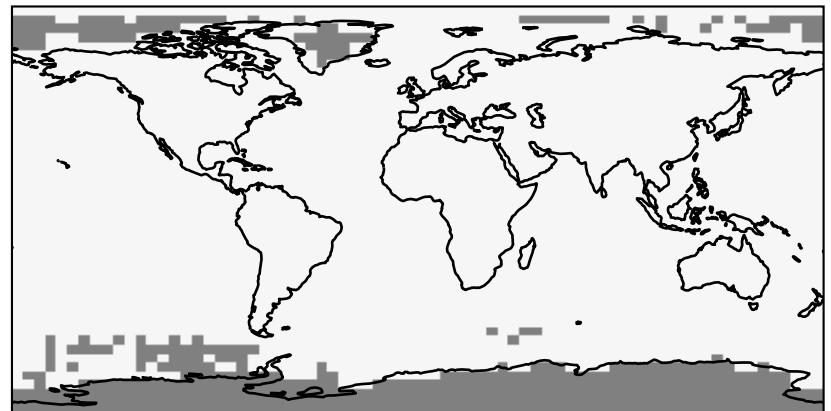
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range



Ratio
Dev/Ref, Fixed Range



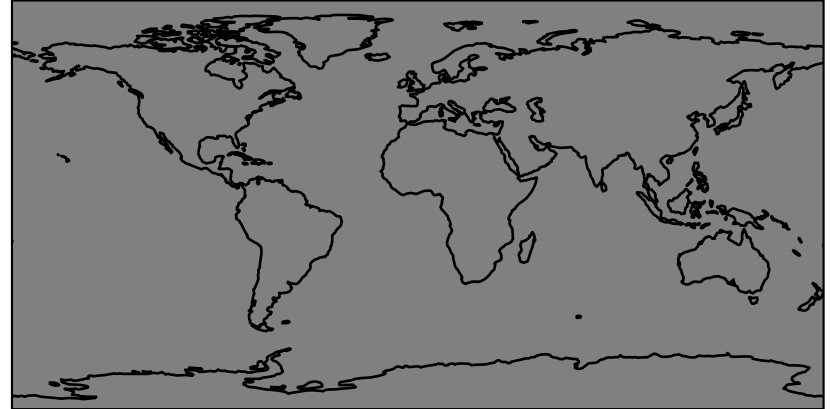
EmisBrSALA_Natural (Oct2019)

GCC_13.4.0-rc.4 (Ref)
4.0x5.0



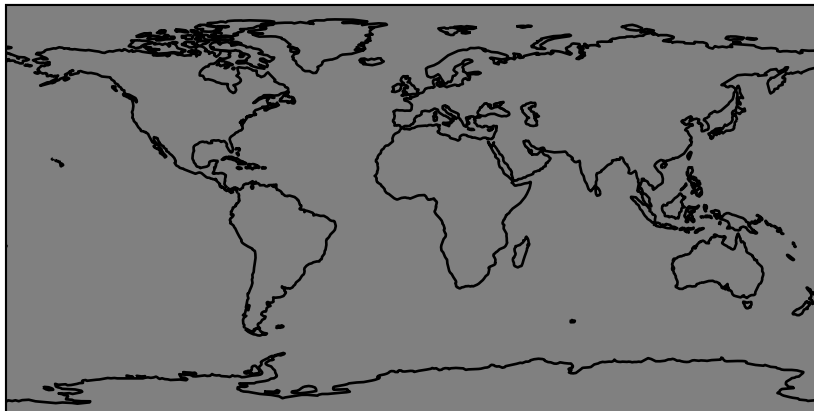
Zero throughout domain
kg/m2/s

GCC_14.0.0-rc.0 (Dev)
4.0x5.0



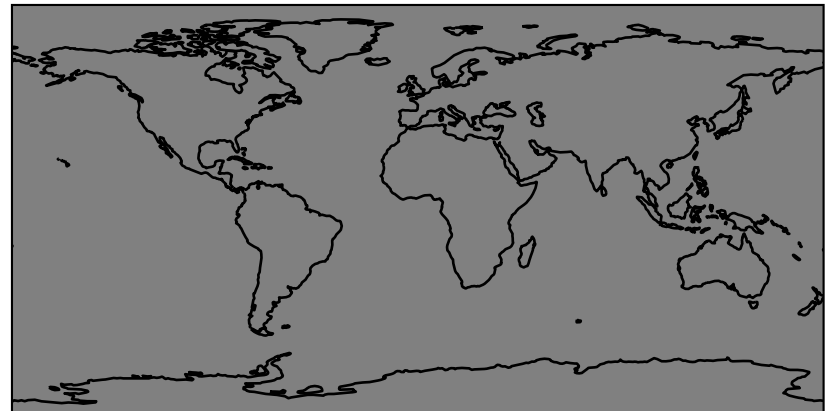
Undefined throughout domain
kg/m2/s

Difference
Dev - Ref, Dynamic Range



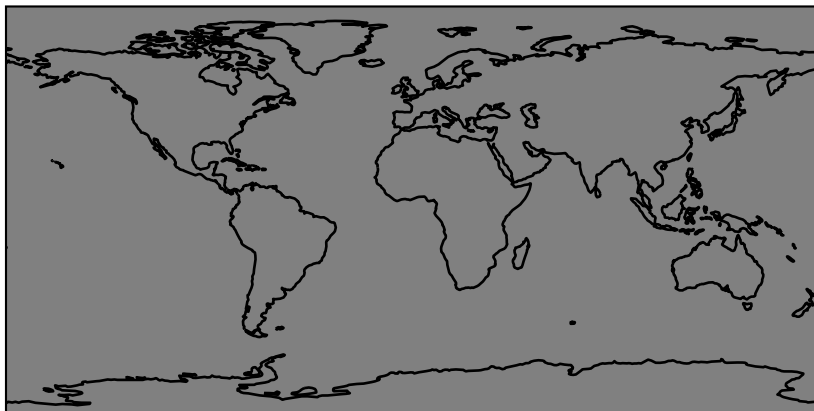
Undefined throughout domain
kg/m2/s

Difference
Dev - Ref, Restricted Range [5%,95%]



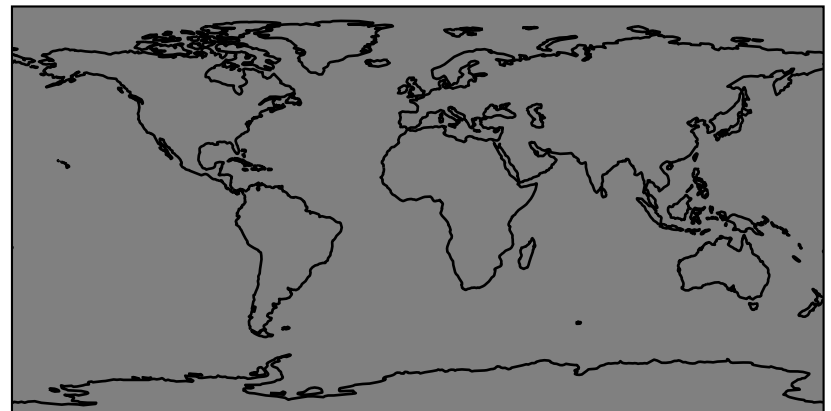
Undefined throughout domain
kg/m2/s

Ratio
Dev/Ref, Dynamic Range



Undefined throughout domain
unitless

Ratio
Dev/Ref, Fixed Range



Undefined throughout domain
unitless

EmisBrSALC_Natural (Oct2019)

GCC_13.4.0-rc.4 (Ref)
4.0x5.0



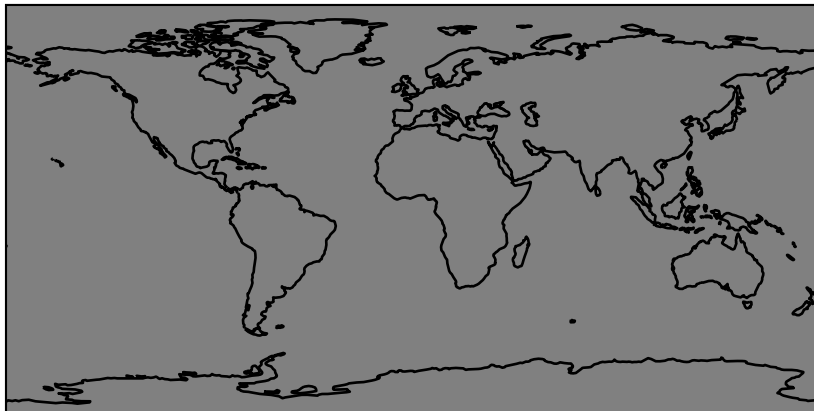
Zero throughout domain
kg/m2/s

GCC_14.0.0-rc.0 (Dev)
4.0x5.0



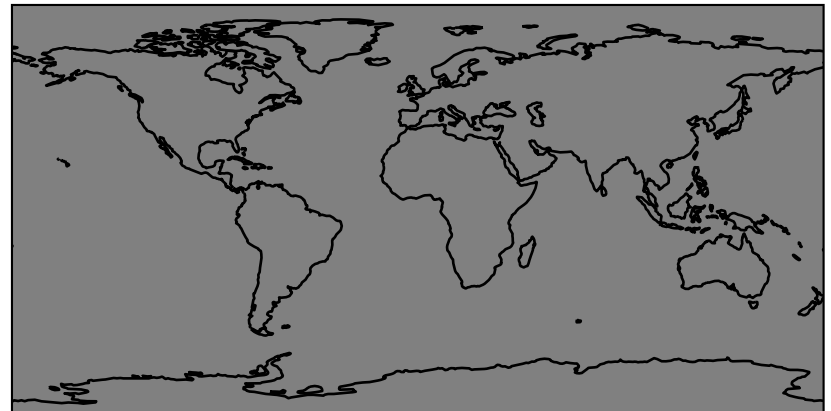
Undefined throughout domain
kg/m2/s

Difference
Dev - Ref, Dynamic Range



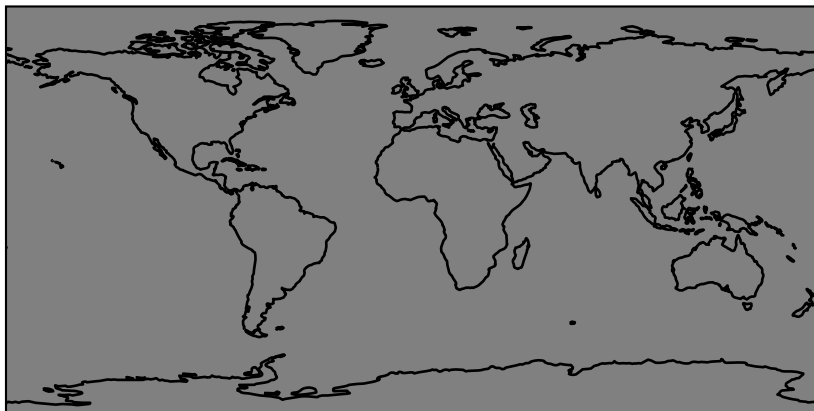
Undefined throughout domain
kg/m2/s

Difference
Dev - Ref, Restricted Range [5%,95%]



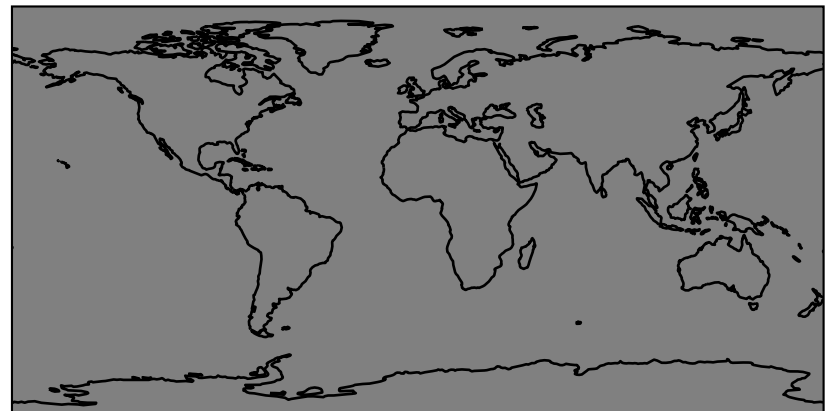
Undefined throughout domain
kg/m2/s

Ratio
Dev/Ref, Dynamic Range



Undefined throughout domain
unitless

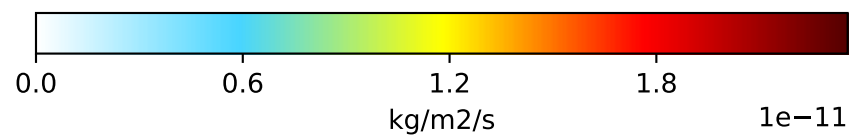
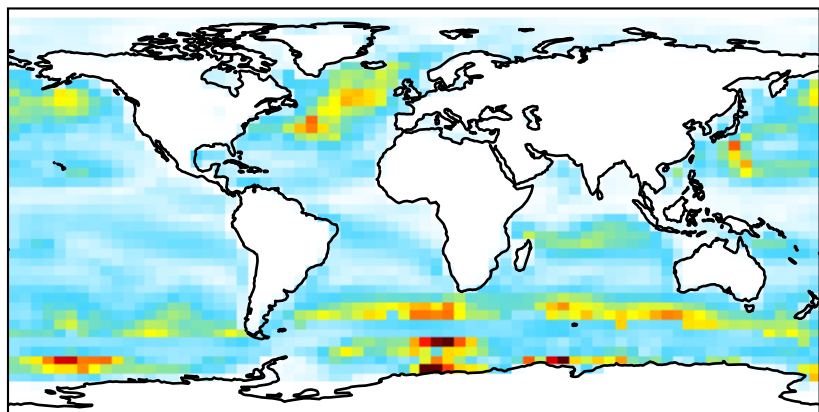
Ratio
Dev/Ref, Fixed Range



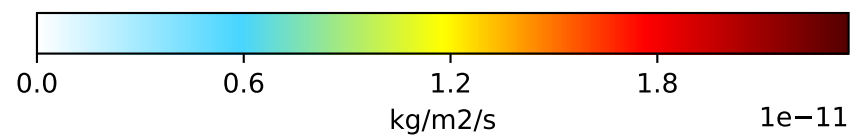
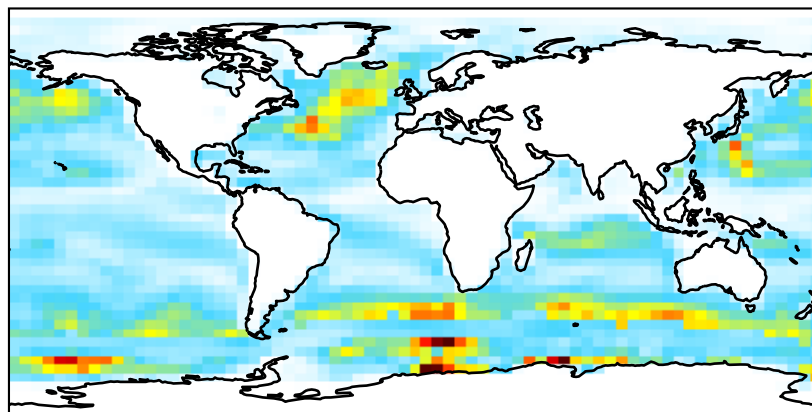
Undefined throughout domain
unitless

EmisSALA_Natural (Oct2019)

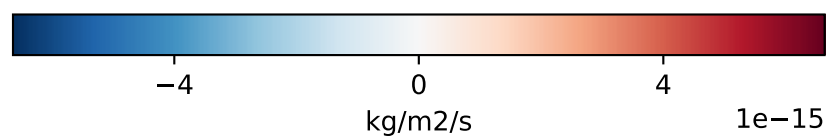
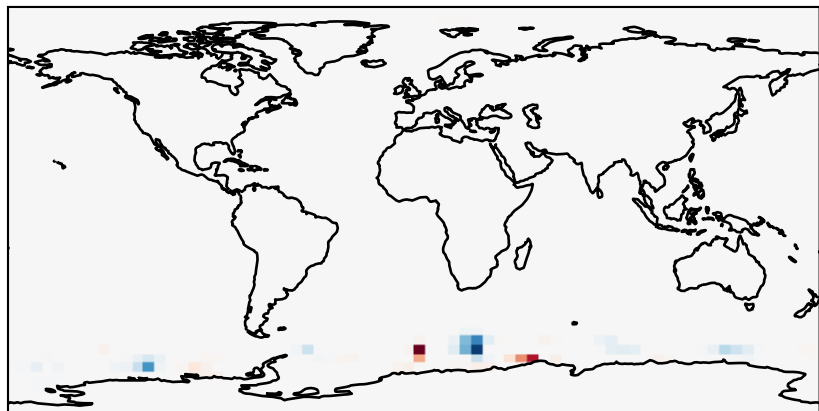
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



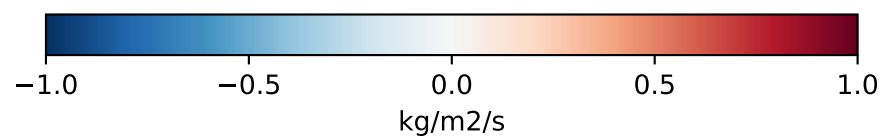
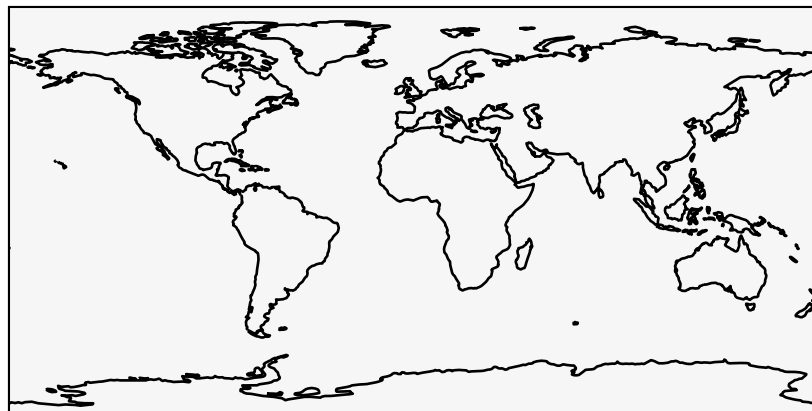
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



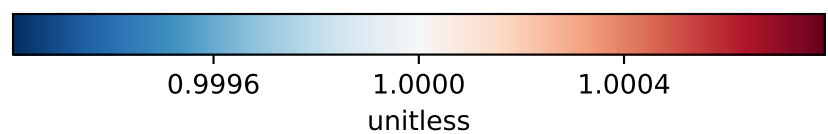
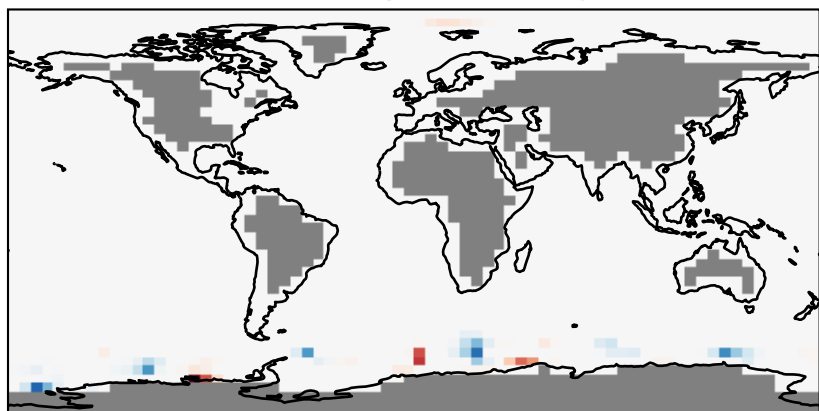
Difference
Dev - Ref, Dynamic Range



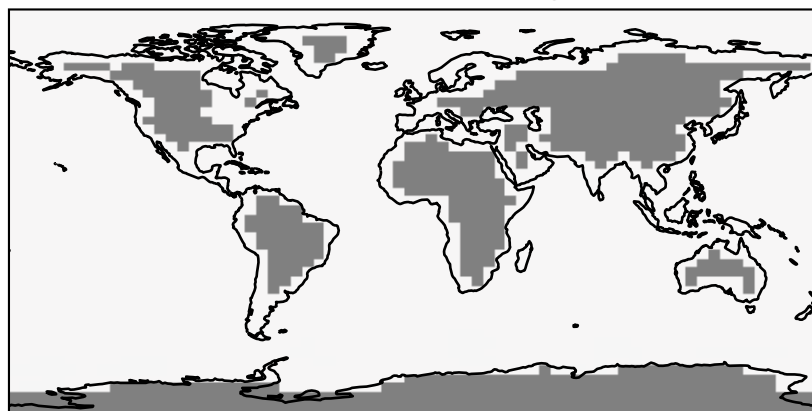
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

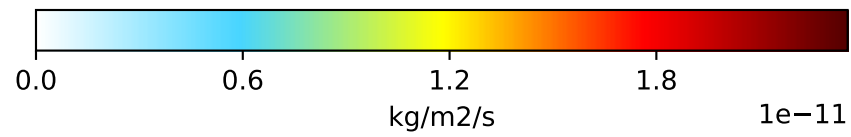
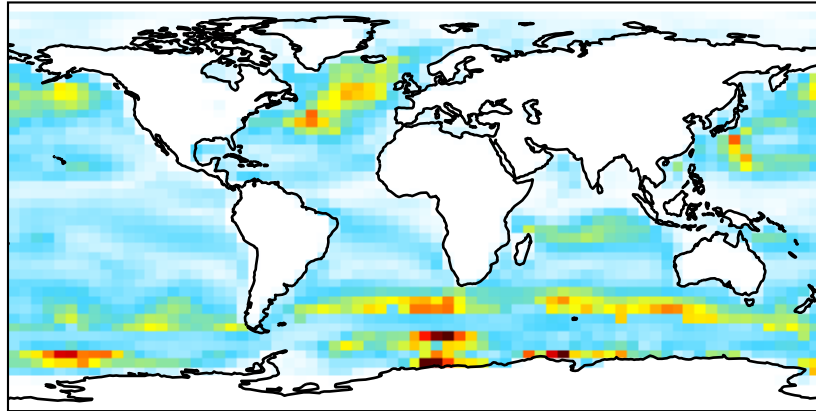


Ratio
Dev/Ref, Fixed Range

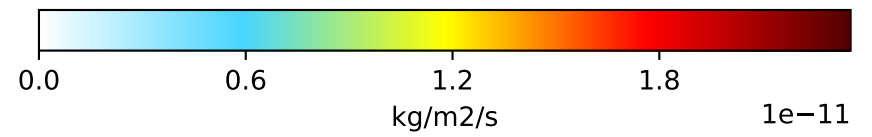
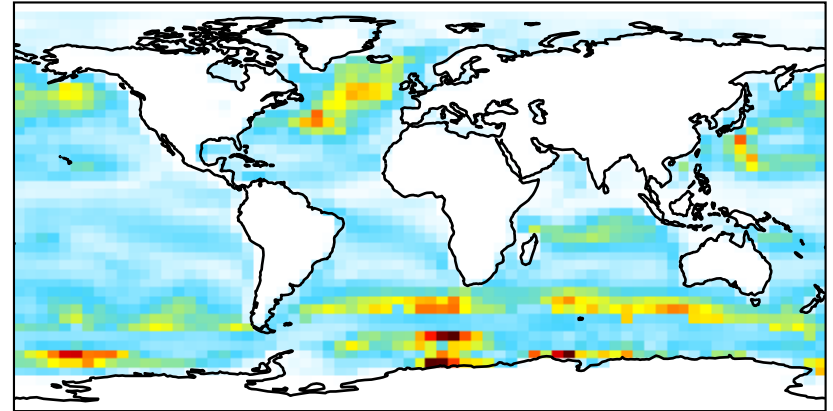


EmisSALAAL_Natural (Oct2019)

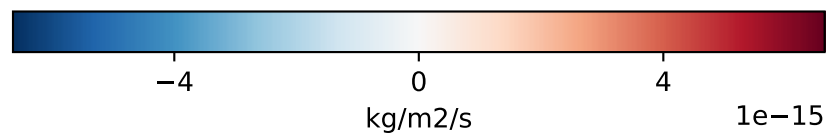
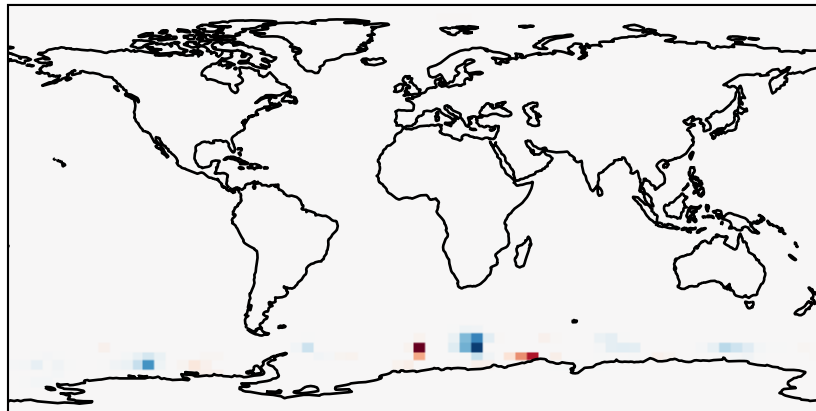
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



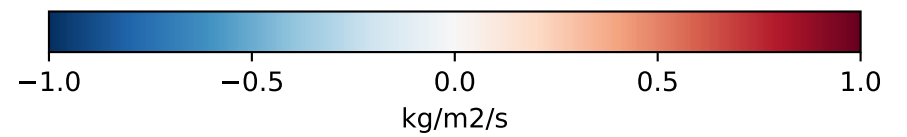
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



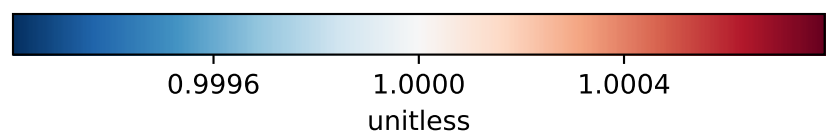
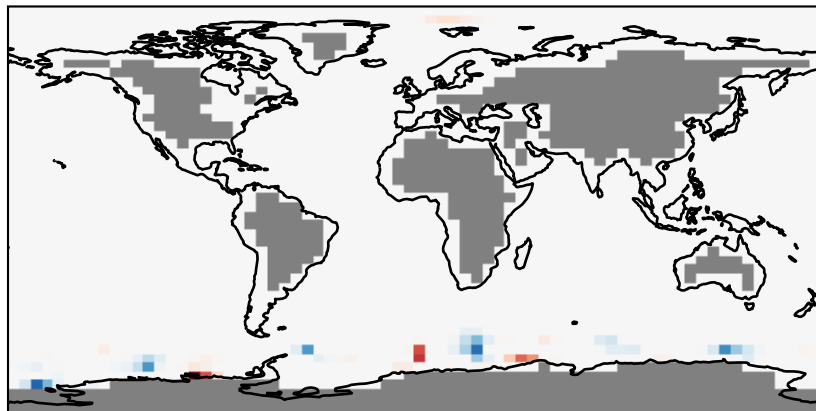
Difference
Dev - Ref, Dynamic Range



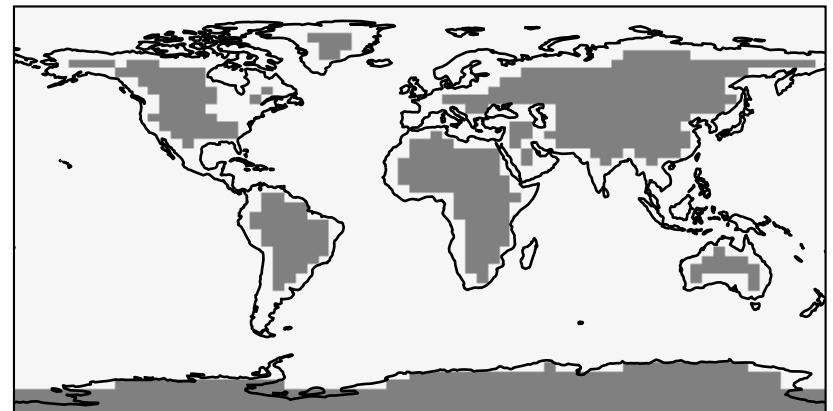
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

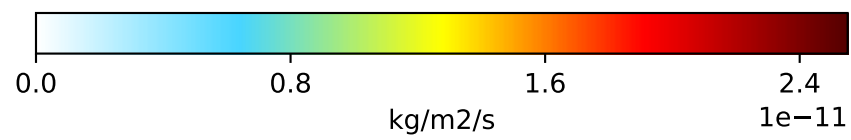
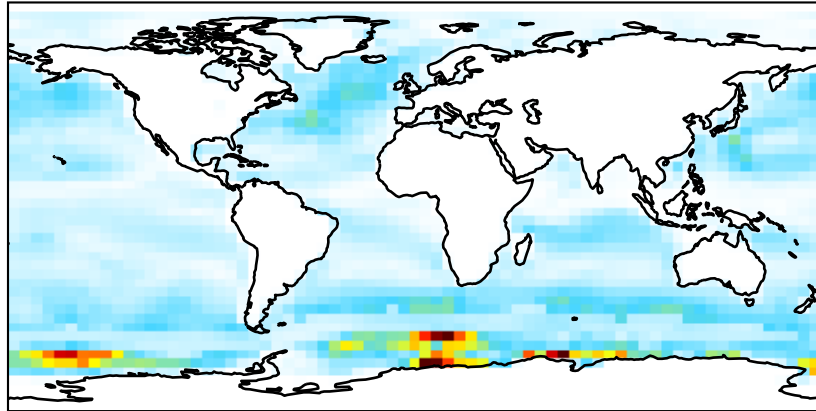


Ratio
Dev/Ref, Fixed Range

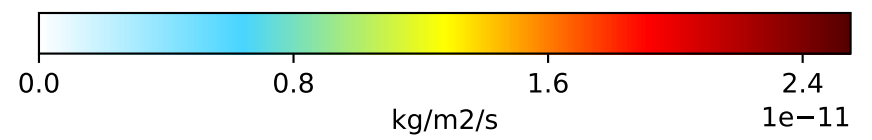
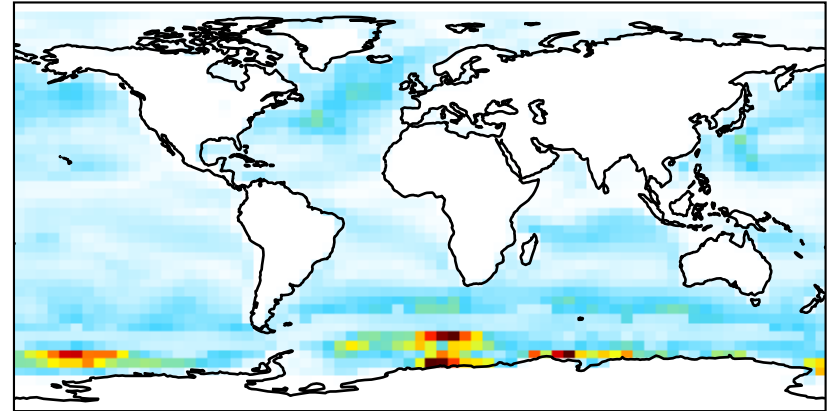


EmisSALACL_Natural (Oct2019)

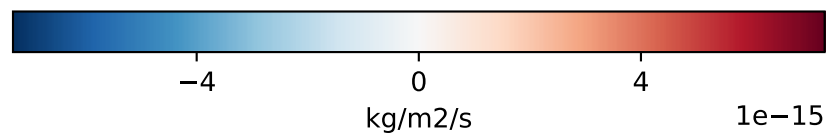
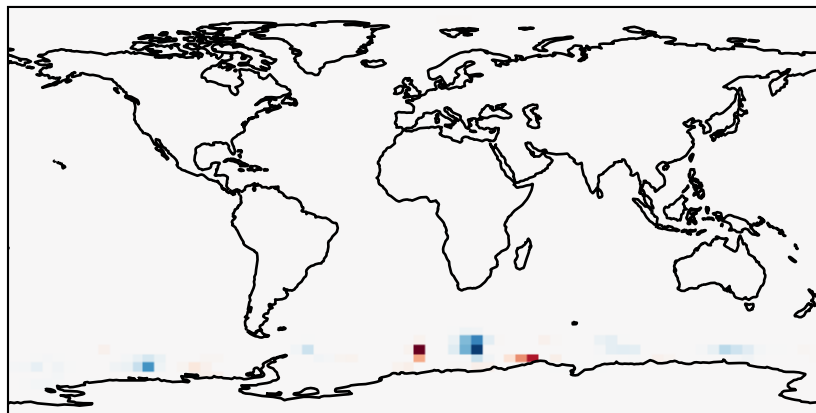
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



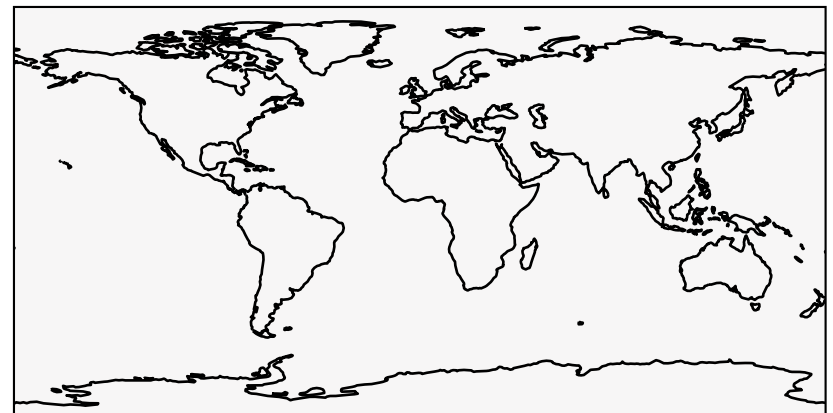
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



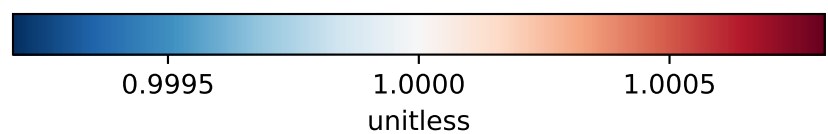
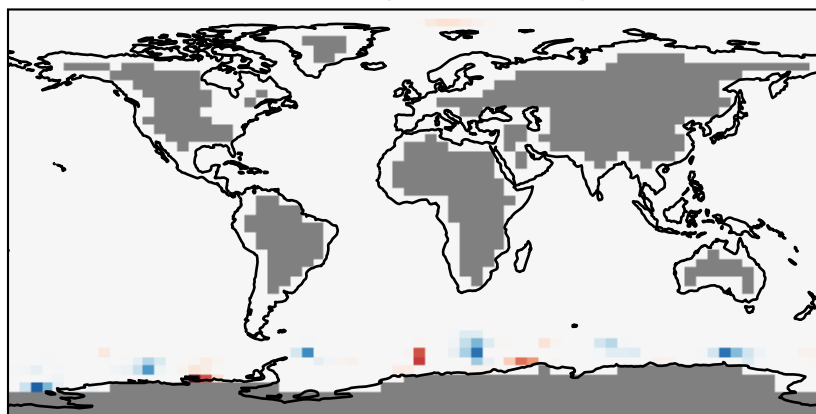
Difference
Dev - Ref, Dynamic Range



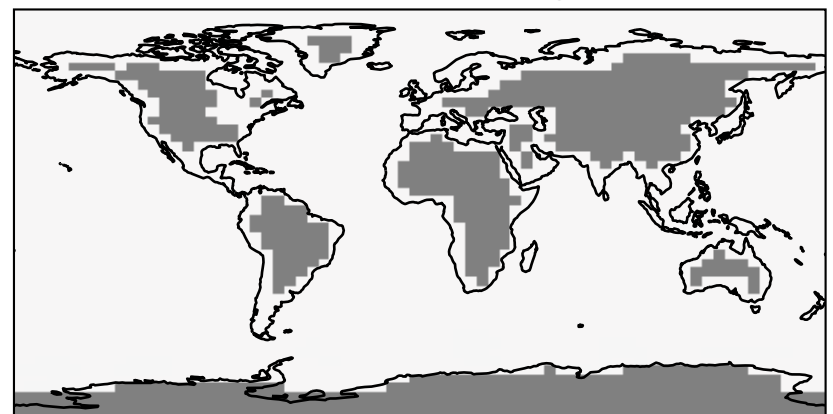
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

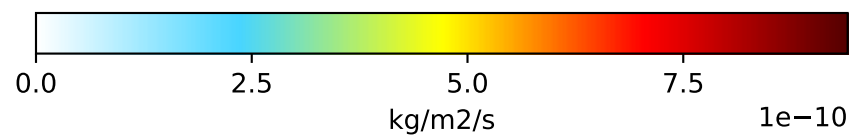
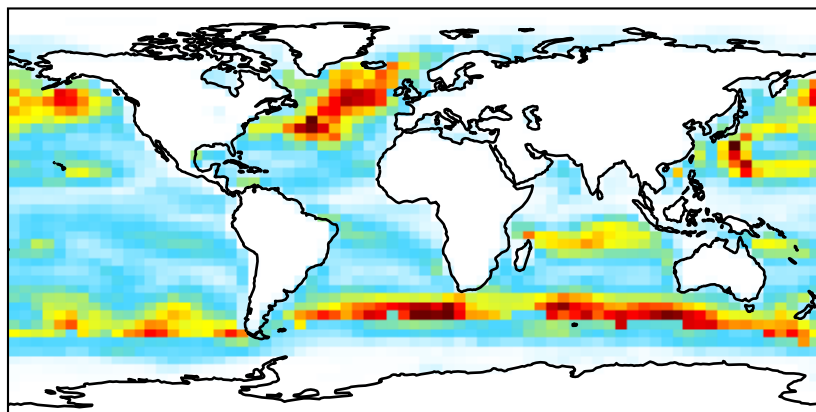


Ratio
Dev/Ref, Fixed Range

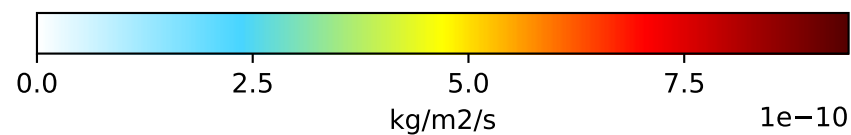
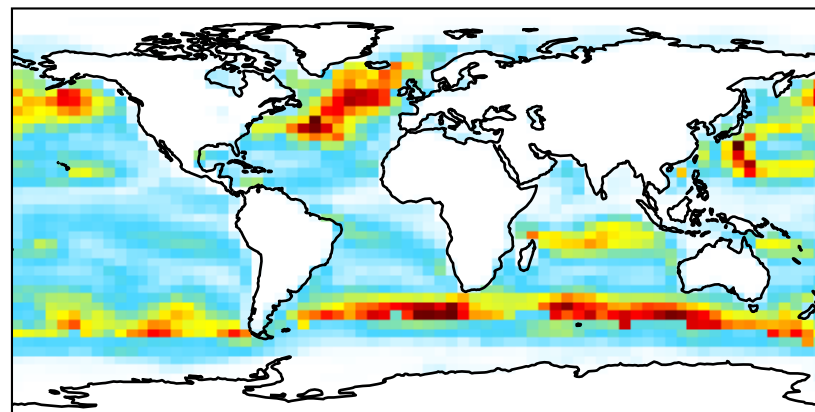


EmisSALC_Natural (Oct2019)

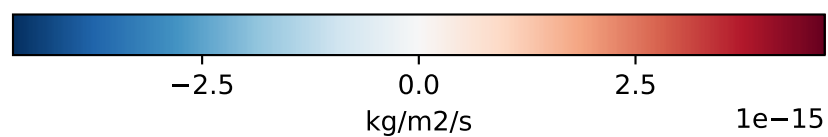
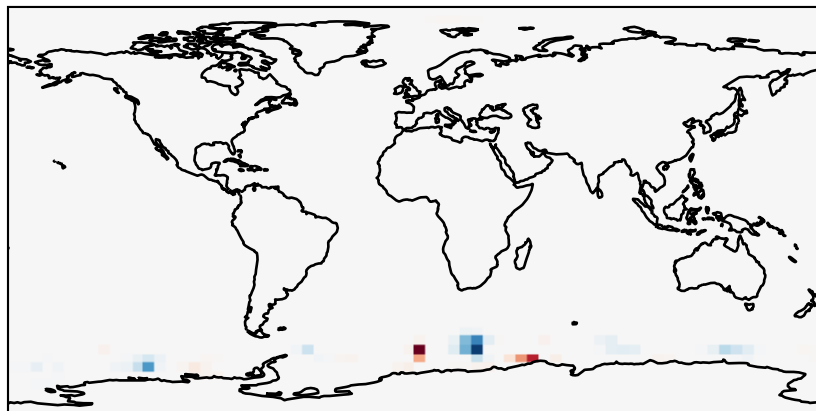
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



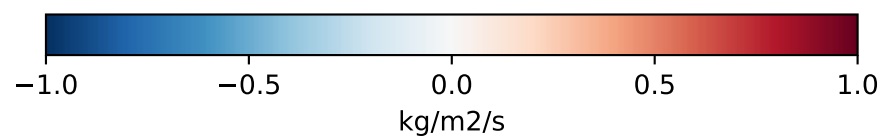
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



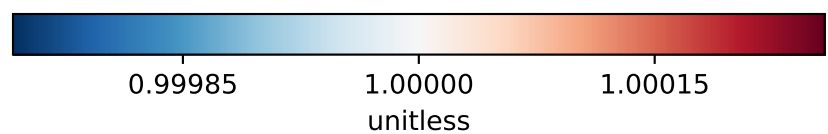
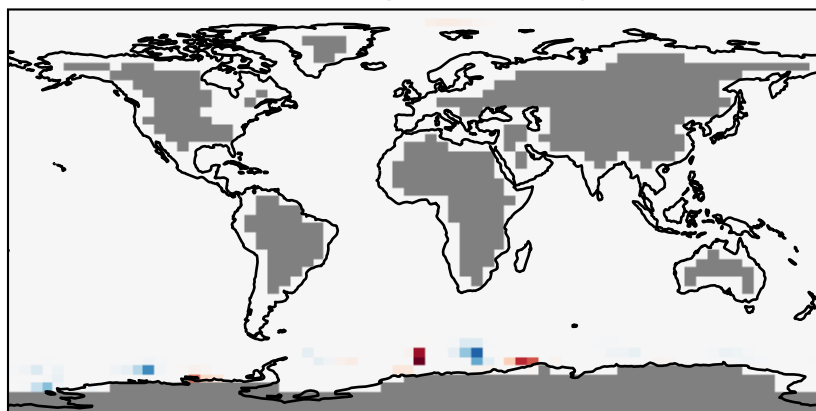
Difference
Dev - Ref, Dynamic Range



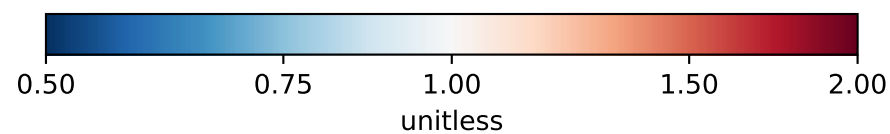
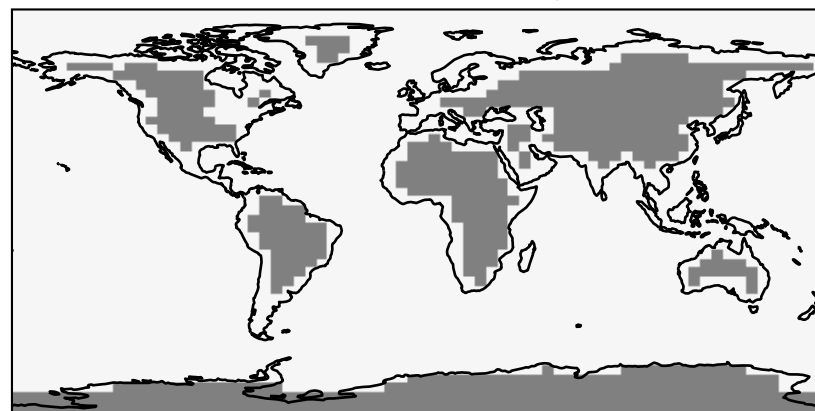
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

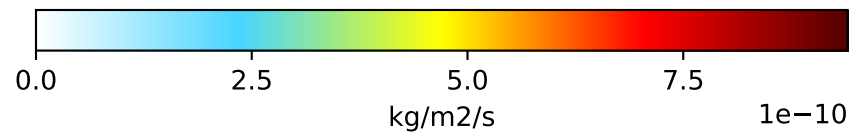
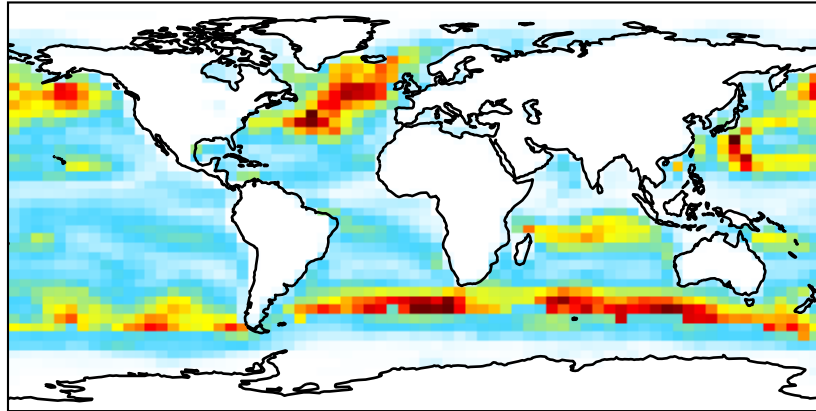


Ratio
Dev/Ref, Fixed Range

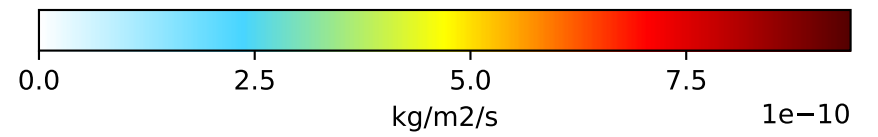
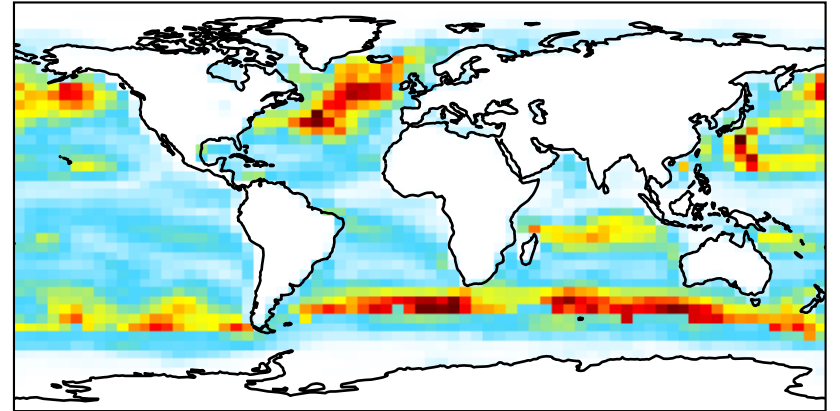


EmisSALCAL_Natural (Oct2019)

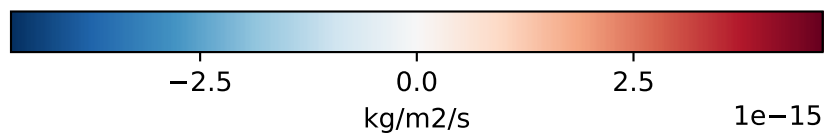
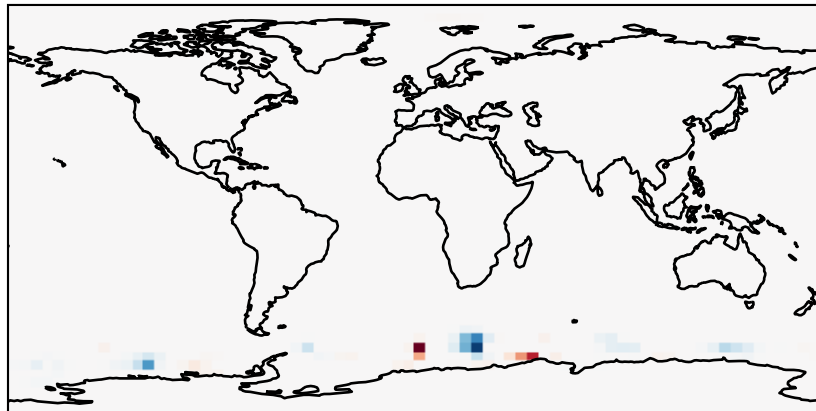
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



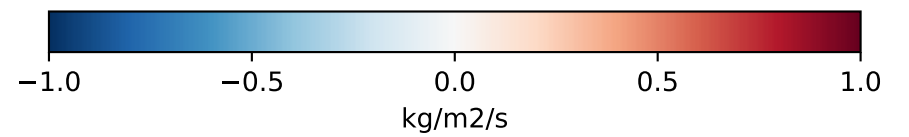
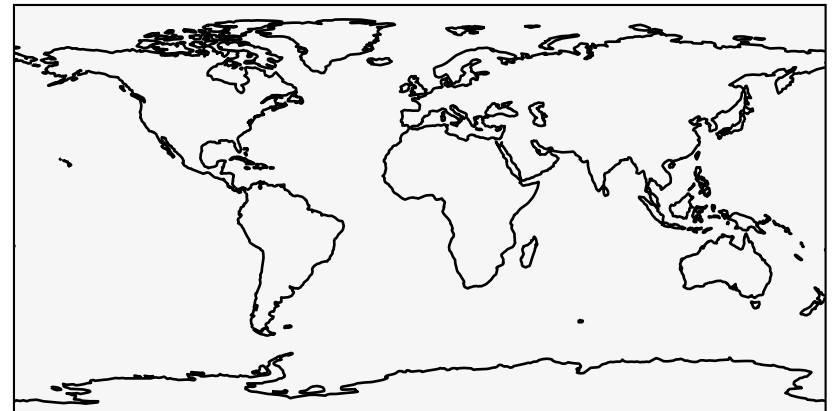
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



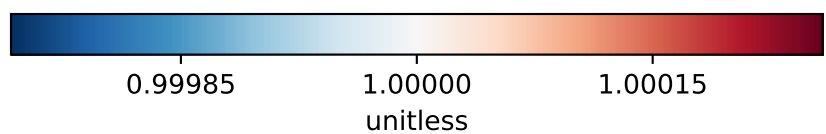
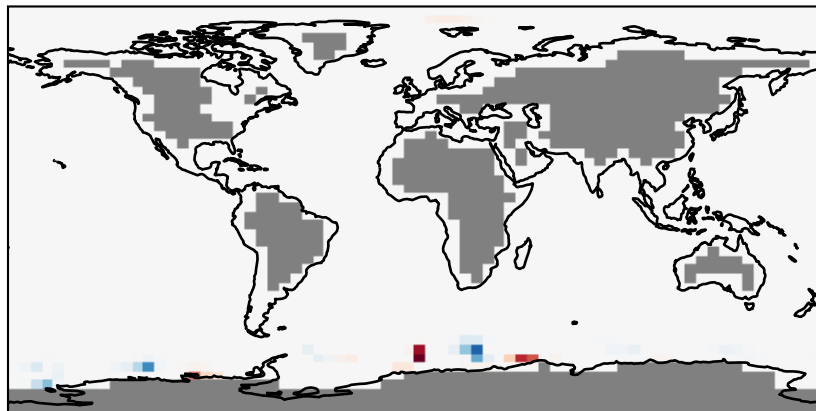
Difference
Dev - Ref, Dynamic Range



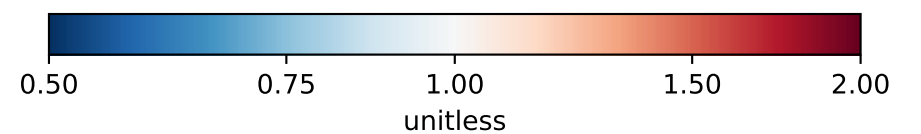
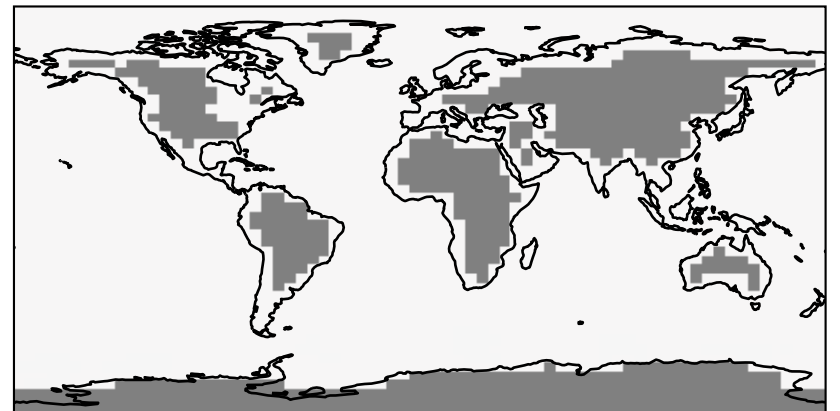
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

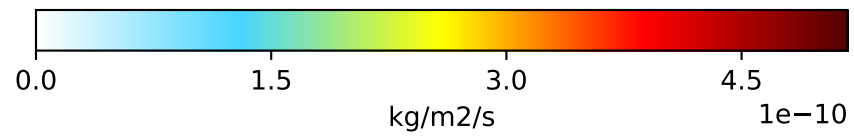
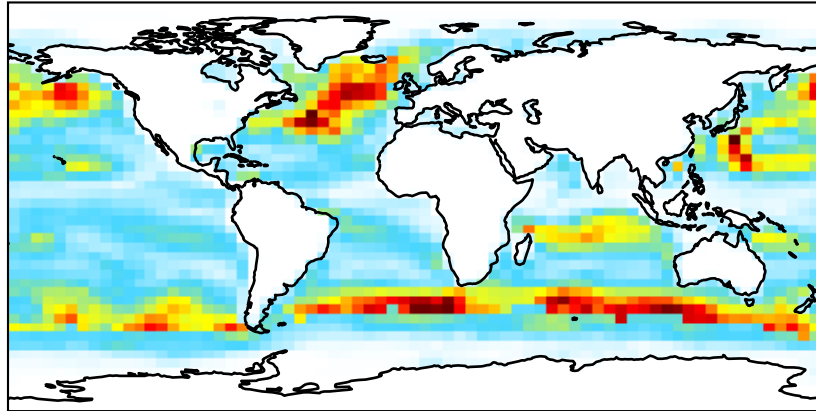


Ratio
Dev/Ref, Fixed Range

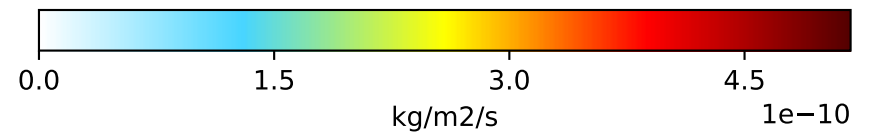
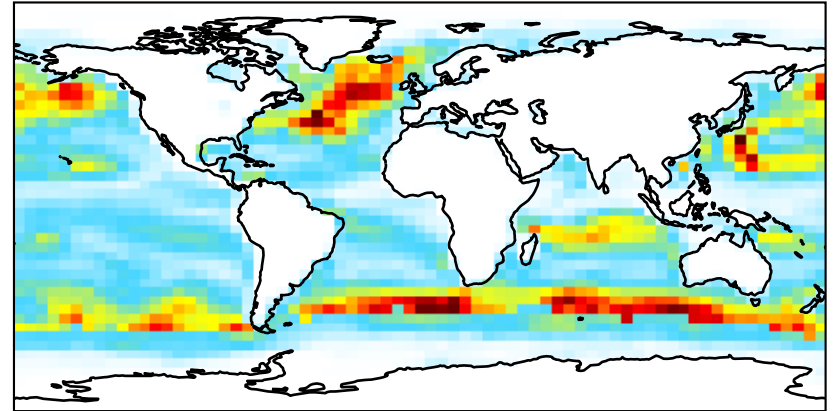


EmisSALCCL_Natural (Oct2019)

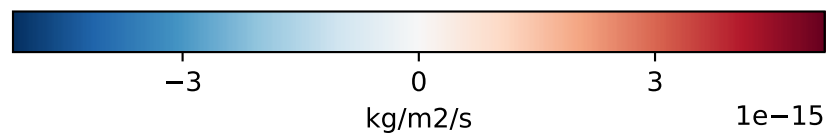
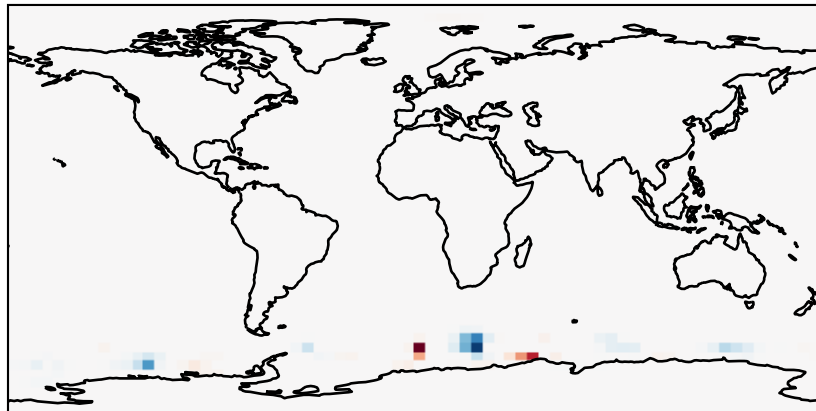
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



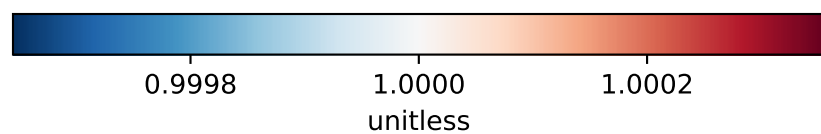
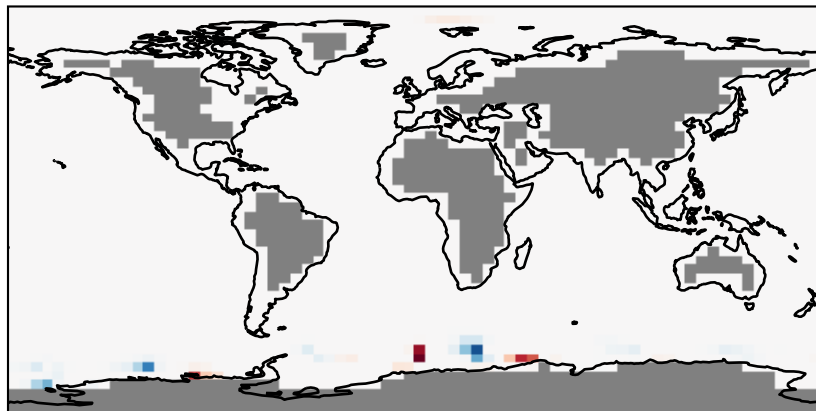
Difference
Dev - Ref, Dynamic Range



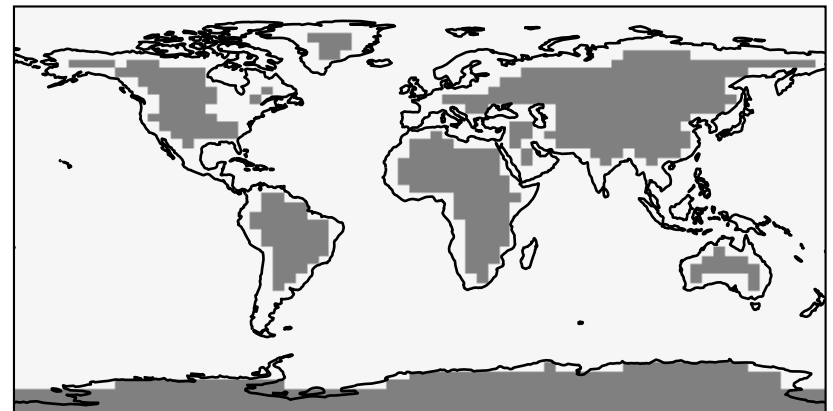
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

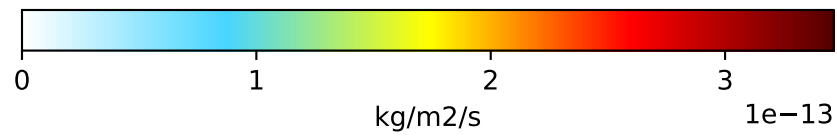
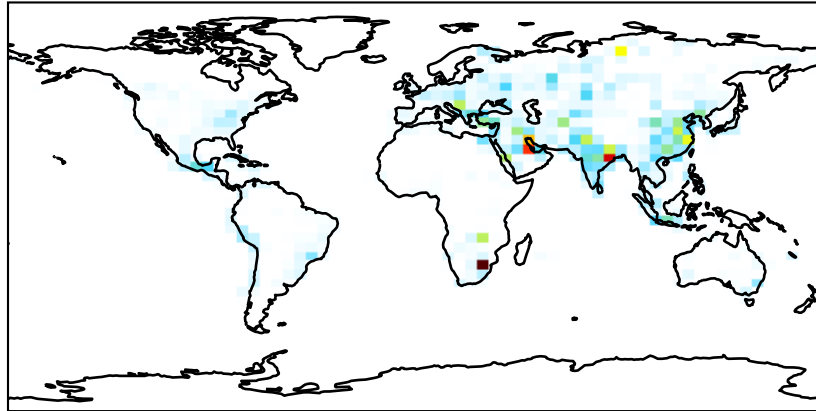


Ratio
Dev/Ref, Fixed Range

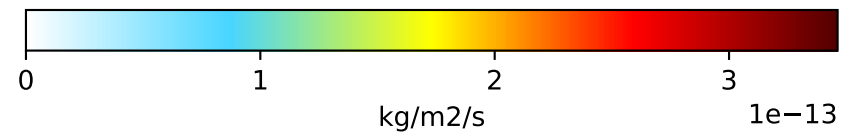
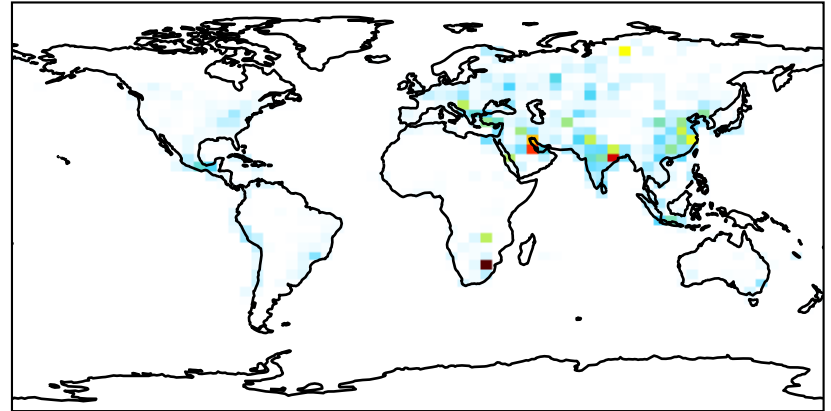


EmispFe_Anthro (Oct2019)

GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



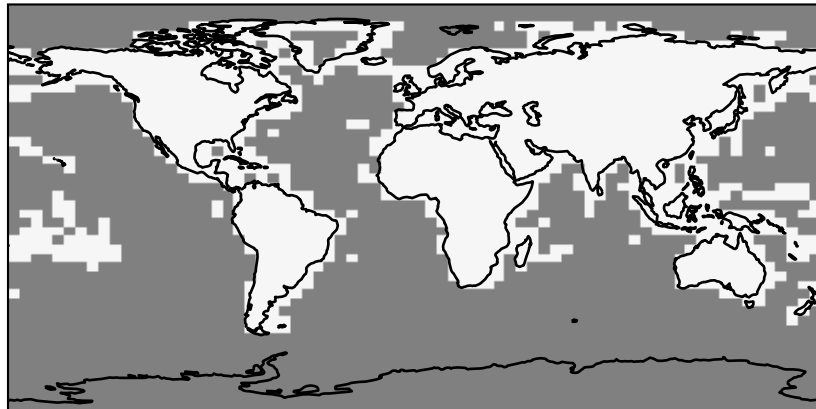
Difference
Dev - Ref, Dynamic Range



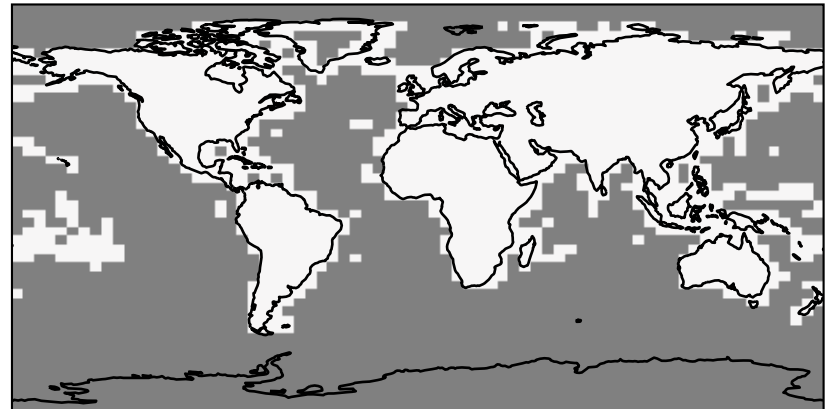
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

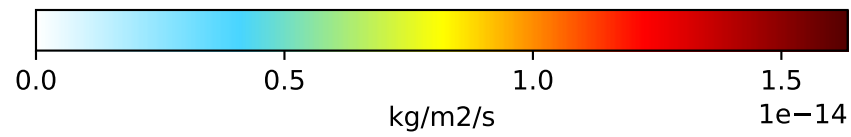
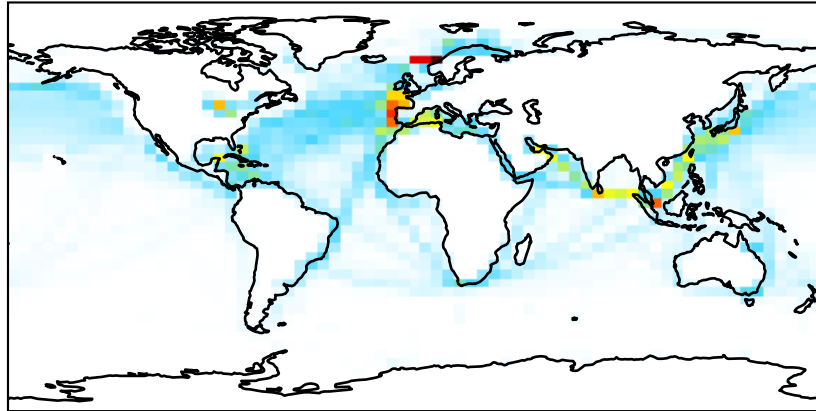


Ratio
Dev/Ref, Fixed Range

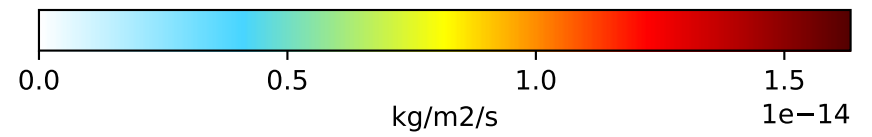
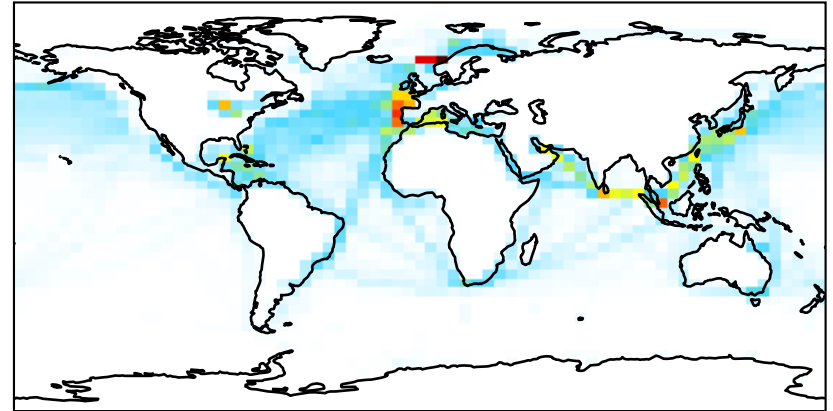


EmispFe_Ship (Oct2019)

GCC_13.4.0-rc.4 (Ref)
4.0x5.0



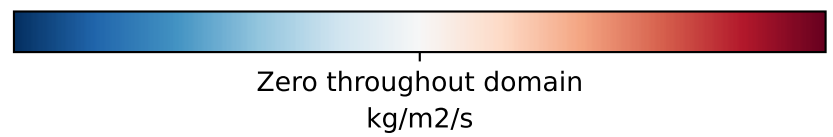
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



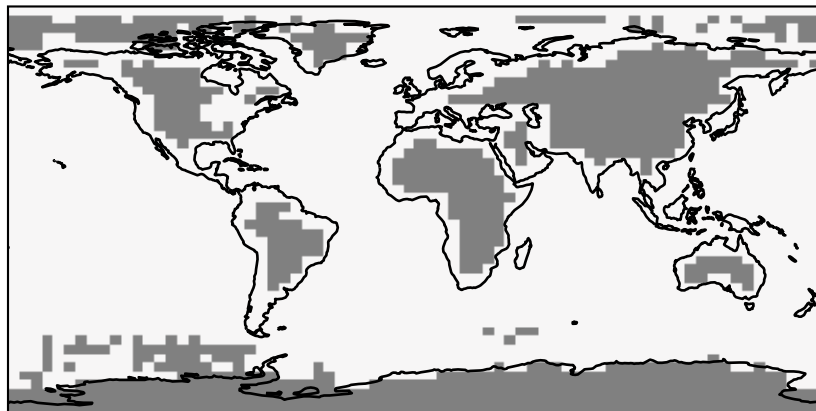
Difference
Dev - Ref, Dynamic Range



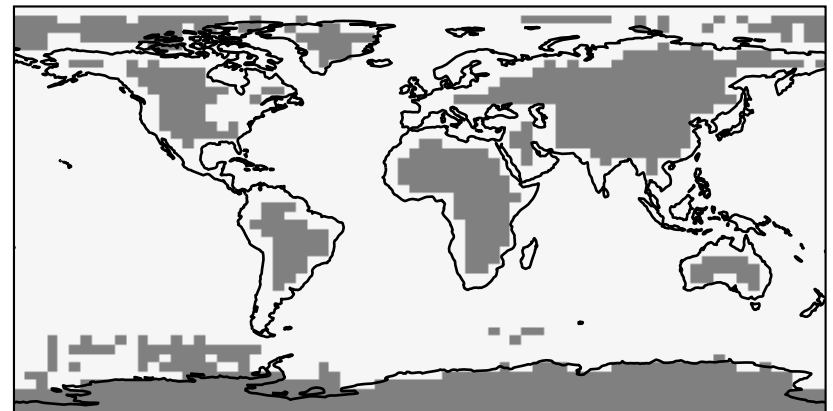
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

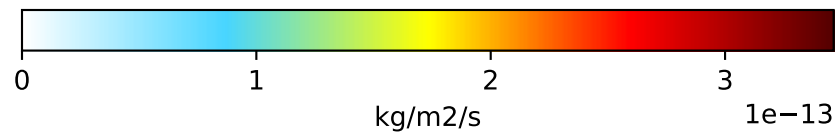
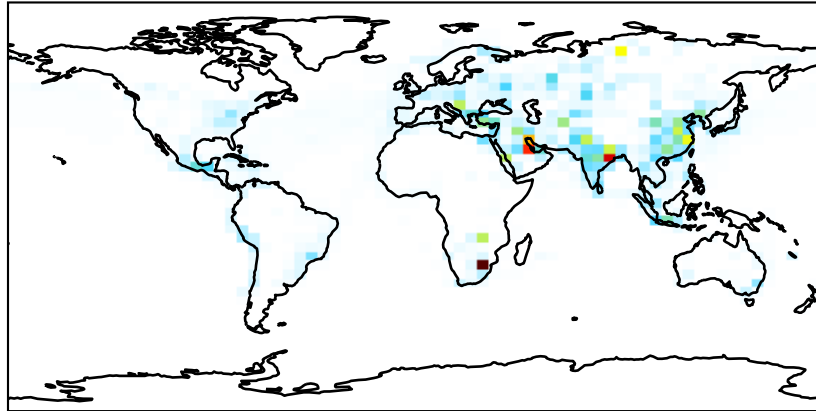


Ratio
Dev/Ref, Fixed Range

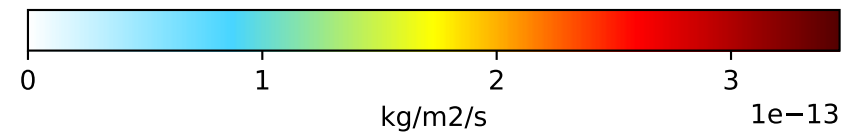
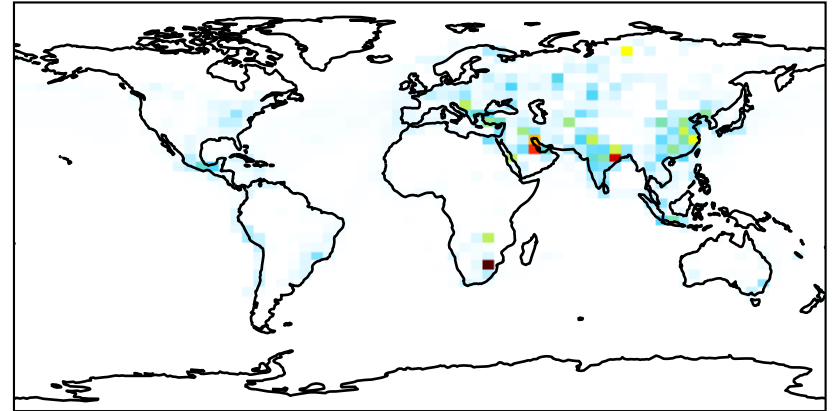


EmispFe_Total (Oct2019)

GCC_13.4.0-rc.4 (Ref)
4.0x5.0



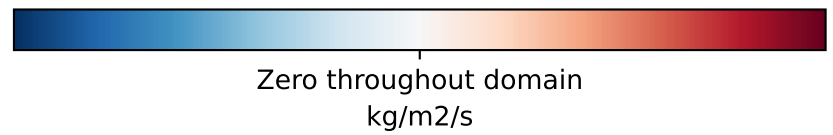
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



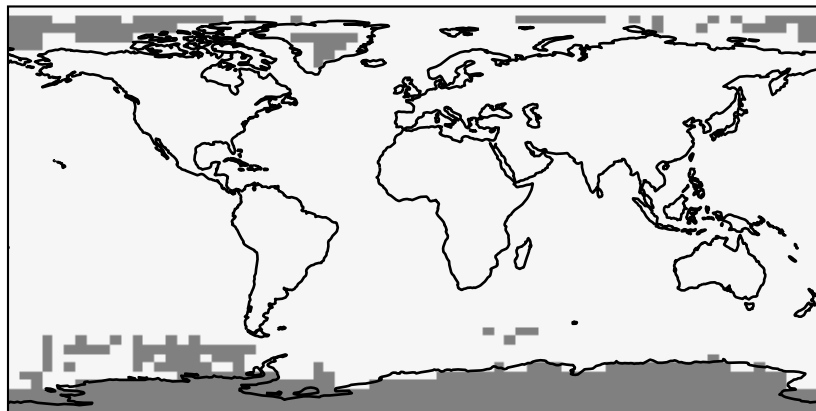
Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range



Ratio
Dev/Ref, Fixed Range

