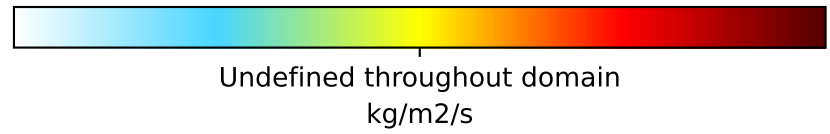
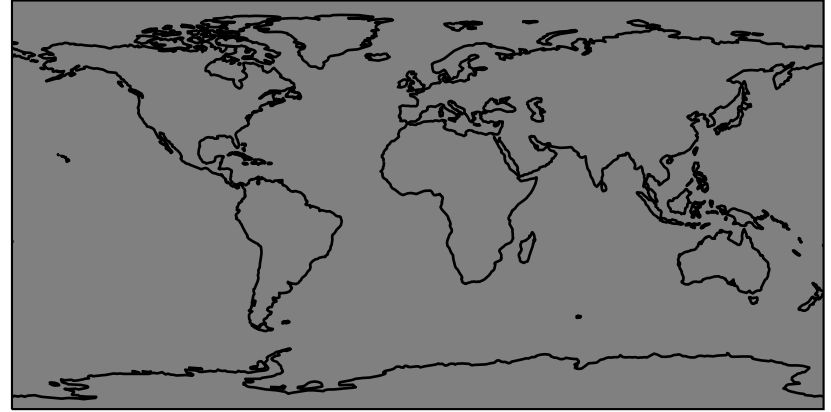


EmisBrSALA_Natural (Apr2019)

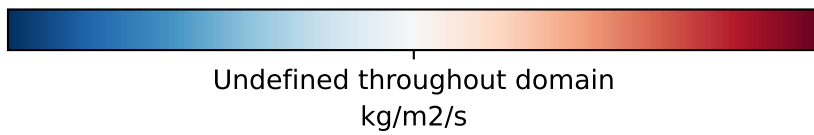
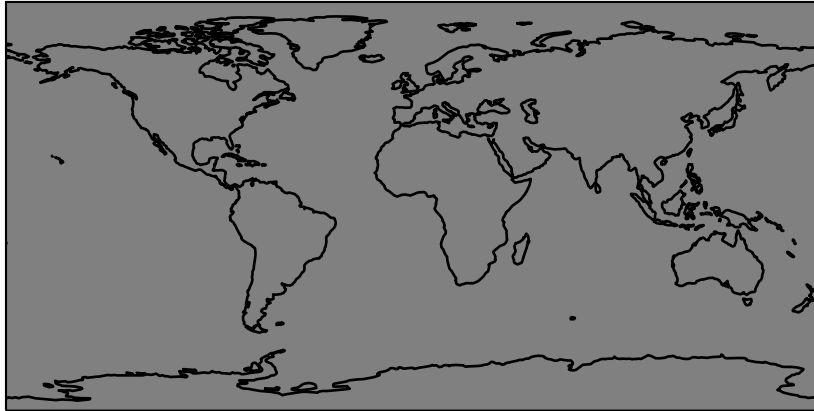
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



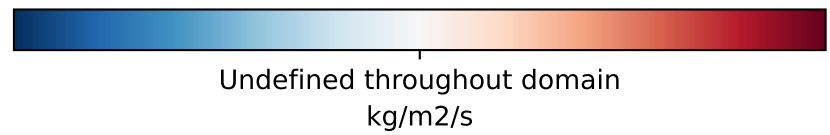
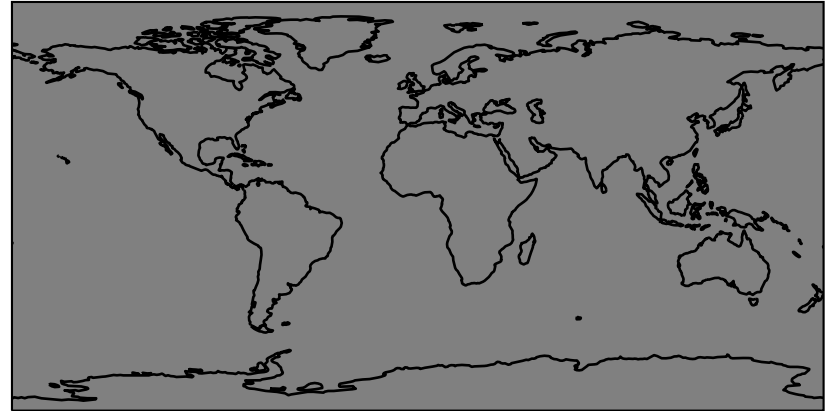
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



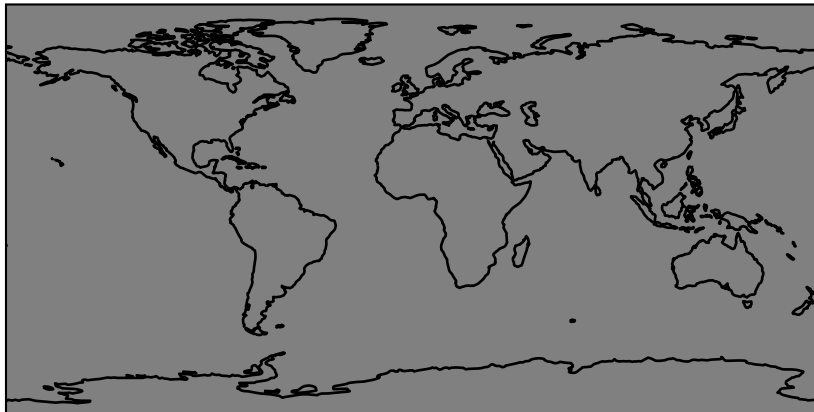
Difference
Dev - Ref, Dynamic Range



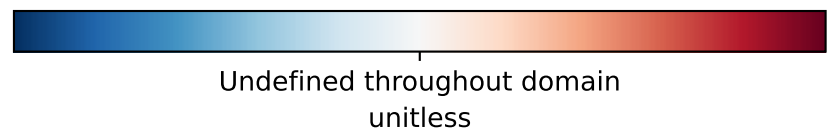
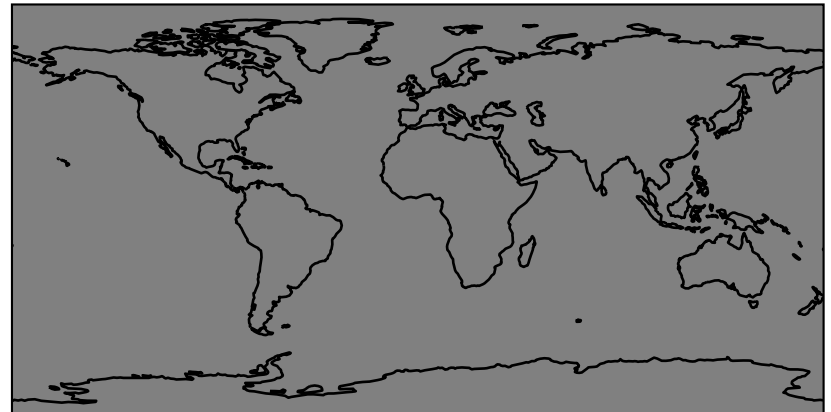
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range



Ratio
Dev/Ref, Fixed Range



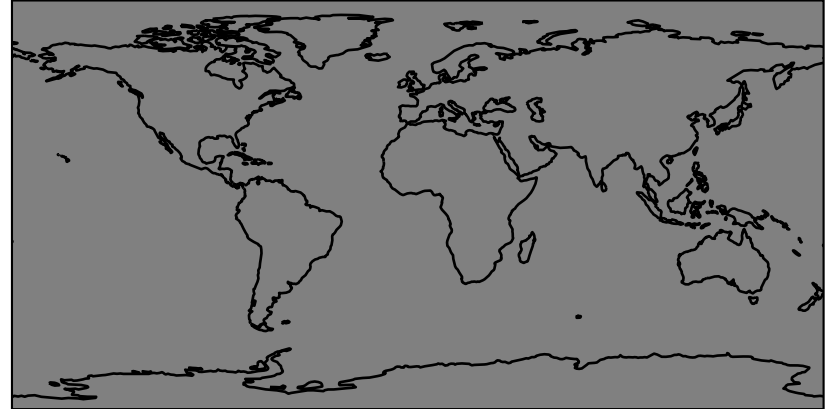
EmisBrSALC_Natural (Apr2019)

GCC_13.4.0-rc.4 (Ref)
4.0x5.0



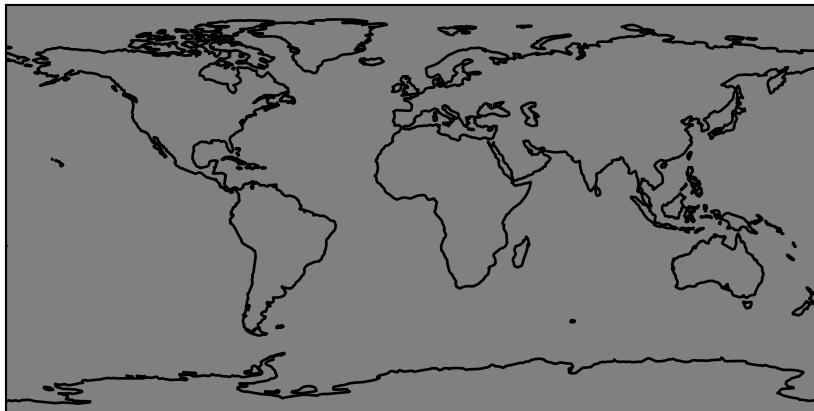
Zero throughout domain
kg/m2/s

GCC_14.0.0-rc.0 (Dev)
4.0x5.0



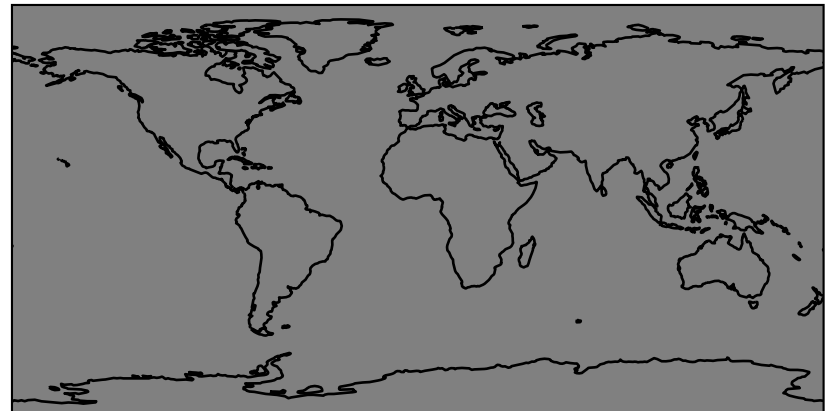
Undefined throughout domain
kg/m2/s

Difference
Dev - Ref, Dynamic Range



Undefined throughout domain
kg/m2/s

Difference
Dev - Ref, Restricted Range [5%,95%]



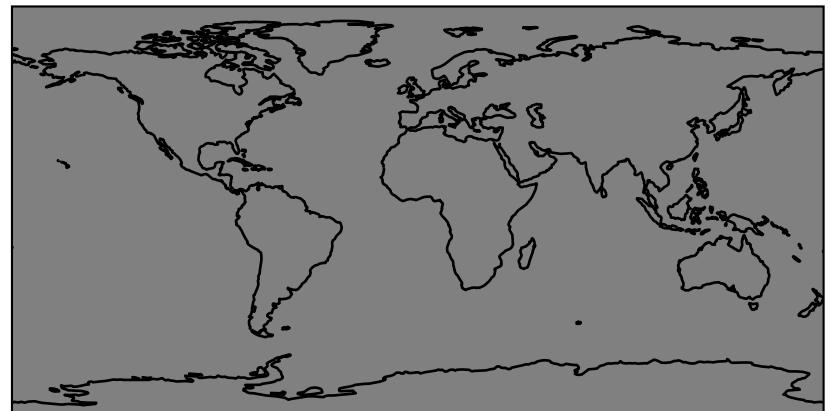
Undefined throughout domain
kg/m2/s

Ratio
Dev/Ref, Dynamic Range



Undefined throughout domain
unitless

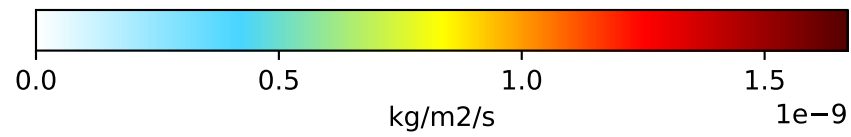
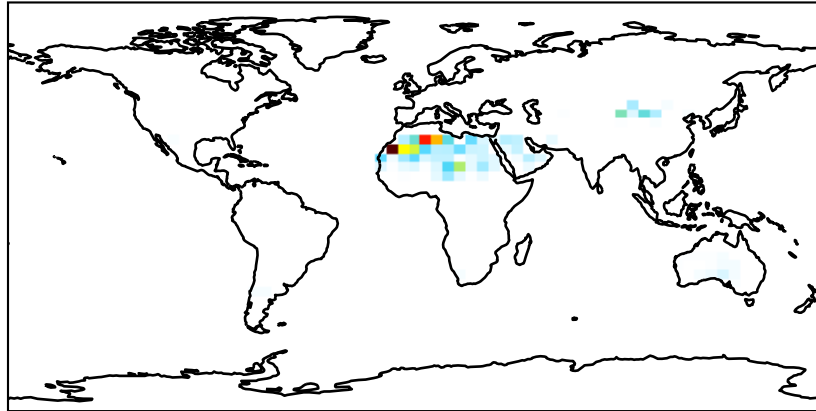
Ratio
Dev/Ref, Fixed Range



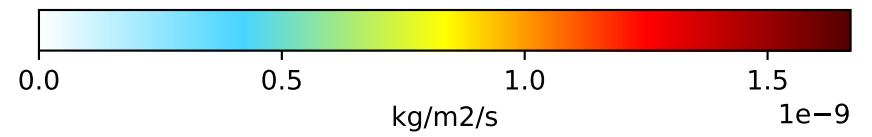
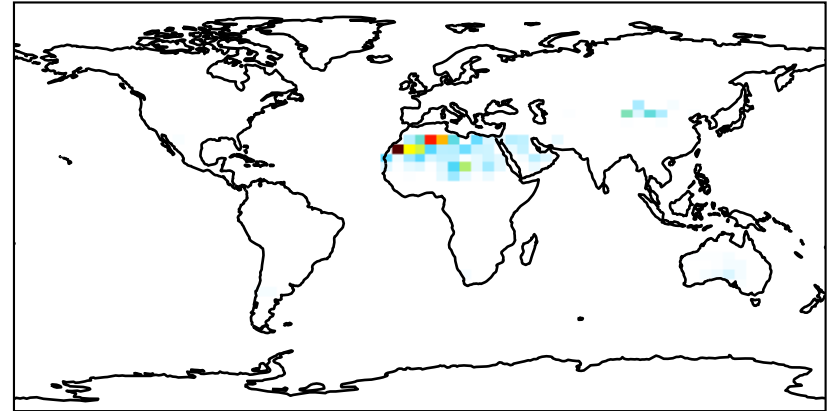
Undefined throughout domain
unitless

EmisDST1_Natural (Apr2019)

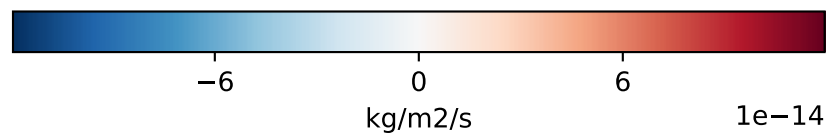
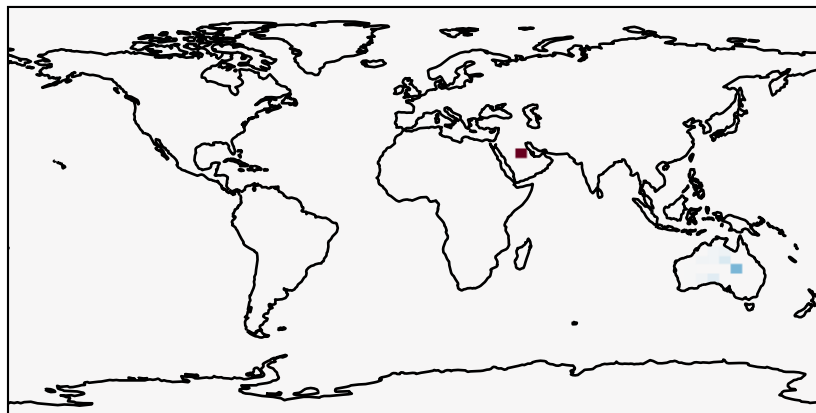
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



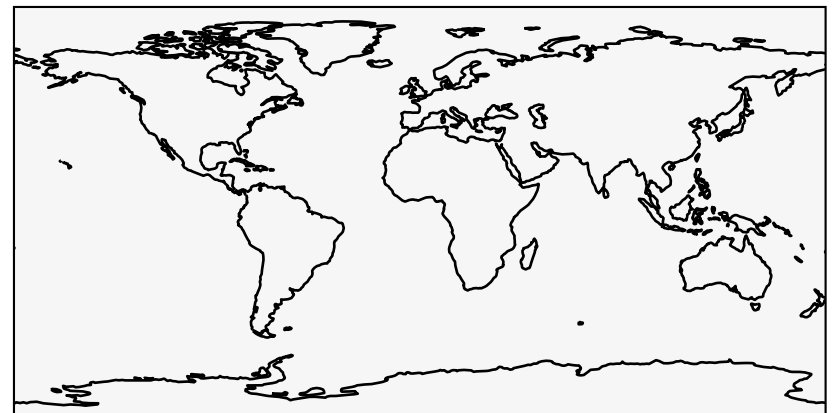
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



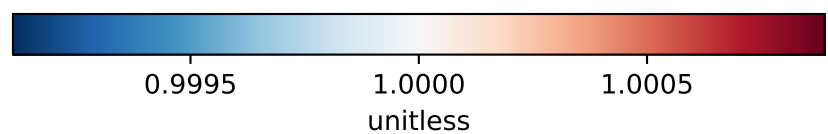
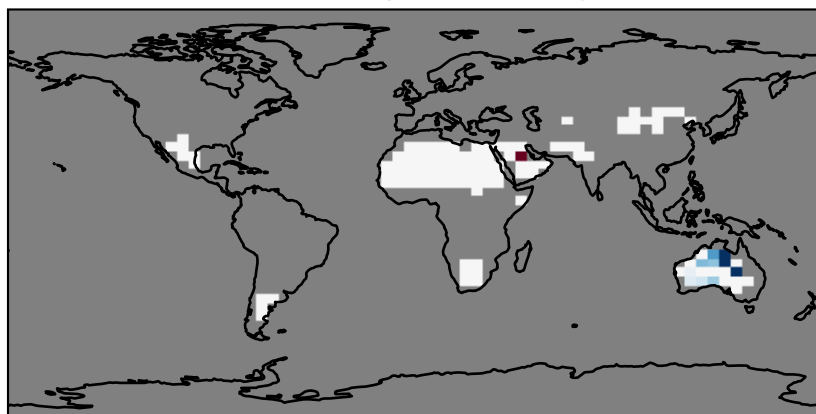
Difference
Dev - Ref, Dynamic Range



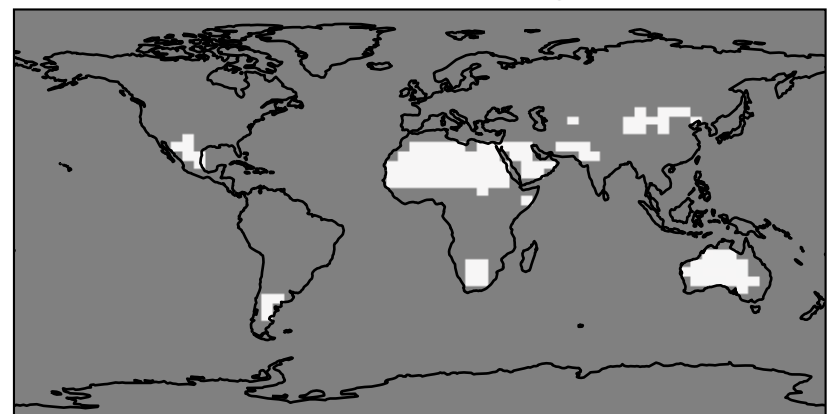
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

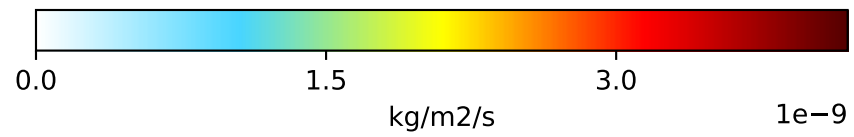
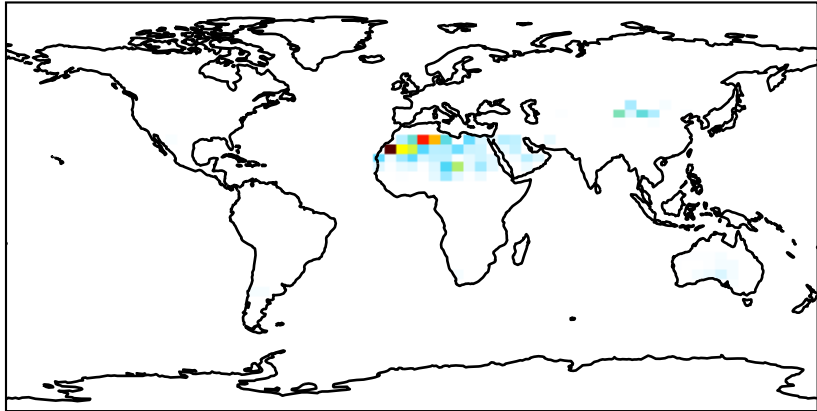


Ratio
Dev/Ref, Fixed Range

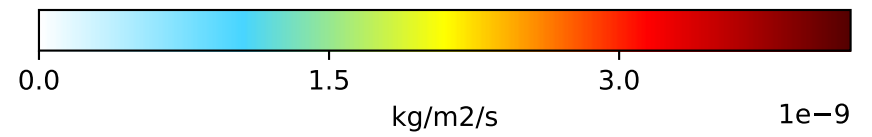
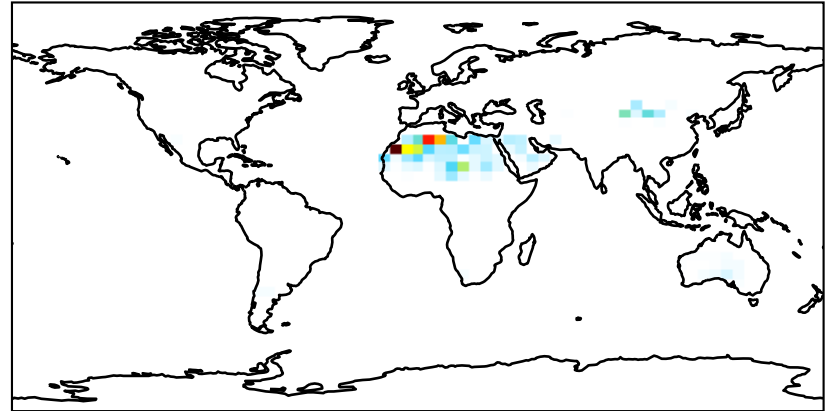


EmisDST2_Natural (Apr2019)

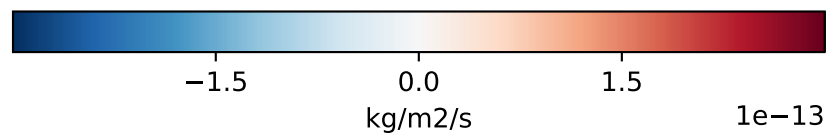
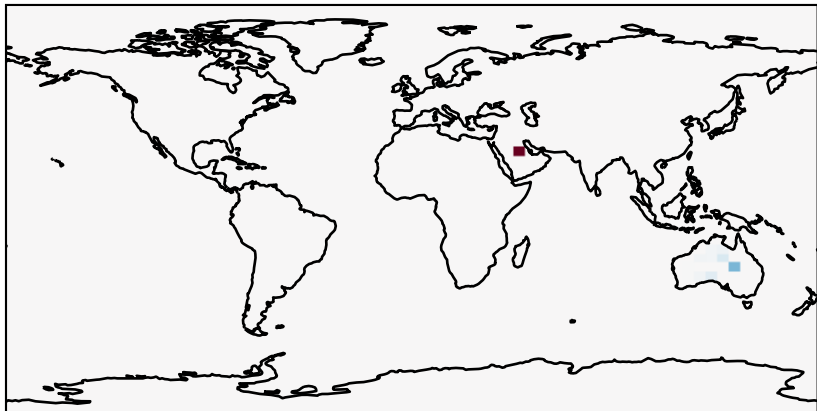
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



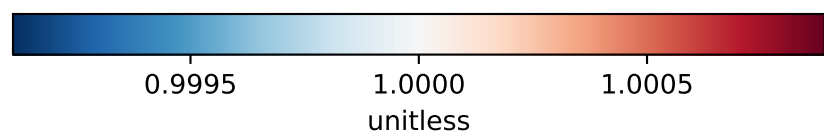
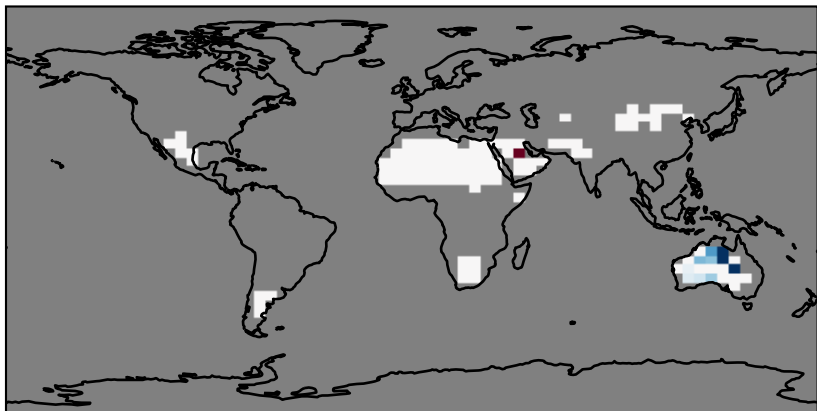
Difference
Dev - Ref, Dynamic Range



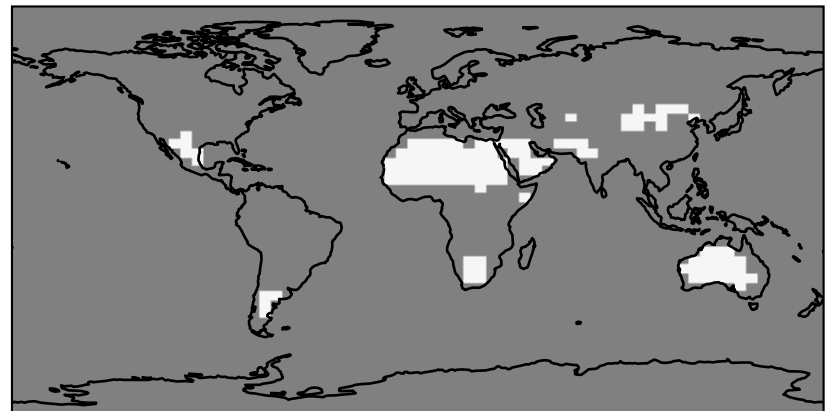
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

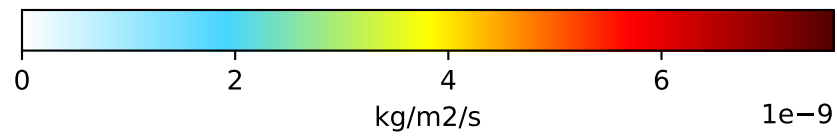
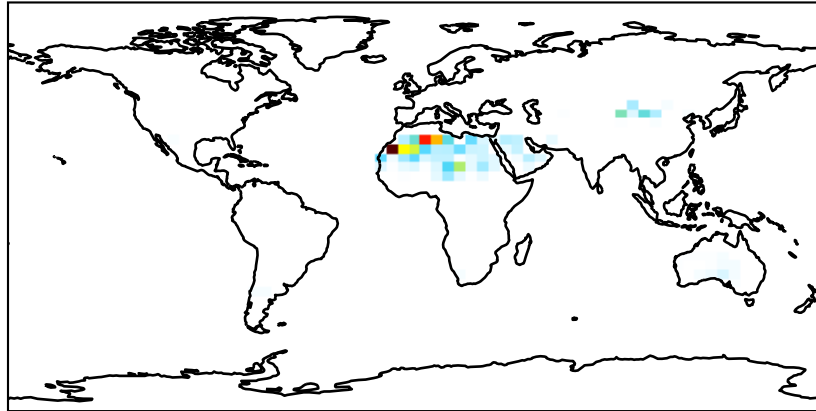


Ratio
Dev/Ref, Fixed Range

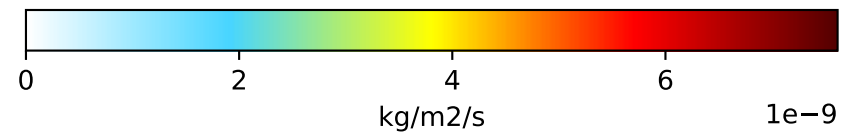
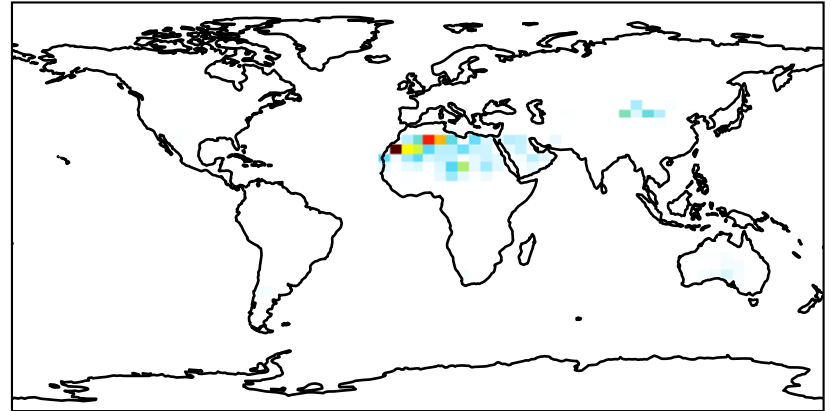


EmisDST3_Natural (Apr2019)

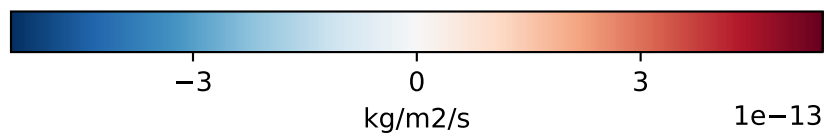
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



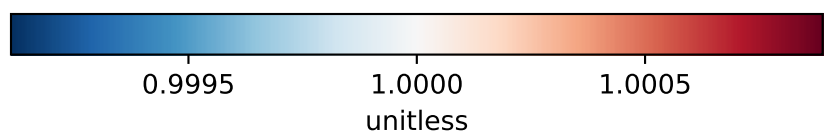
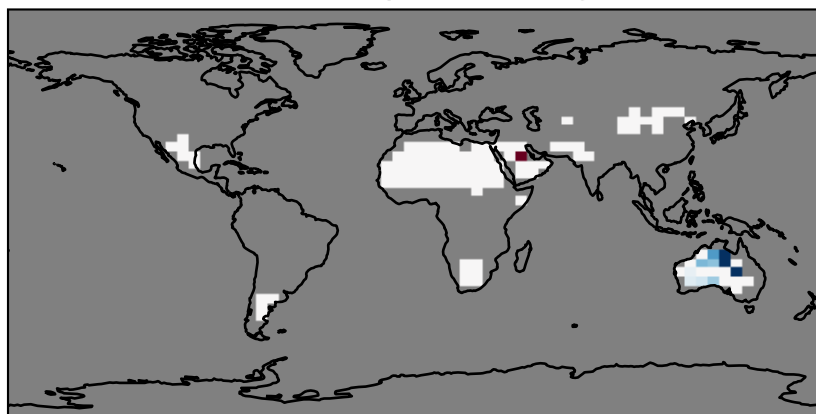
Difference
Dev - Ref, Dynamic Range



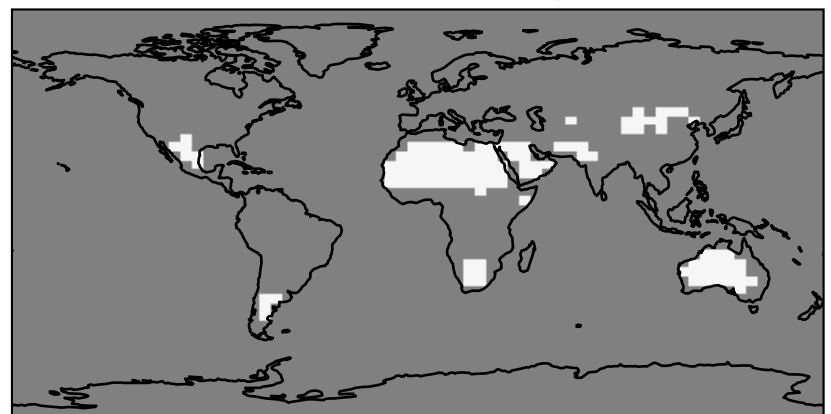
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

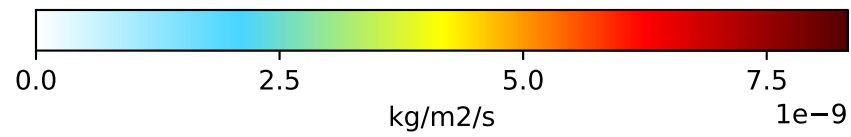
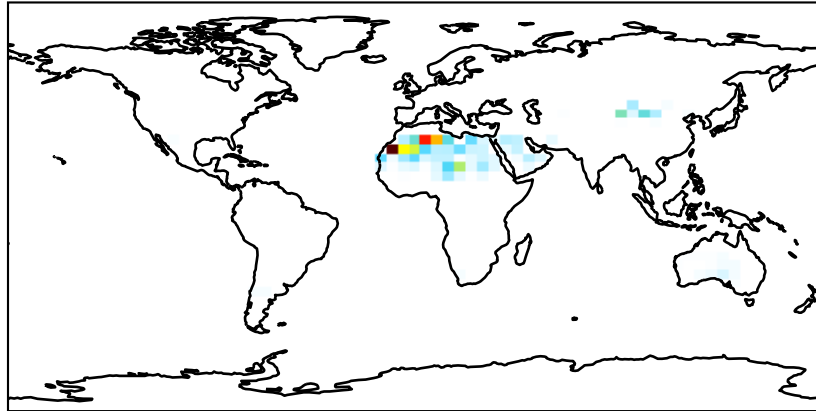


Ratio
Dev/Ref, Fixed Range

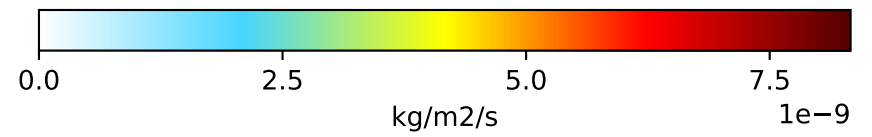
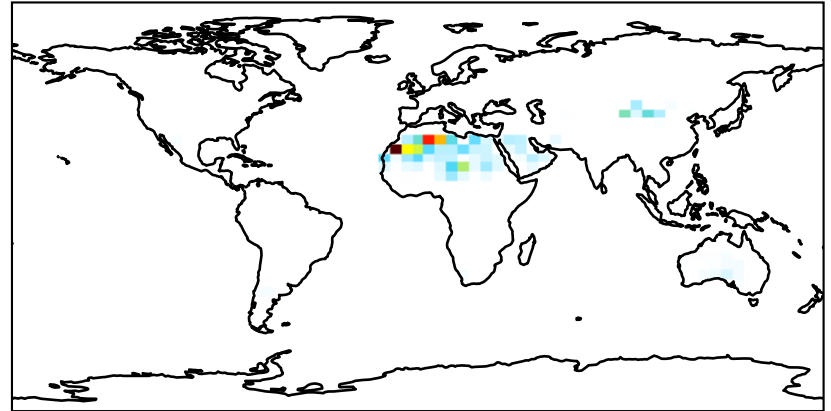


EmisDST4_Natural (Apr2019)

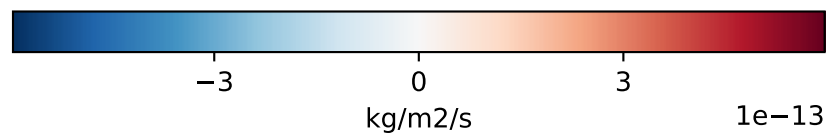
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



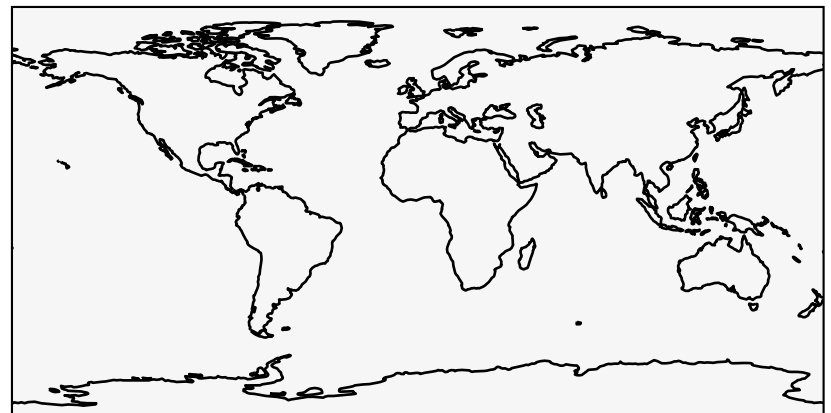
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



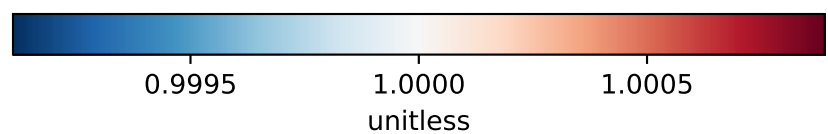
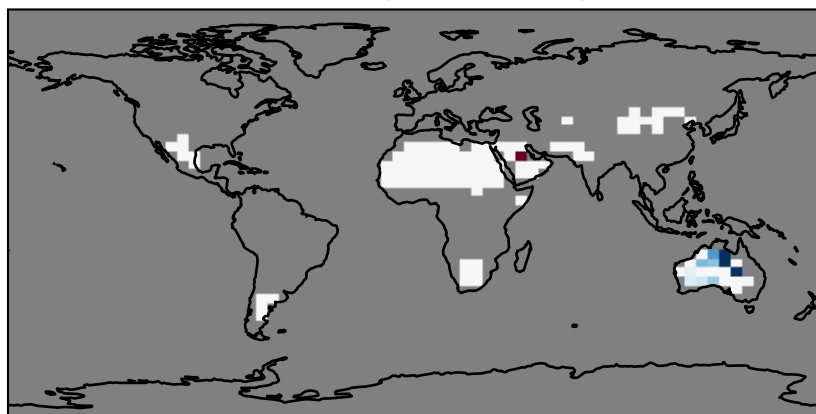
Difference
Dev - Ref, Dynamic Range



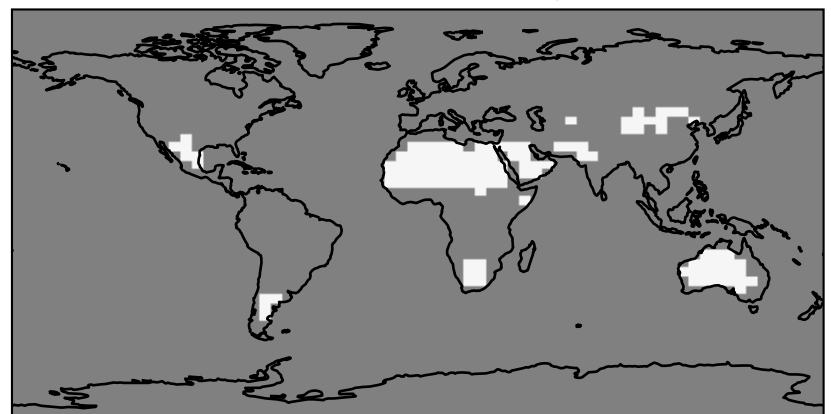
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

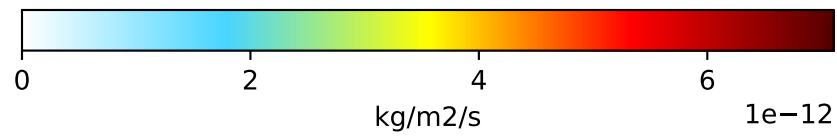
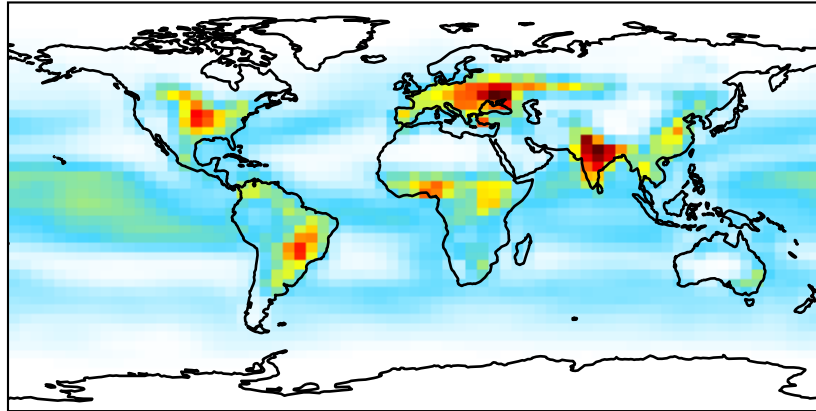


Ratio
Dev/Ref, Fixed Range

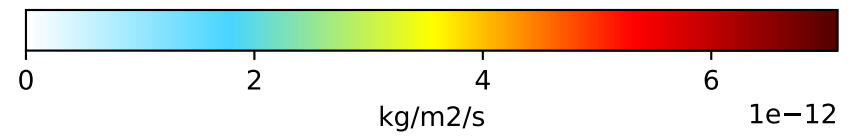
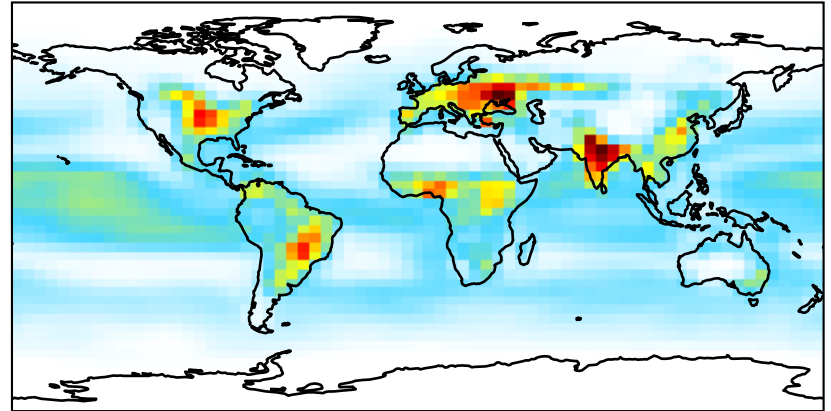


EmisNH3_Natural (Apr2019)

GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



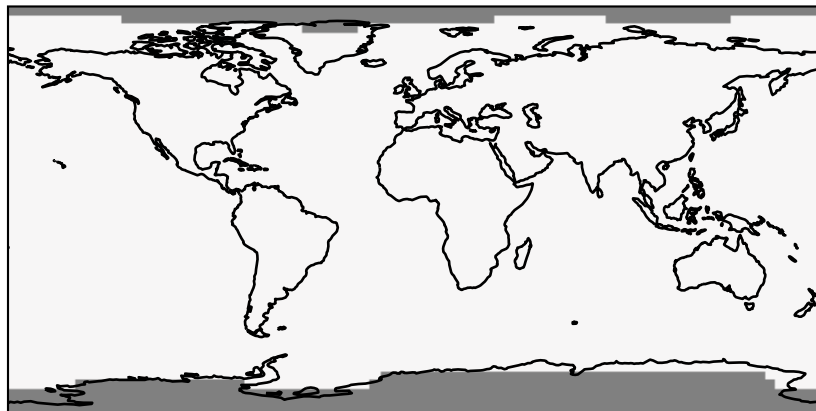
Difference
Dev - Ref, Dynamic Range



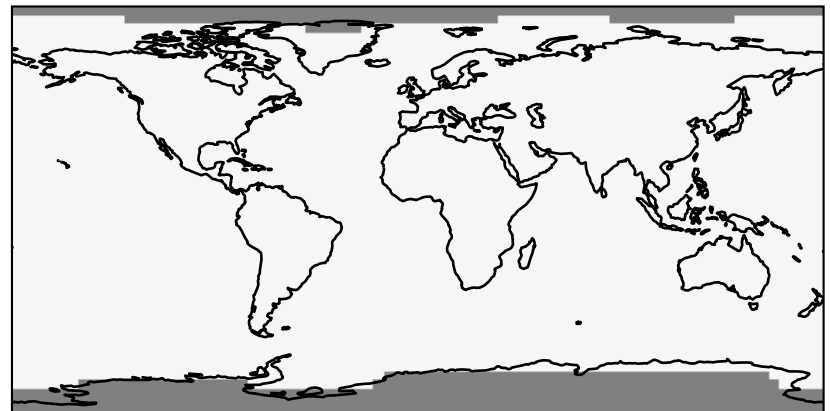
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

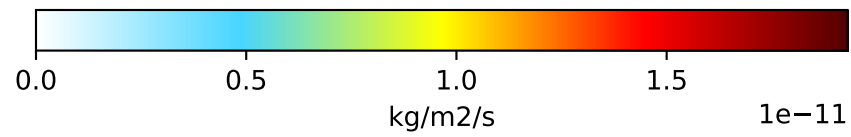
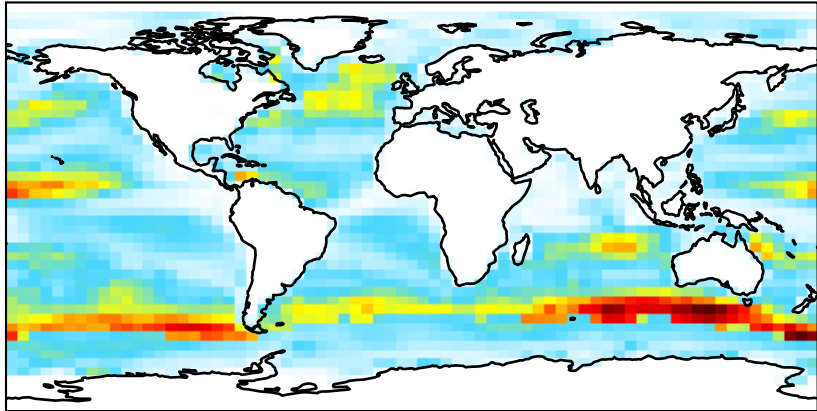


Ratio
Dev/Ref, Fixed Range

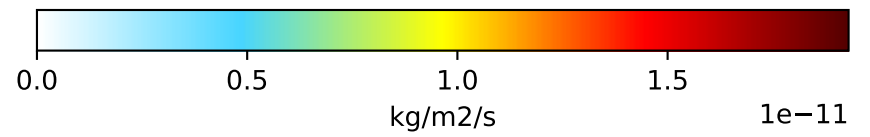
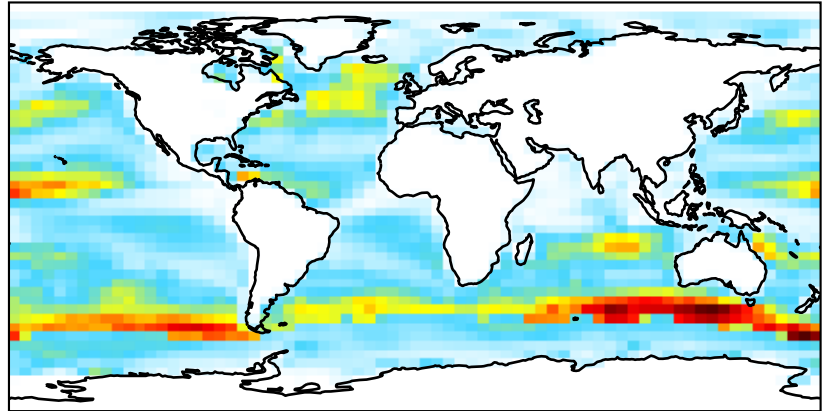


EmisSALA_Natural (Apr2019)

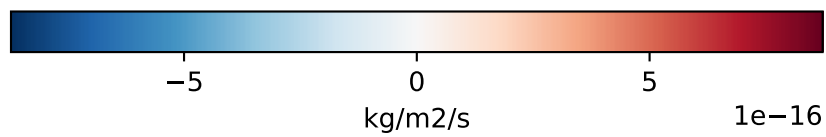
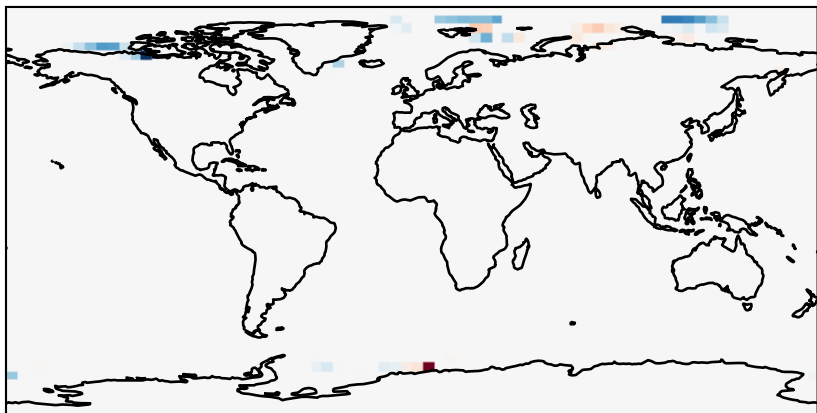
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



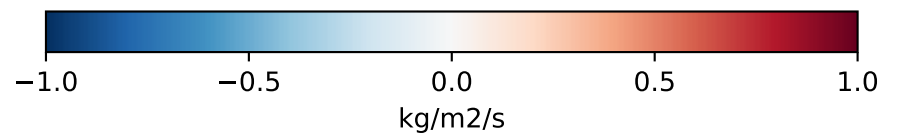
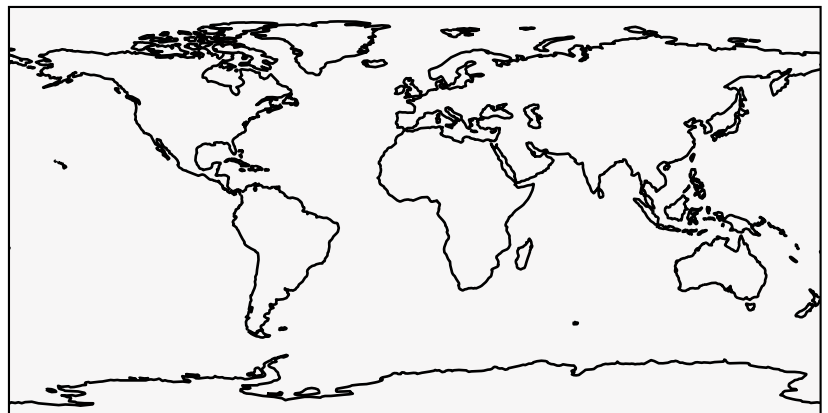
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



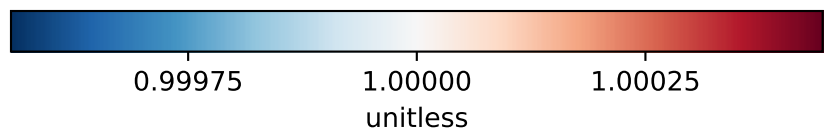
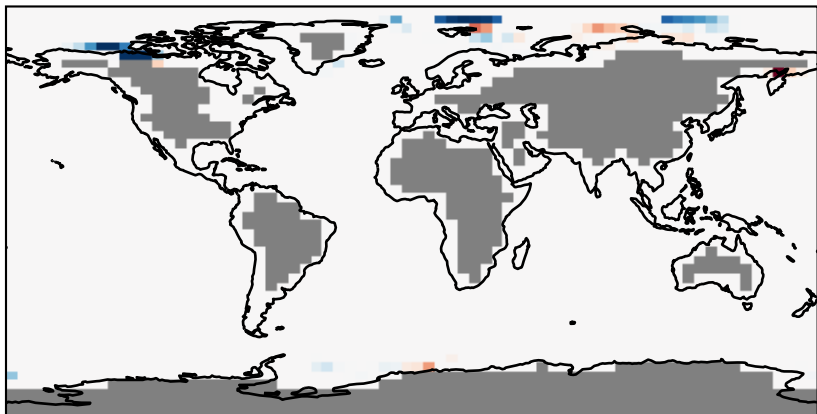
Difference
Dev - Ref, Dynamic Range



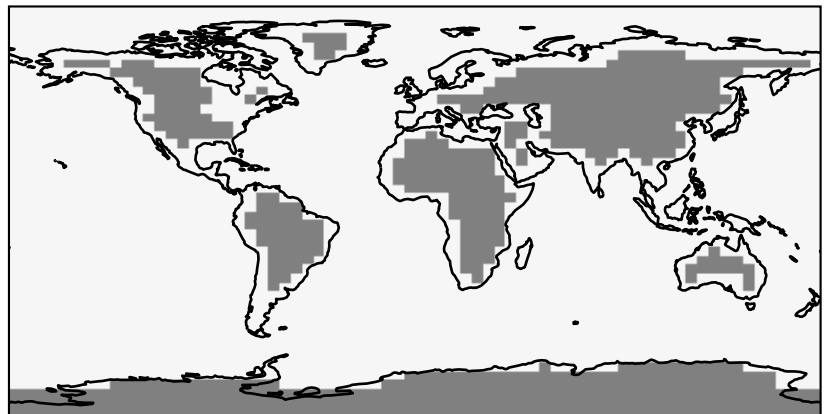
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

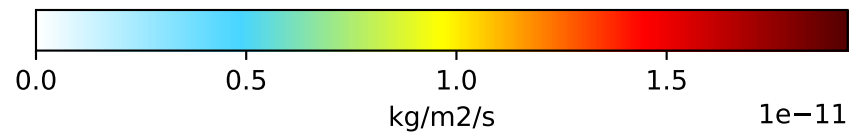
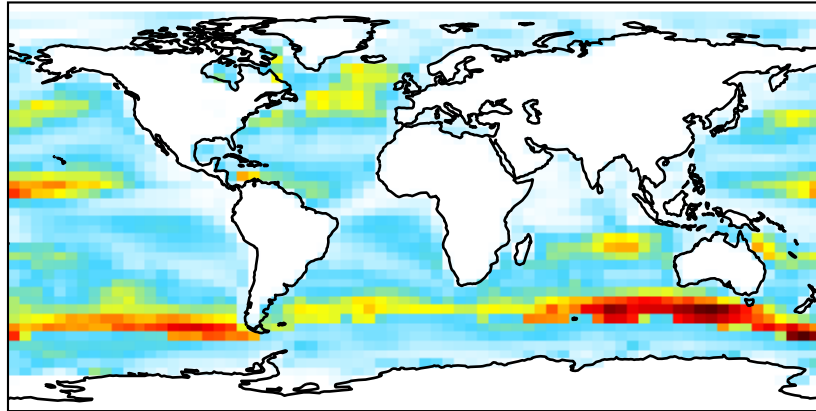


Ratio
Dev/Ref, Fixed Range

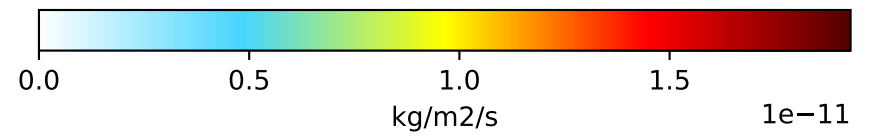
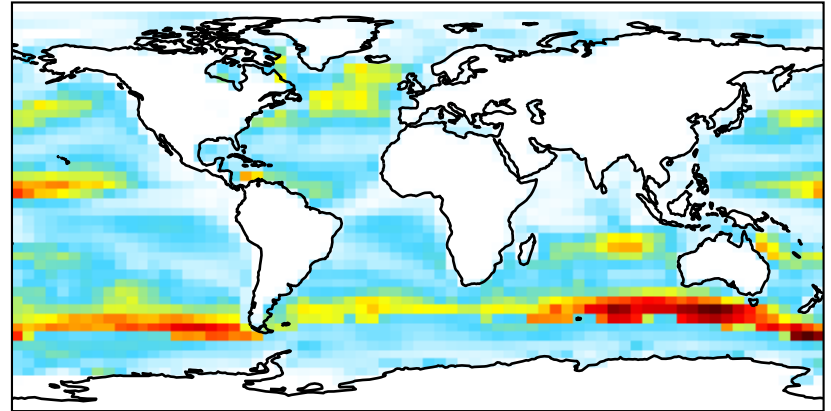


EmisSALAAL_Natural (Apr2019)

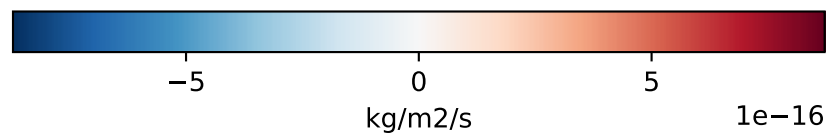
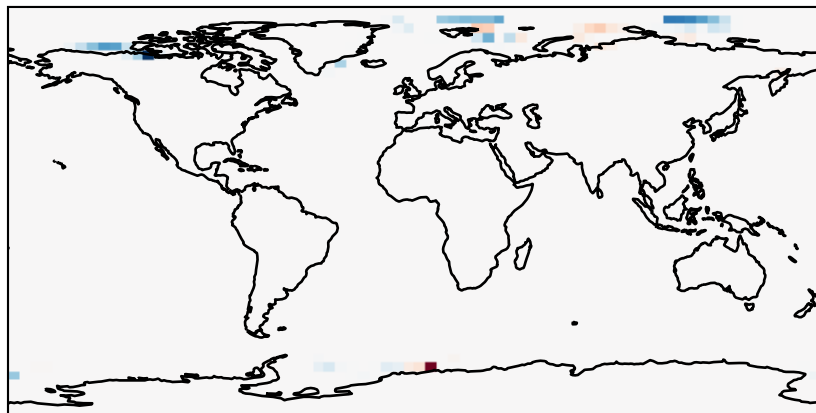
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



GCC_14.0.0-rc.0 (Dev)
4.0x5.0



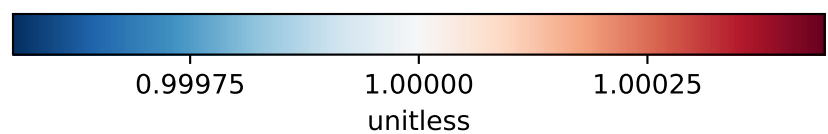
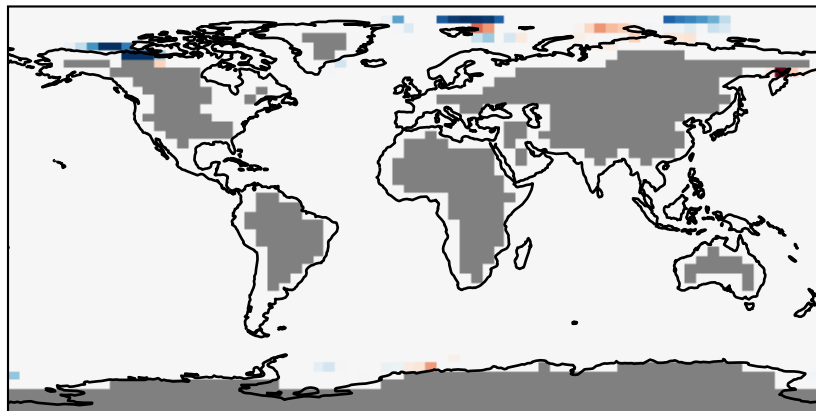
Difference
Dev - Ref, Dynamic Range



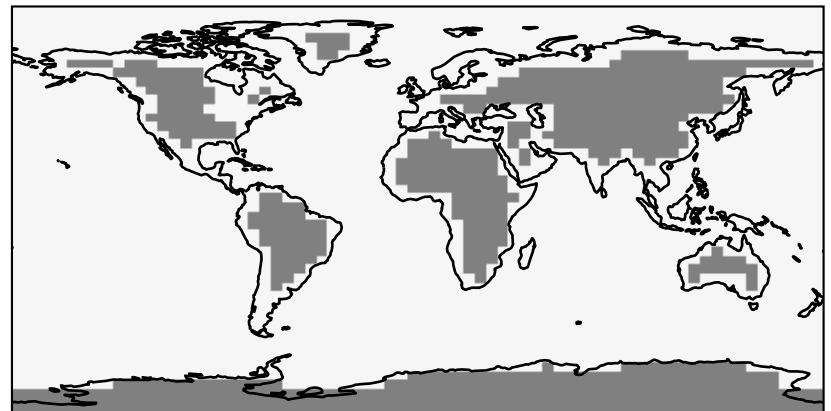
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

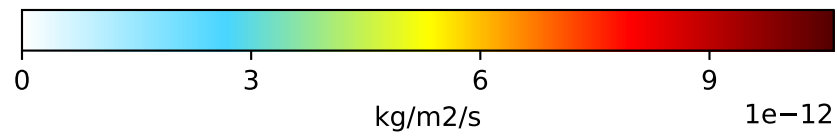
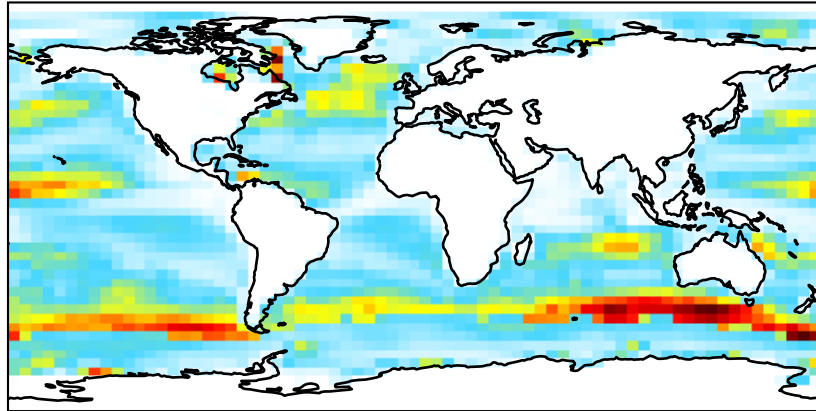


Ratio
Dev/Ref, Fixed Range

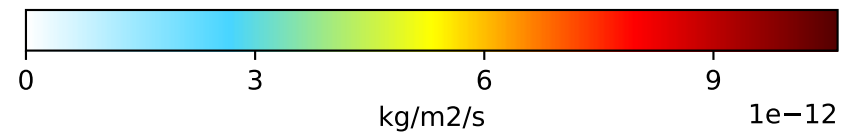
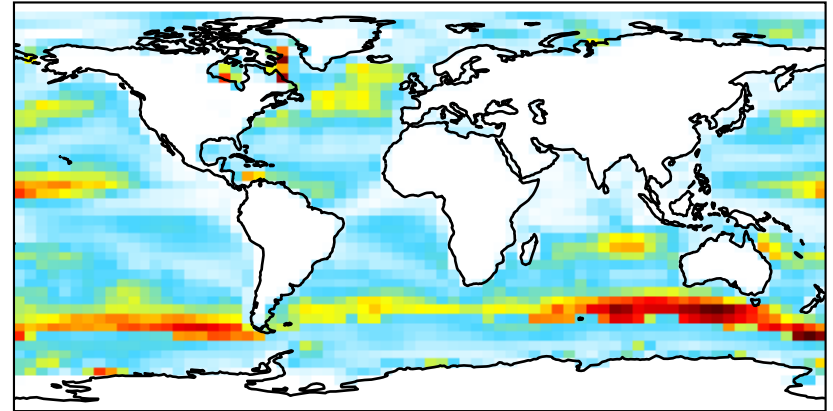


EmisSALACL_Natural (Apr2019)

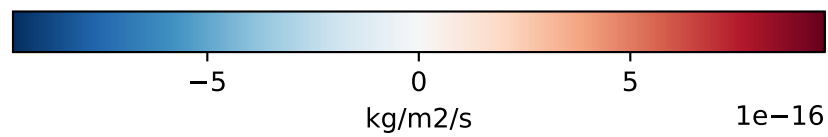
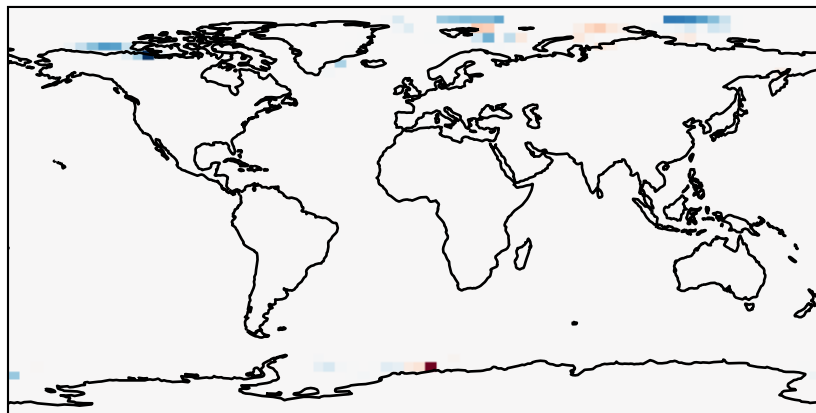
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



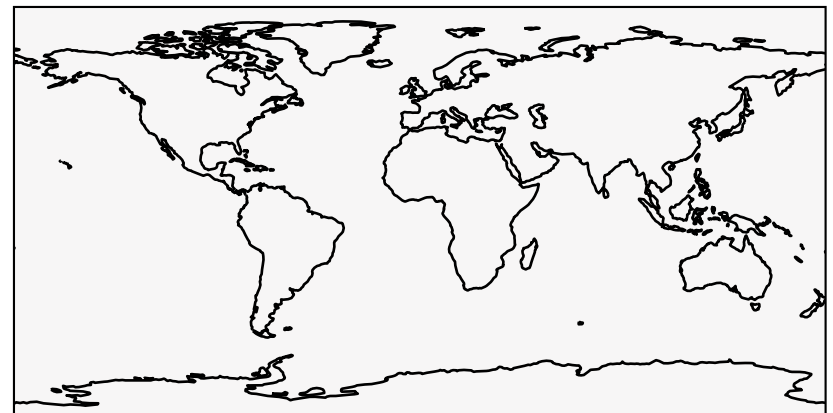
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



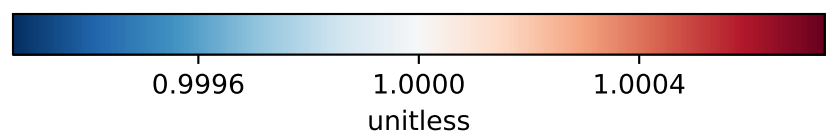
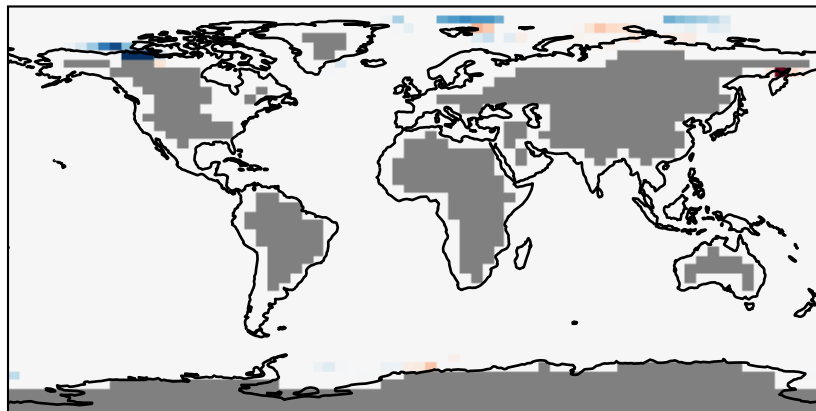
Difference
Dev - Ref, Dynamic Range



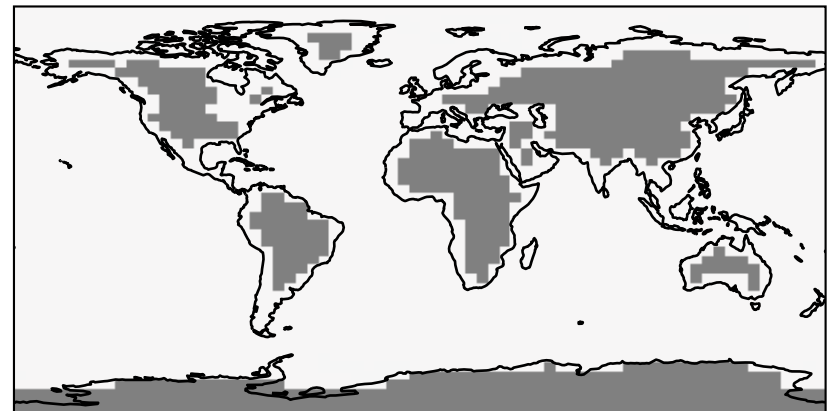
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

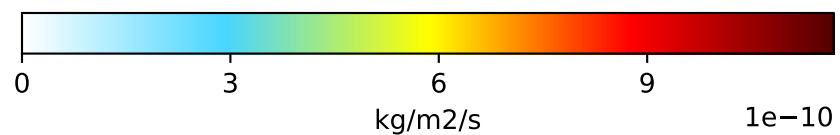
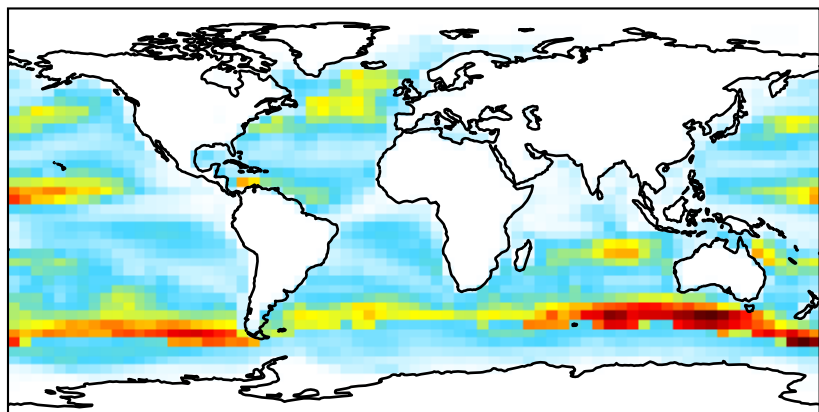


Ratio
Dev/Ref, Fixed Range

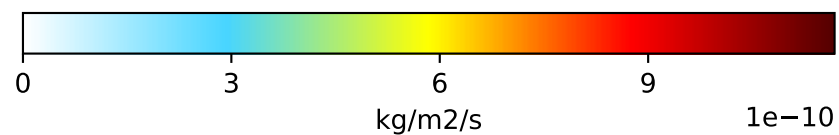
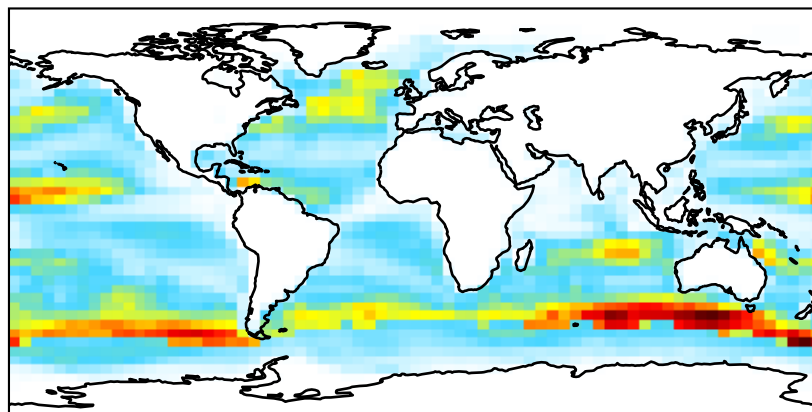


EmisSALC_Natural (Apr2019)

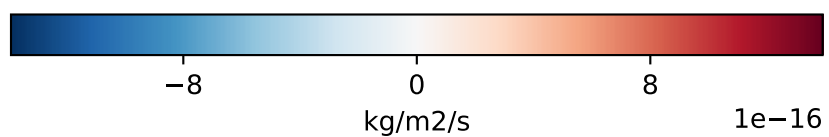
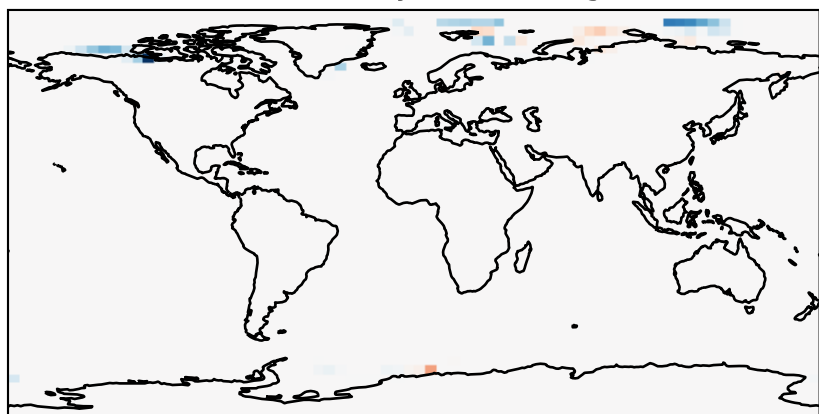
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



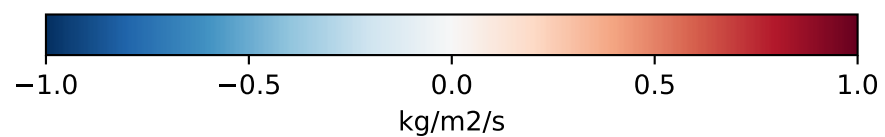
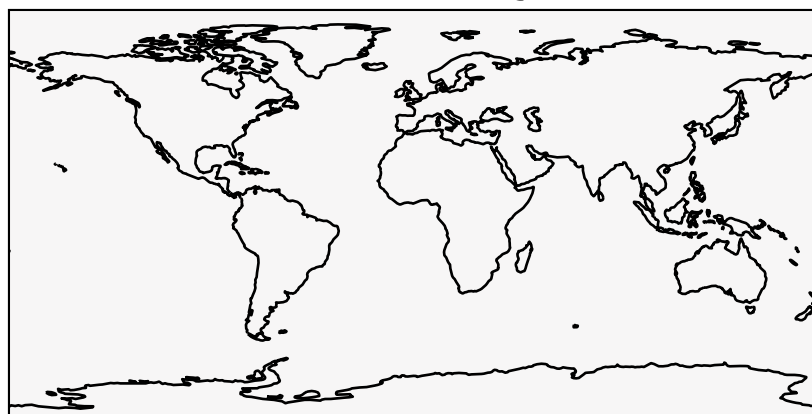
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



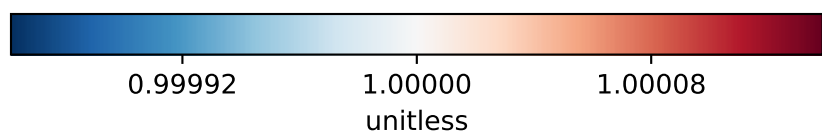
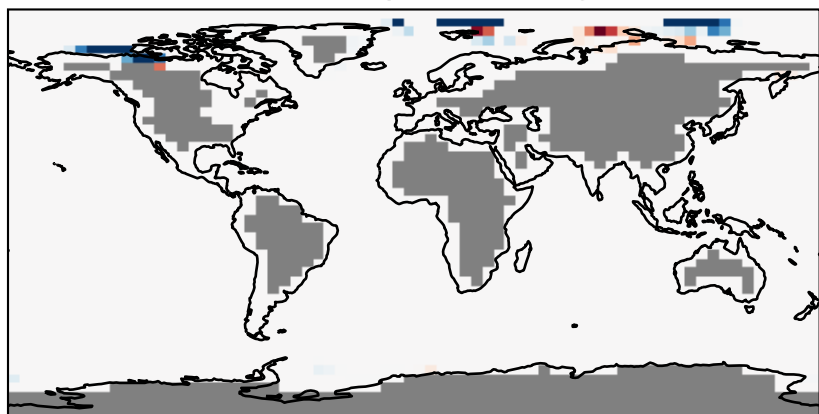
Difference
Dev - Ref, Dynamic Range



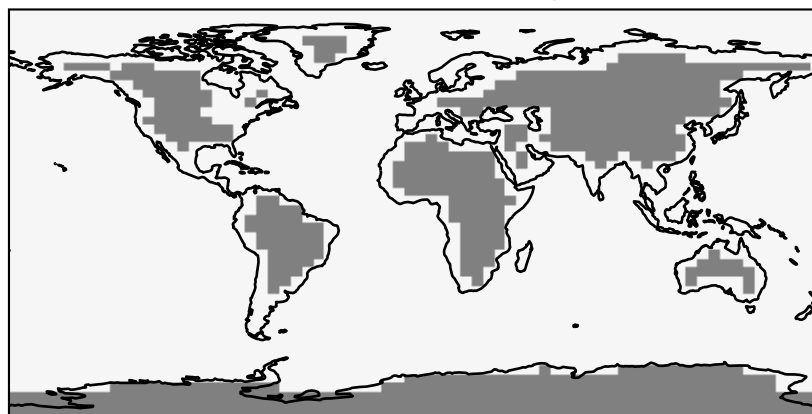
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

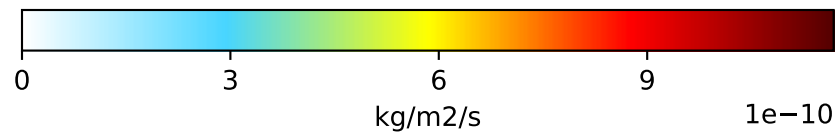
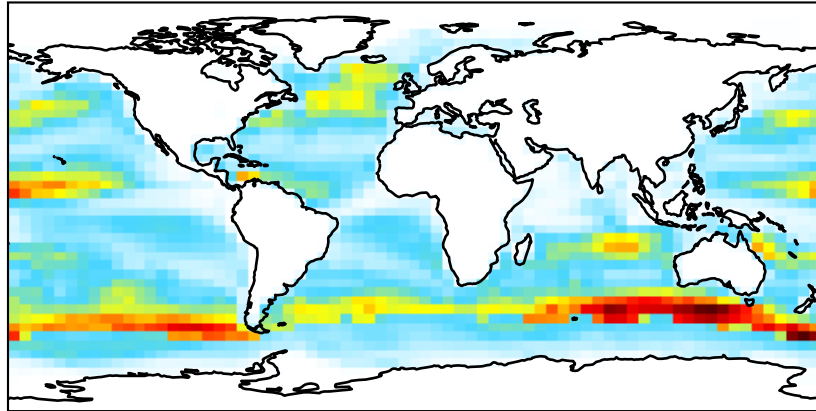


Ratio
Dev/Ref, Fixed Range

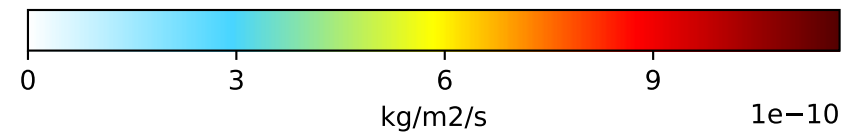
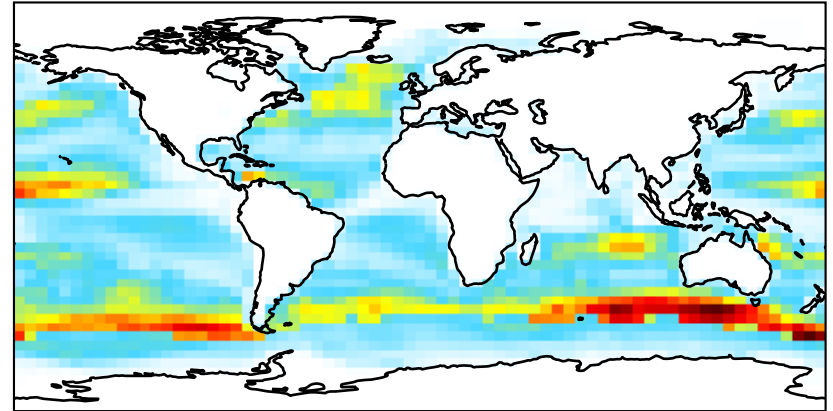


EmisSALCAL_Natural (Apr2019)

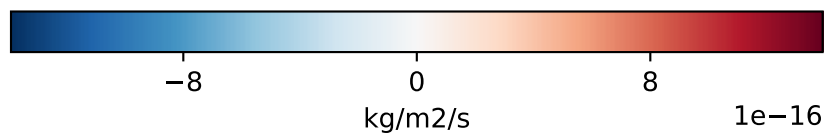
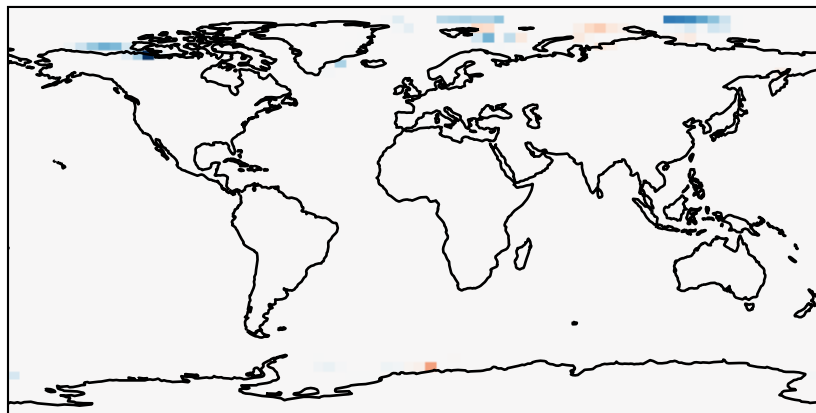
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



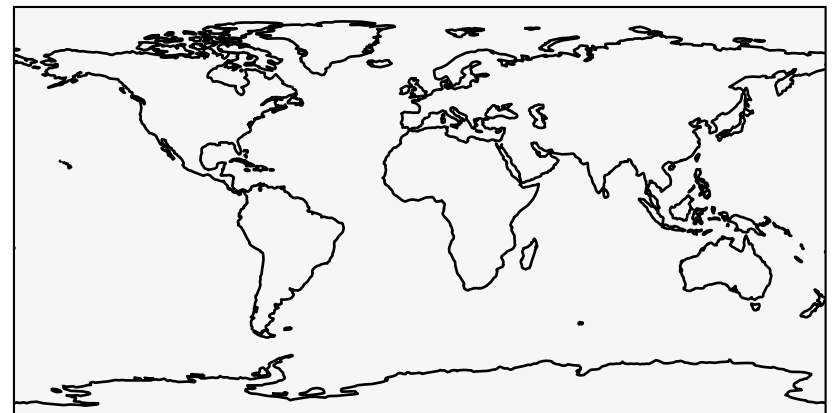
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



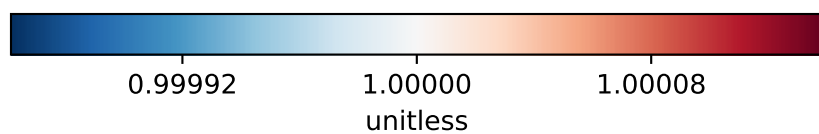
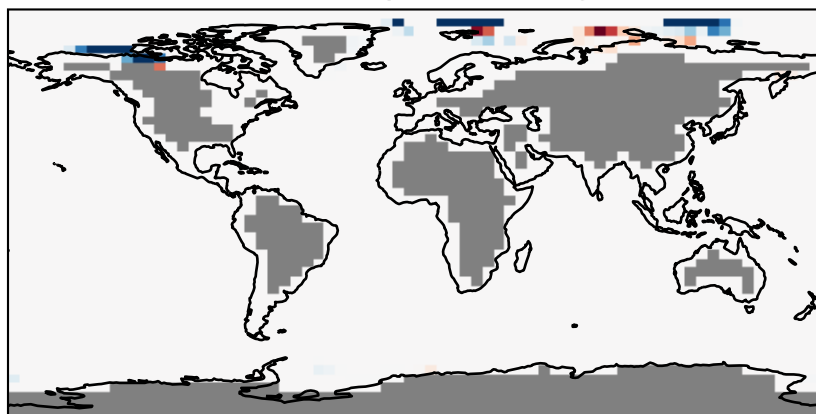
Difference
Dev - Ref, Dynamic Range



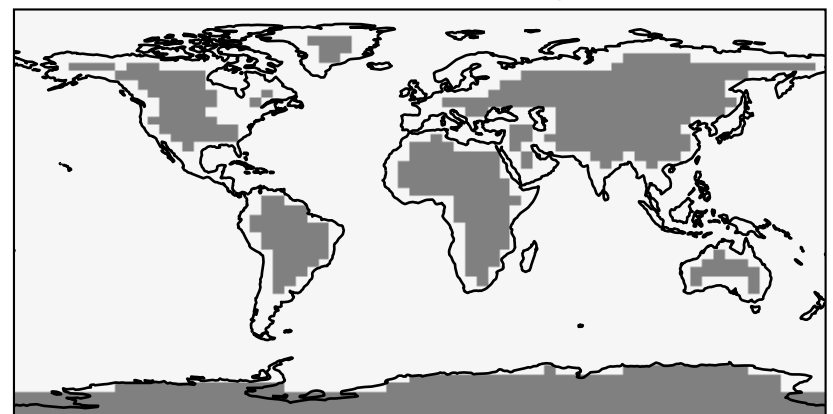
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

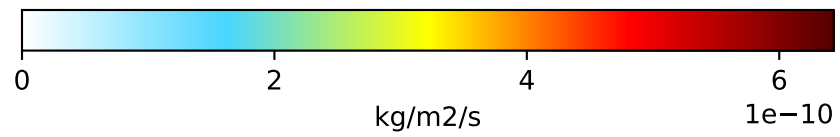
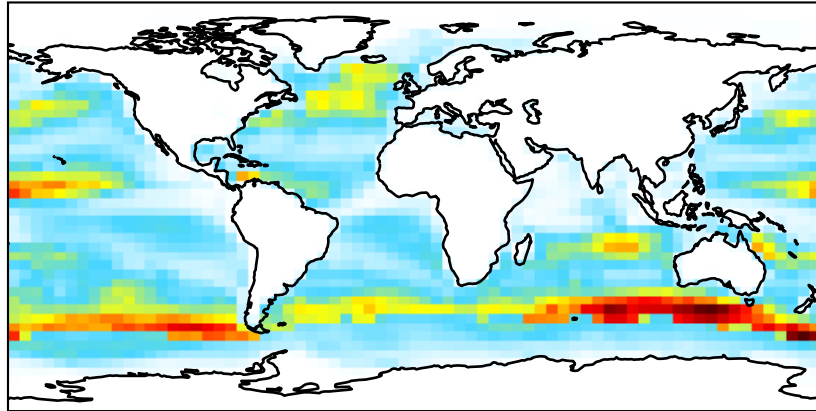


Ratio
Dev/Ref, Fixed Range

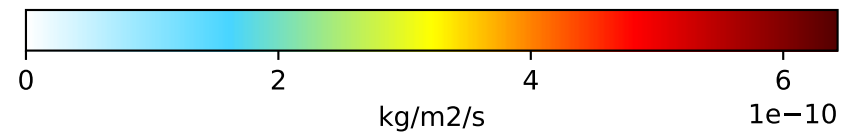
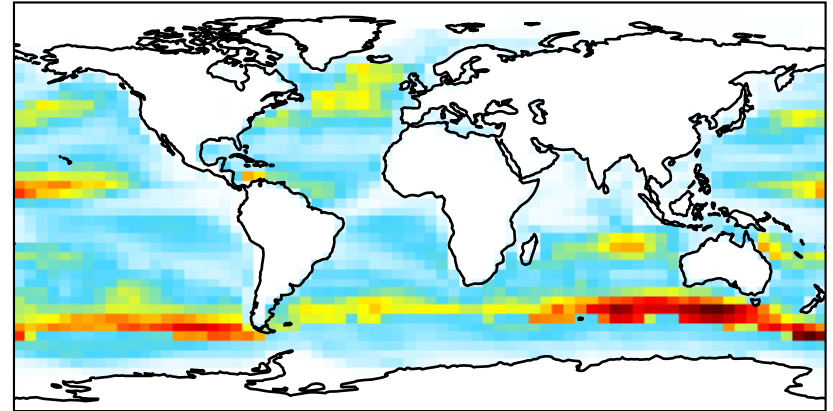


EmisSALCCL_Natural (Apr2019)

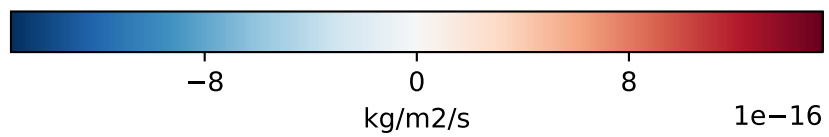
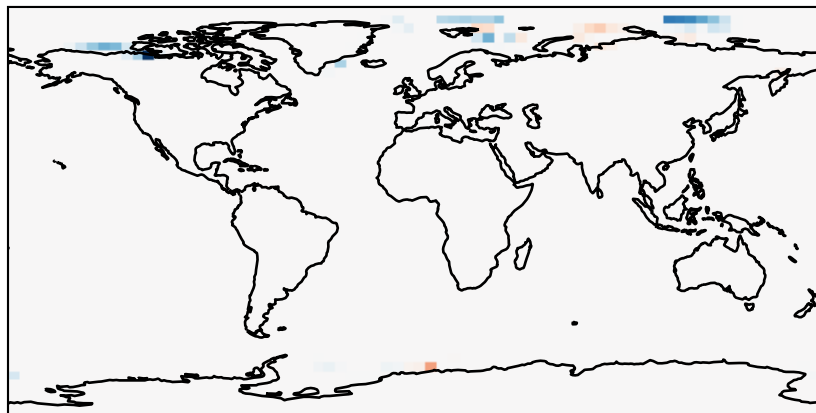
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



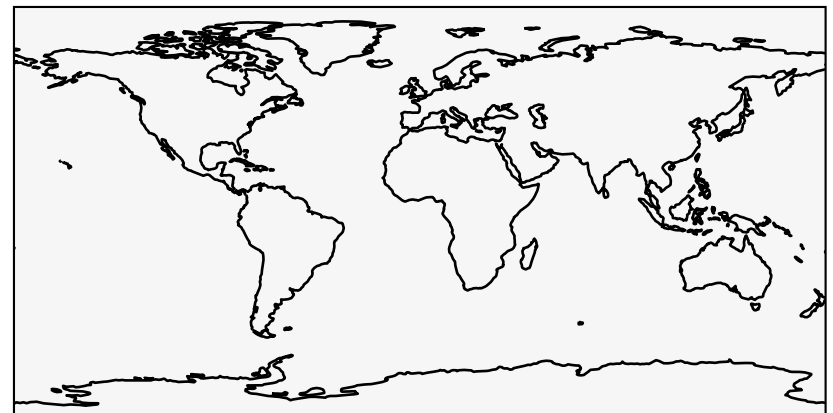
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



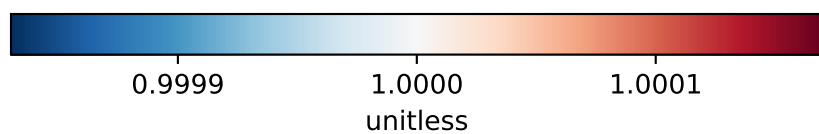
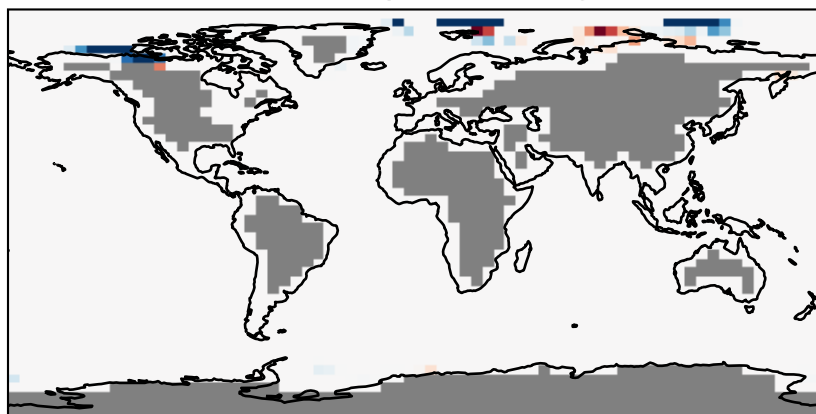
Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range



Ratio
Dev/Ref, Fixed Range

