

# EmisBrSALA\_Natural (Oct2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



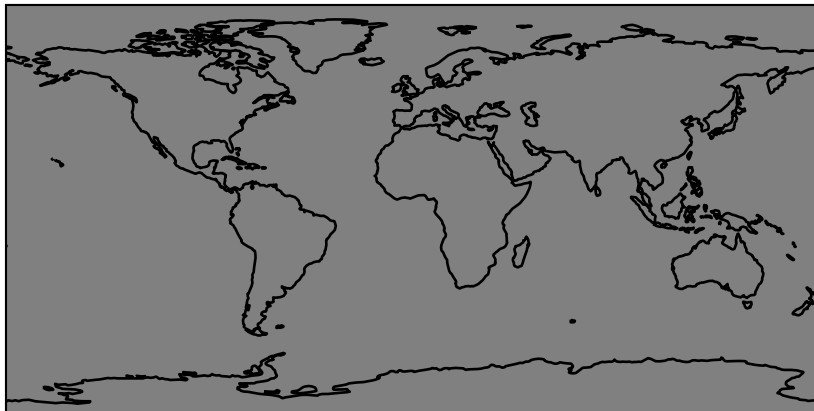
Zero throughout domain  
kg/m2/s

GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



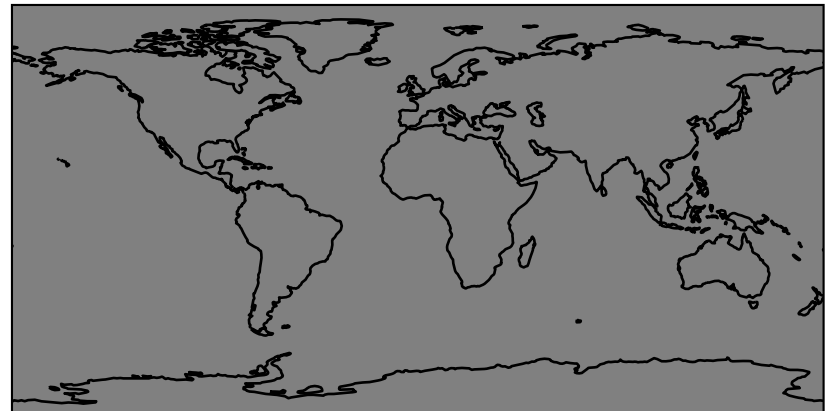
Undefined throughout domain  
kg/m2/s

Difference  
Dev - Ref, Dynamic Range



Undefined throughout domain  
kg/m2/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



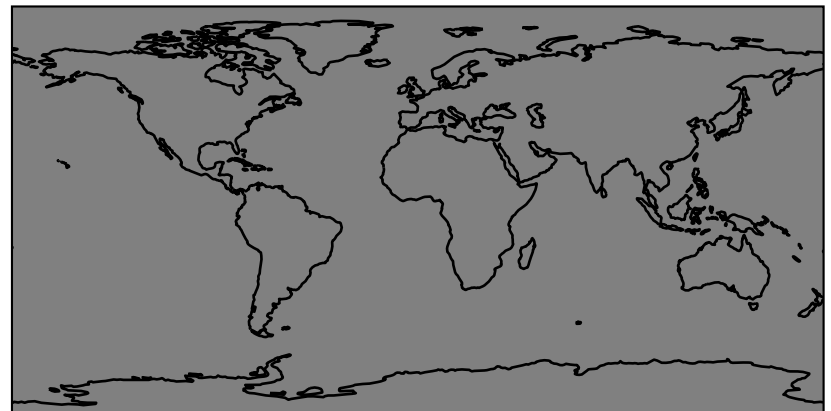
Undefined throughout domain  
kg/m2/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

Ratio  
Dev/Ref, Fixed Range



Undefined throughout domain  
unitless

# EmisBrSALC\_Natural (Oct2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



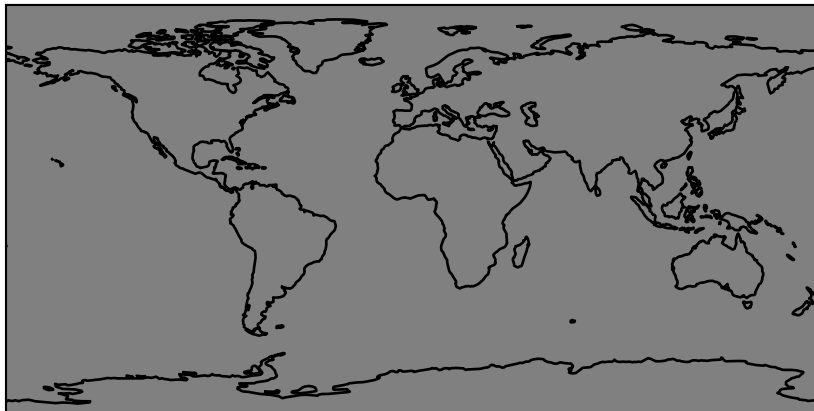
Zero throughout domain  
kg/m2/s

GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



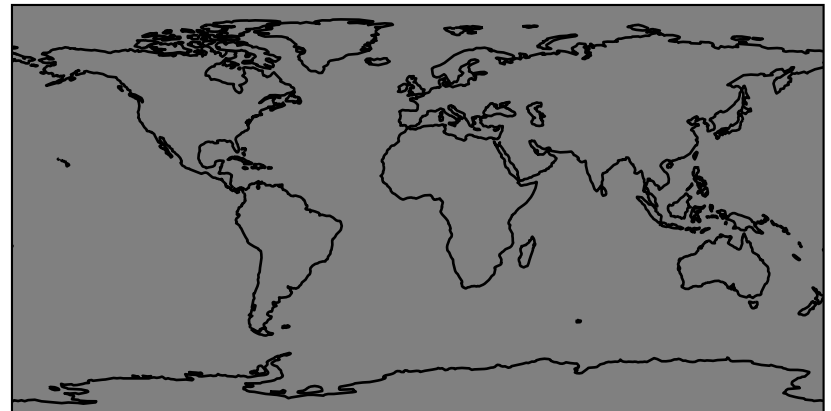
Undefined throughout domain  
kg/m2/s

Difference  
Dev - Ref, Dynamic Range



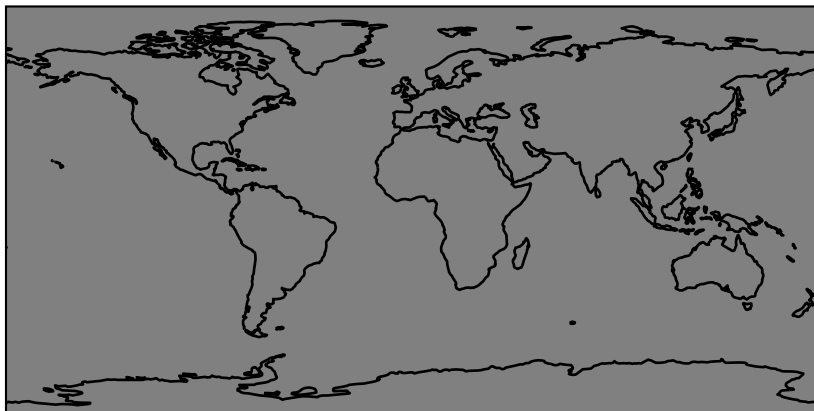
Undefined throughout domain  
kg/m2/s

Difference  
Dev - Ref, Restricted Range [5%,95%]



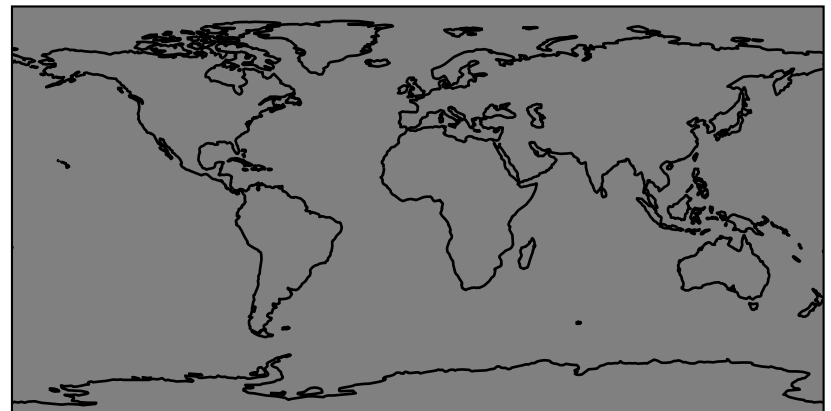
Undefined throughout domain  
kg/m2/s

Ratio  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

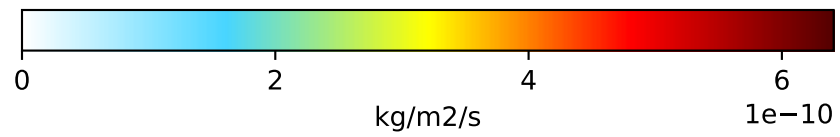
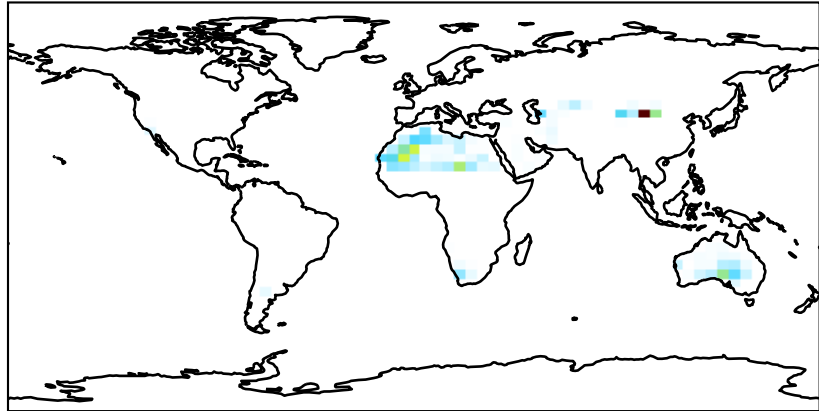
Ratio  
Dev/Ref, Fixed Range



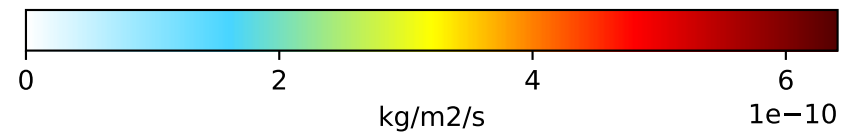
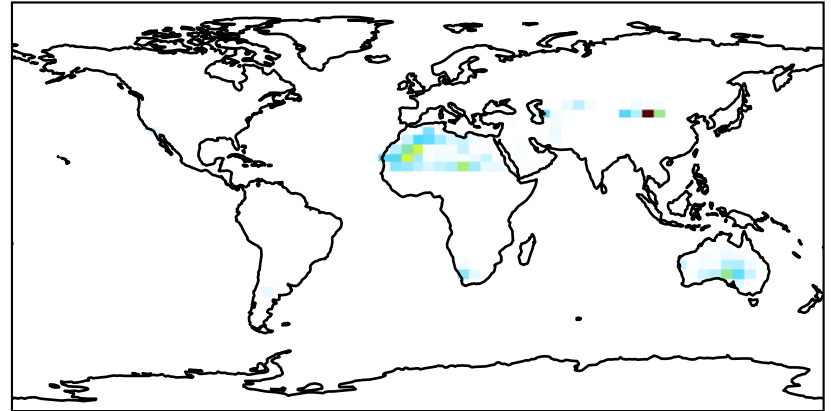
Undefined throughout domain  
unitless

# EmisDST1\_Natural (Oct2019)

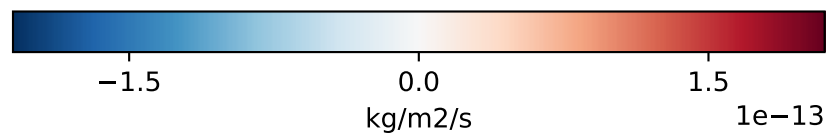
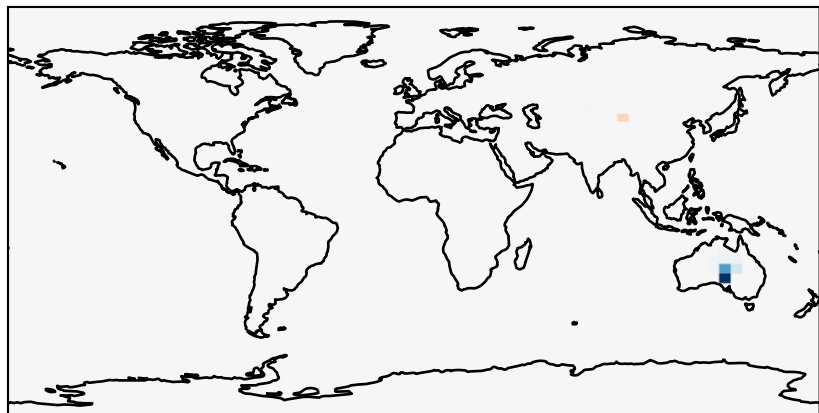
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



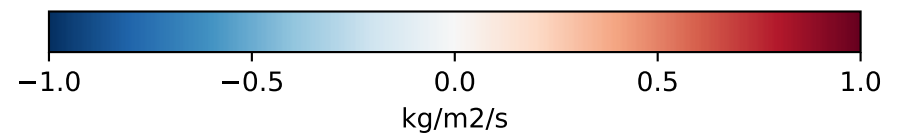
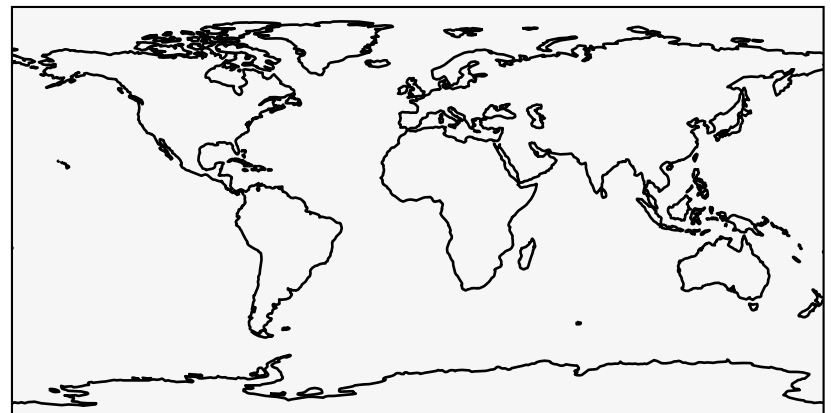
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



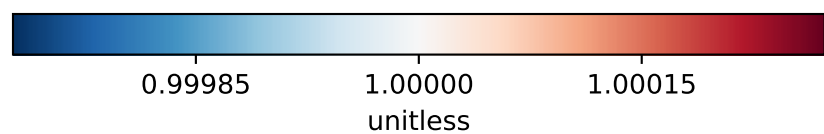
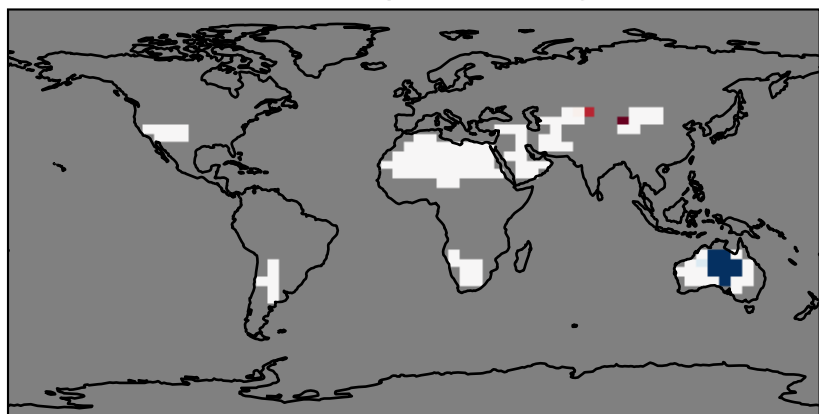
Difference  
Dev - Ref, Dynamic Range



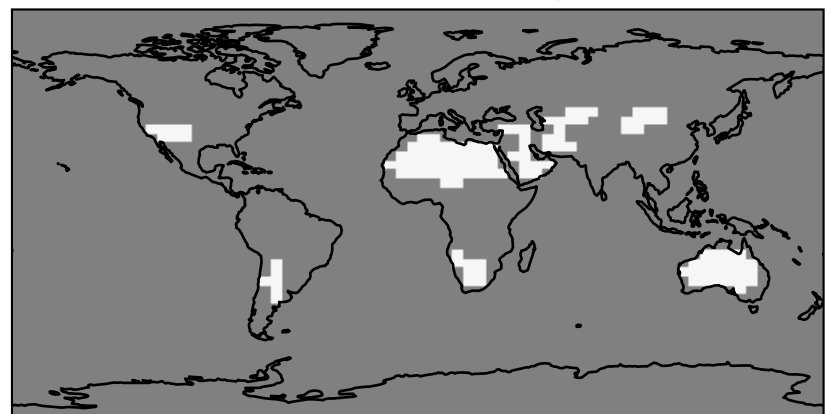
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

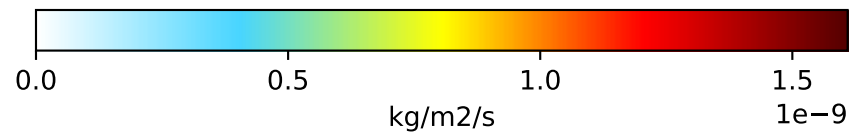
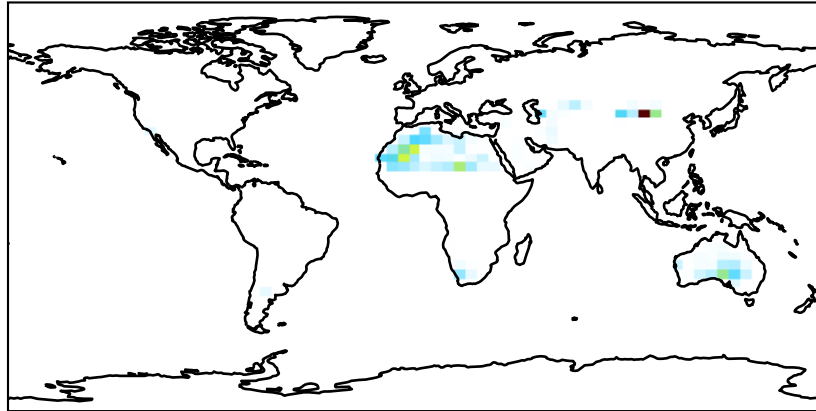


Ratio  
Dev/Ref, Fixed Range

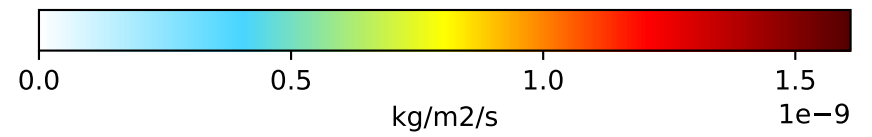
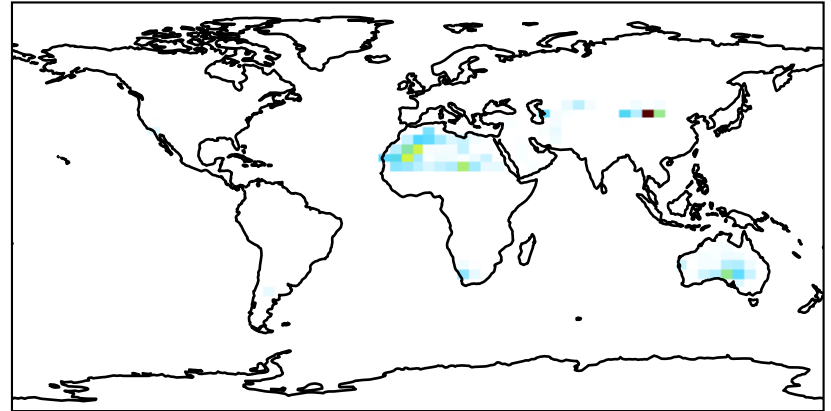


# EmisDST2\_Natural (Oct2019)

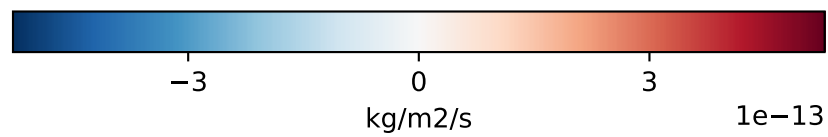
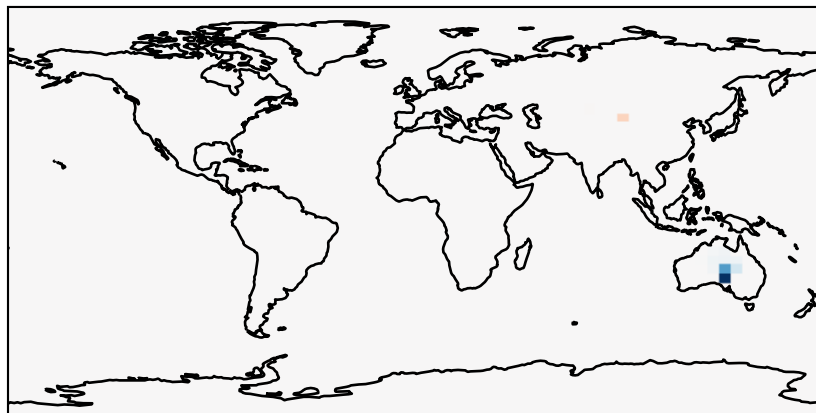
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



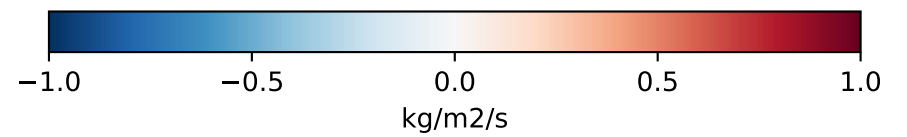
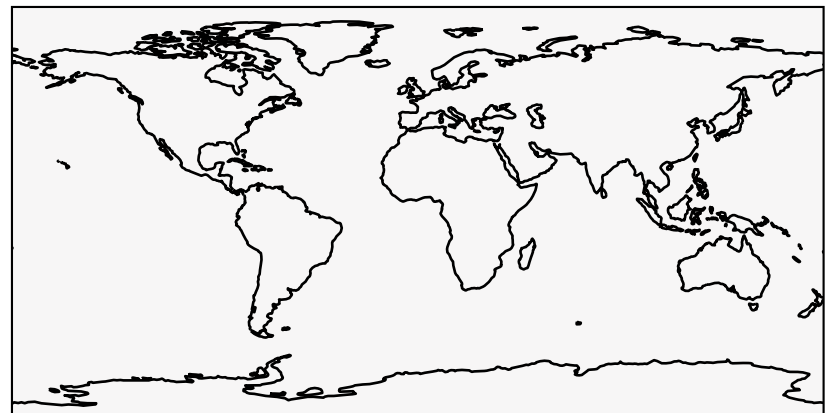
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



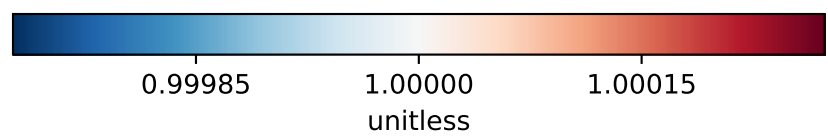
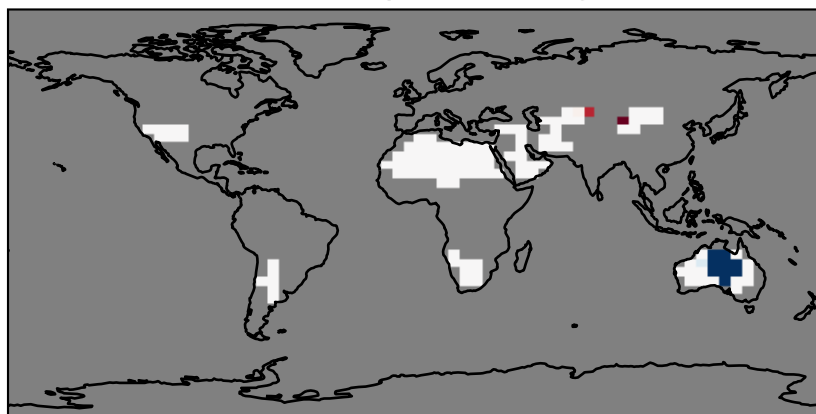
Difference  
Dev - Ref, Dynamic Range



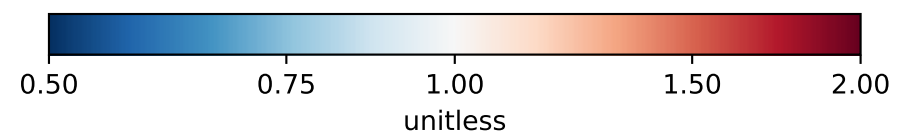
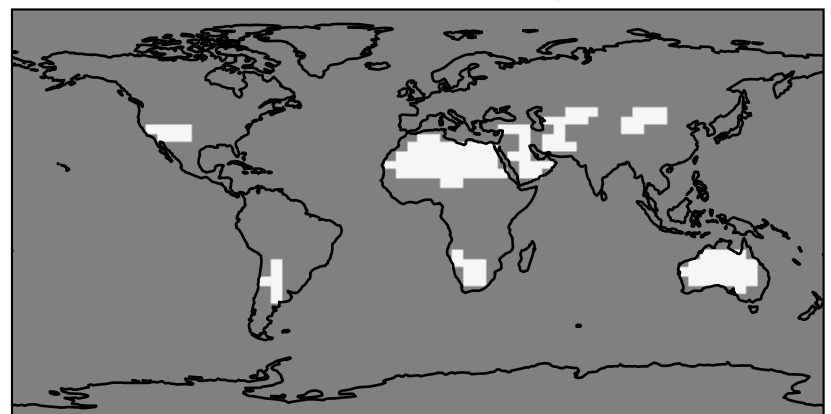
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

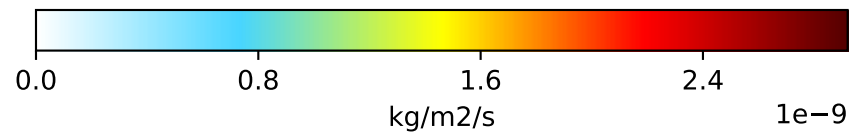
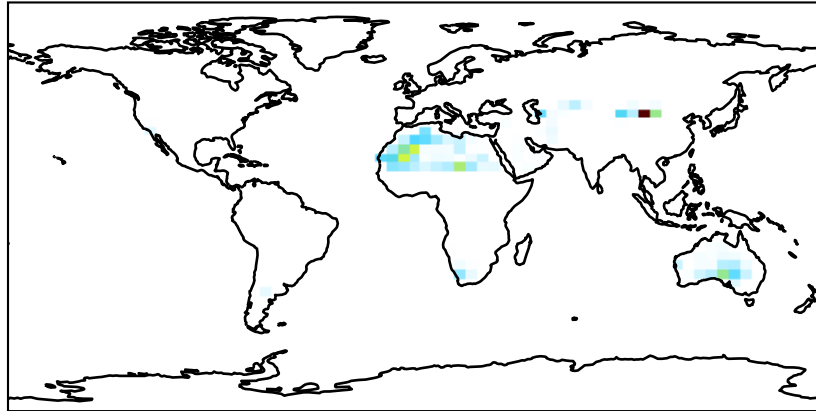


Ratio  
Dev/Ref, Fixed Range

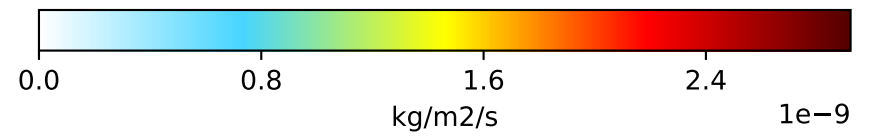
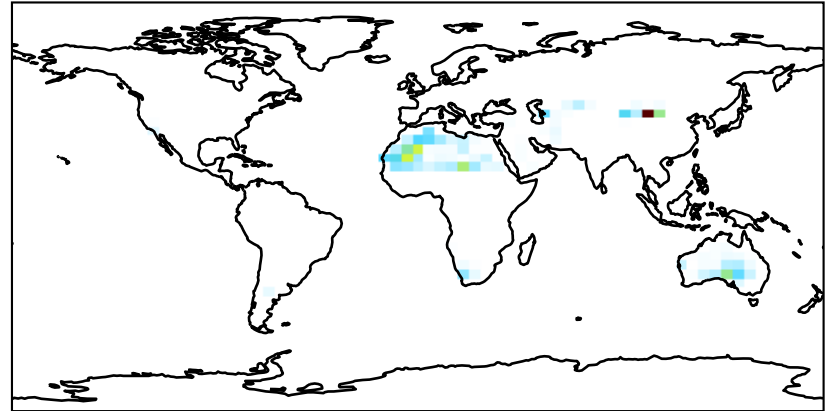


# EmisDST3\_Natural (Oct2019)

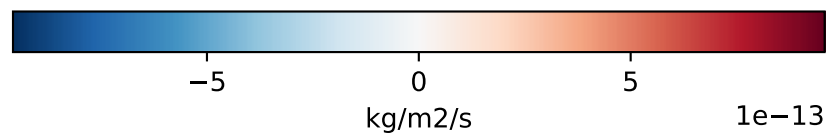
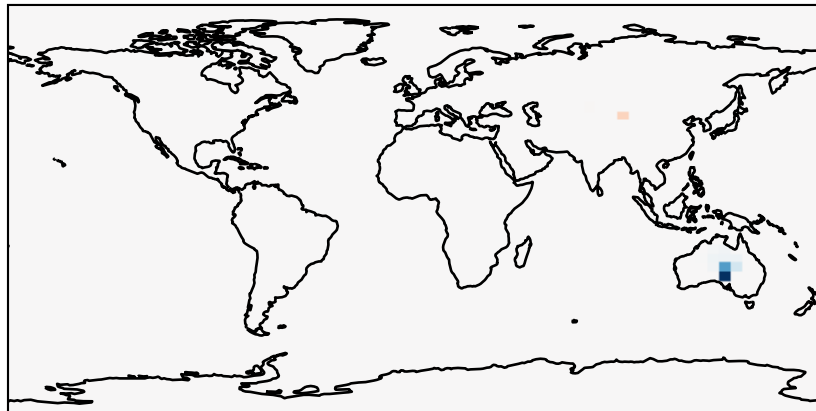
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



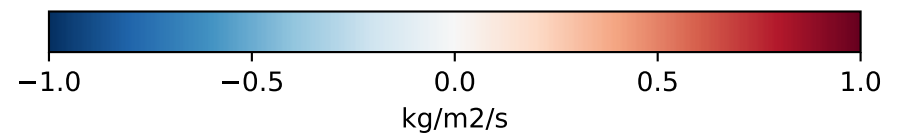
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



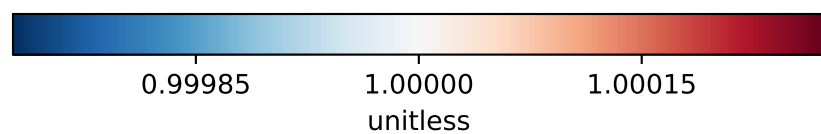
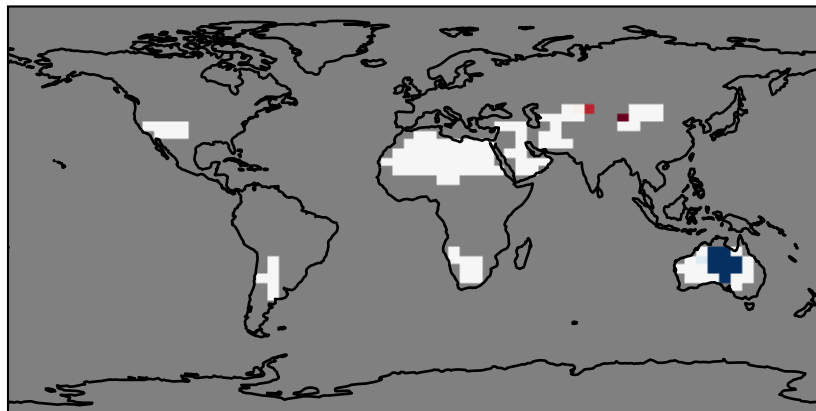
Difference  
Dev - Ref, Dynamic Range



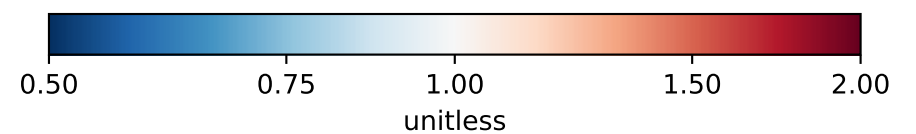
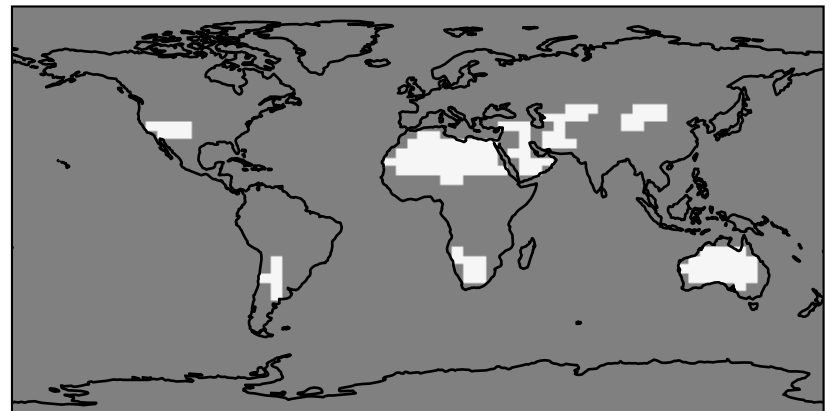
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

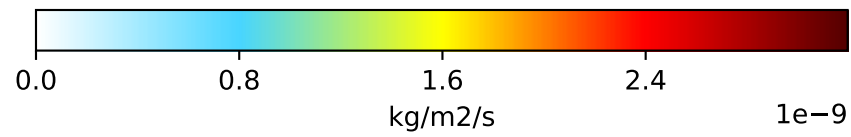
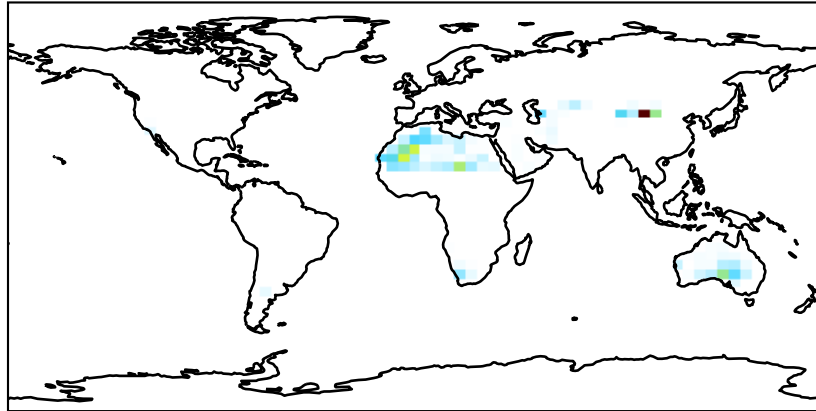


Ratio  
Dev/Ref, Fixed Range

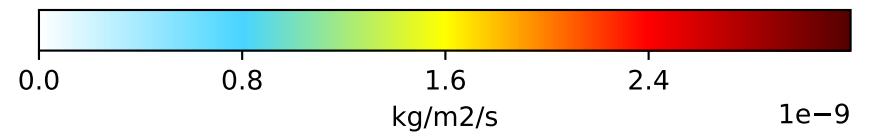
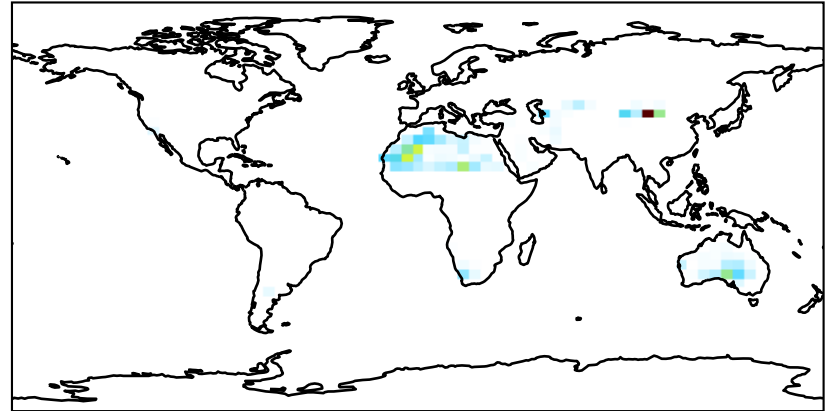


# EmisDST4\_Natural (Oct2019)

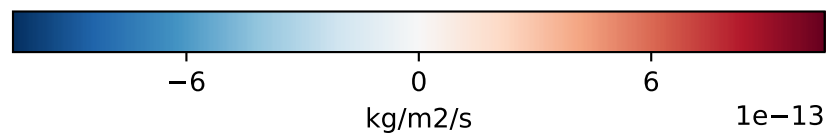
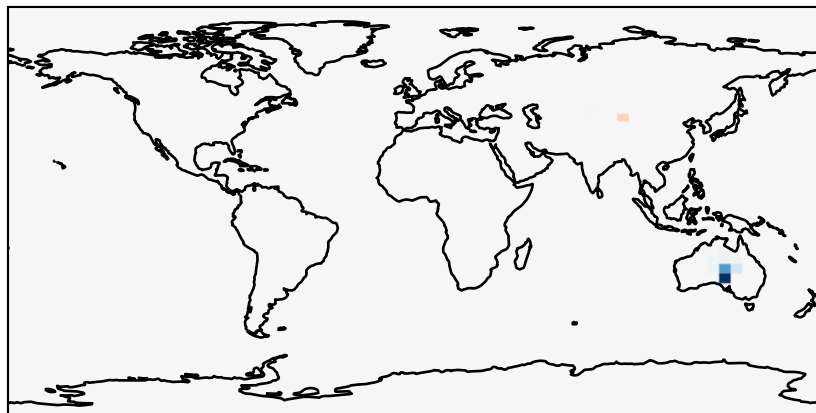
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



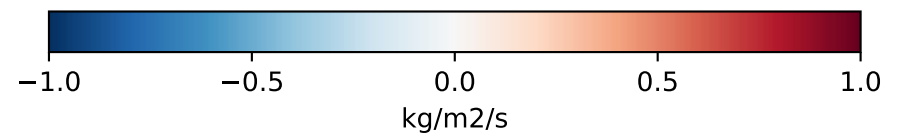
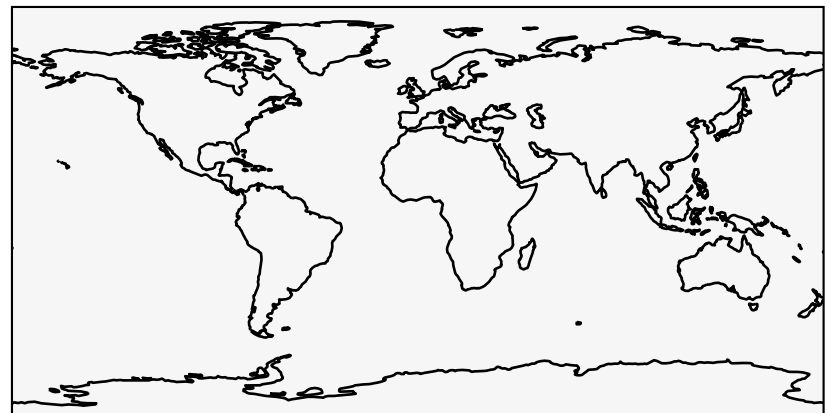
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



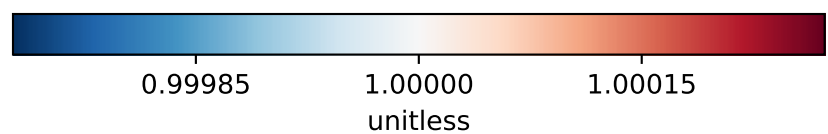
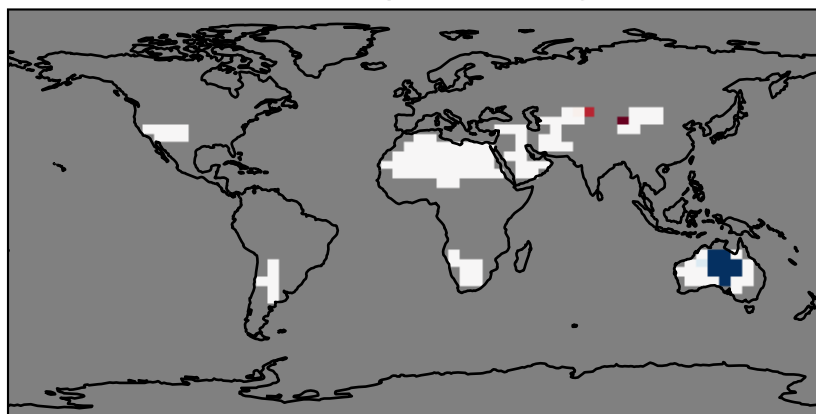
Difference  
Dev - Ref, Dynamic Range



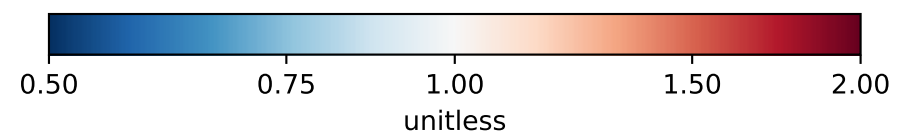
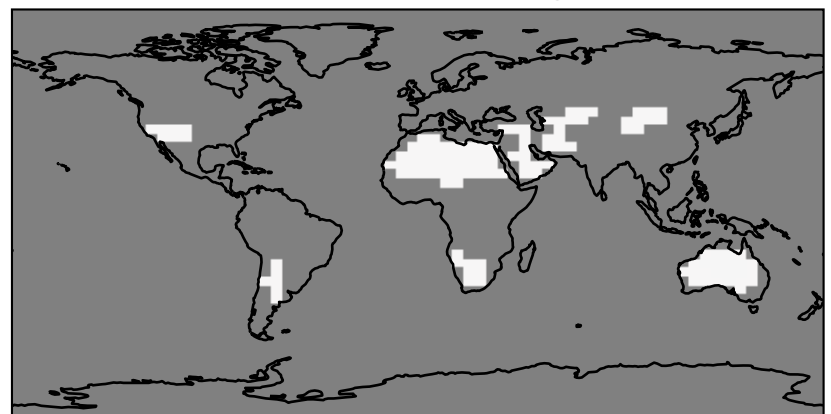
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



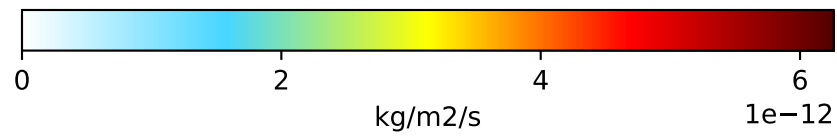
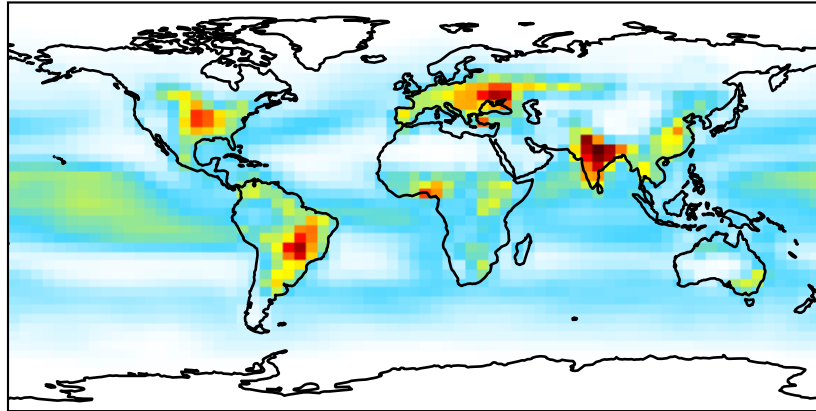
Ratio  
Dev/Ref, Fixed Range



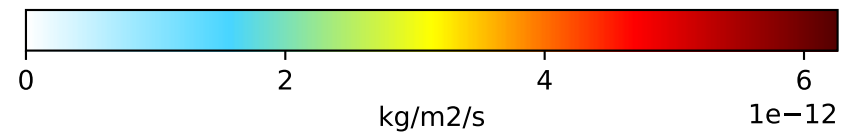
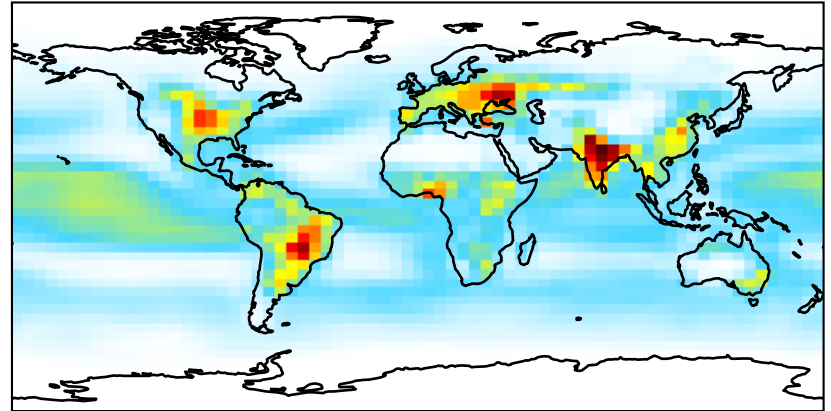


# EmisNH3\_Natural (Oct2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



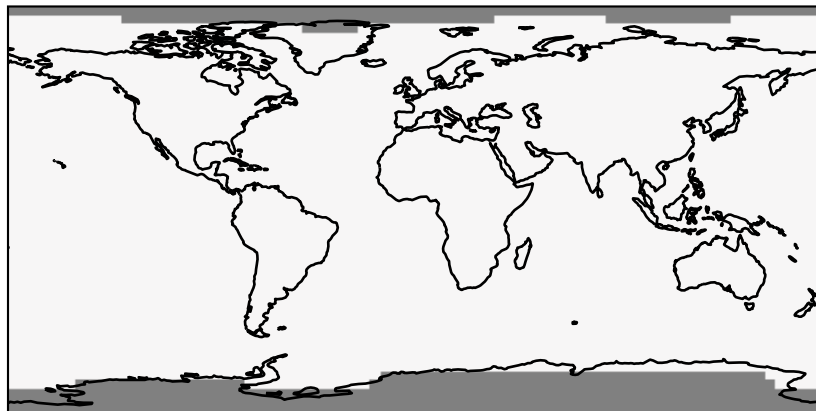
Difference  
Dev - Ref, Dynamic Range



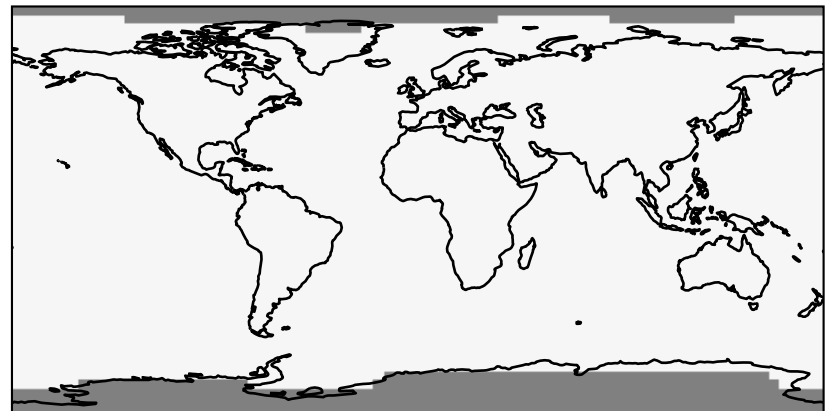
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

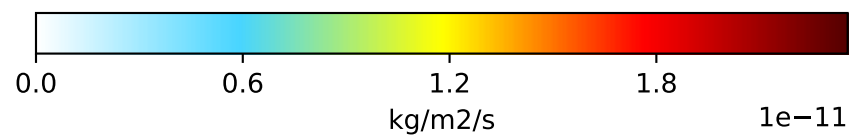
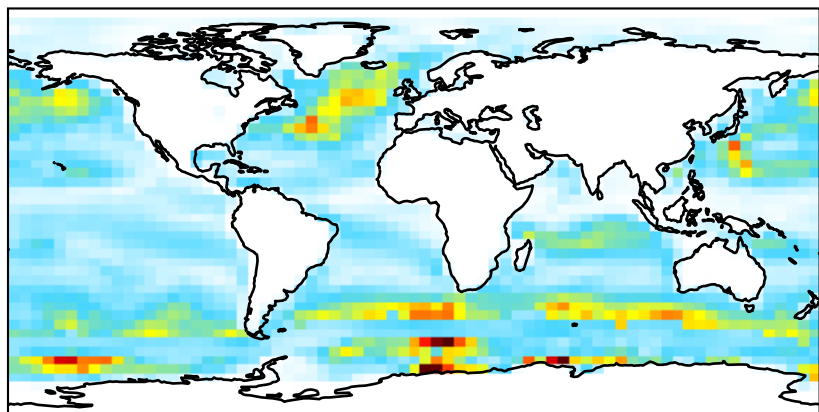


Ratio  
Dev/Ref, Fixed Range

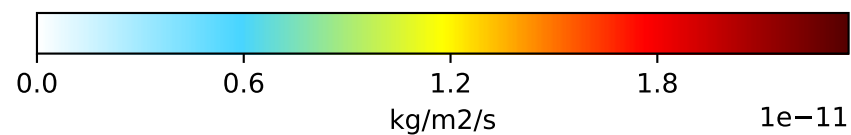
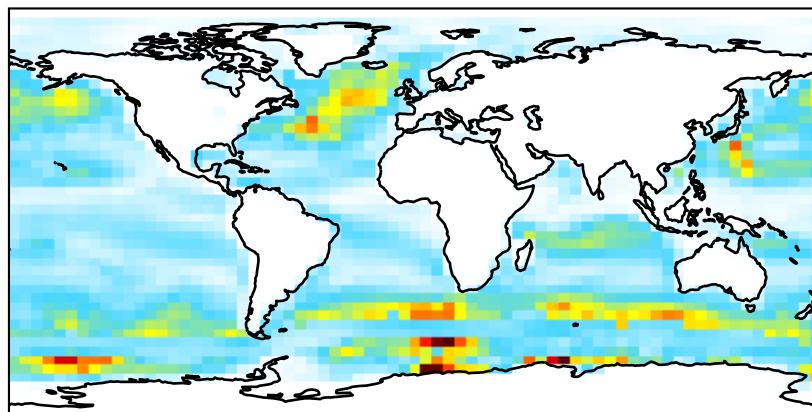


# EmisSALA\_Natural (Oct2019)

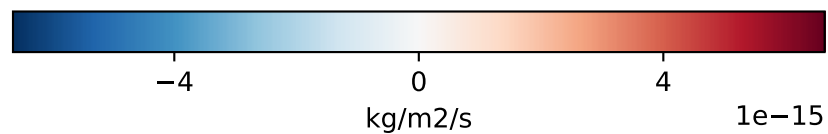
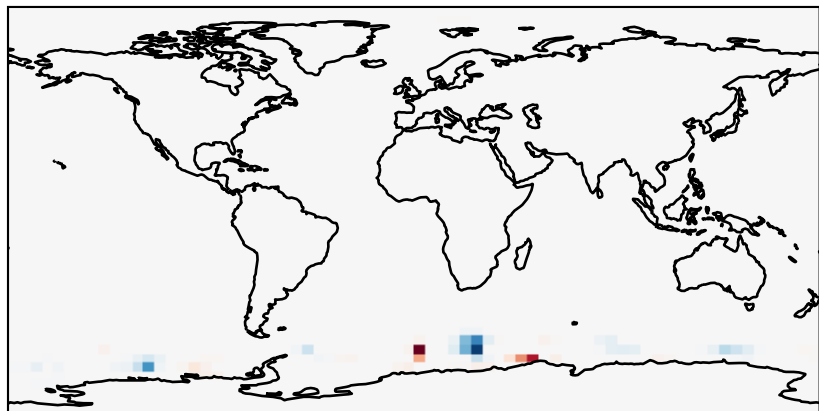
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



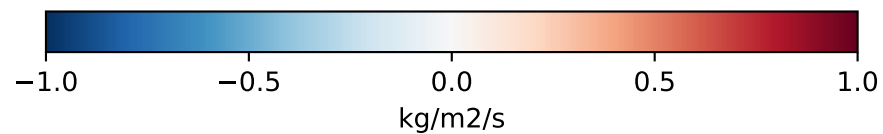
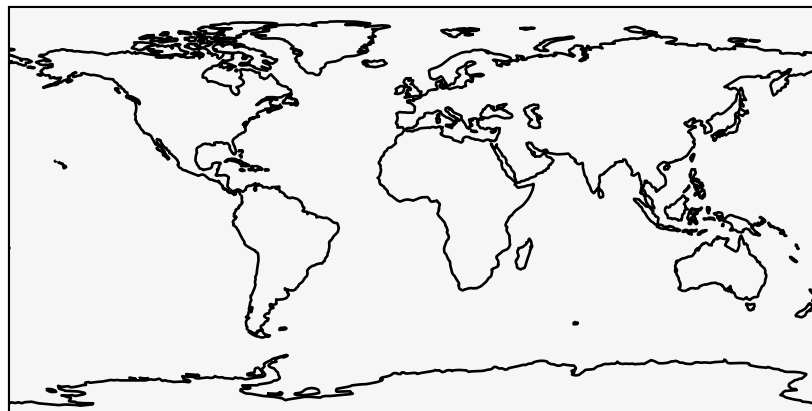
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



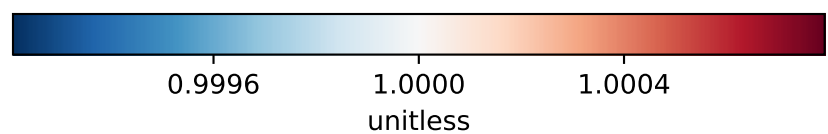
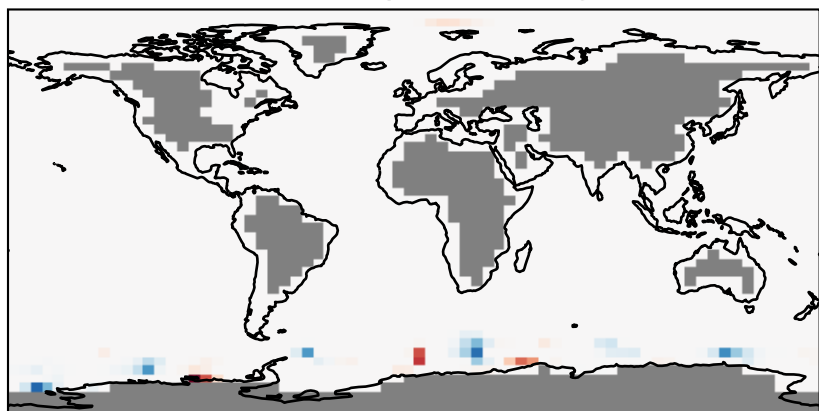
Difference  
Dev - Ref, Dynamic Range



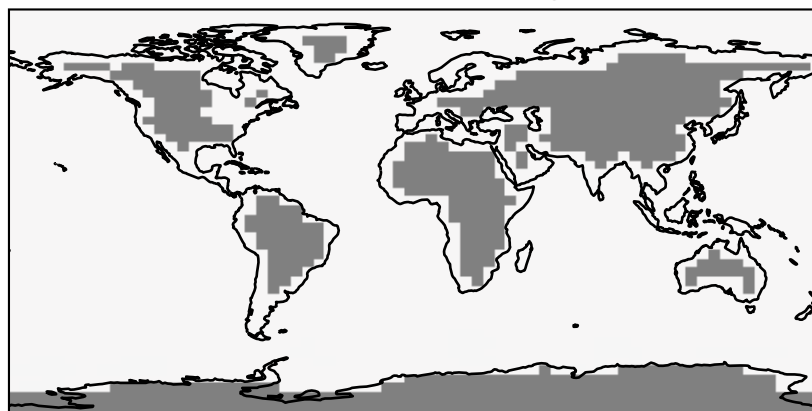
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



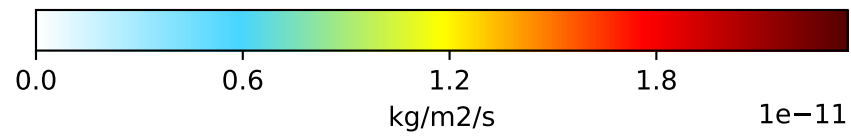
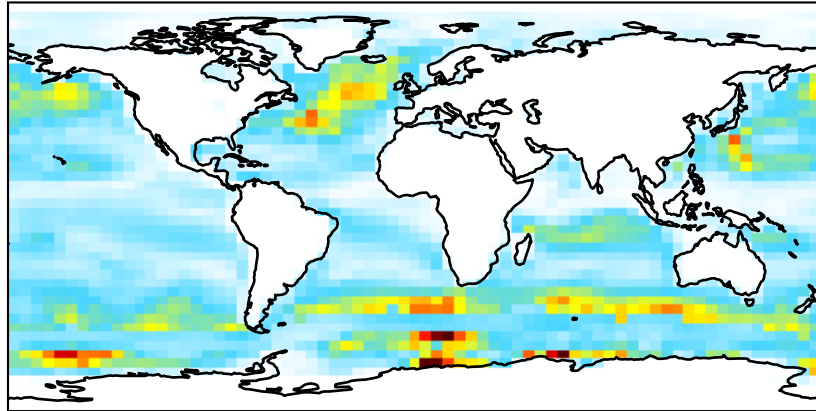
Ratio  
Dev/Ref, Fixed Range



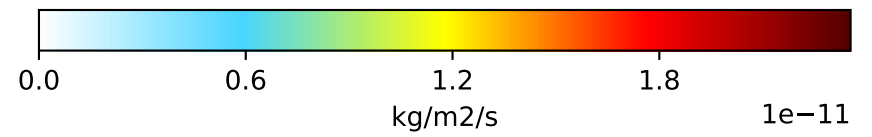
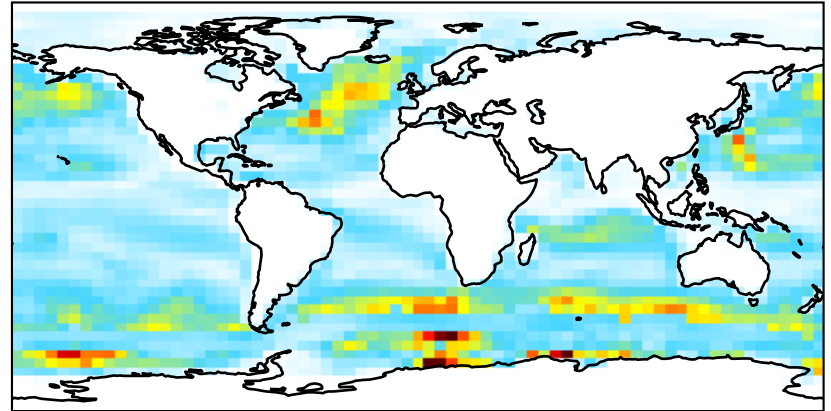


# EmisSALAAL\_Natural (Oct2019)

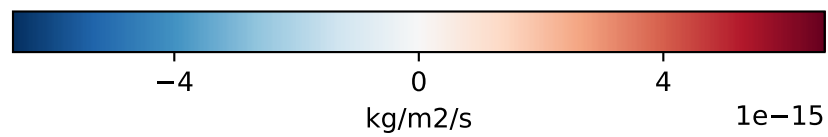
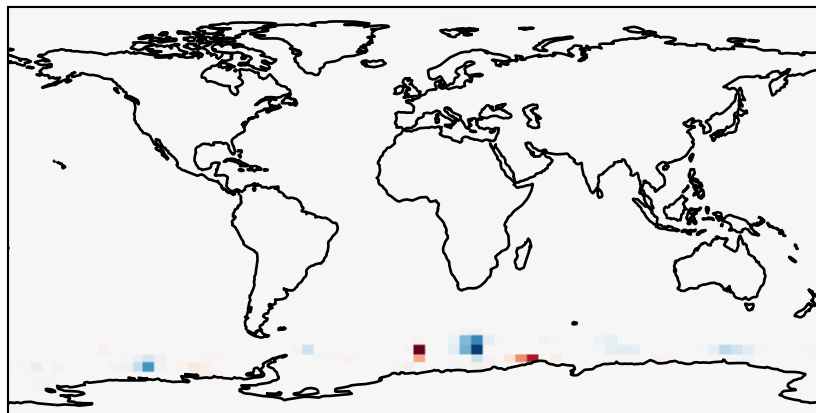
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



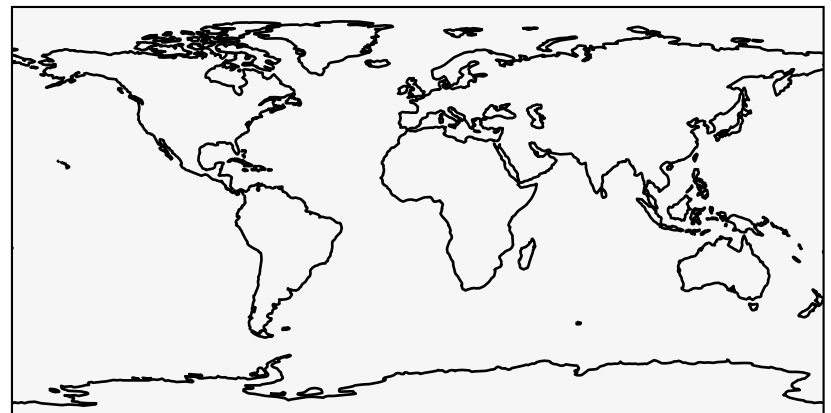
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



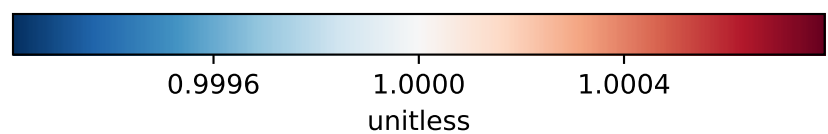
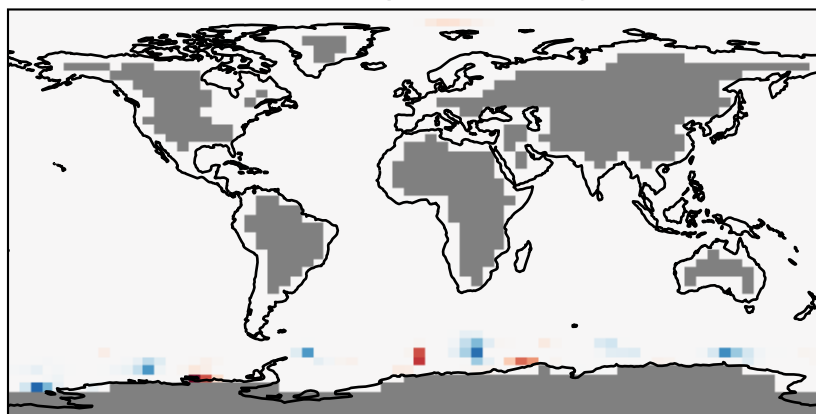
Difference  
Dev - Ref, Dynamic Range



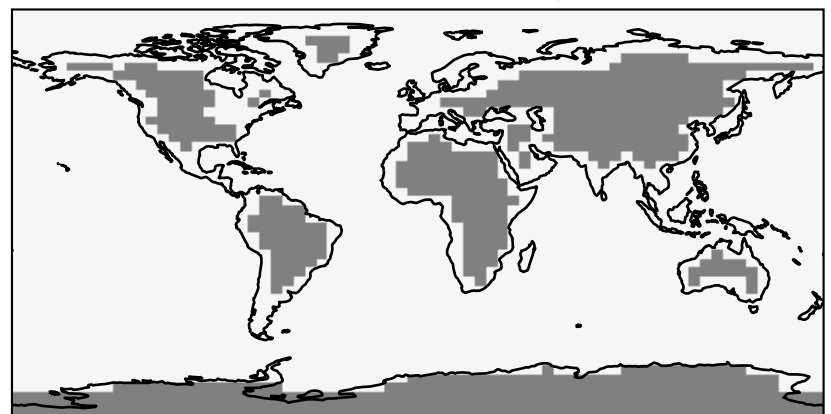
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

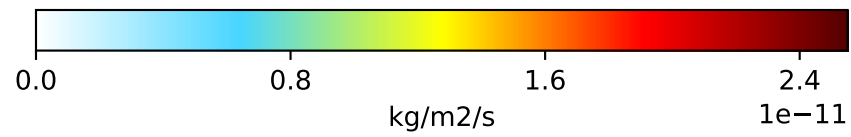
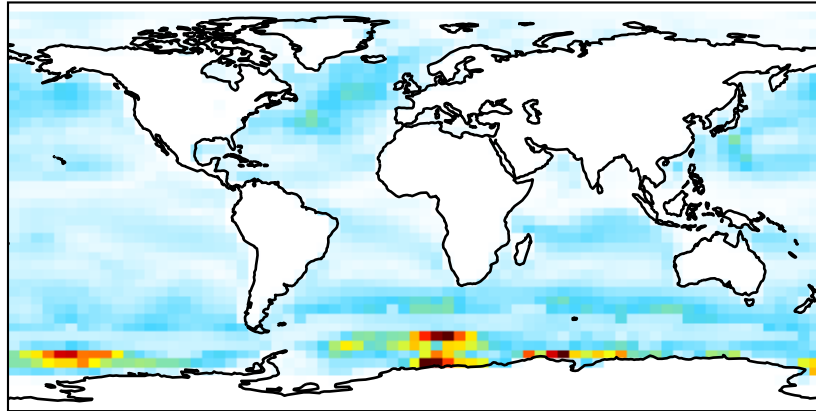


Ratio  
Dev/Ref, Fixed Range

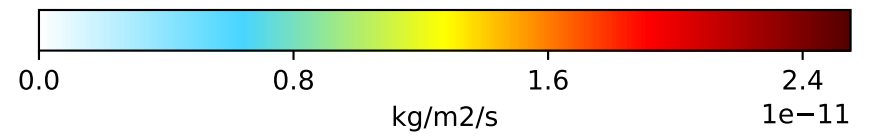
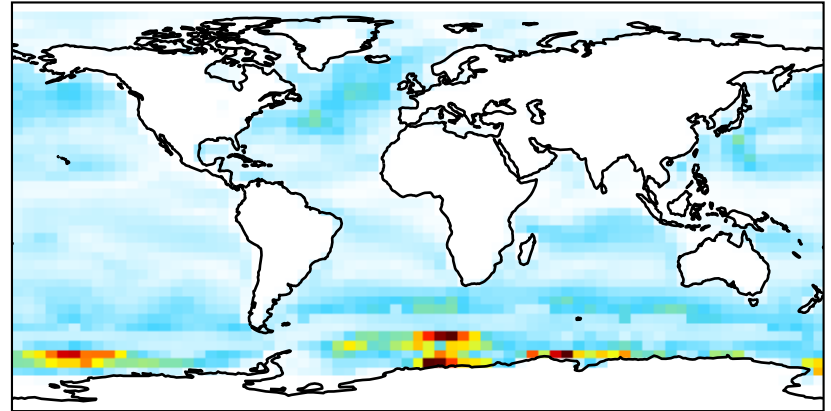


# EmisSALACL\_Natural (Oct2019)

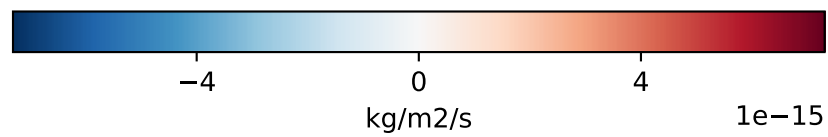
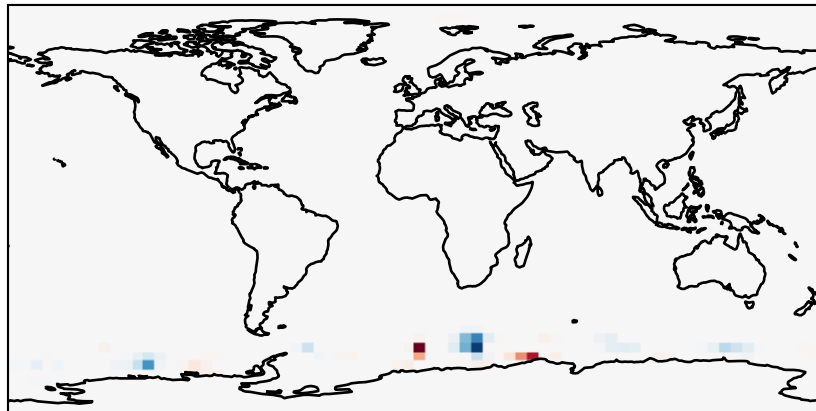
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



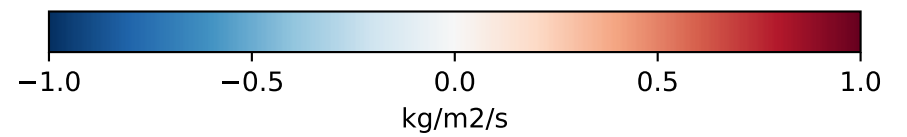
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



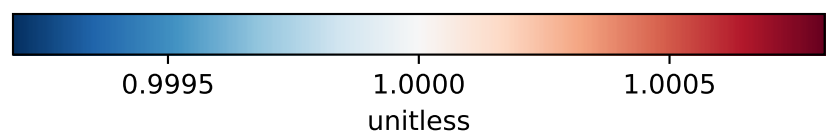
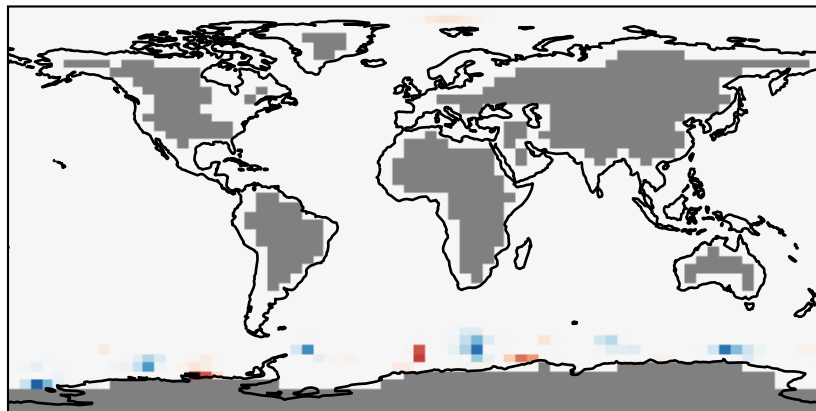
Difference  
Dev - Ref, Dynamic Range



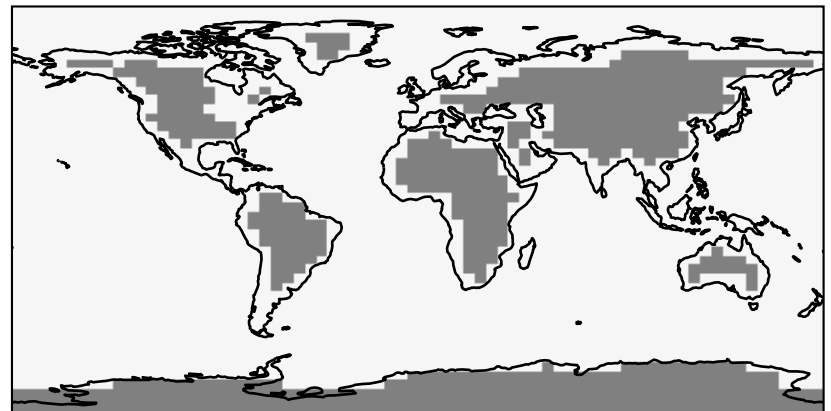
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

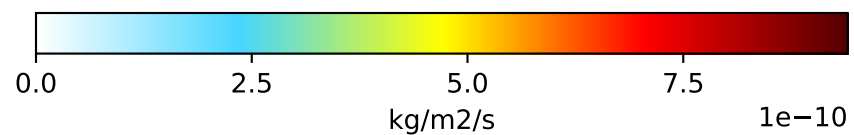
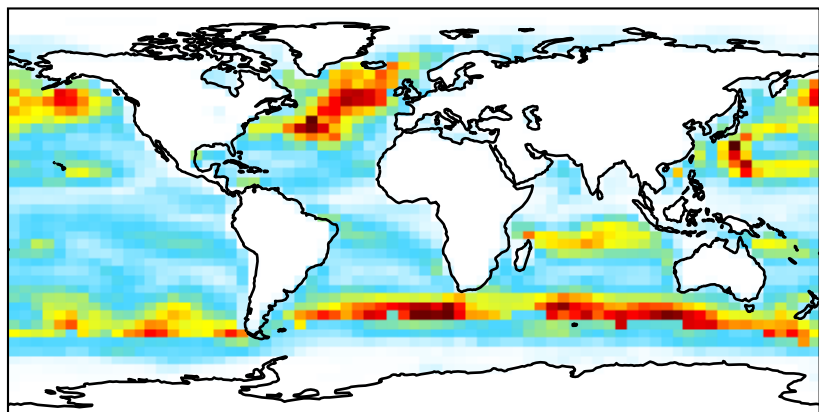


Ratio  
Dev/Ref, Fixed Range

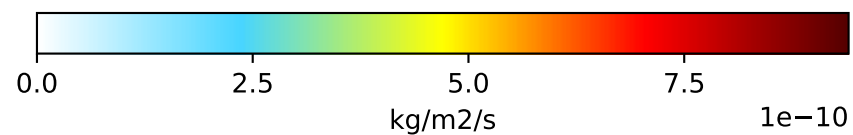
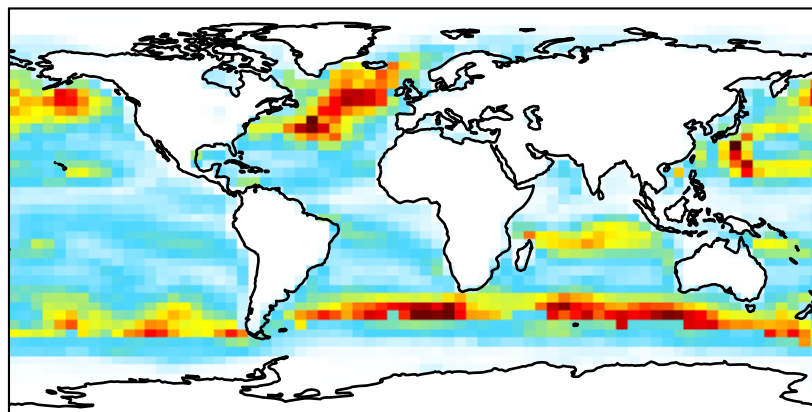


# EmisSALC\_Natural (Oct2019)

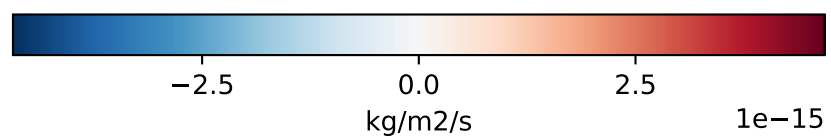
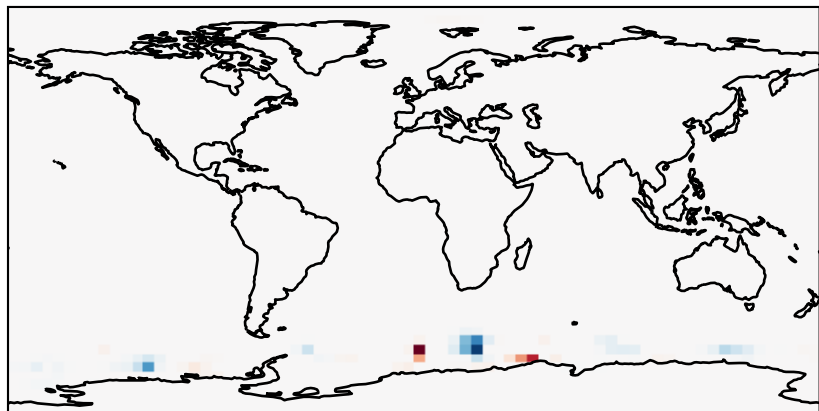
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



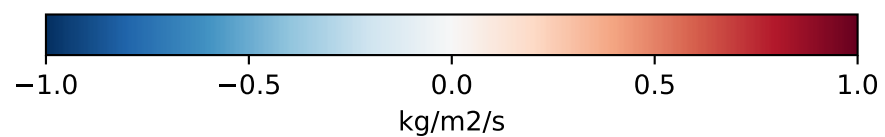
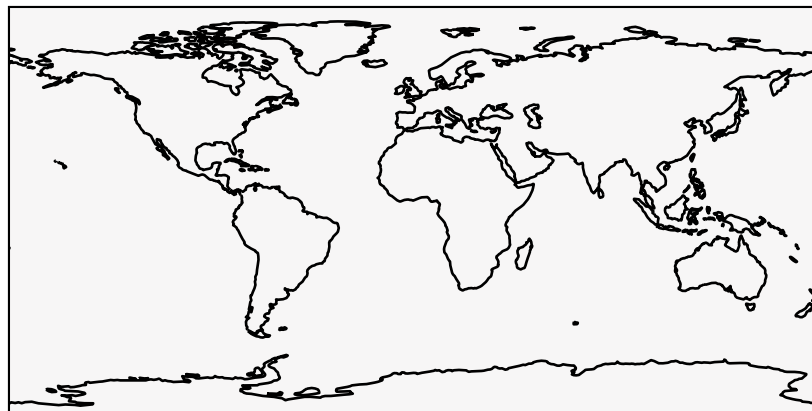
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



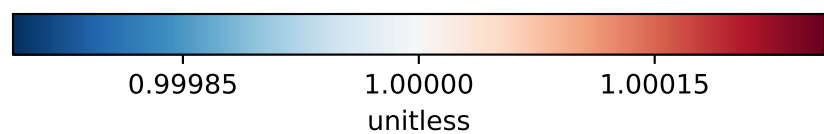
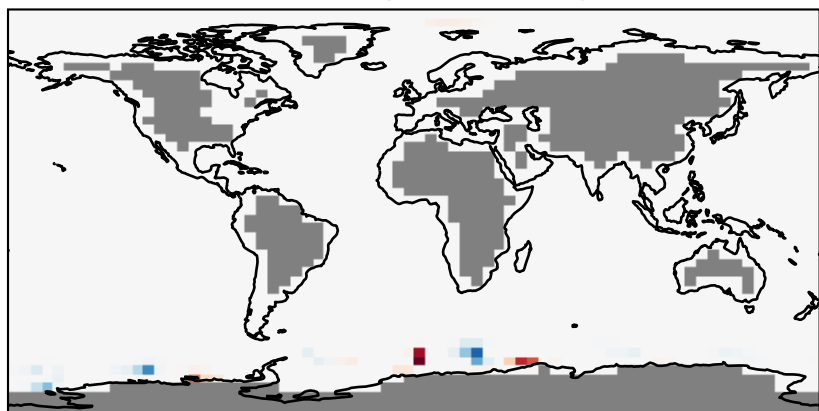
Difference  
Dev - Ref, Dynamic Range



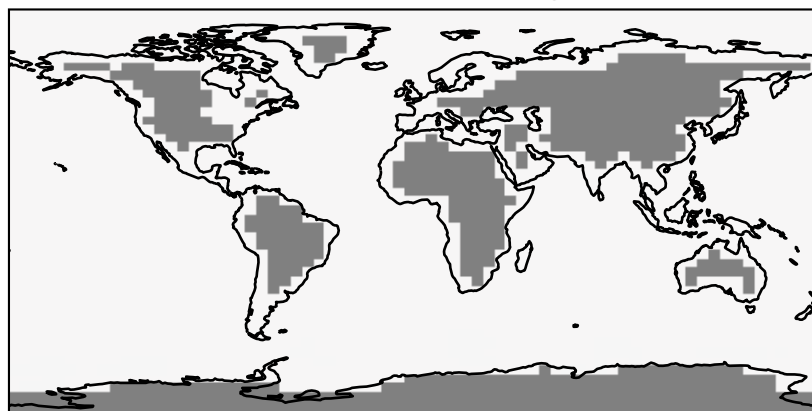
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

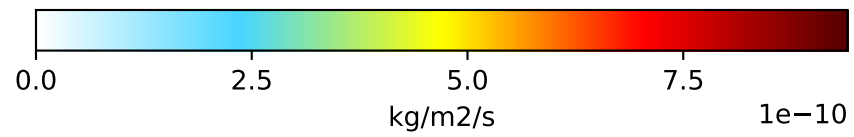
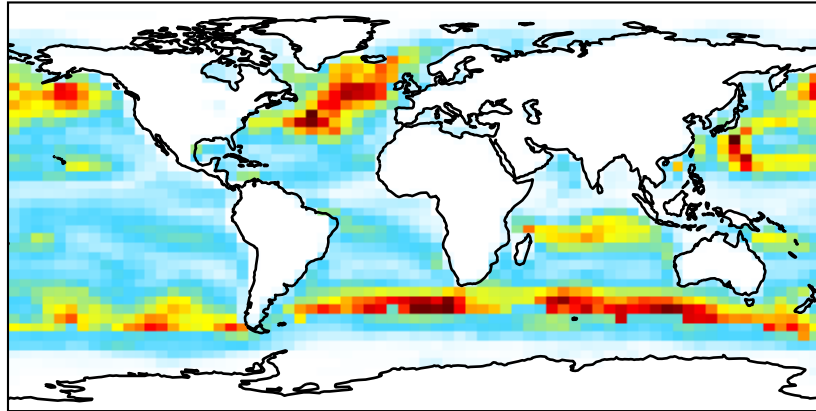


Ratio  
Dev/Ref, Fixed Range

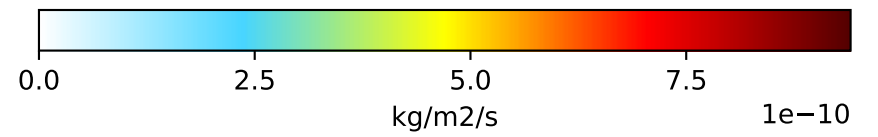
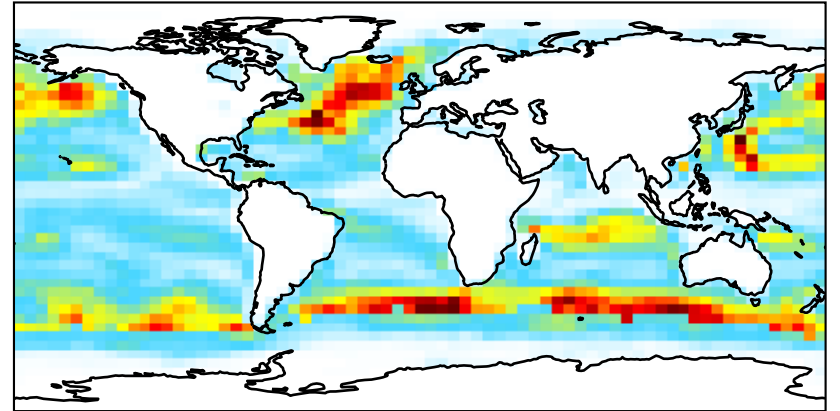


# EmisSALCAL\_Natural (Oct2019)

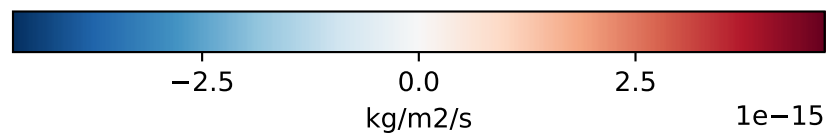
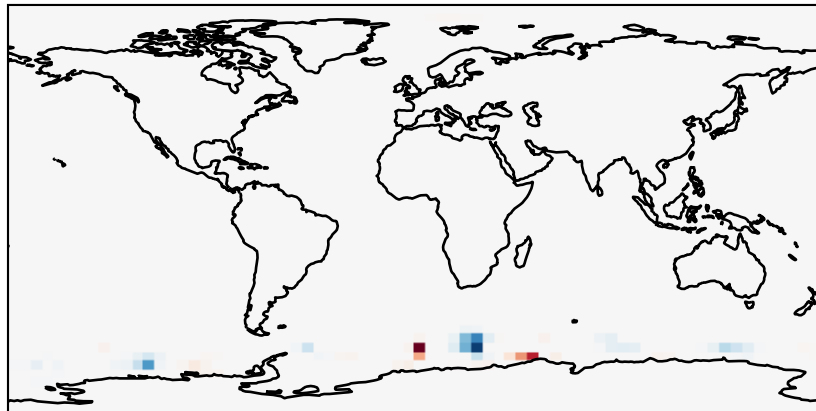
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



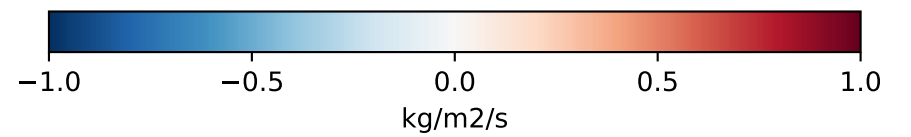
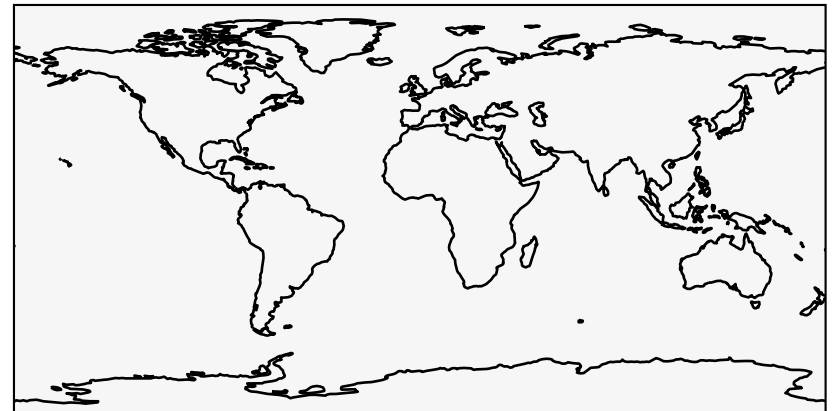
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



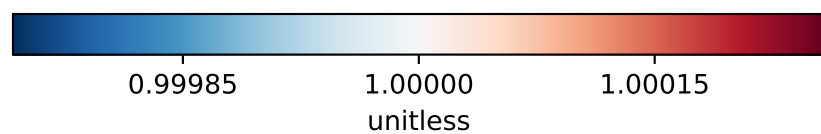
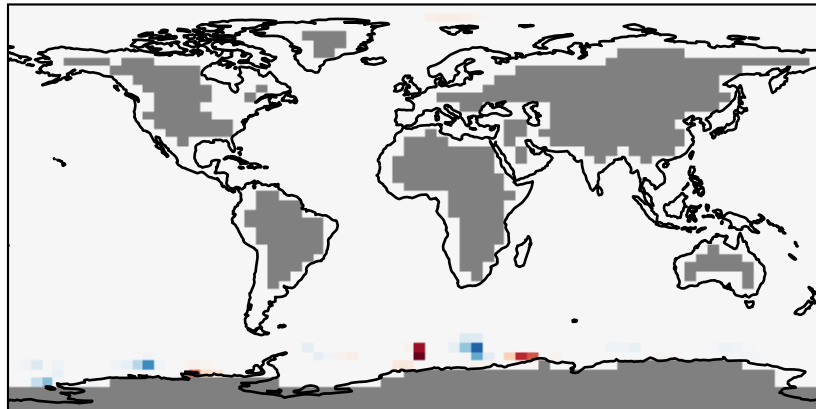
Difference  
Dev - Ref, Dynamic Range



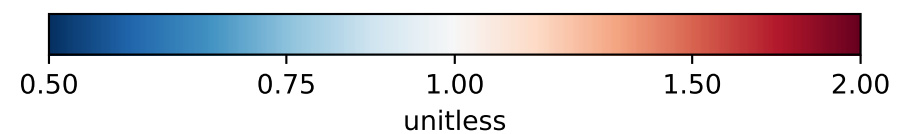
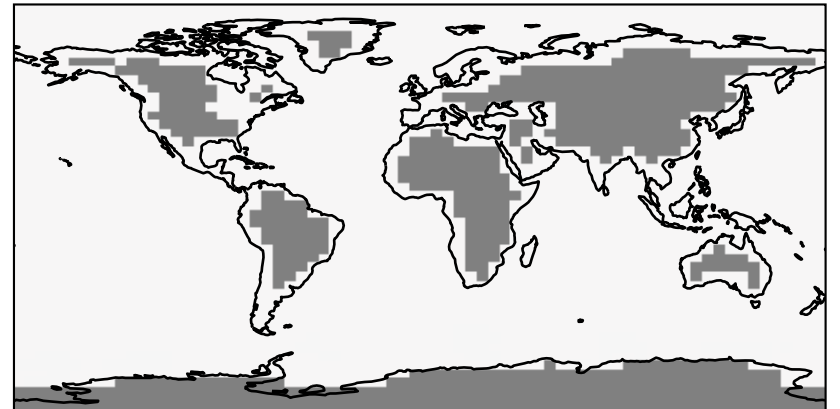
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

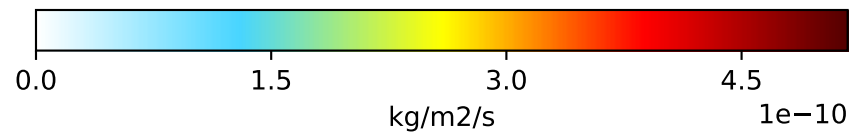
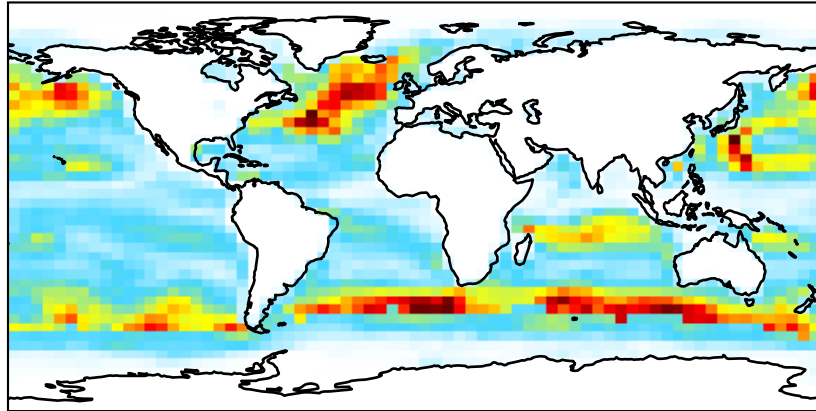


Ratio  
Dev/Ref, Fixed Range

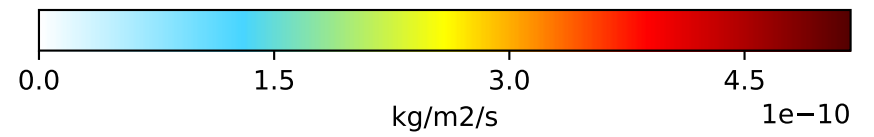
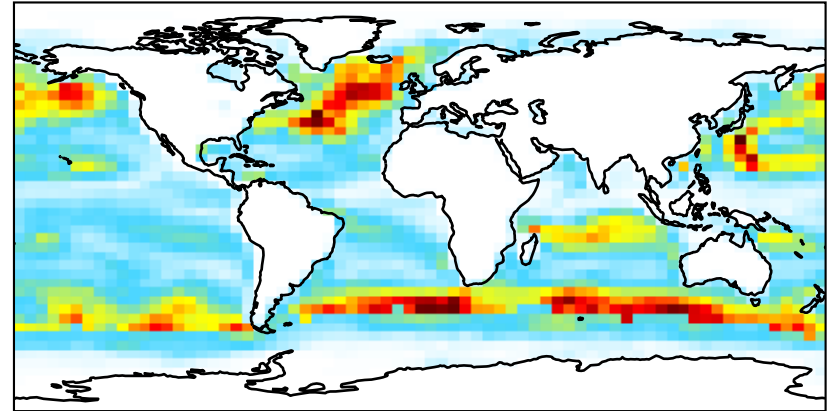


# EmisSALCCL\_Natural (Oct2019)

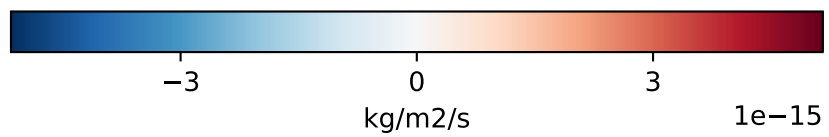
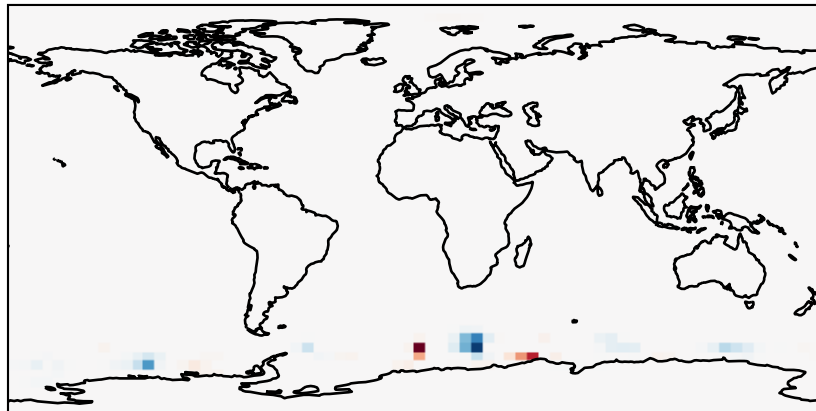
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



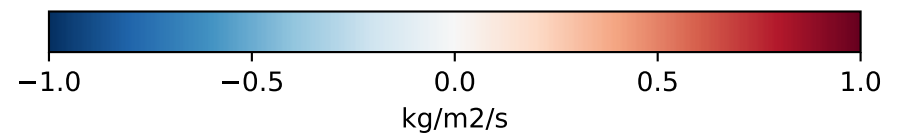
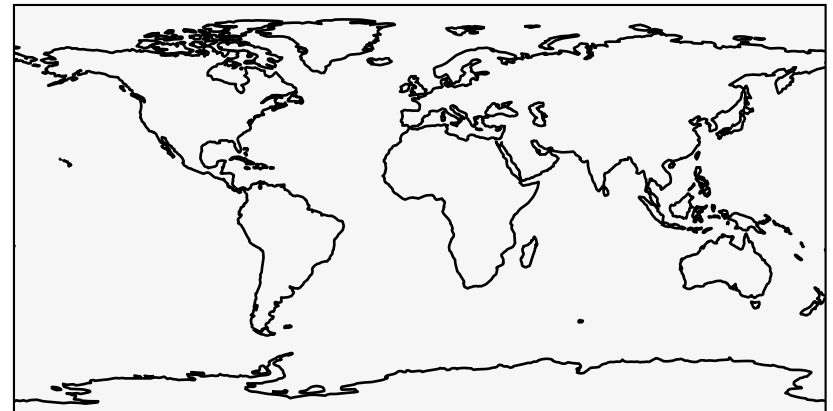
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



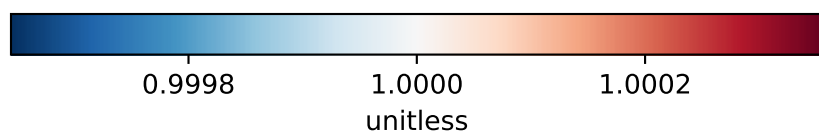
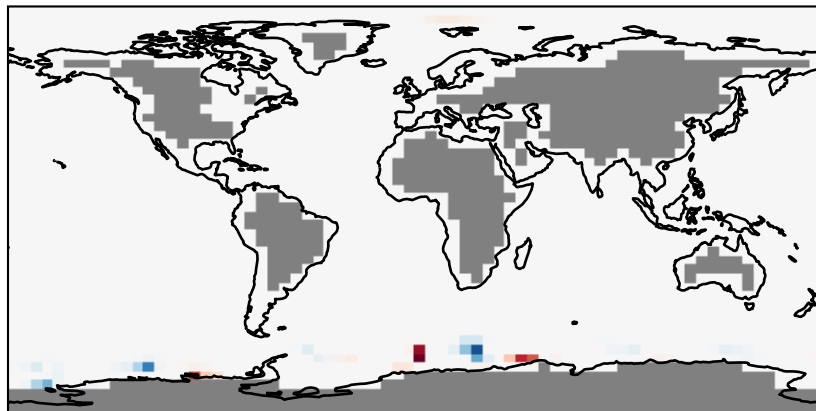
Difference  
Dev - Ref, Dynamic Range



Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

