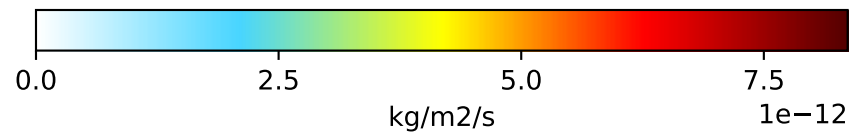
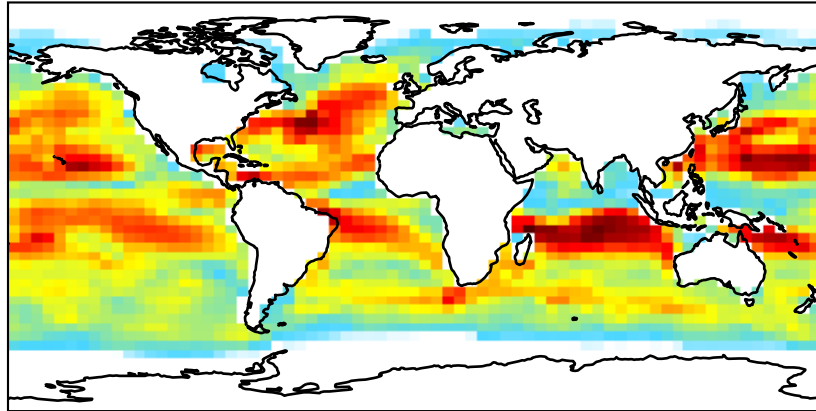
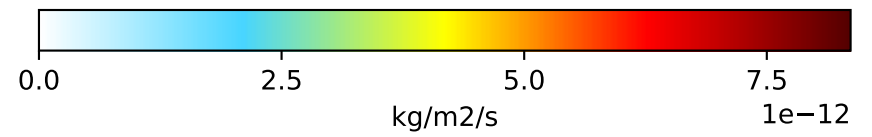
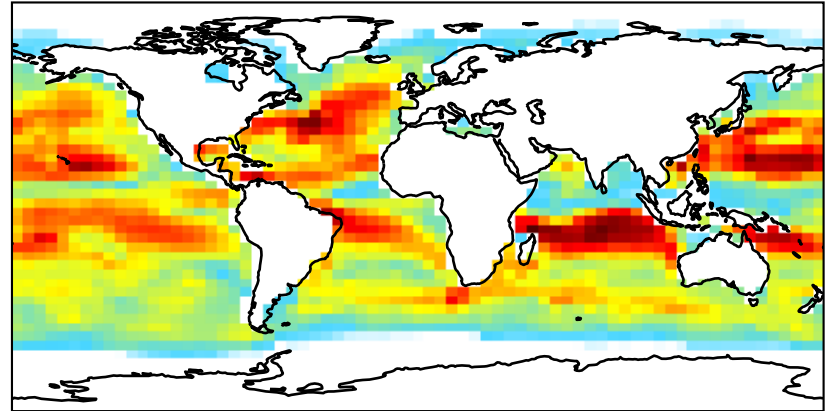


EmisACET_Ocean (Oct2019)

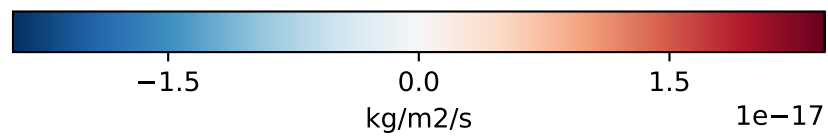
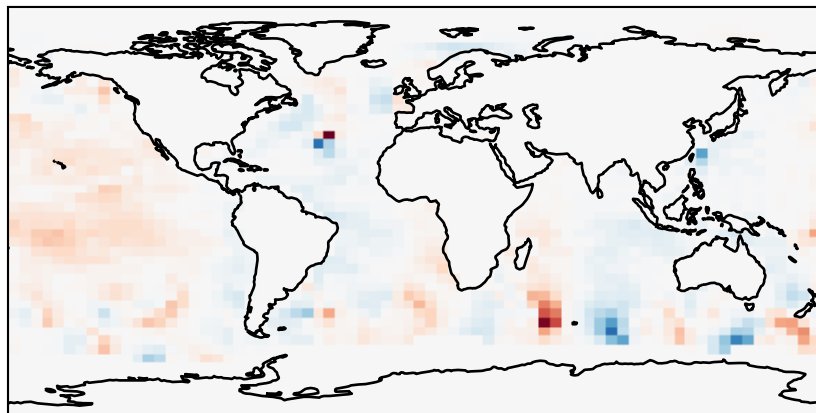
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



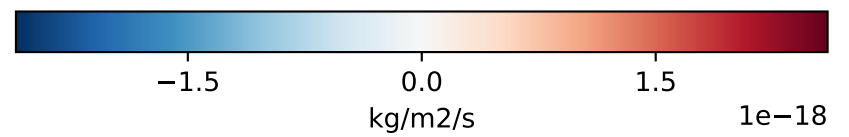
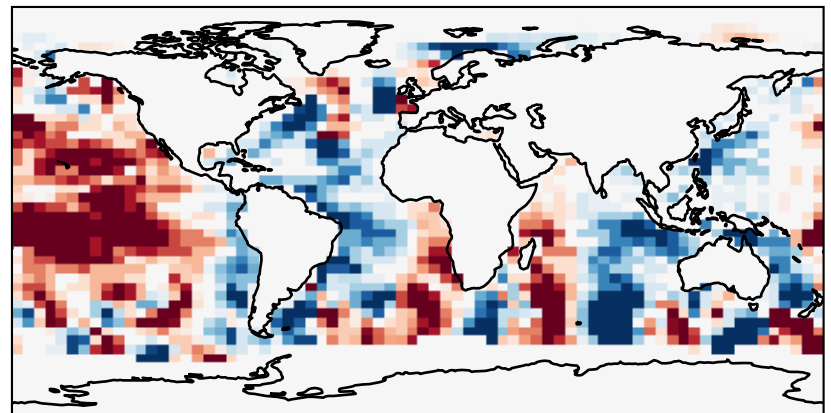
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



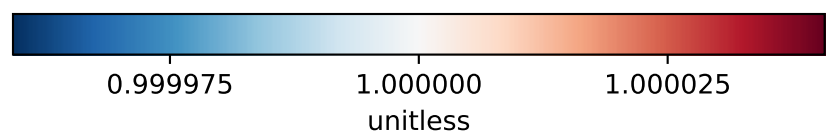
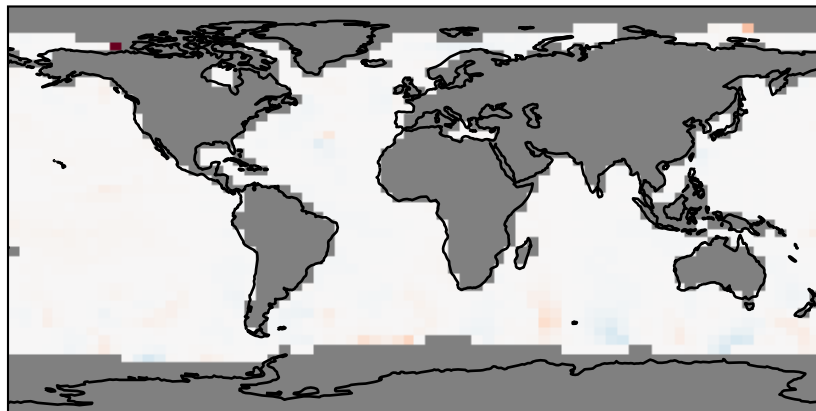
Difference
Dev - Ref, Dynamic Range



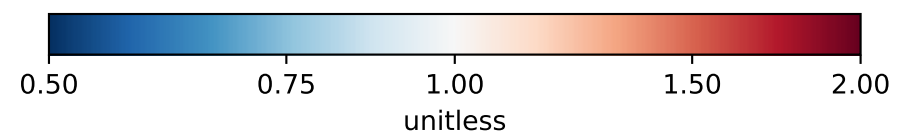
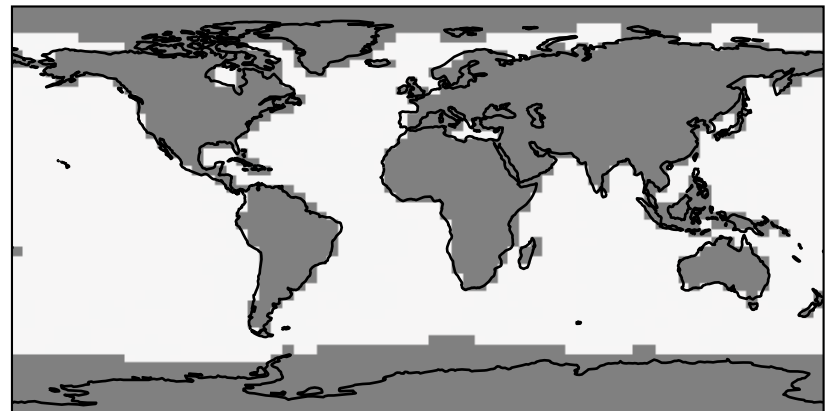
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

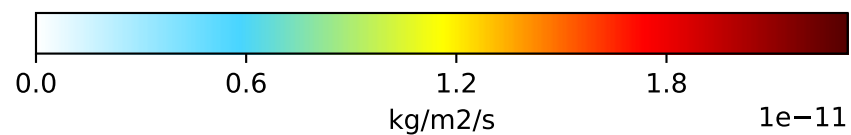
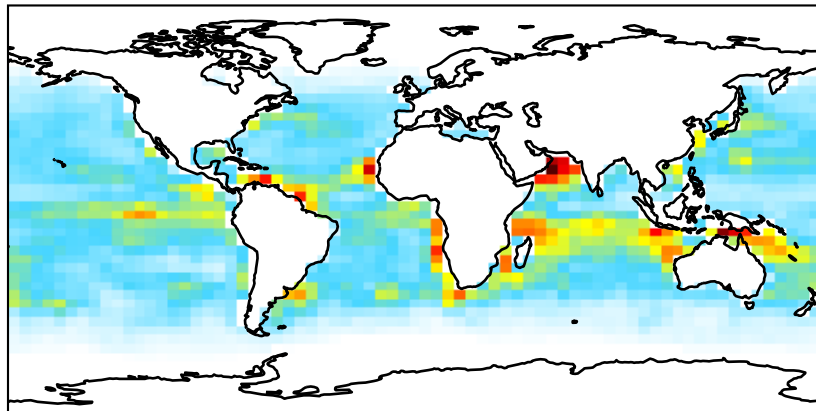


Ratio
Dev/Ref, Fixed Range

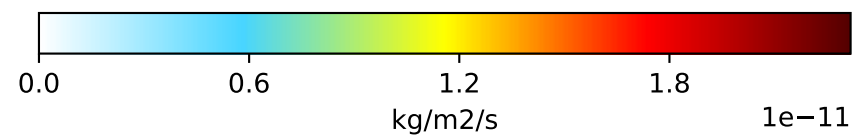
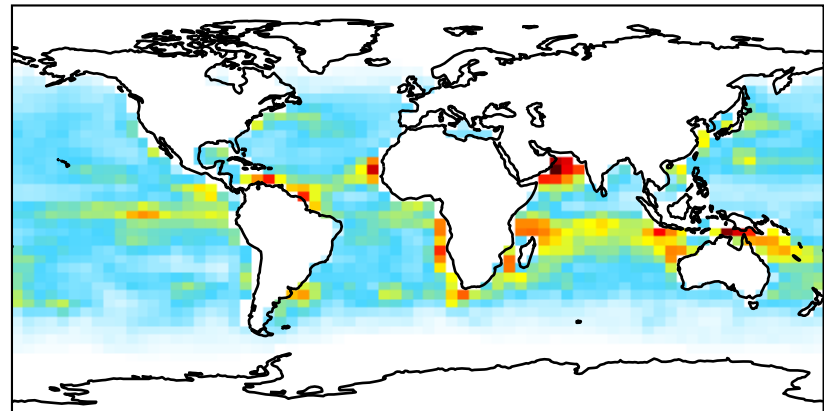


EmisALD2_Ocean (Oct2019)

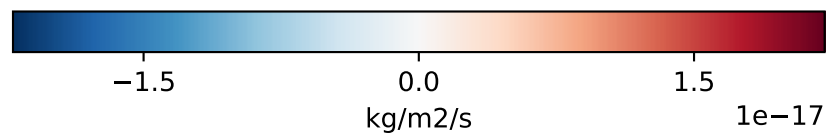
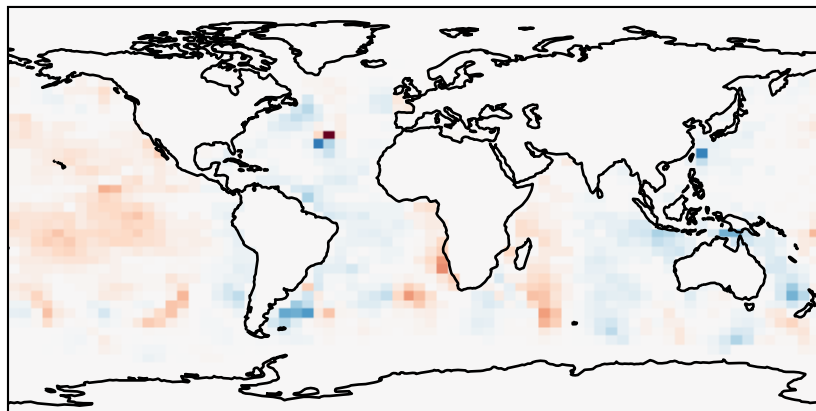
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



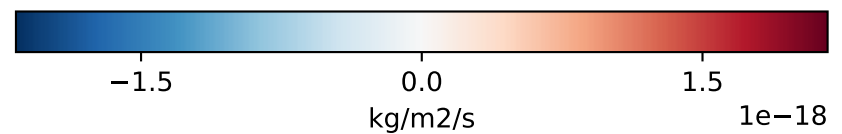
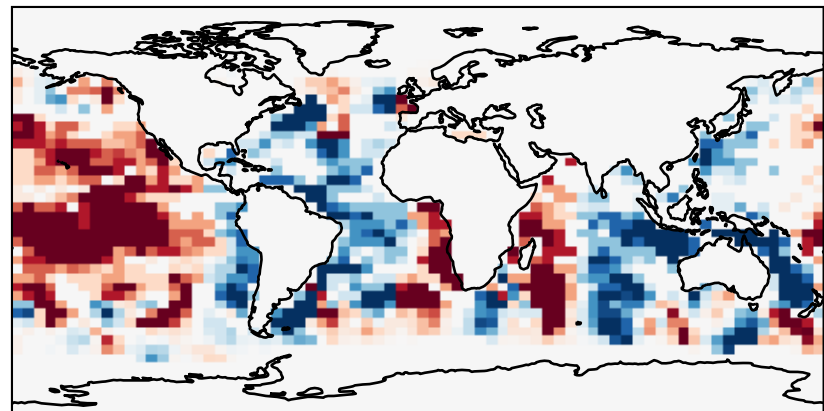
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



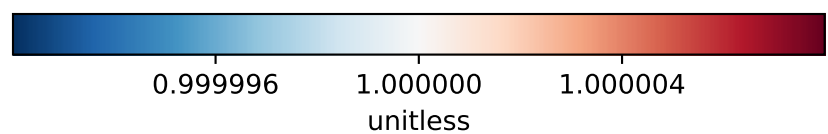
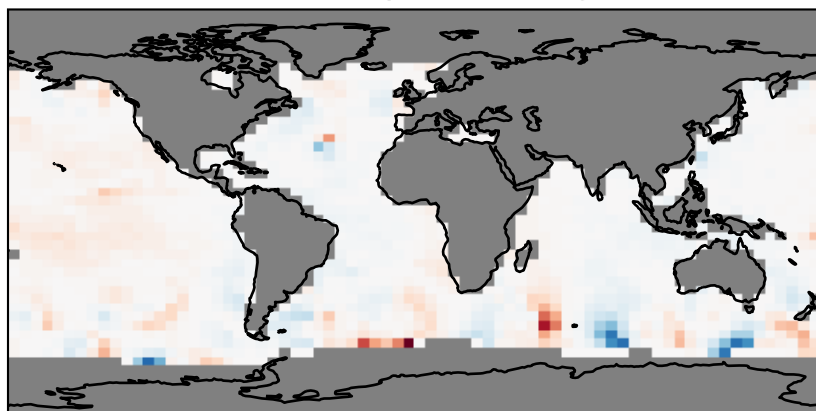
Difference
Dev - Ref, Dynamic Range



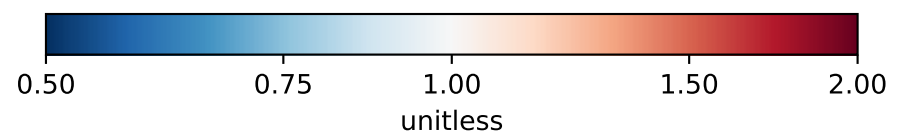
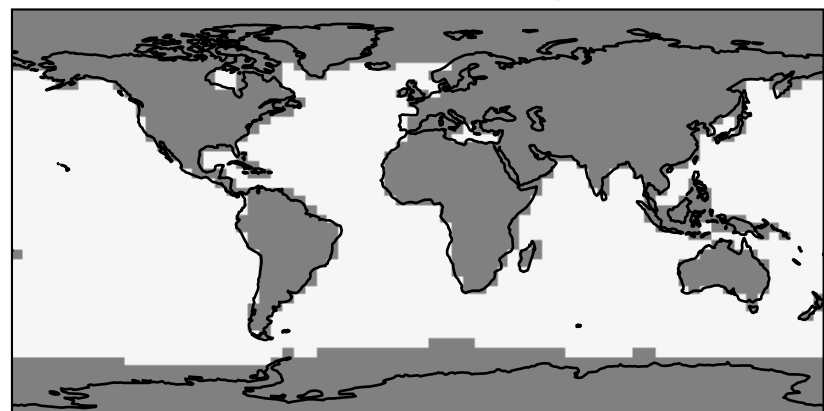
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

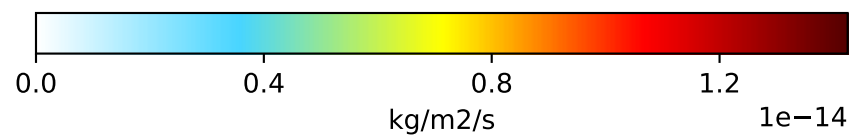
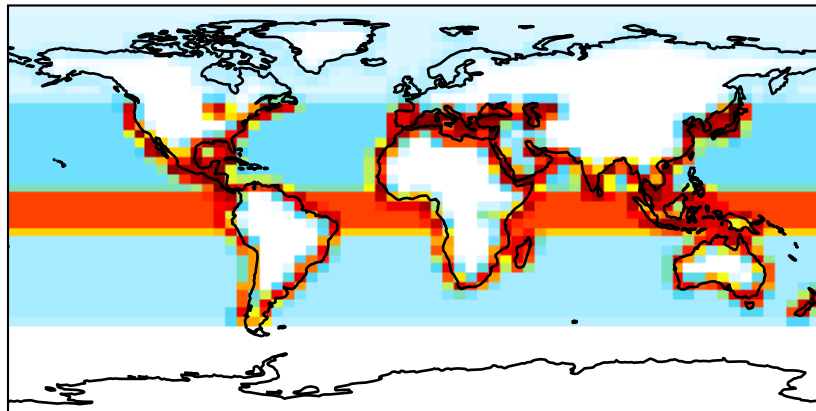


Ratio
Dev/Ref, Fixed Range

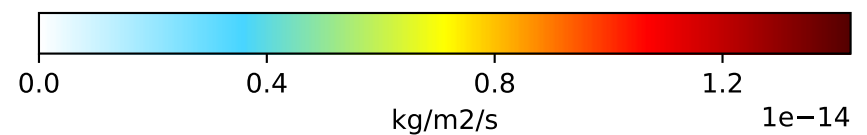
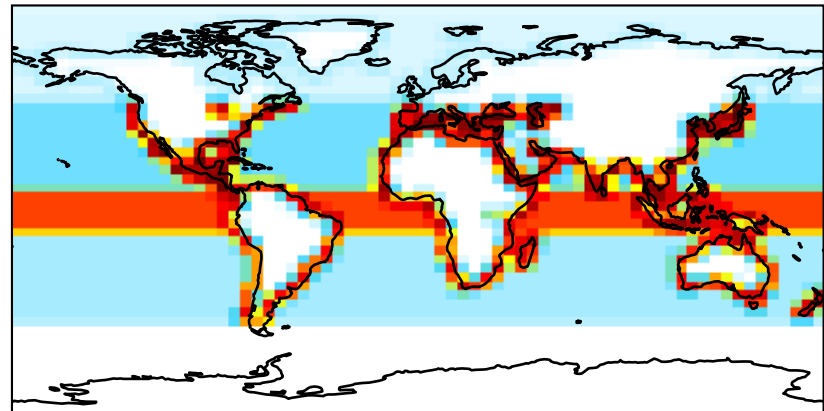


EmisCH2Br2_Ocean (Oct2019)

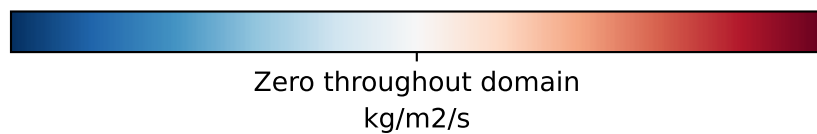
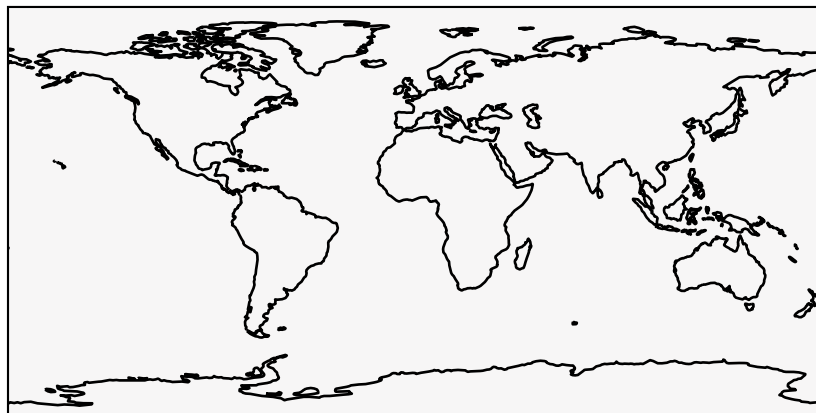
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



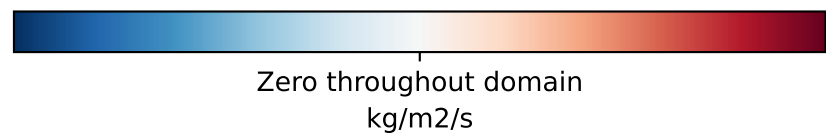
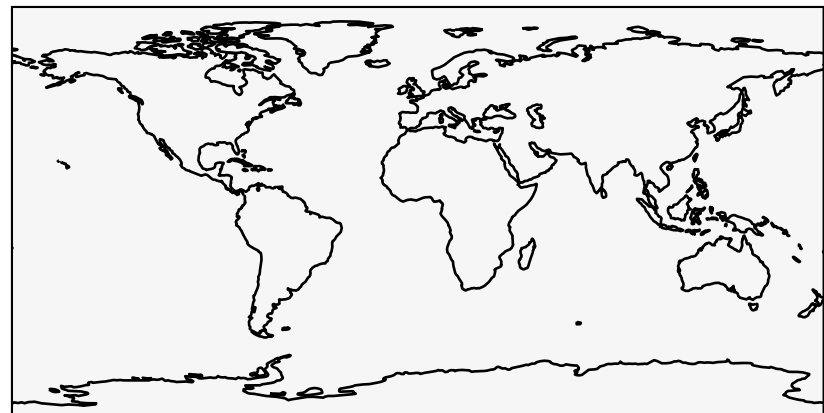
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



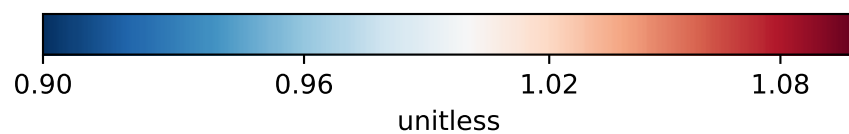
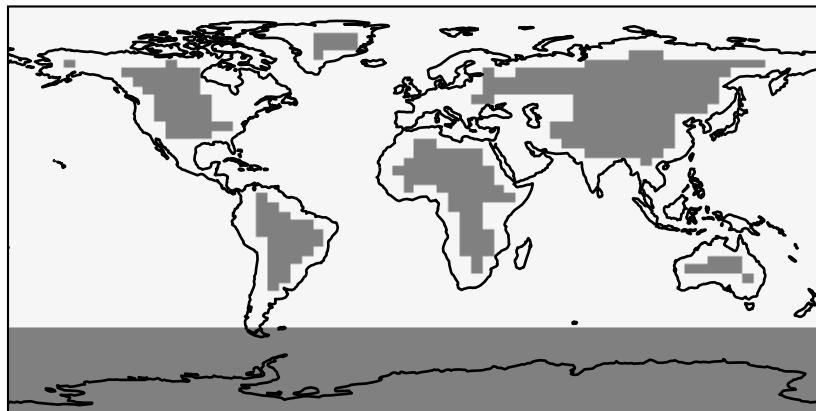
Difference
Dev - Ref, Dynamic Range



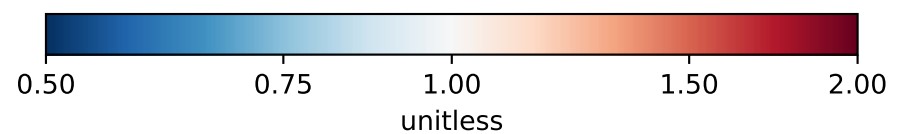
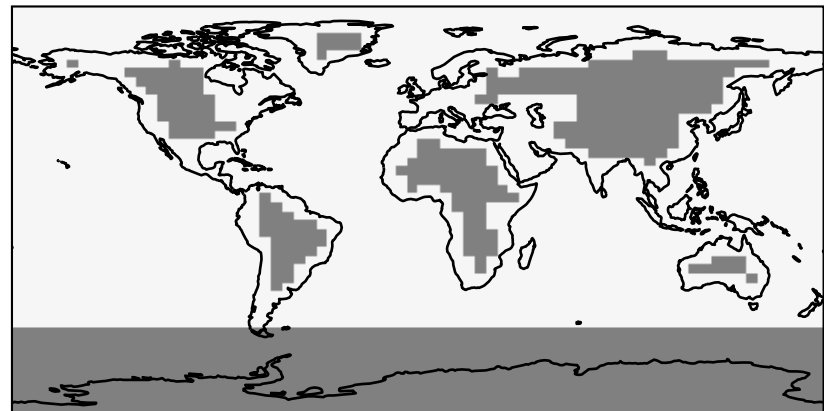
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

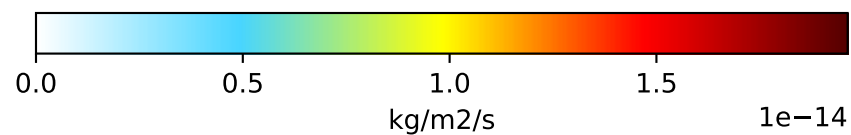
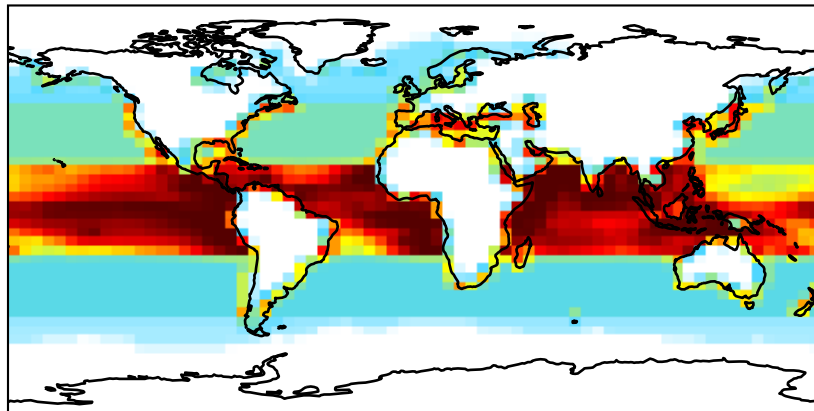


Ratio
Dev/Ref, Fixed Range

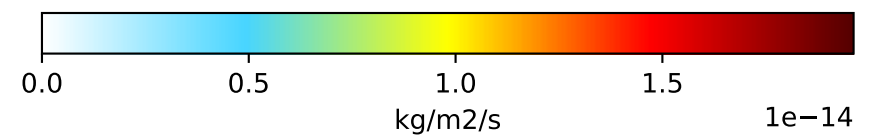
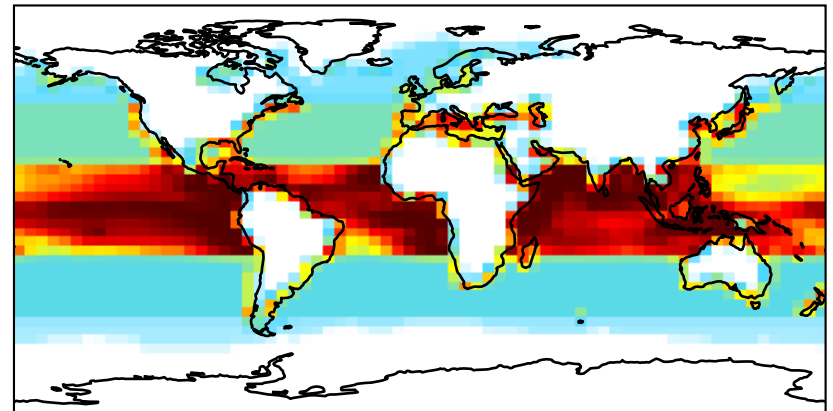


EmisCH2I2_Ocean (Oct2019)

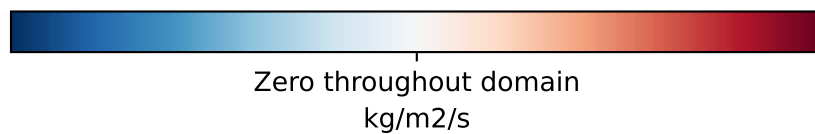
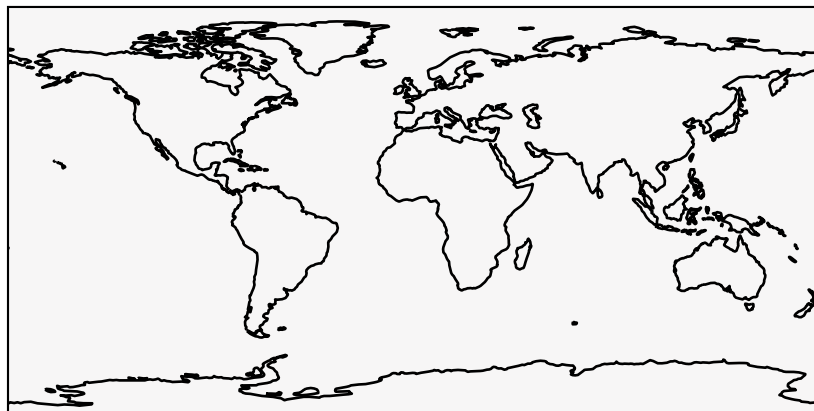
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



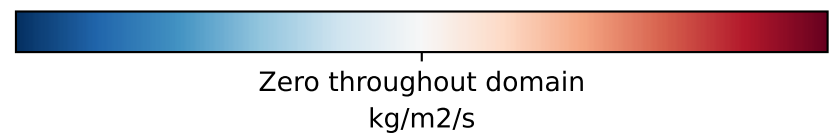
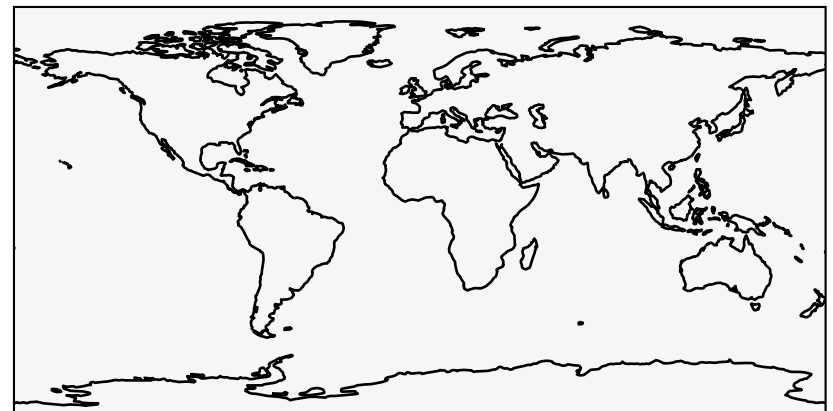
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



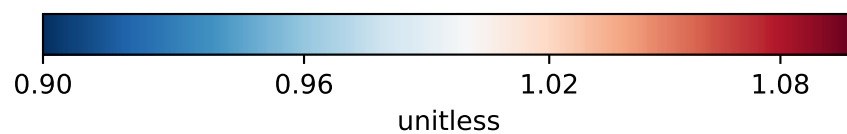
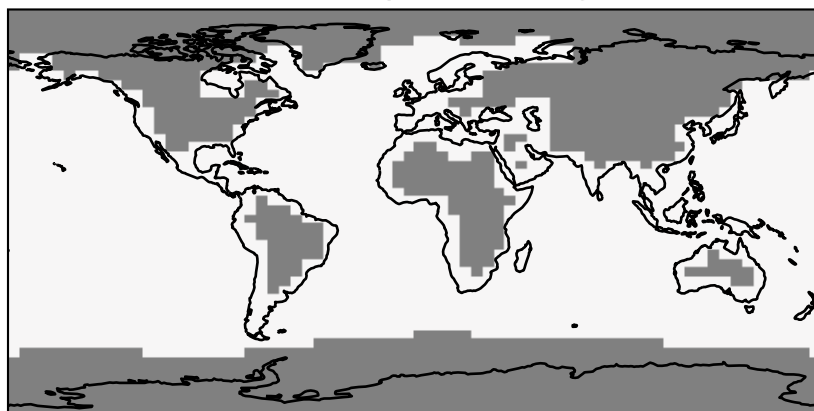
Difference
Dev - Ref, Dynamic Range



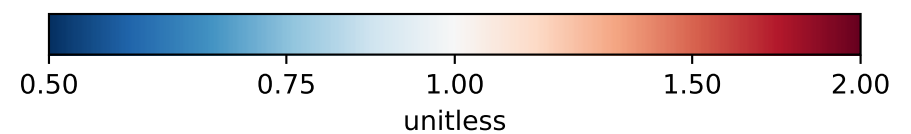
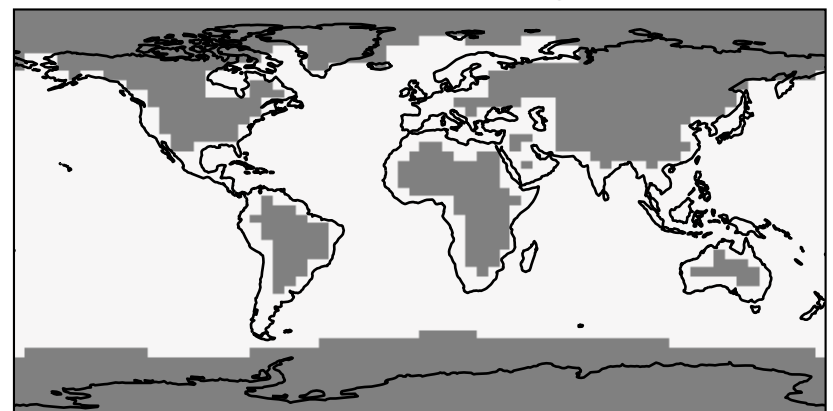
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

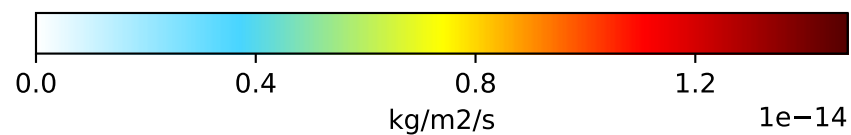
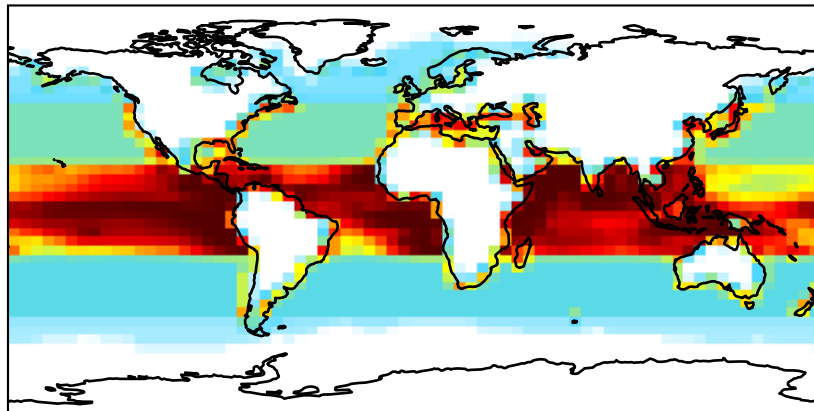


Ratio
Dev/Ref, Fixed Range

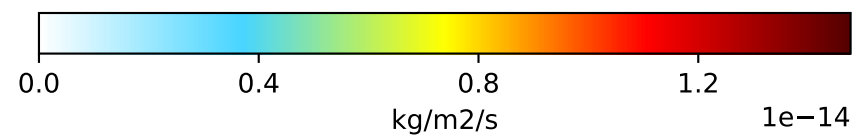
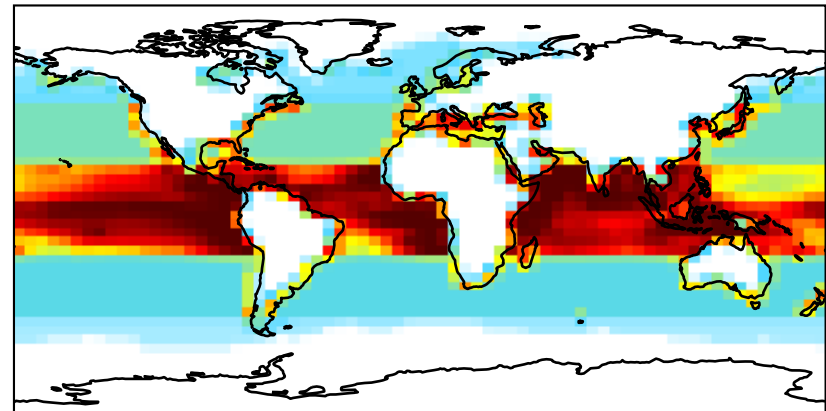


EmisCH2IBr_Ocean (Oct2019)

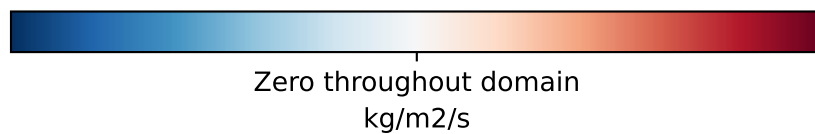
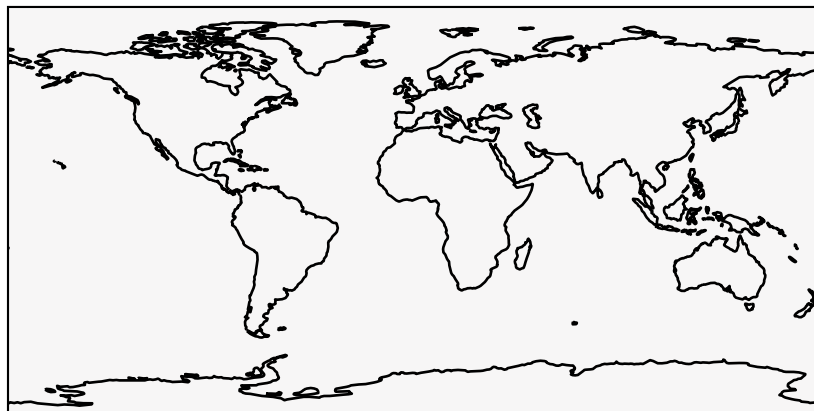
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



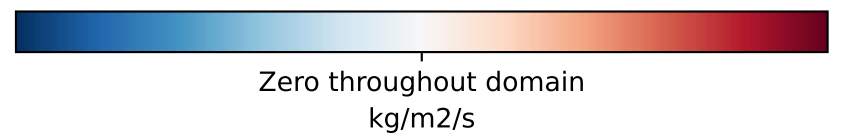
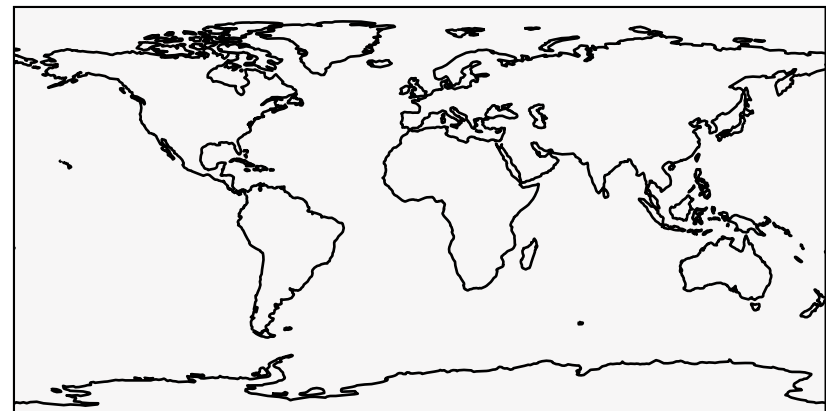
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



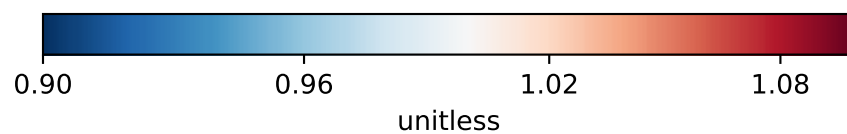
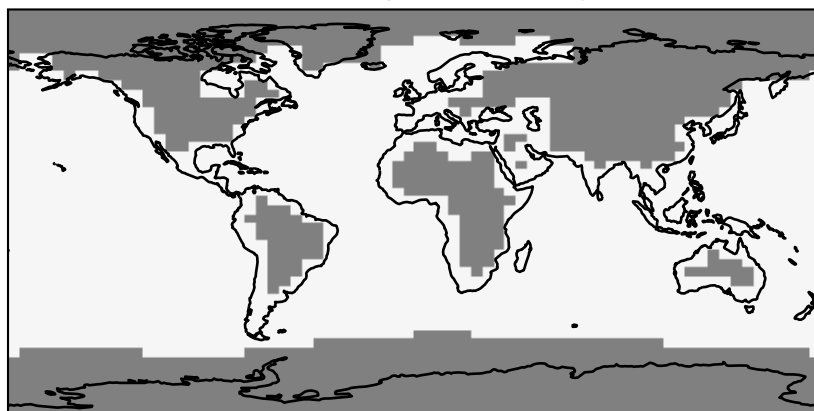
Difference
Dev - Ref, Dynamic Range



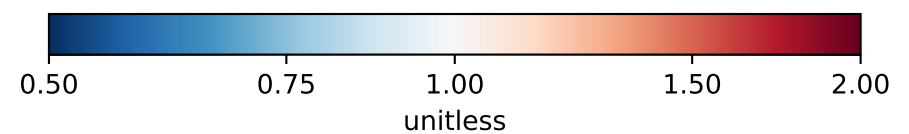
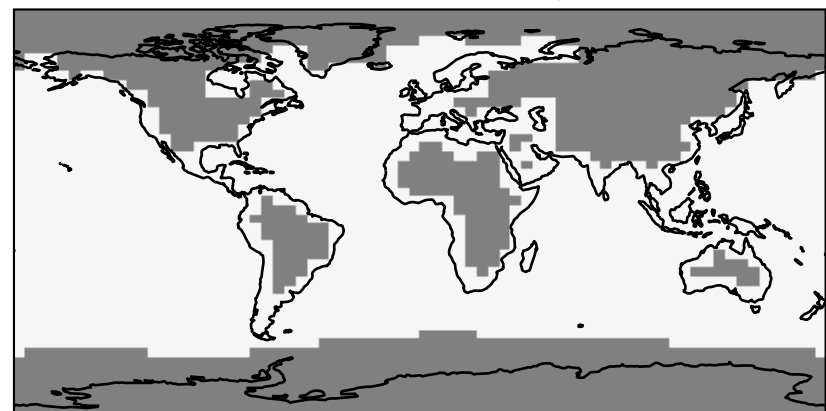
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

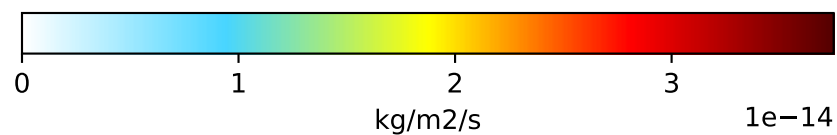
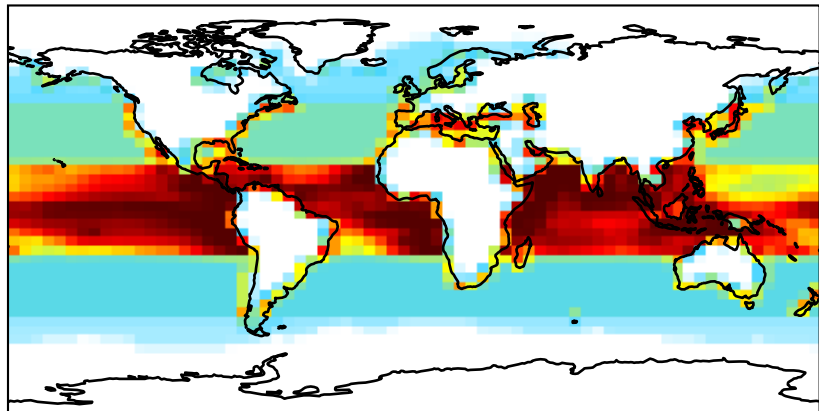


Ratio
Dev/Ref, Fixed Range

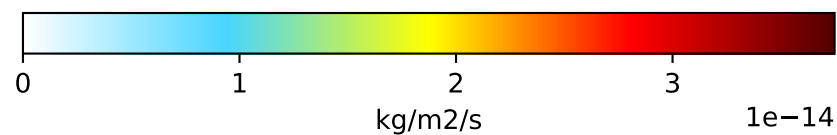
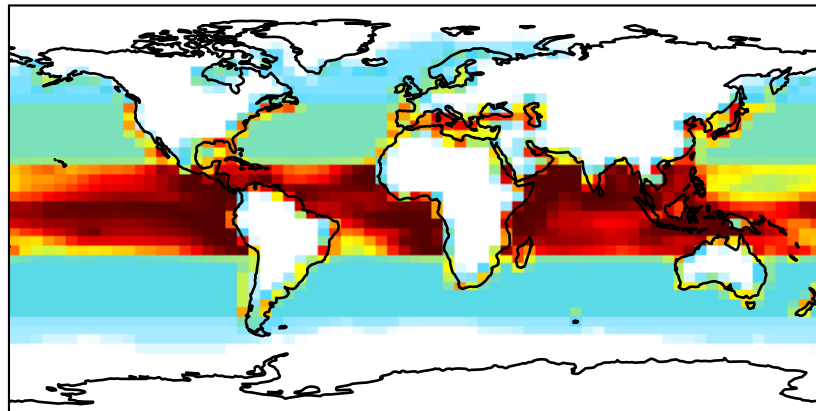


EmisCH2ICl_Ocean (Oct2019)

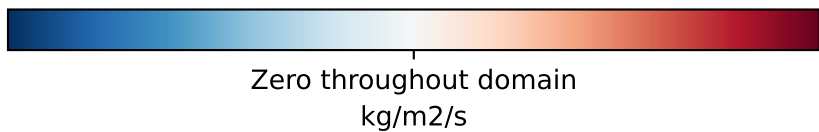
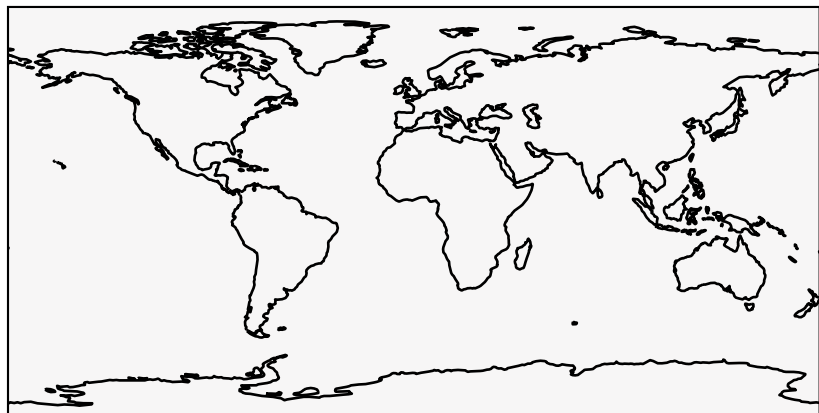
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



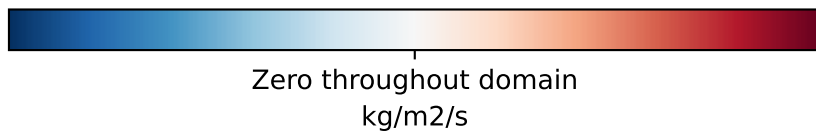
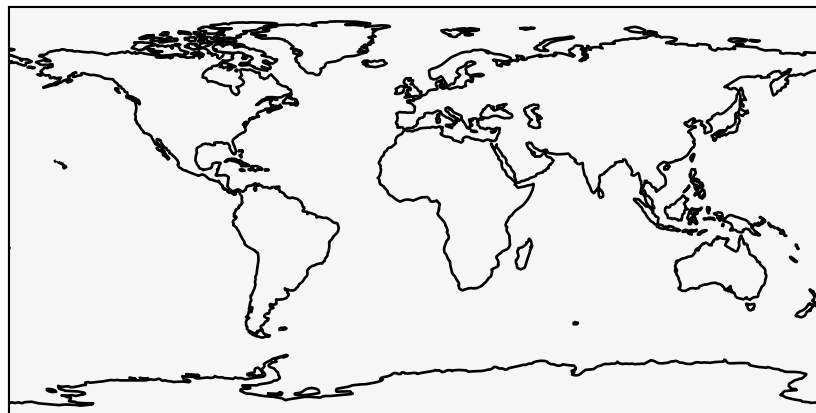
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



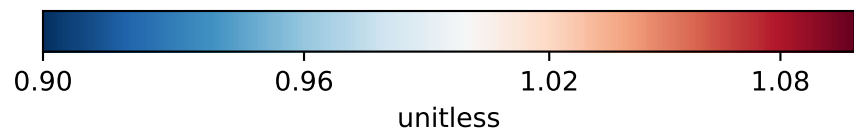
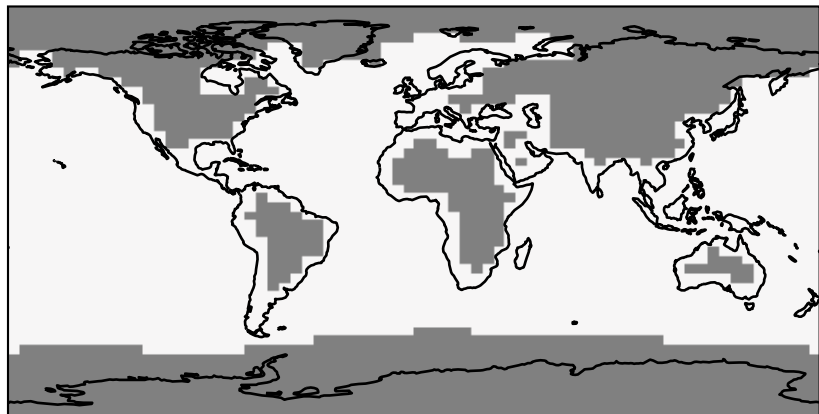
Difference
Dev - Ref, Dynamic Range



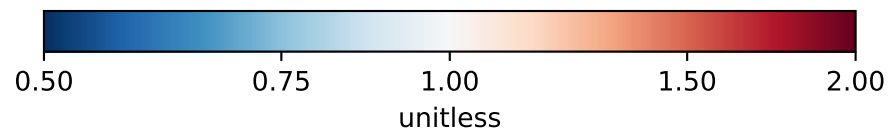
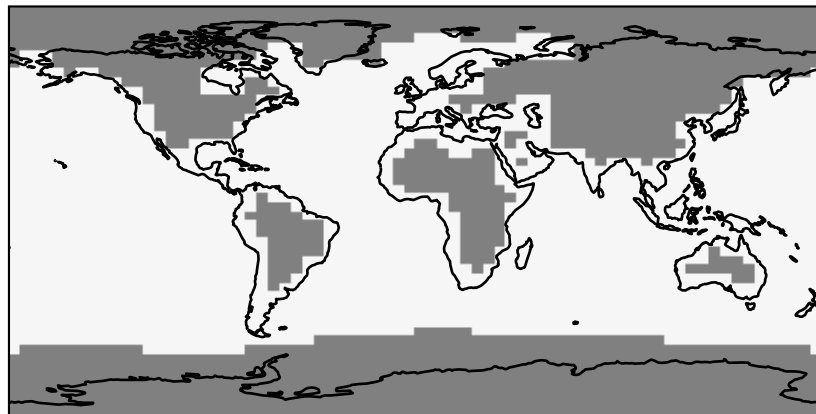
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

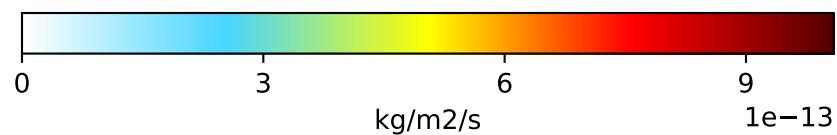
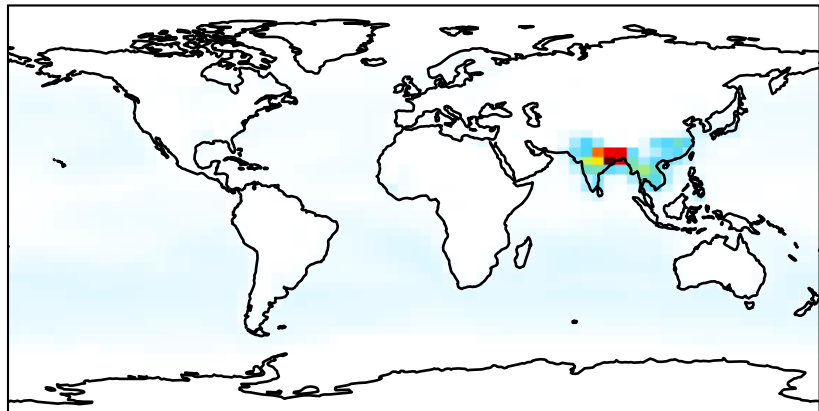


Ratio
Dev/Ref, Fixed Range

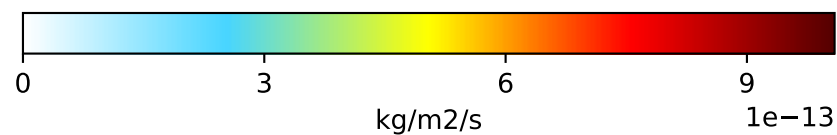
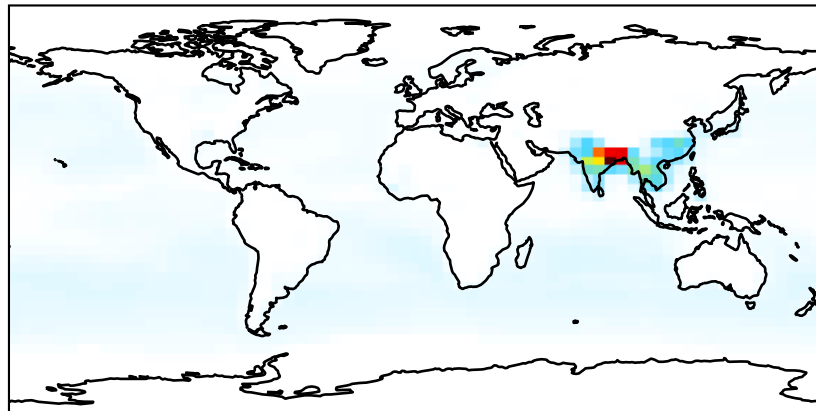


EmisCH3I_Ocean (Oct2019)

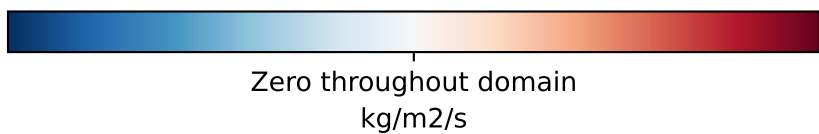
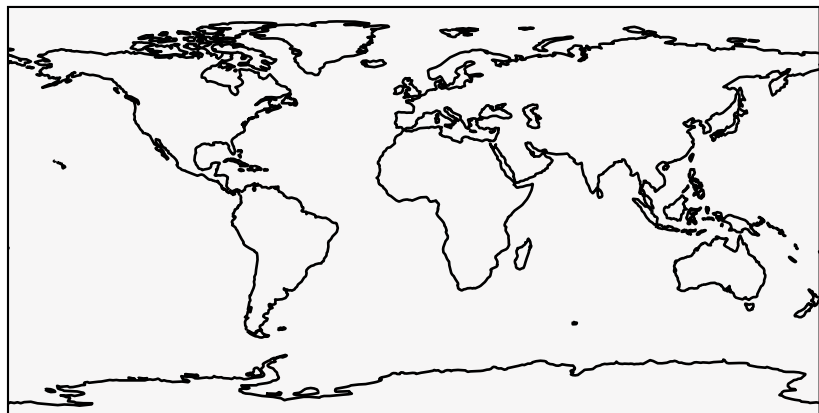
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



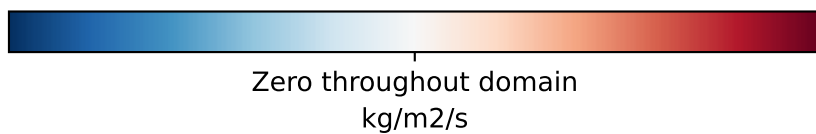
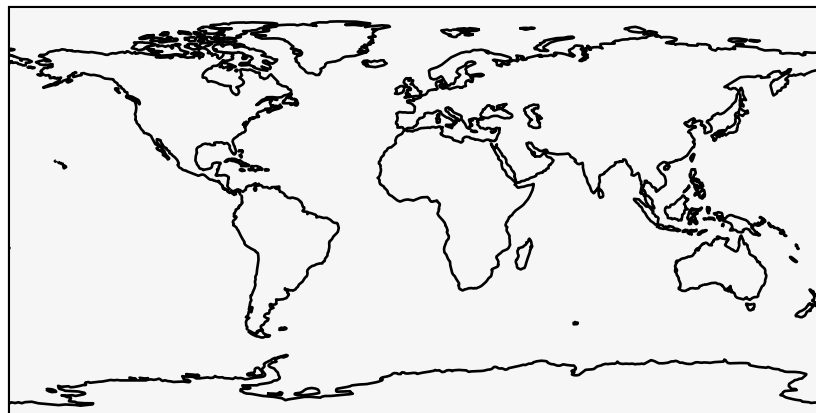
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



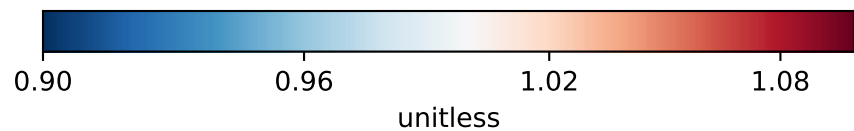
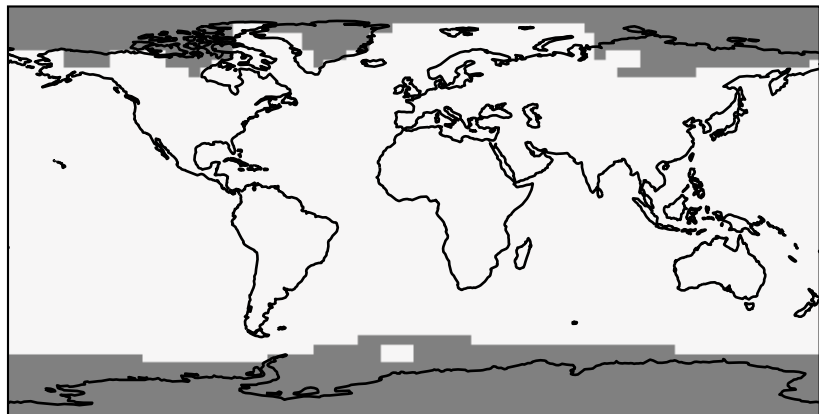
Difference
Dev - Ref, Dynamic Range



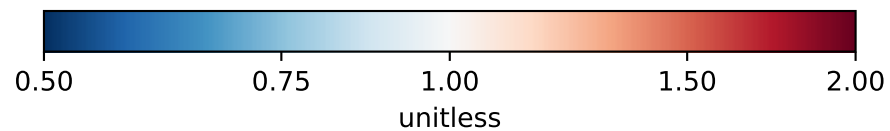
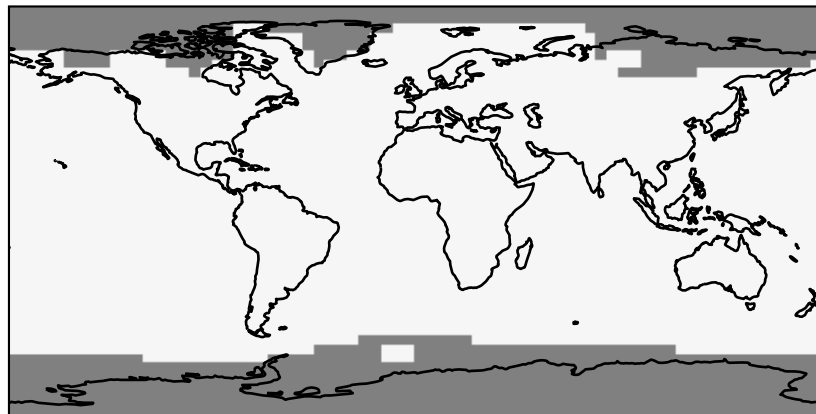
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

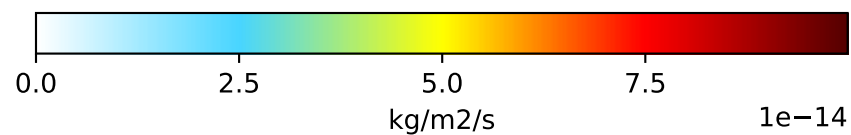
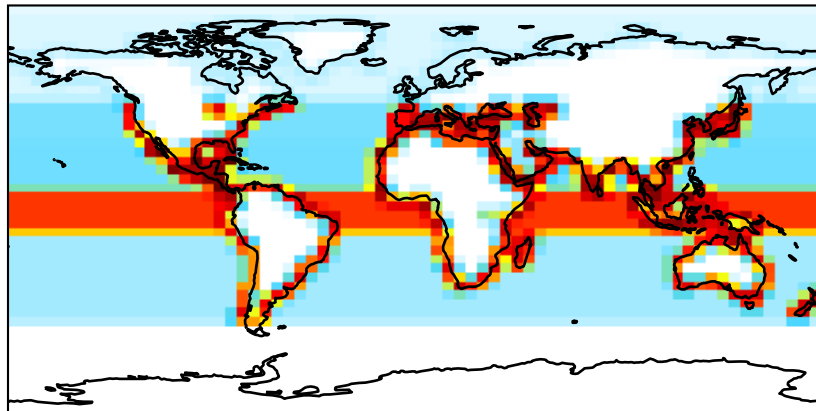


Ratio
Dev/Ref, Fixed Range

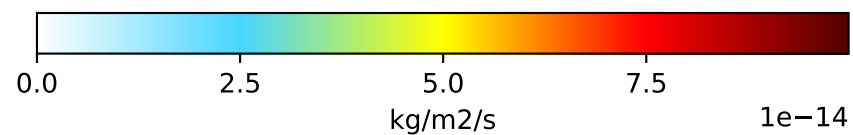
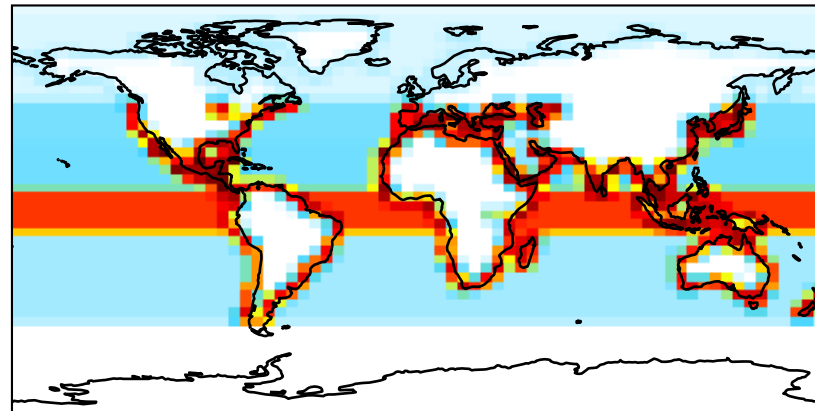


EmisCHBr3_Ocean (Oct2019)

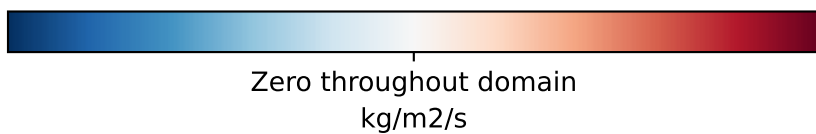
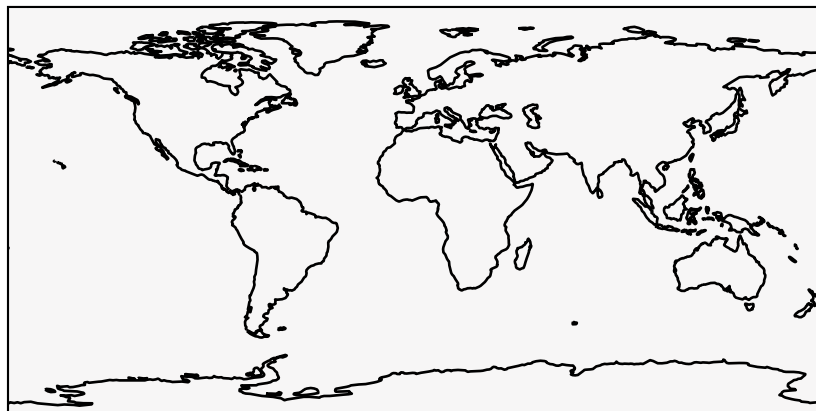
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



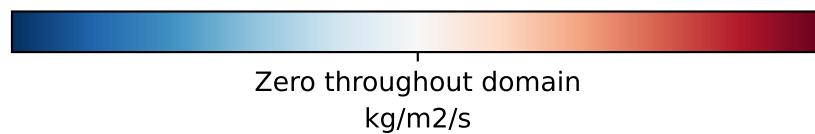
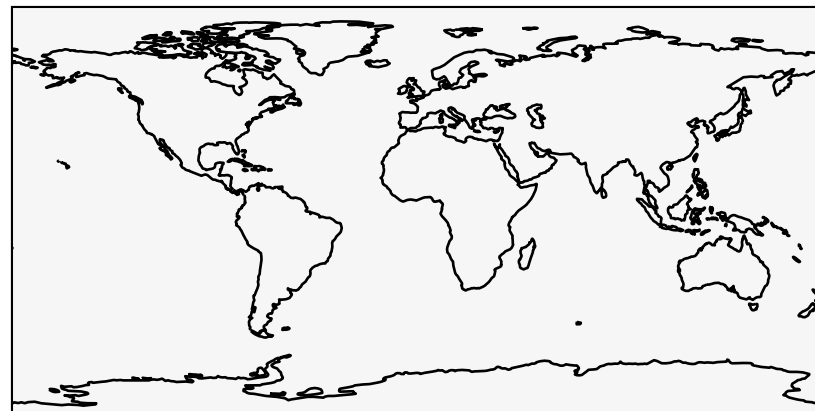
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



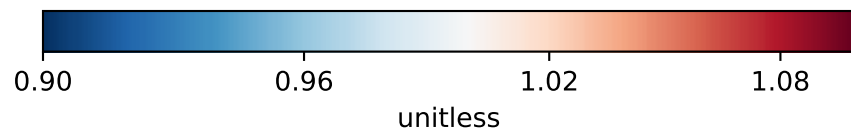
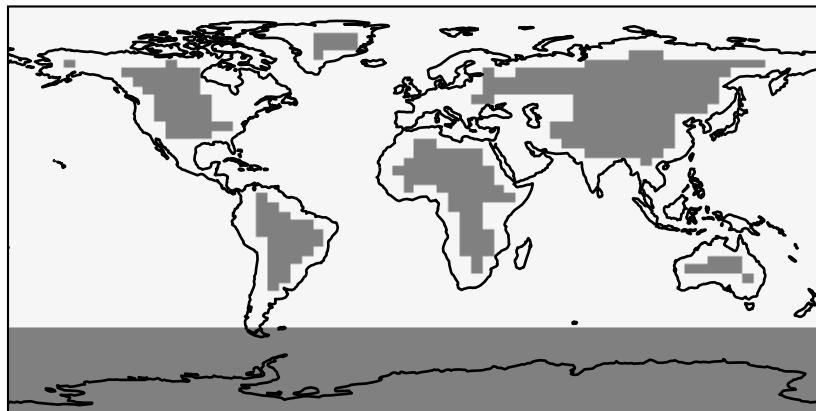
Difference
Dev - Ref, Dynamic Range



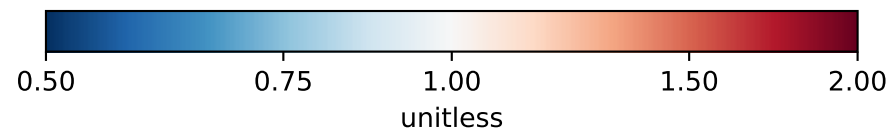
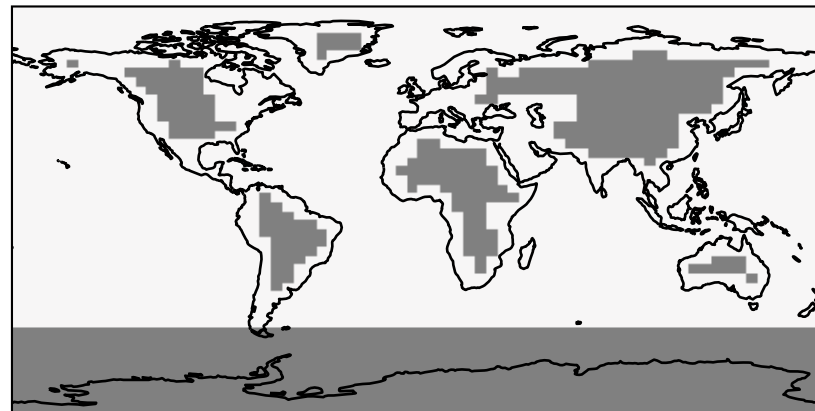
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

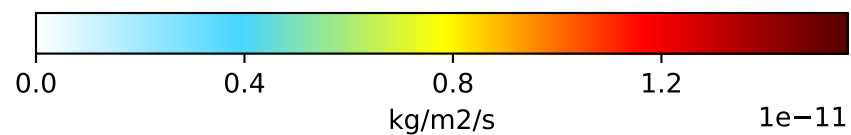
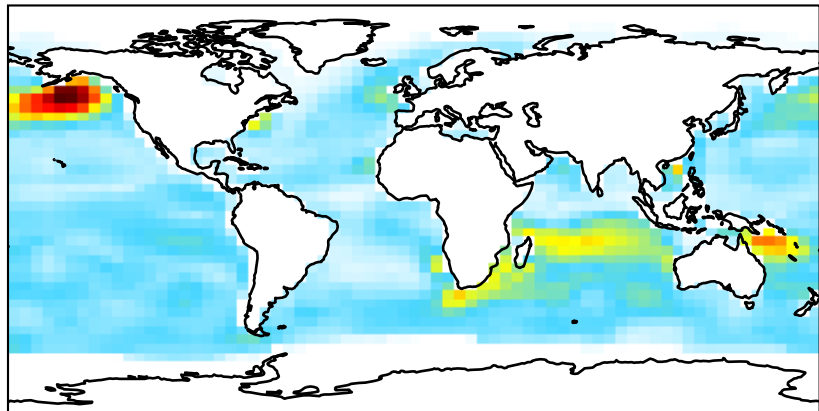


Ratio
Dev/Ref, Fixed Range

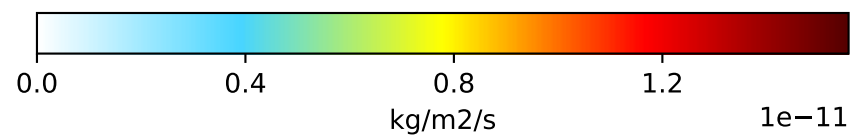
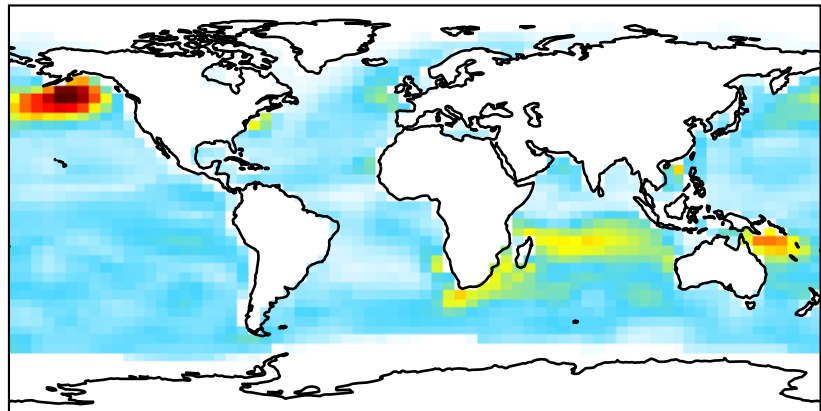


EmisDMS_Ocean (Oct2019)

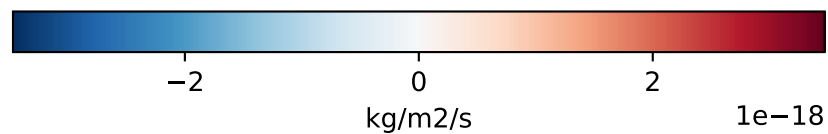
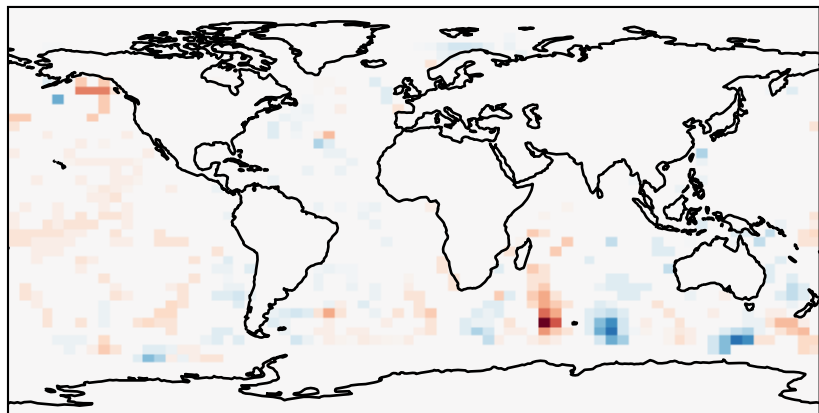
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



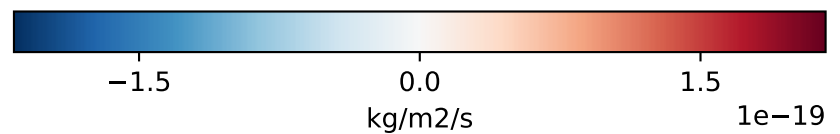
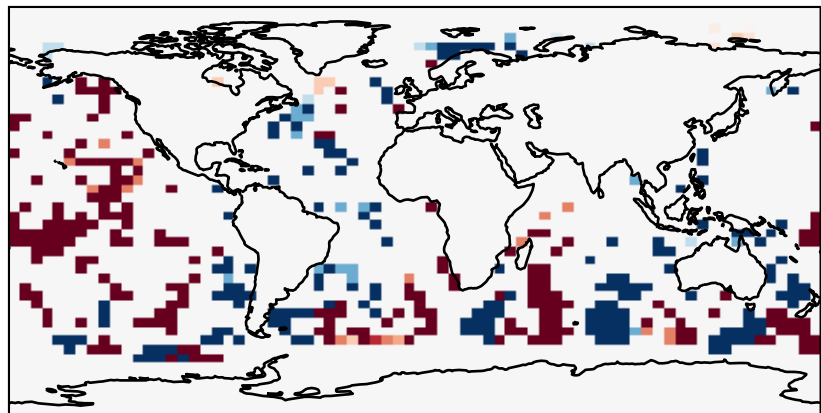
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



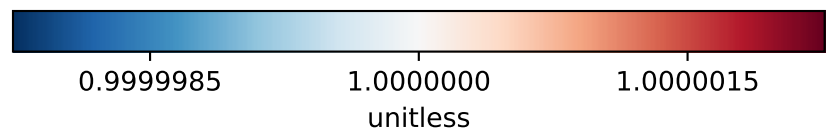
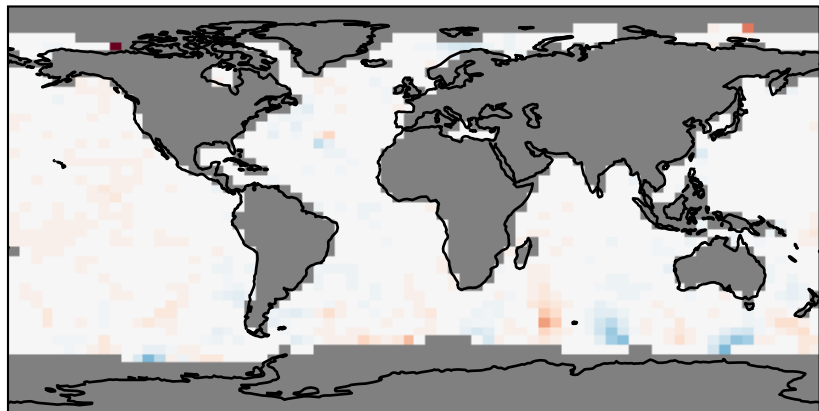
Difference
Dev - Ref, Dynamic Range



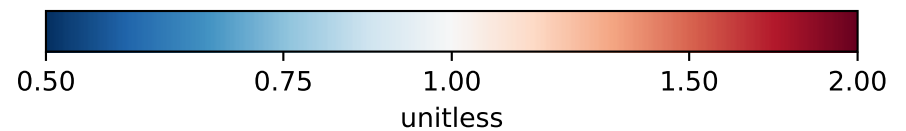
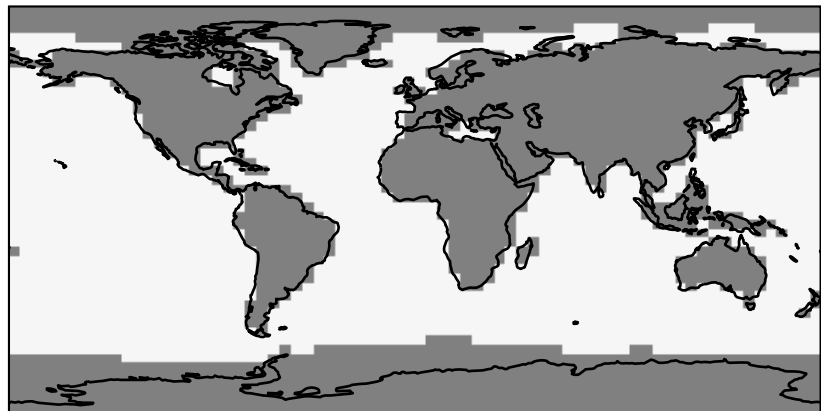
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

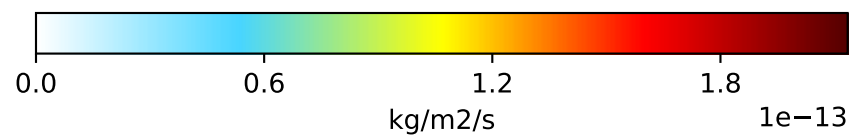
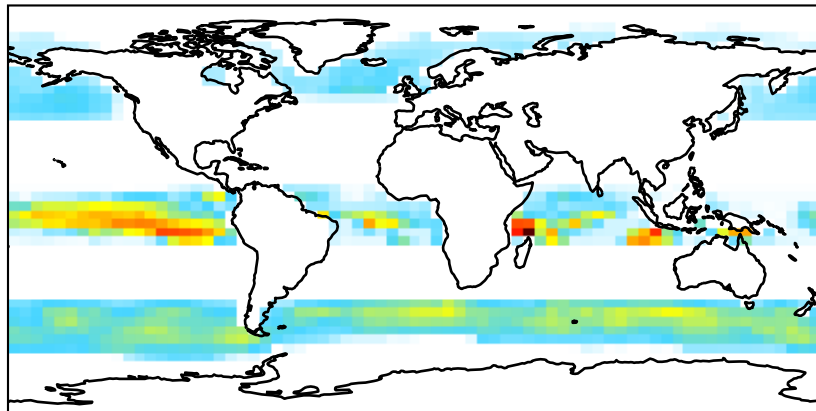


Ratio
Dev/Ref, Fixed Range

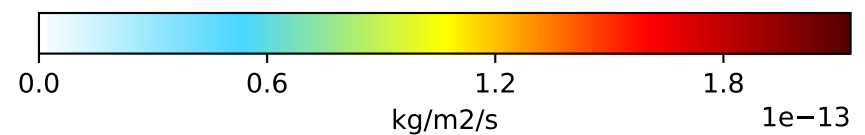
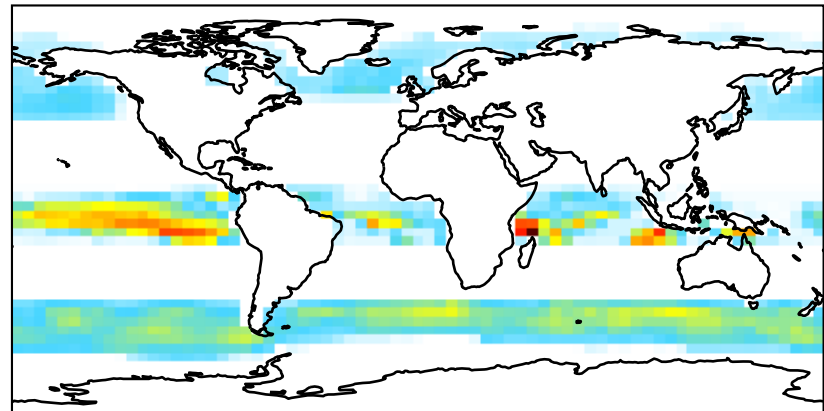


EmisETNO3_Ocean (Oct2019)

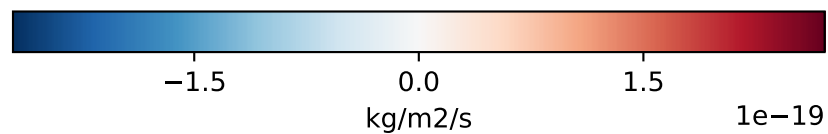
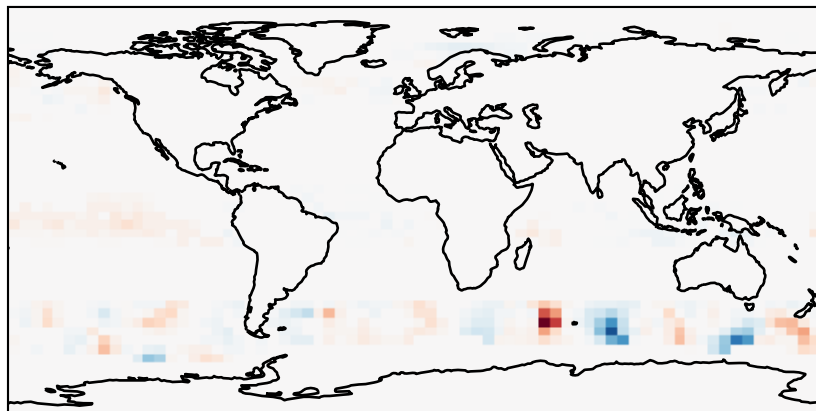
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



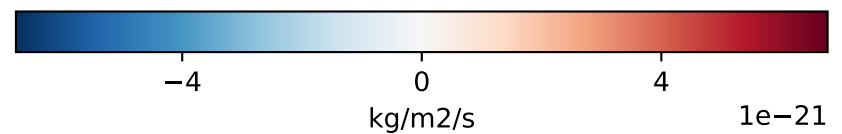
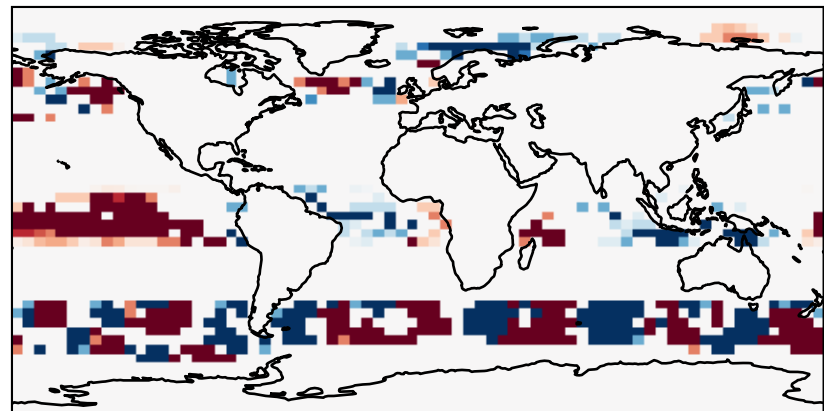
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



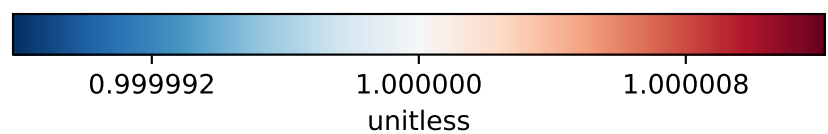
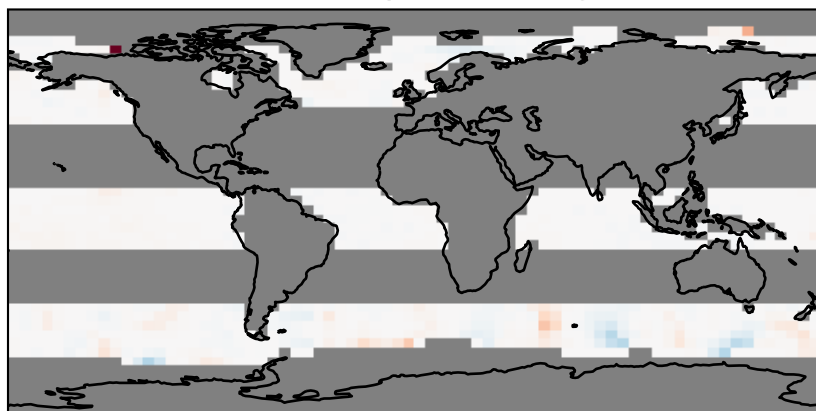
Difference
Dev - Ref, Dynamic Range



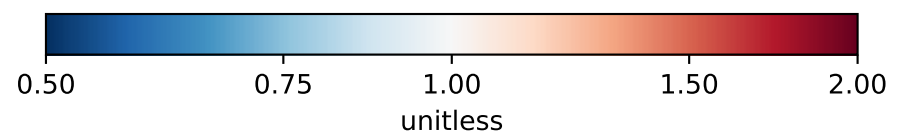
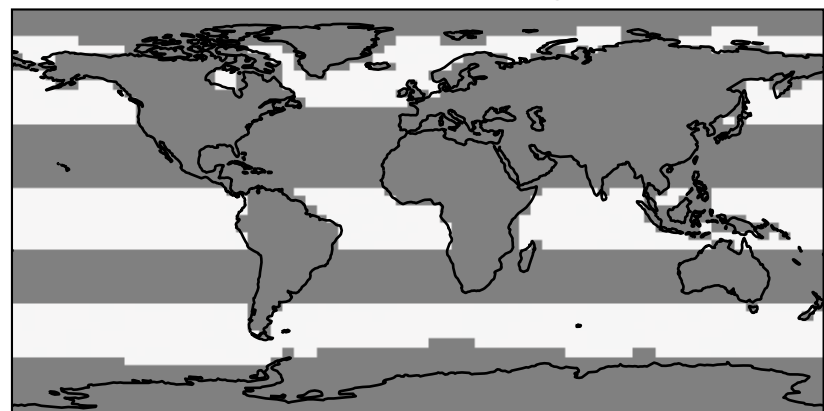
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

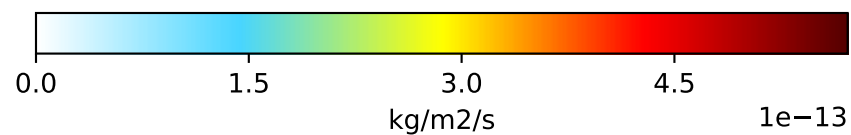
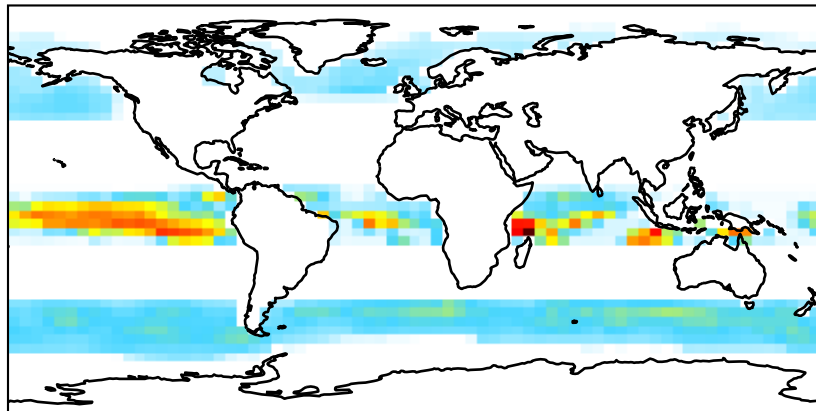


Ratio
Dev/Ref, Fixed Range

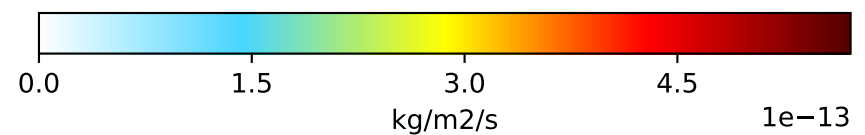
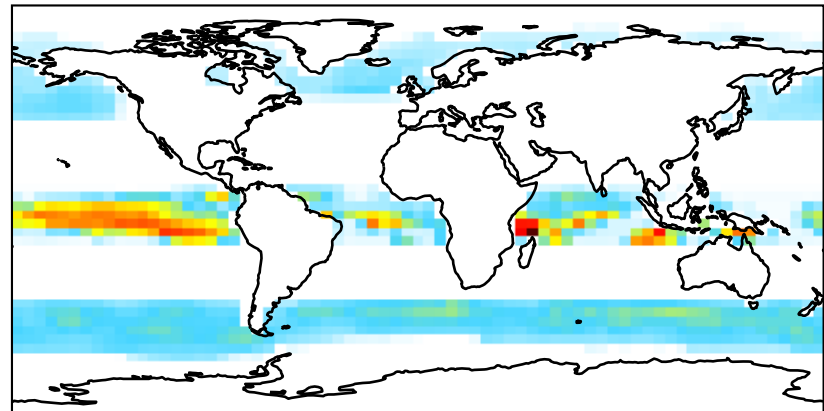


EmisMEN03_Ocean (Oct2019)

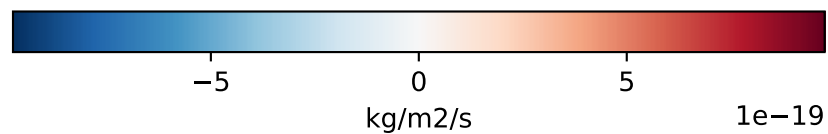
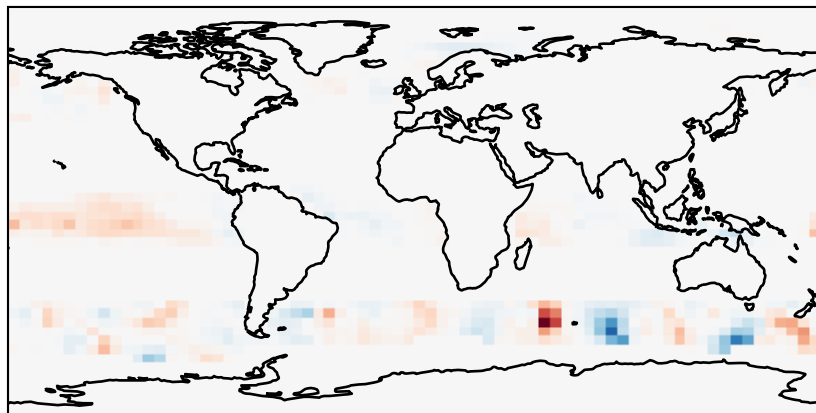
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



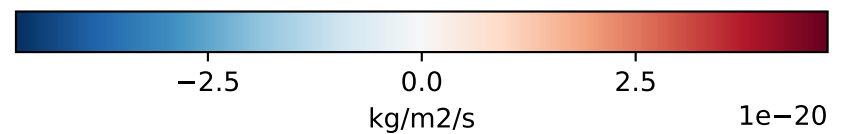
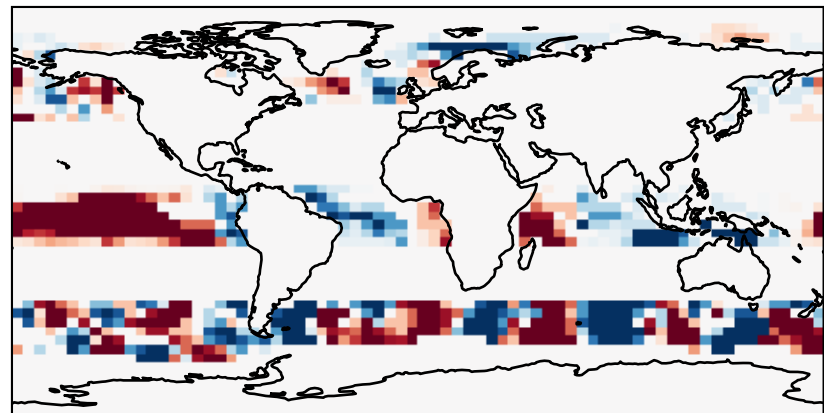
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



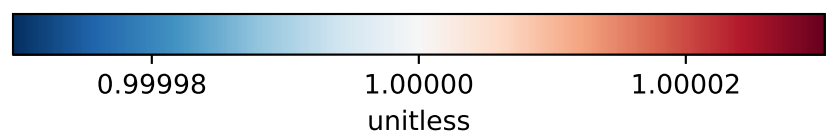
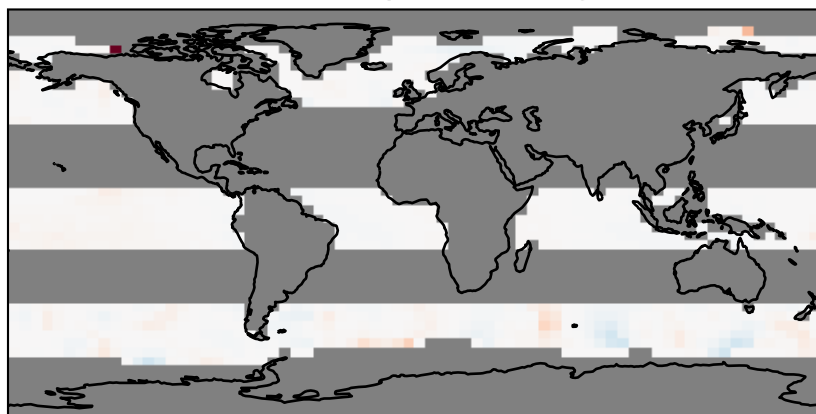
Difference
Dev - Ref, Dynamic Range



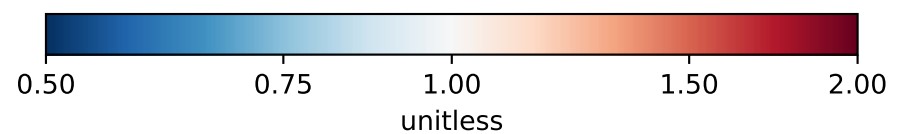
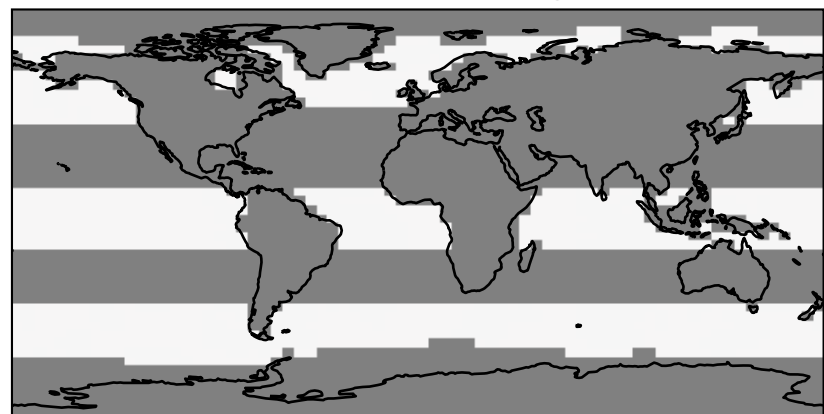
Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range

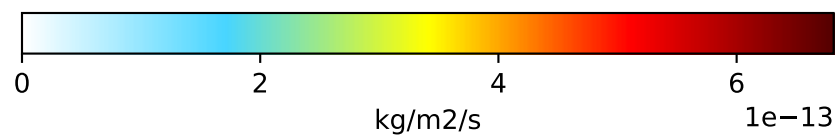
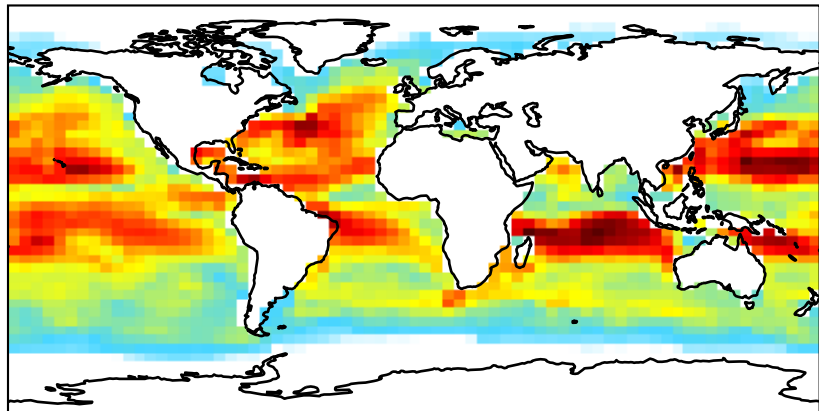


Ratio
Dev/Ref, Fixed Range

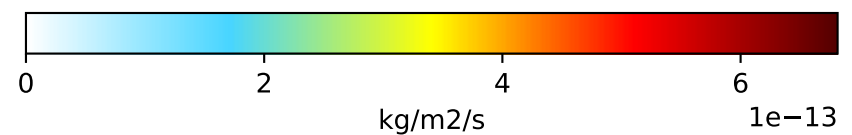
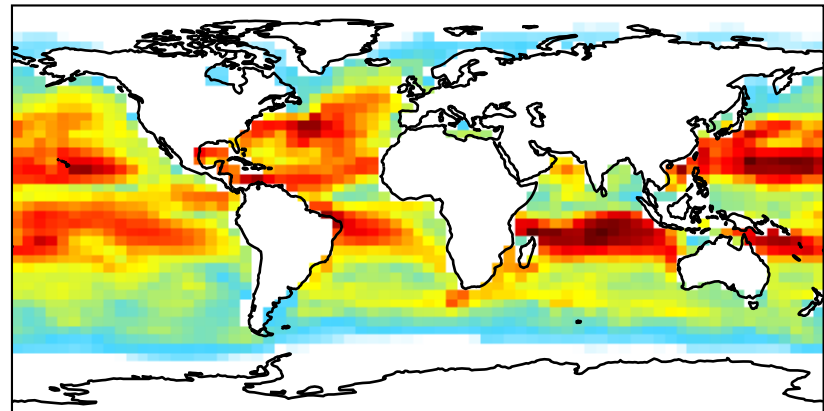


EmisMOH_Ocean (Oct2019)

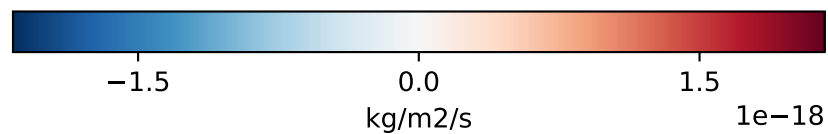
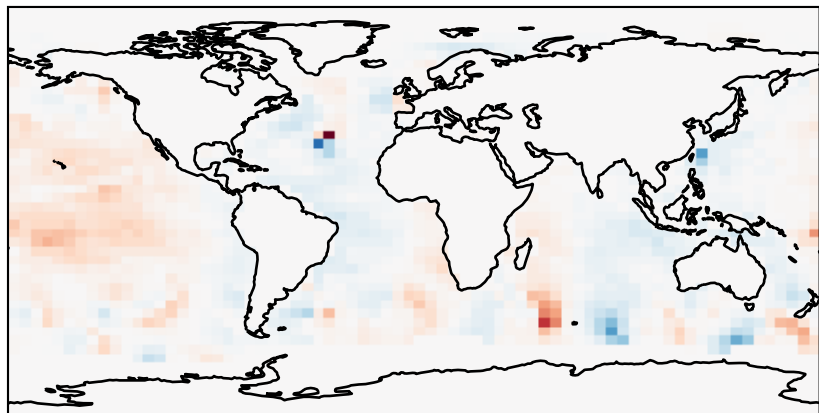
GCC_13.4.0-rc.4 (Ref)
4.0x5.0



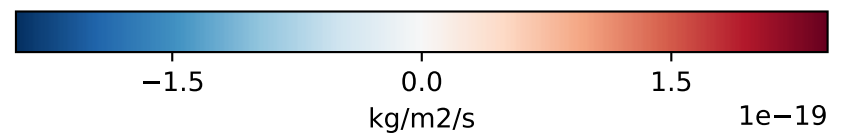
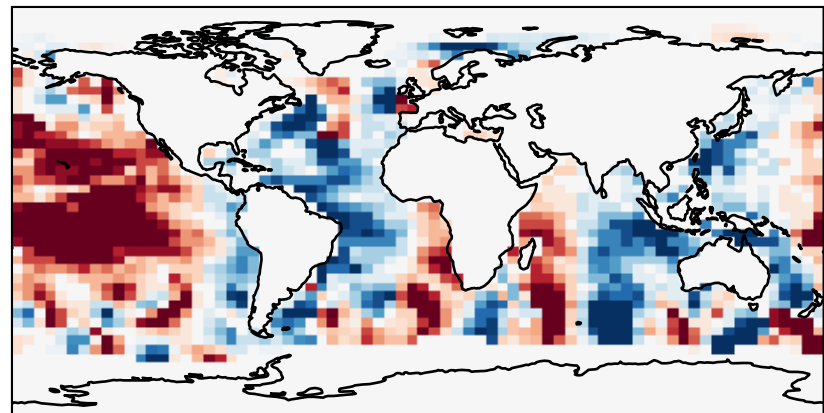
GCC_14.0.0-rc.0 (Dev)
4.0x5.0



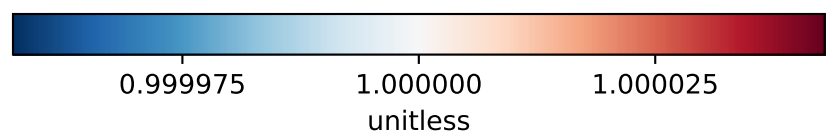
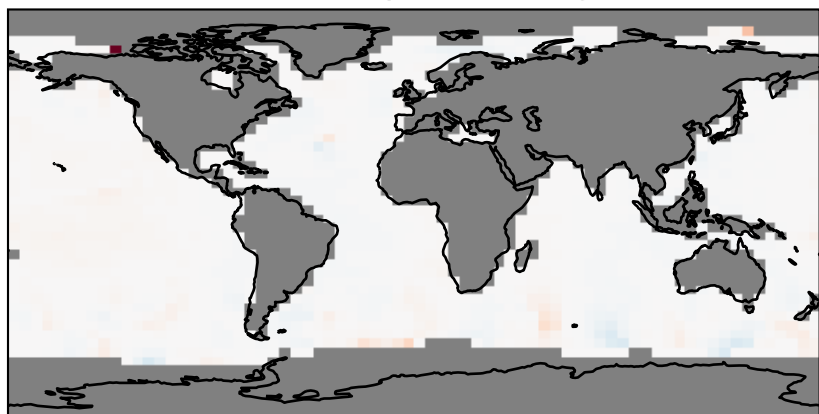
Difference
Dev - Ref, Dynamic Range



Difference
Dev - Ref, Restricted Range [5%,95%]



Ratio
Dev/Ref, Dynamic Range



Ratio
Dev/Ref, Fixed Range

