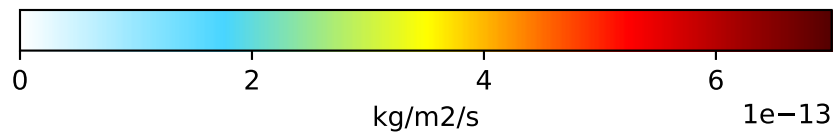
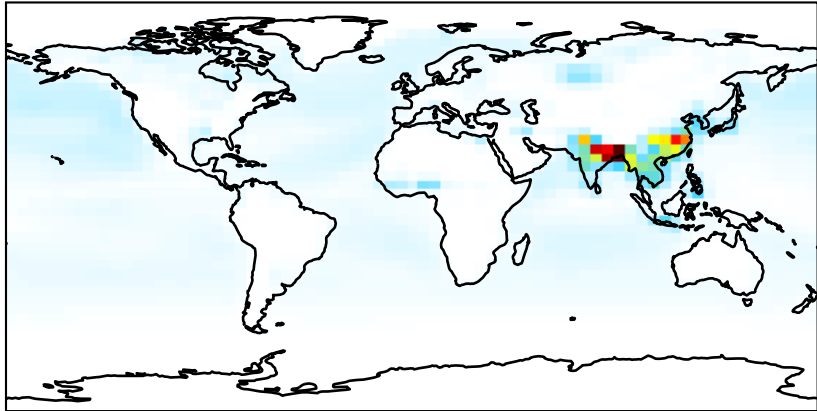
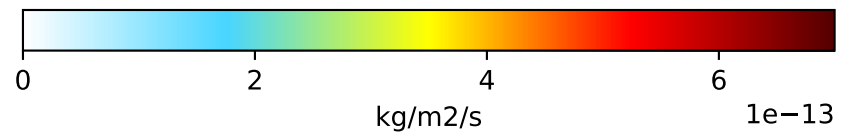
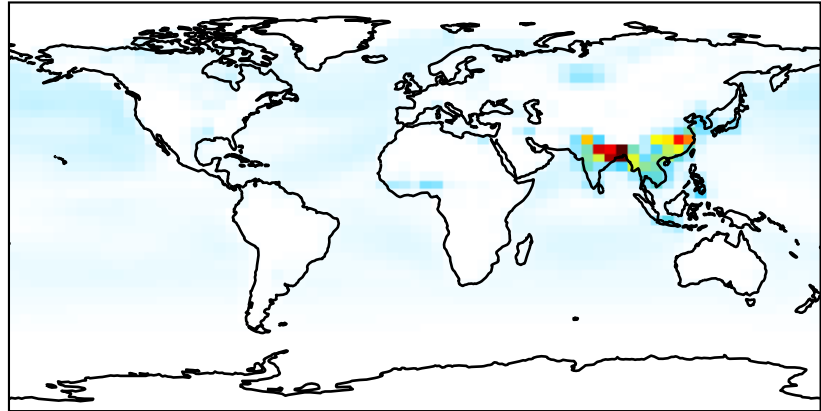


# EmisCH3I\_Ocean (Jul2019)

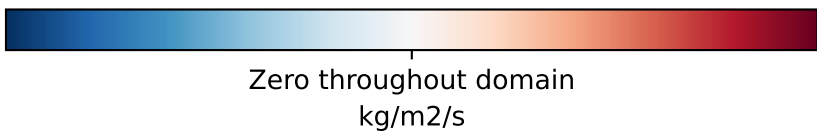
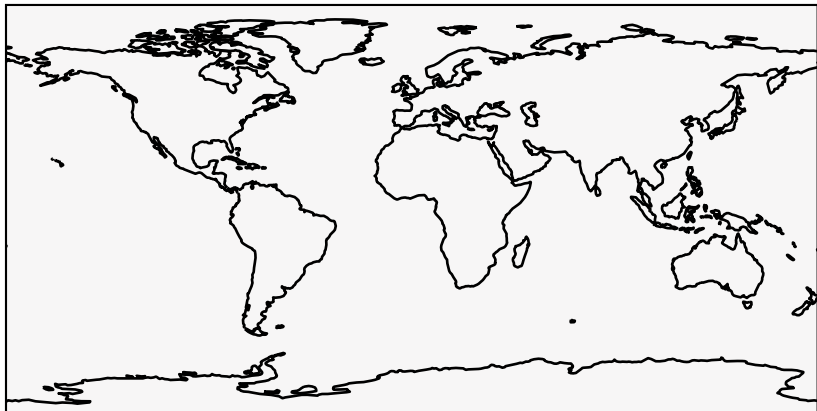
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



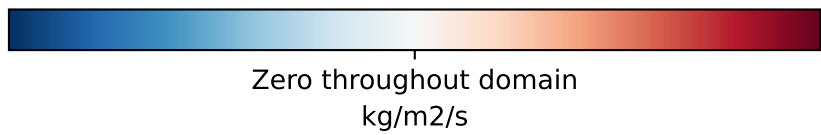
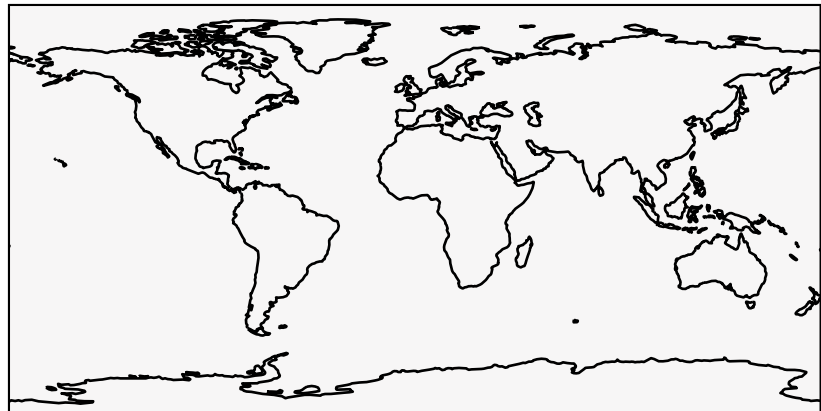
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



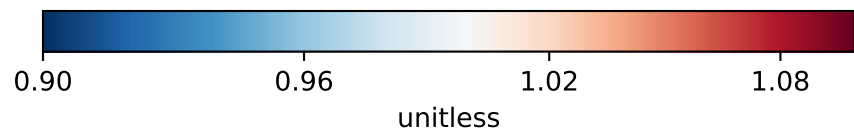
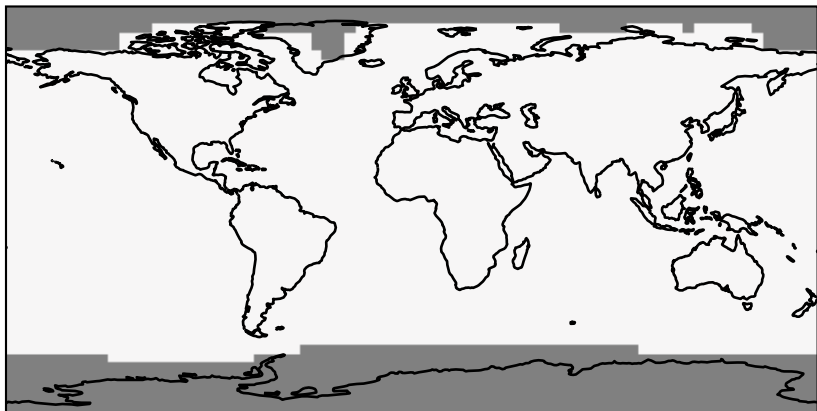
Difference  
Dev - Ref, Dynamic Range



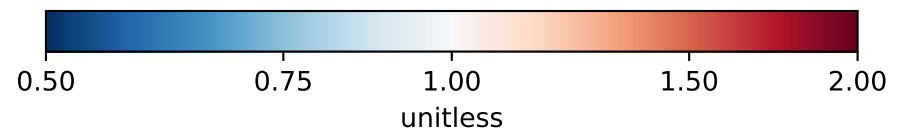
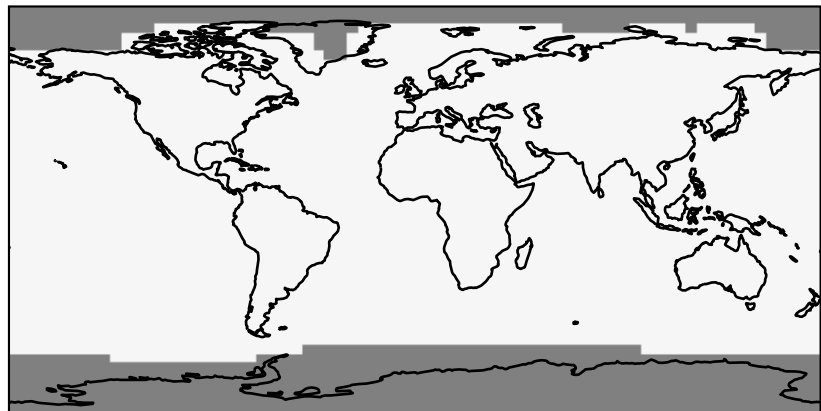
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

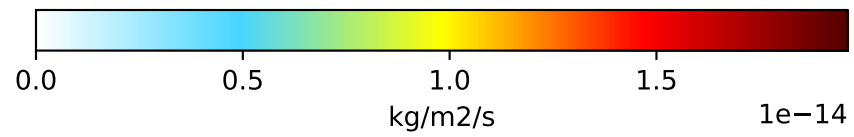
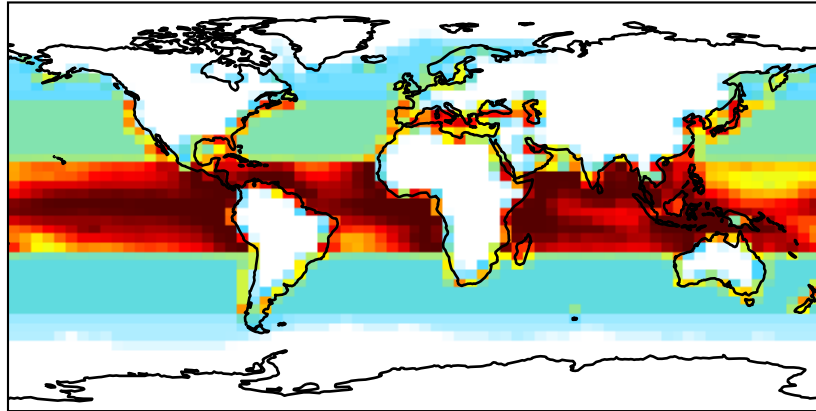


Ratio  
Dev/Ref, Fixed Range

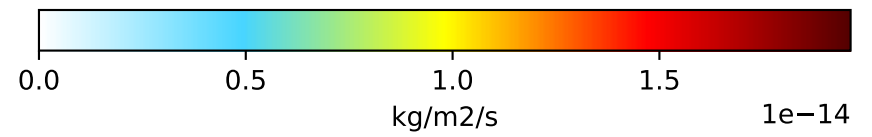
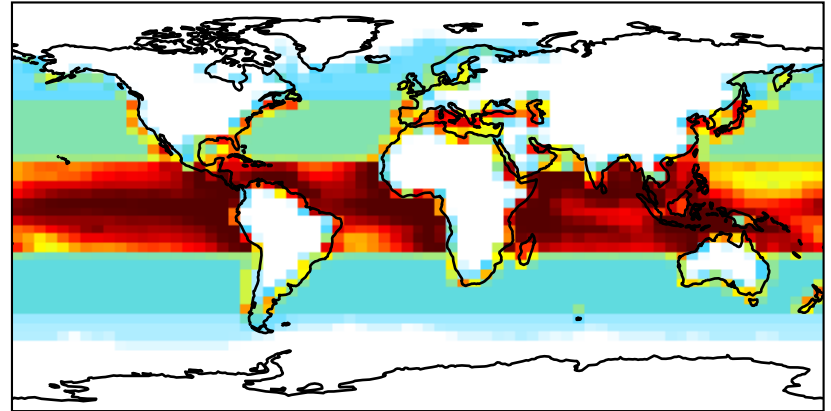


# EmisCH2I2\_Ocean (Jul2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



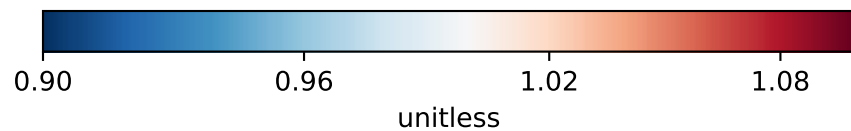
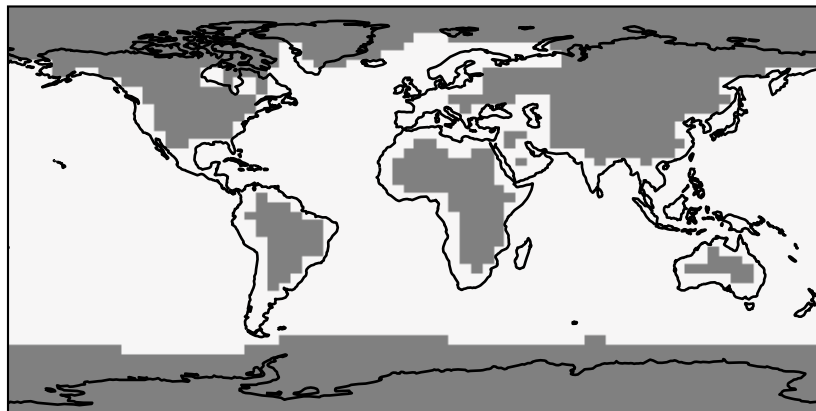
Difference  
Dev - Ref, Dynamic Range



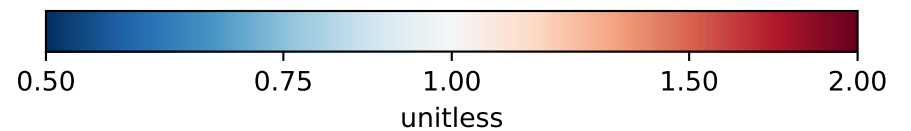
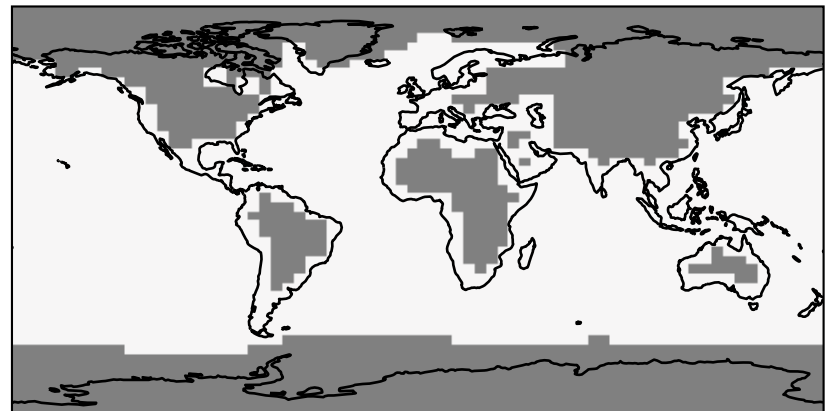
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

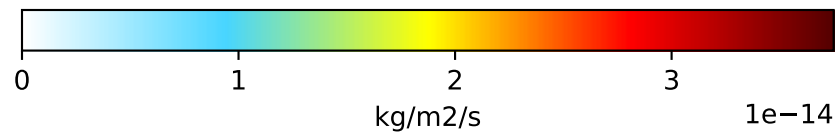
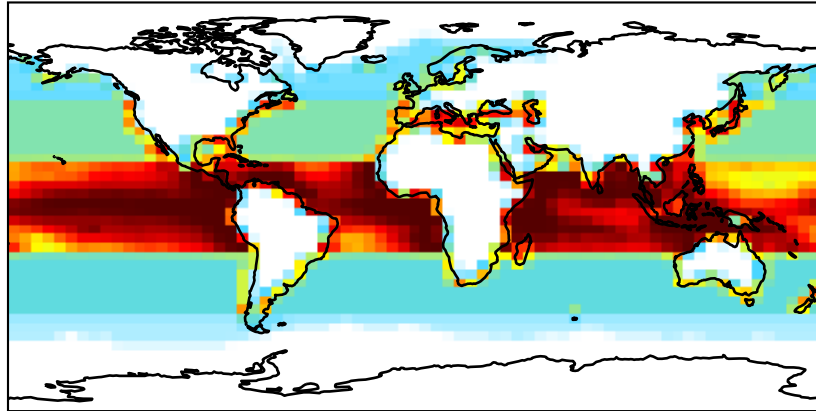


Ratio  
Dev/Ref, Fixed Range

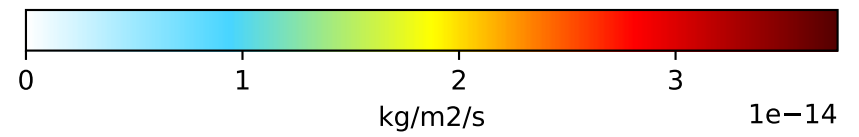
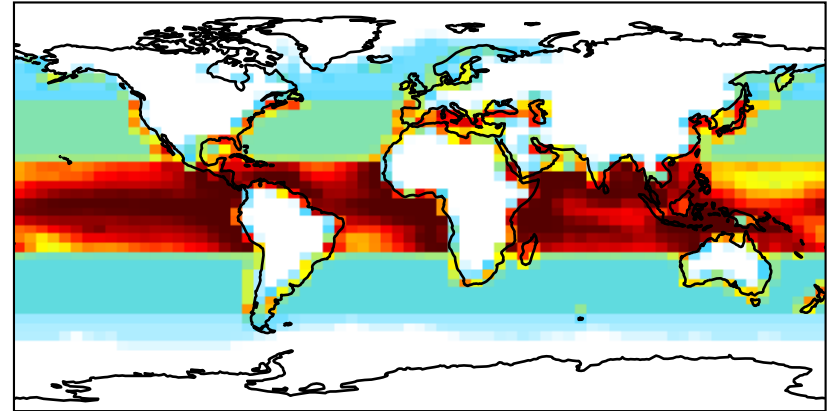


# EmisCH2ICl\_Ocean (Jul2019)

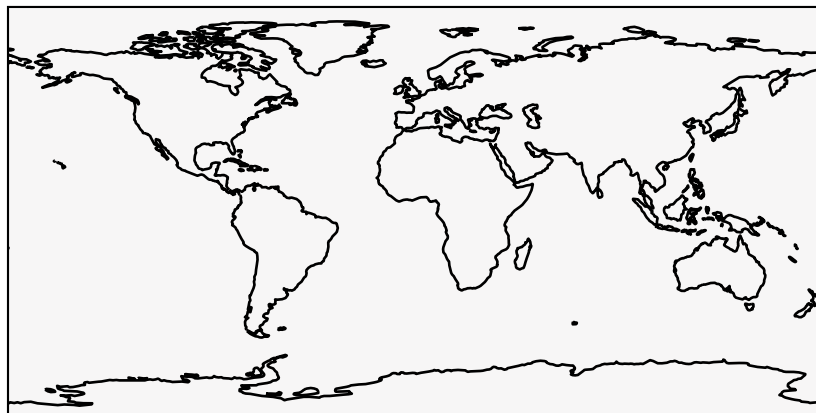
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



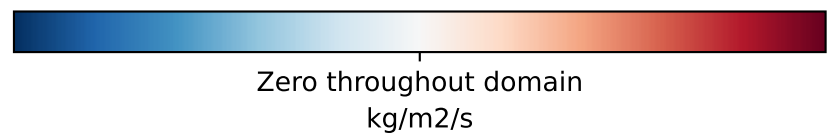
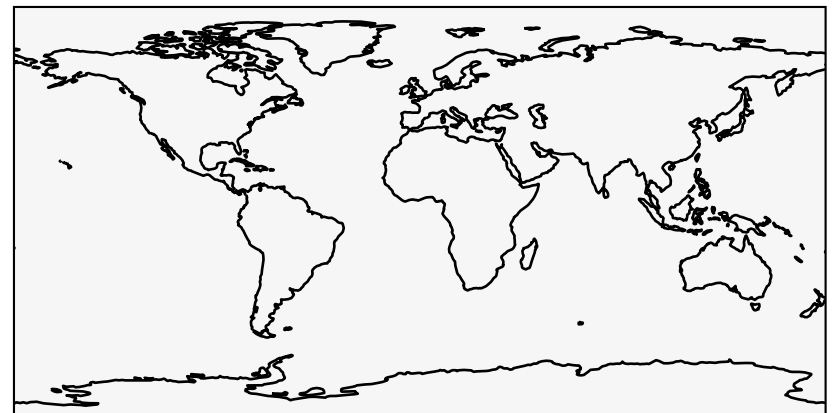
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



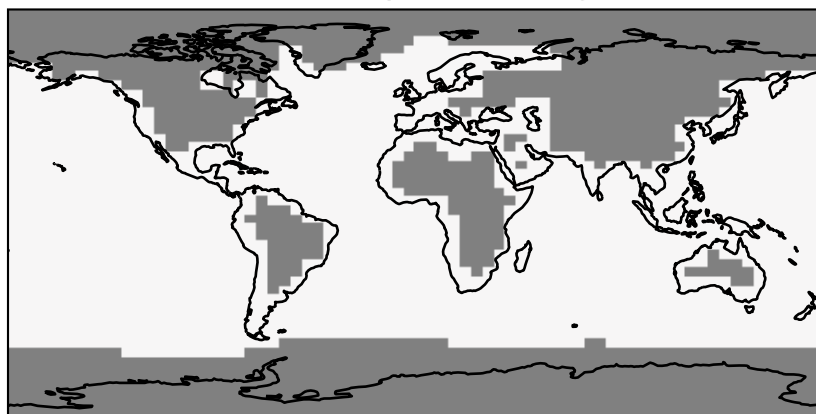
Difference  
Dev - Ref, Dynamic Range



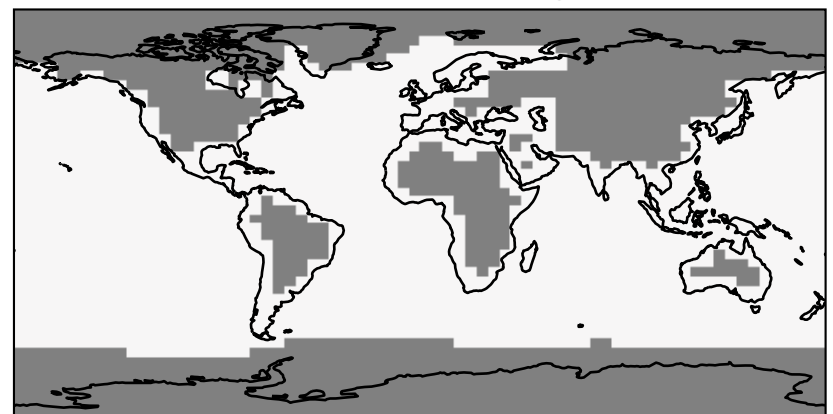
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

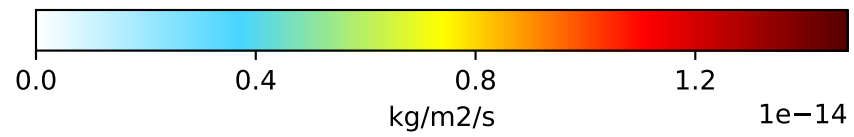
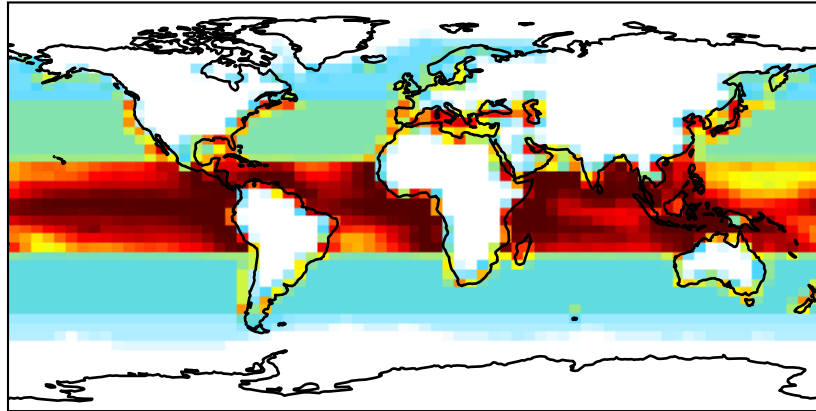


Ratio  
Dev/Ref, Fixed Range

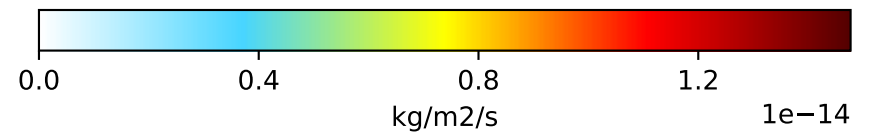
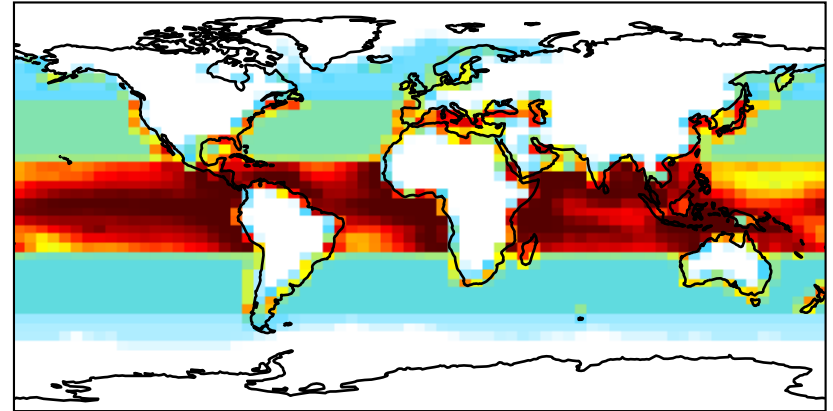


# EmisCH2IBr\_Ocean (Jul2019)

GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



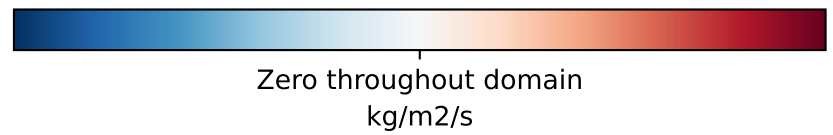
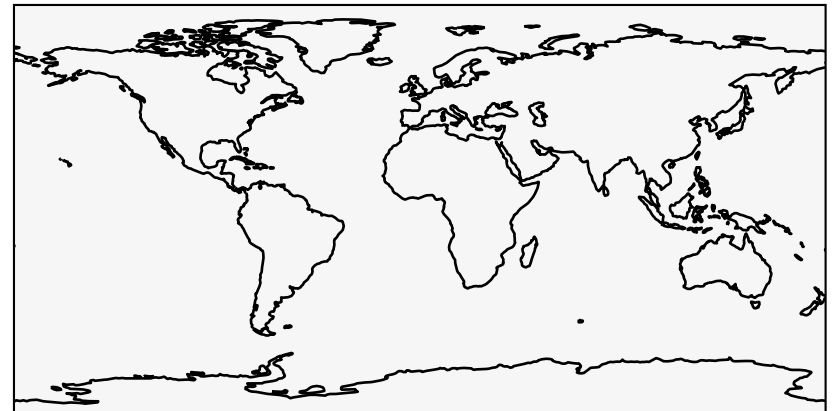
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



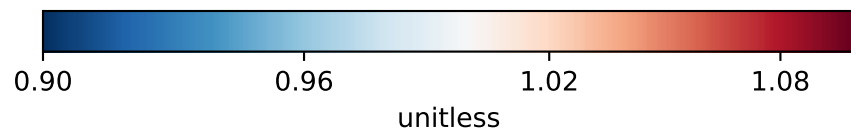
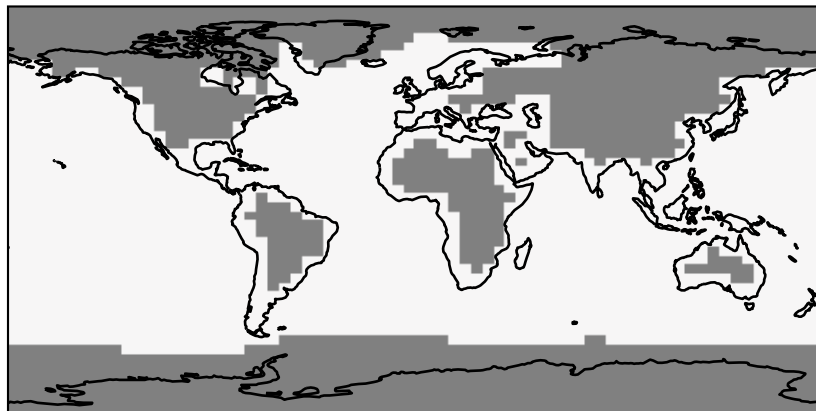
Difference  
Dev - Ref, Dynamic Range



Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

