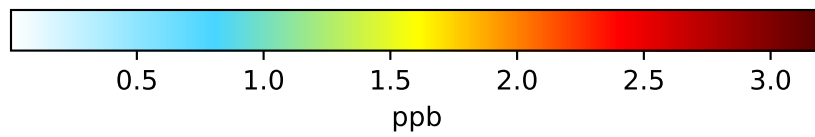
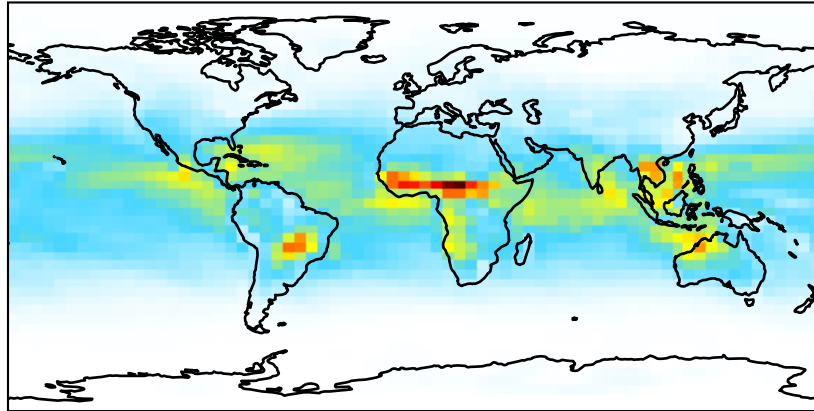
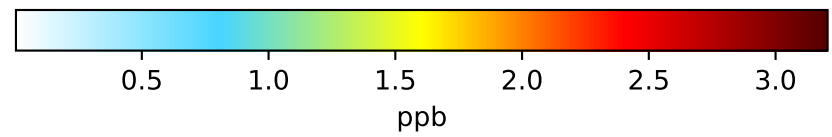
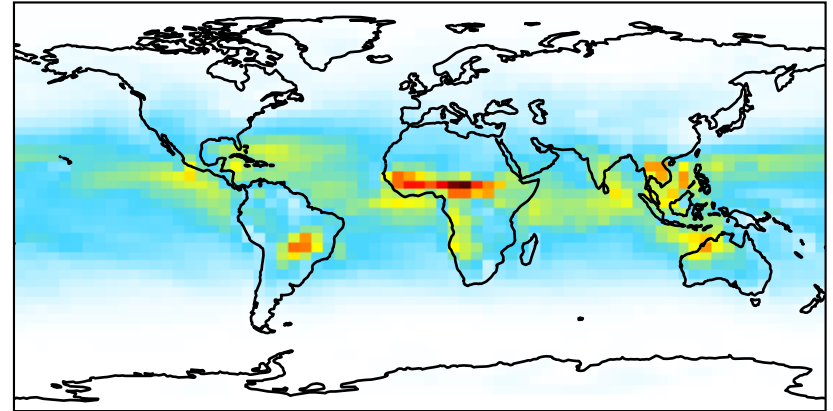


# SpeciesConc\_H2O2 (Apr2019)

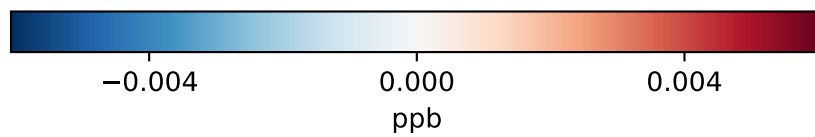
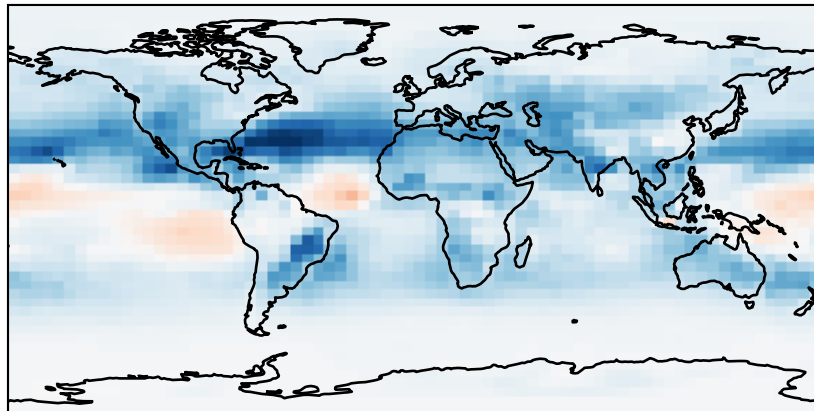
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



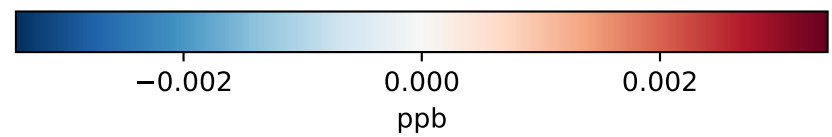
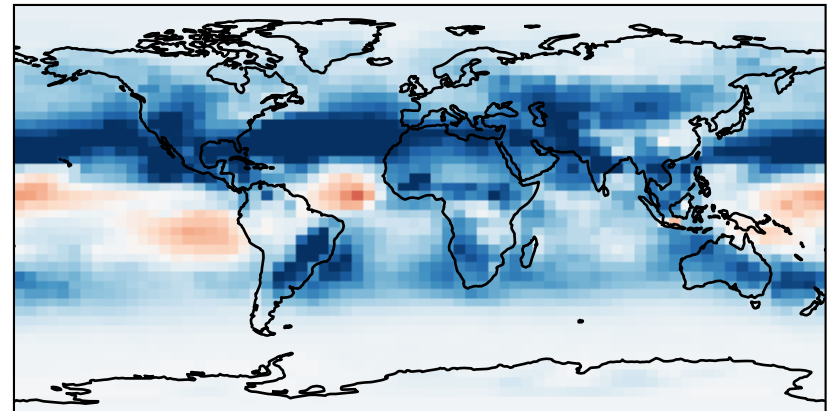
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



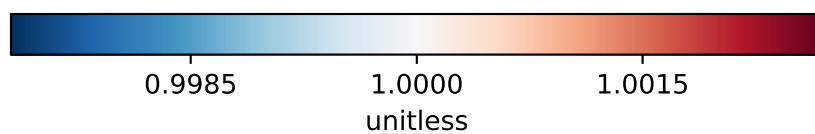
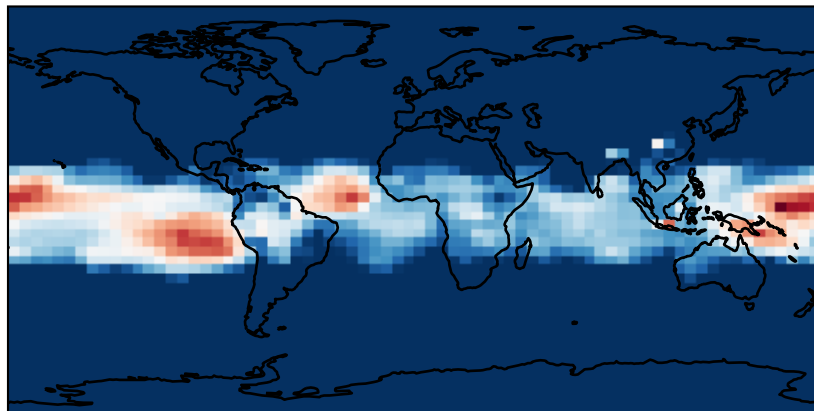
Difference  
Dev - Ref, Dynamic Range



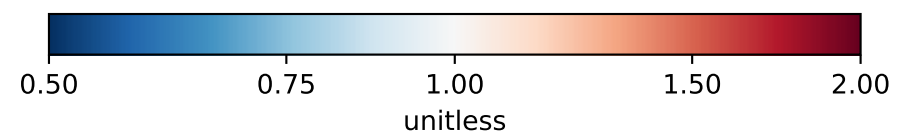
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

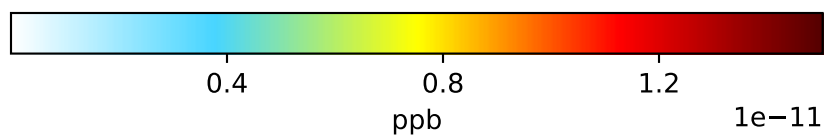
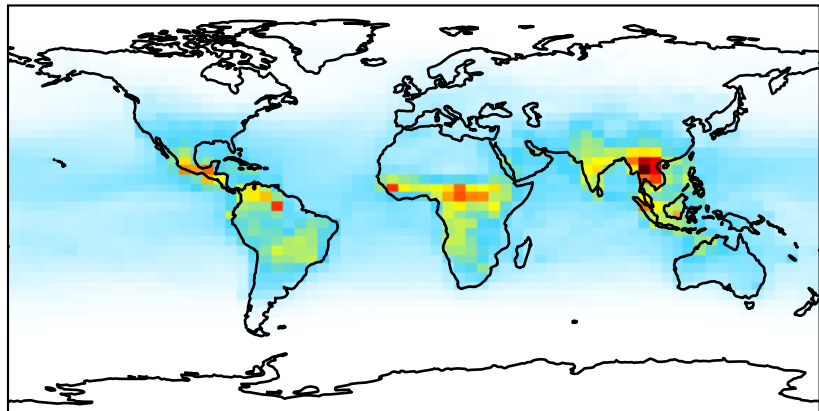


Ratio  
Dev/Ref, Fixed Range

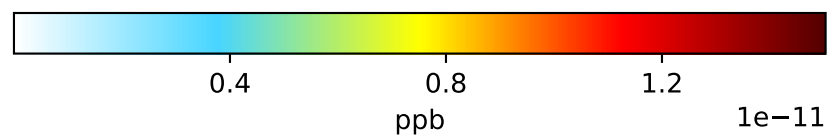
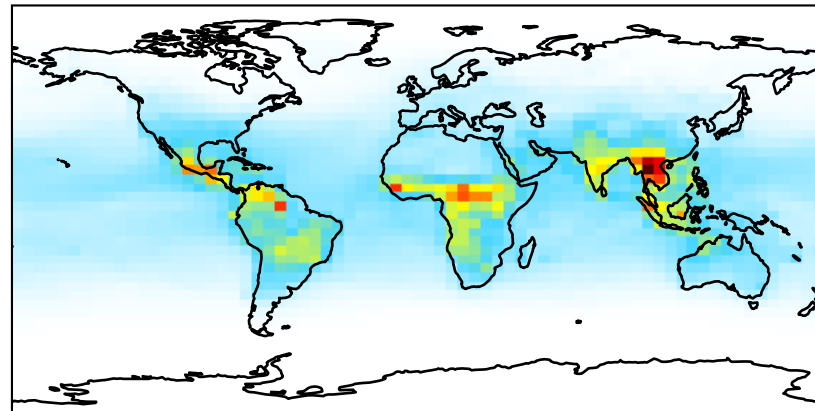


# SpeciesConc\_H (Apr2019)

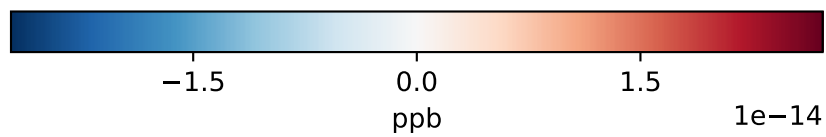
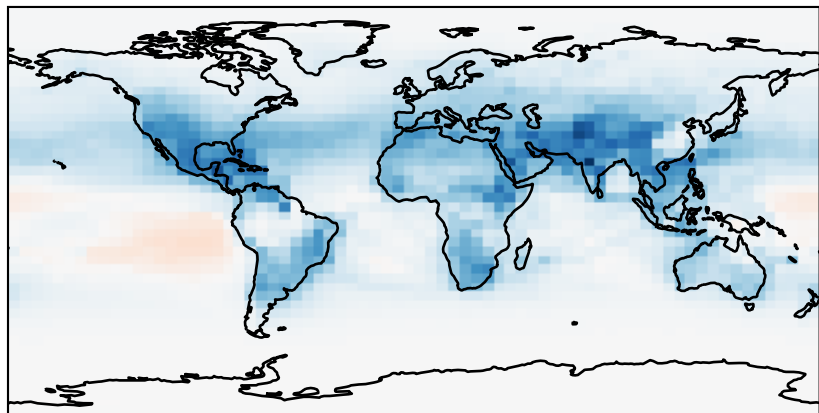
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



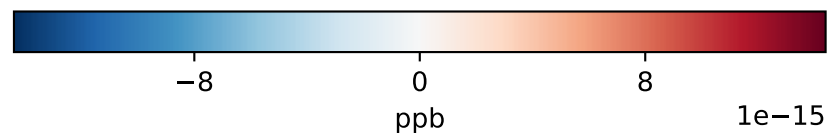
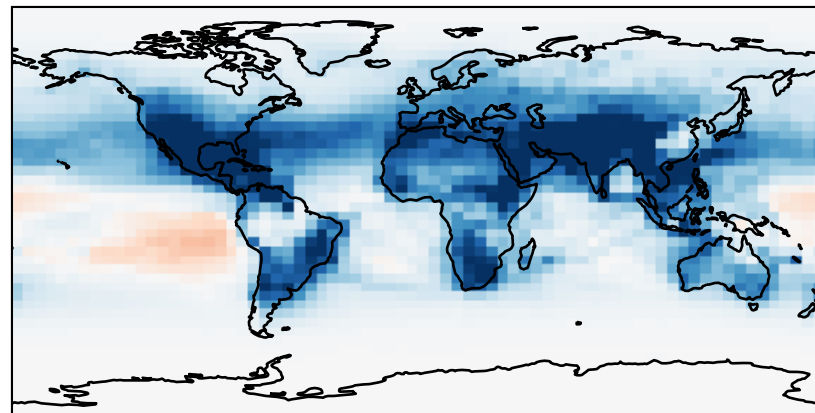
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



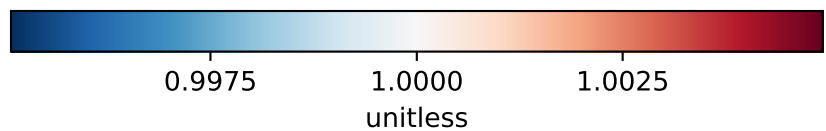
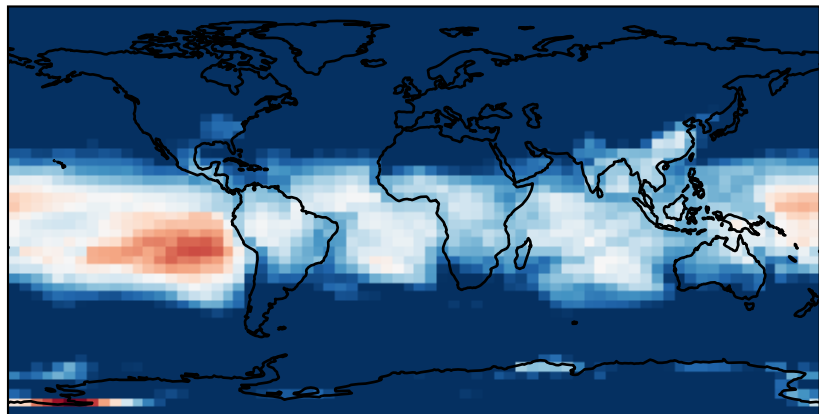
Difference  
Dev - Ref, Dynamic Range



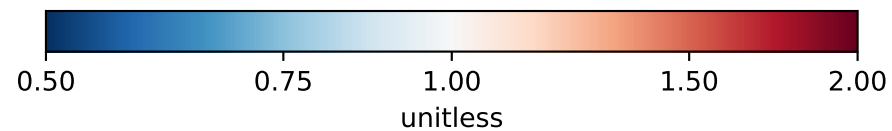
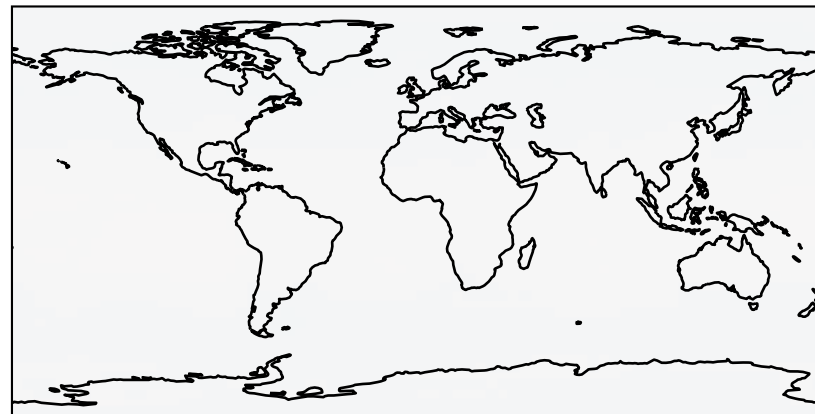
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

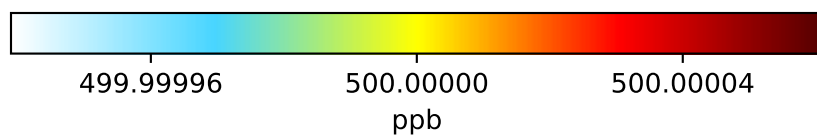
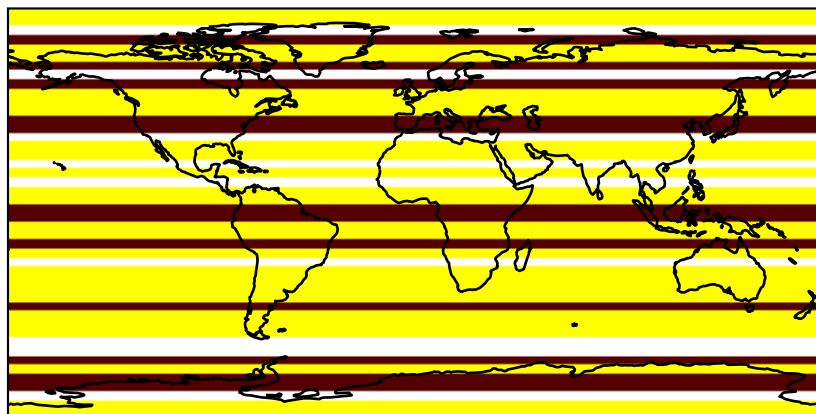


Ratio  
Dev/Ref, Fixed Range

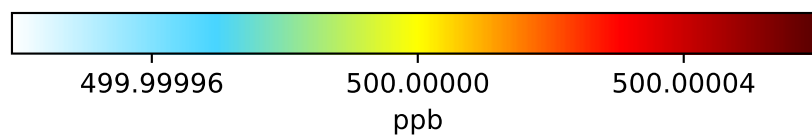
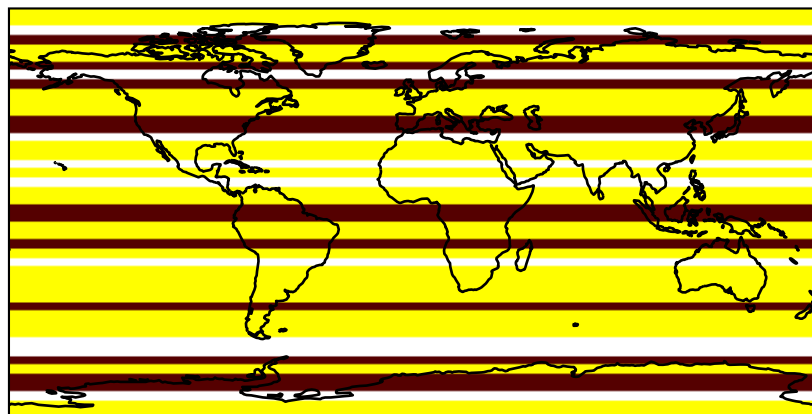


# SpeciesConc\_H2 (Apr2019)

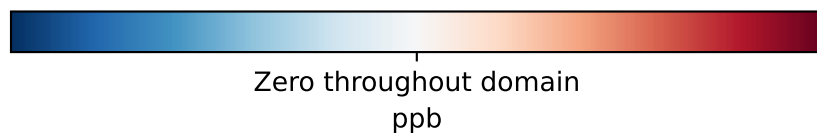
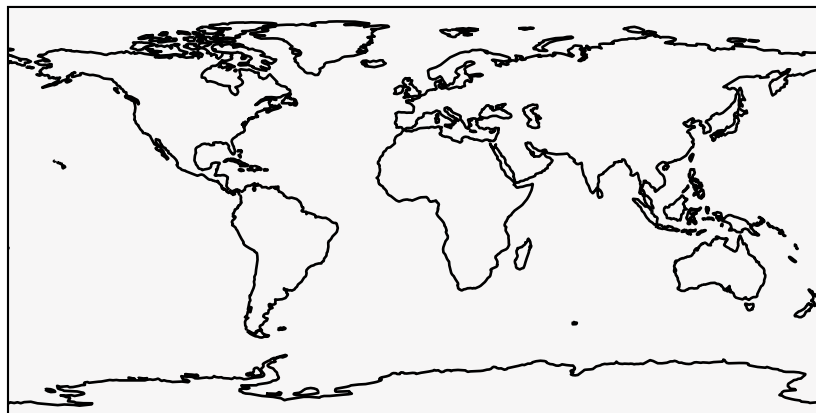
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



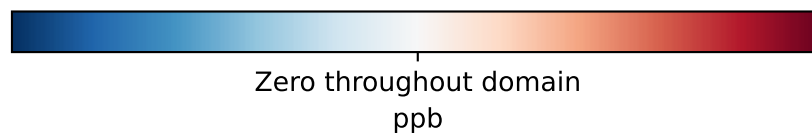
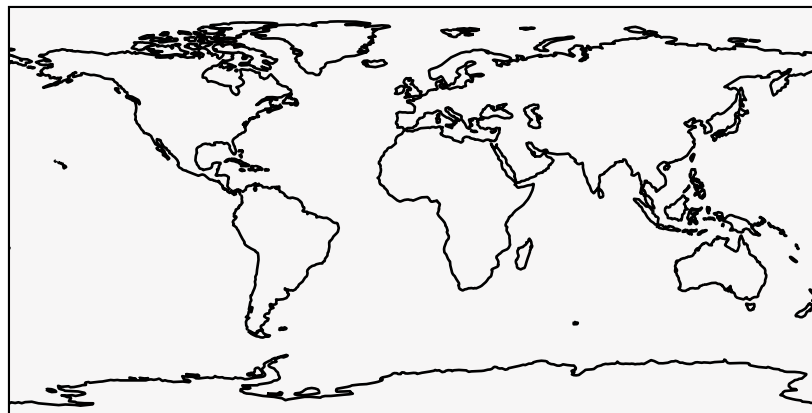
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



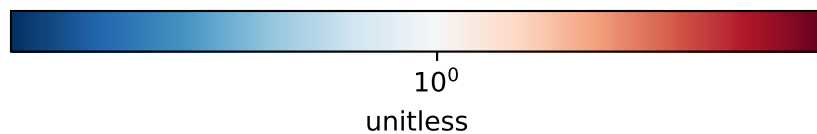
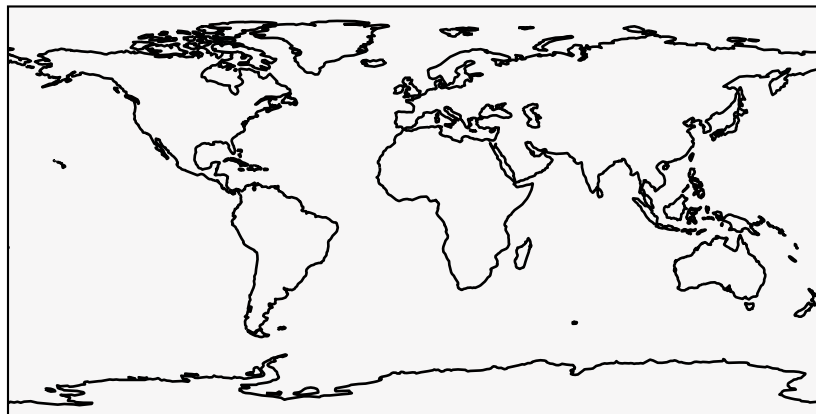
Difference  
Dev - Ref, Dynamic Range



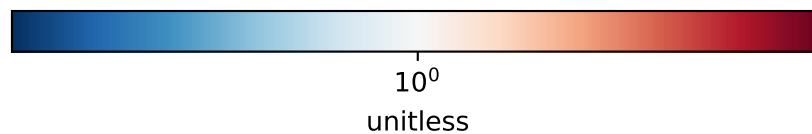
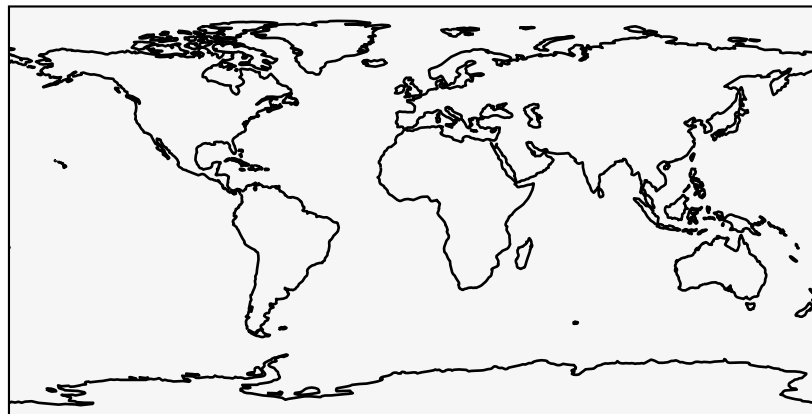
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

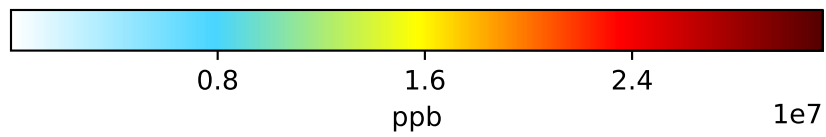
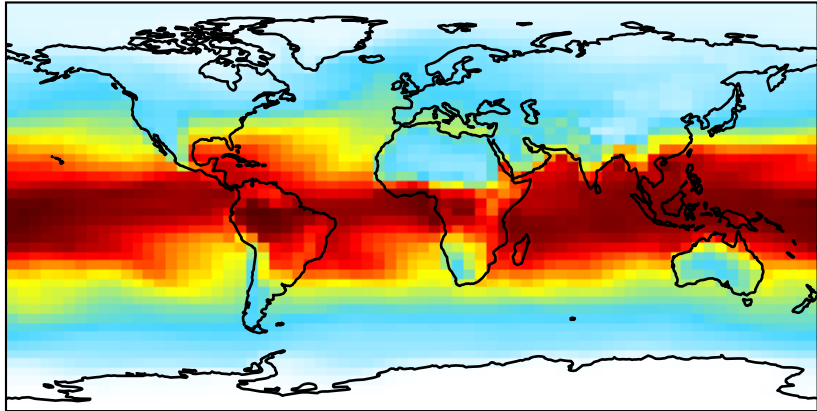


Ratio  
Dev/Ref, Fixed Range

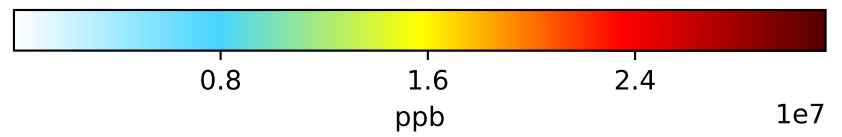
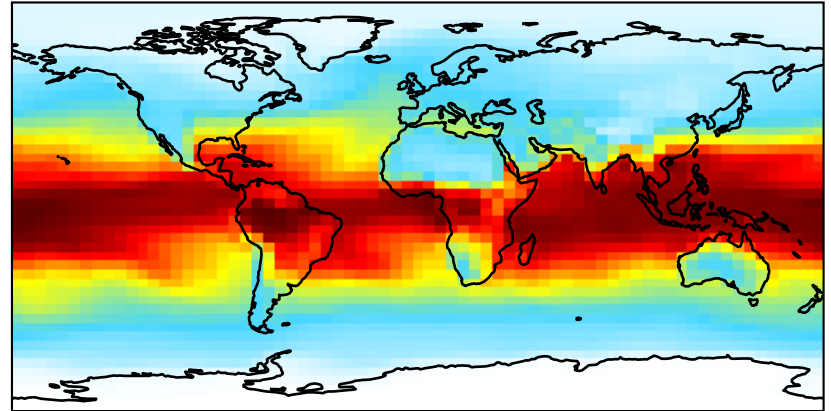


# SpeciesConc\_H2O (Apr2019)

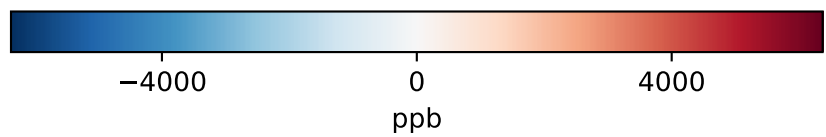
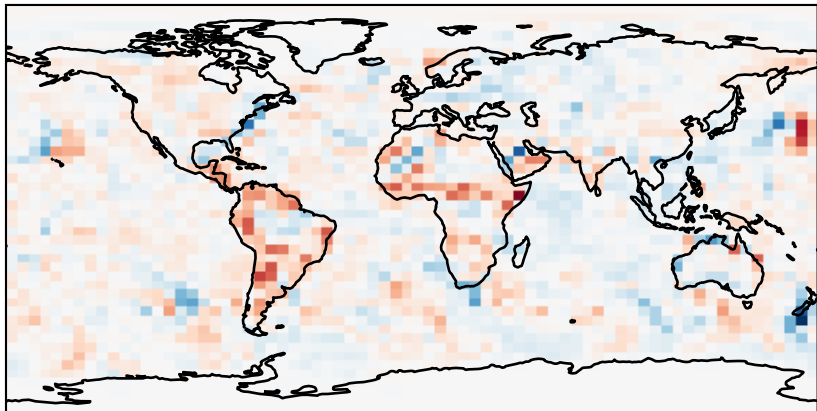
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



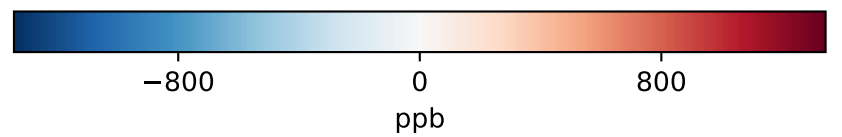
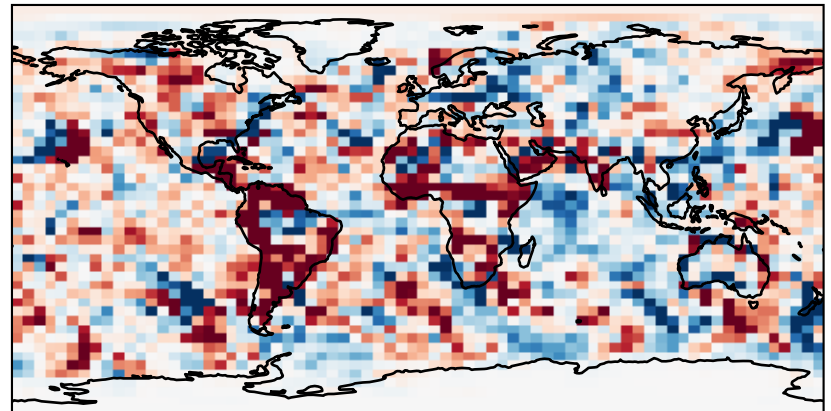
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



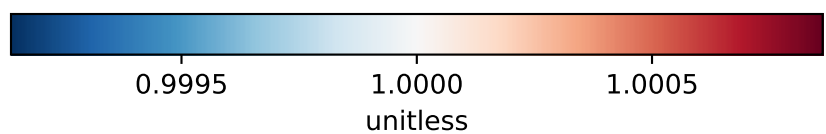
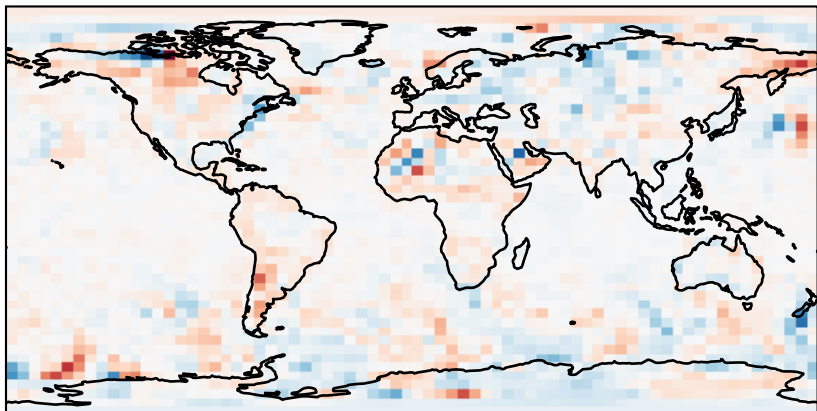
Difference  
Dev - Ref, Dynamic Range



Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

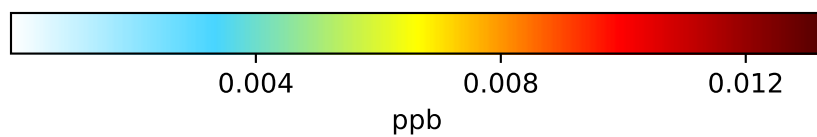
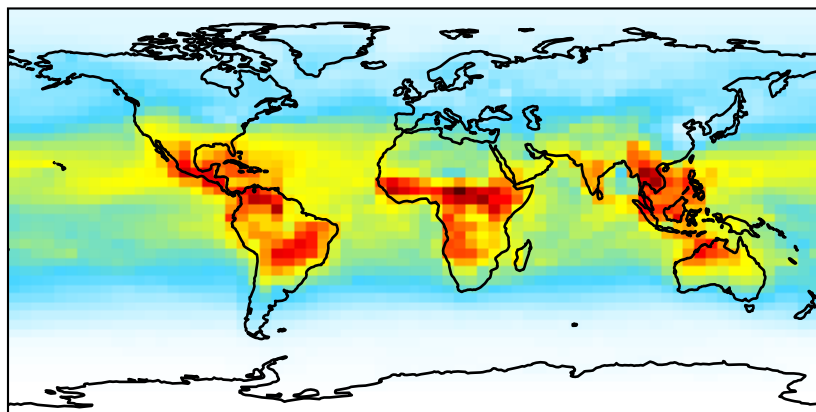


Ratio  
Dev/Ref, Fixed Range

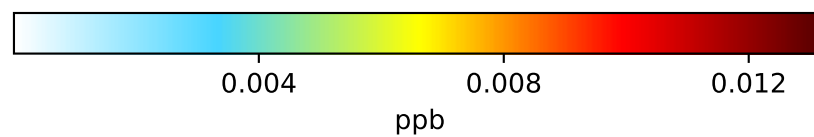
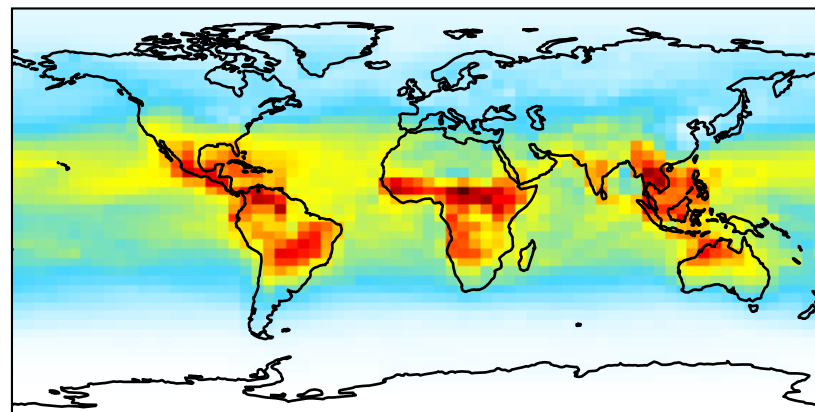


# SpeciesConc\_HO2 (Apr2019)

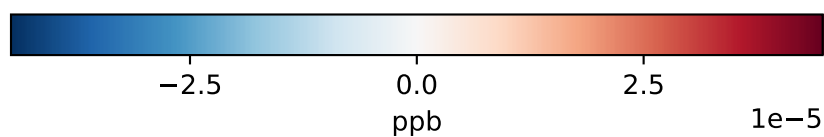
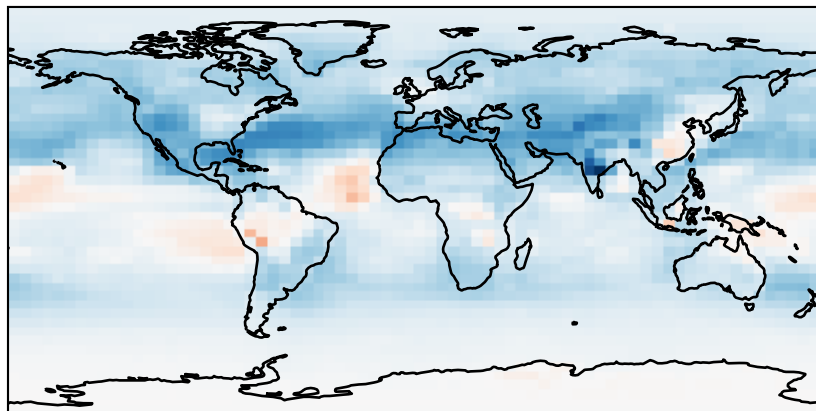
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



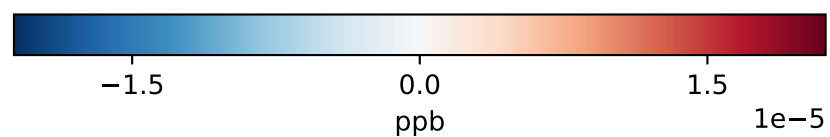
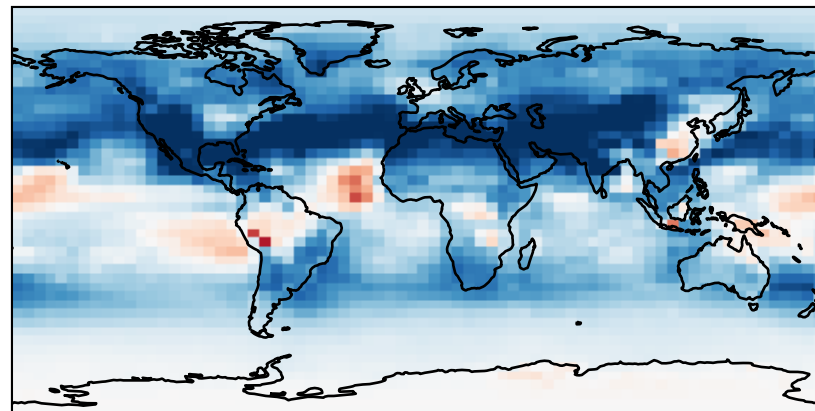
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



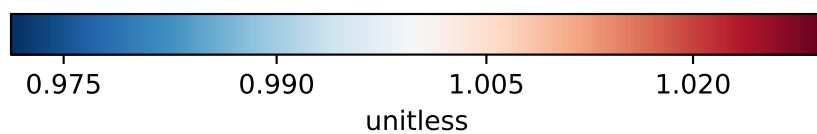
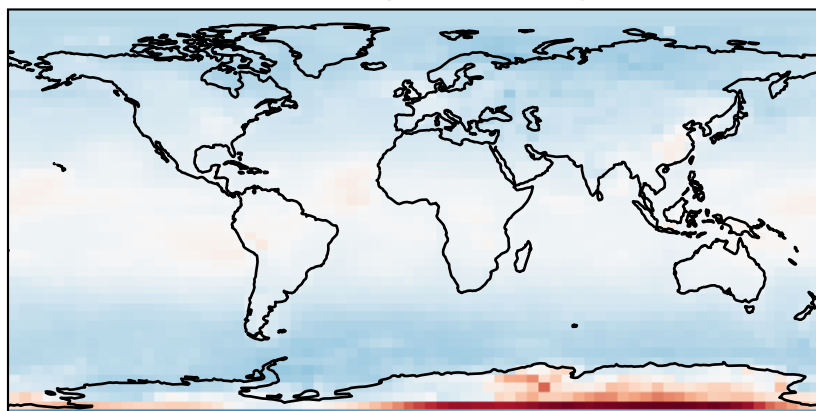
Difference  
Dev - Ref, Dynamic Range



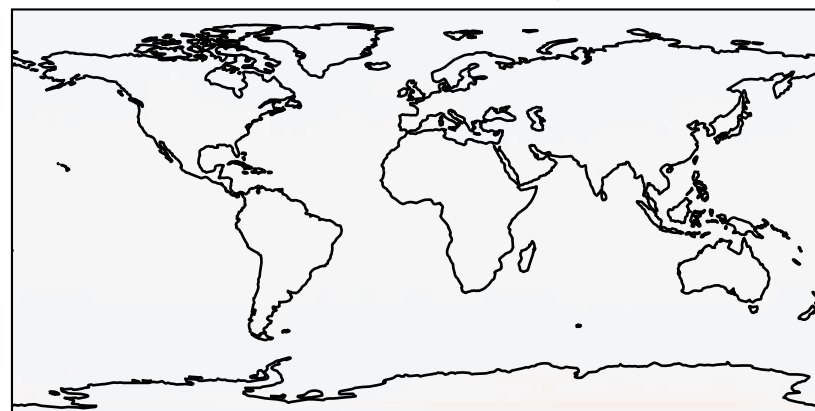
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

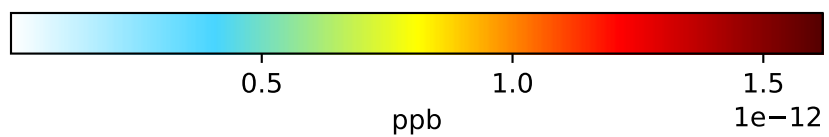
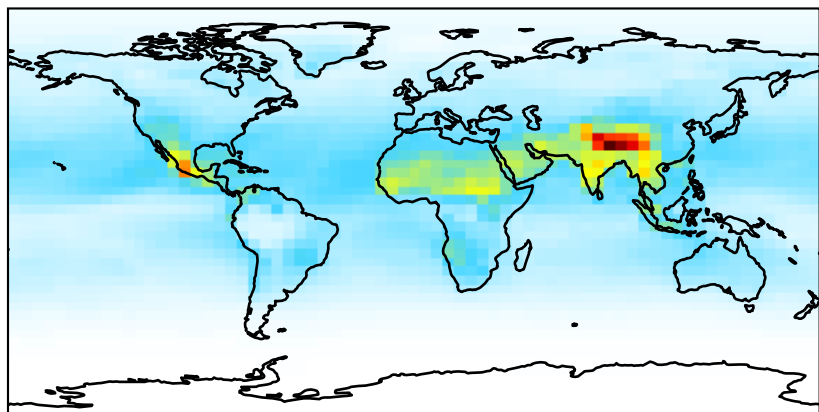


Ratio  
Dev/Ref, Fixed Range

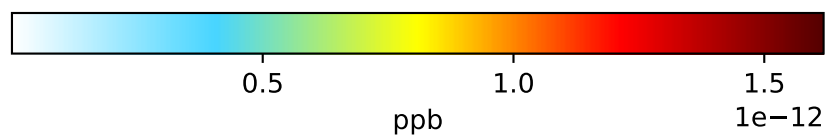
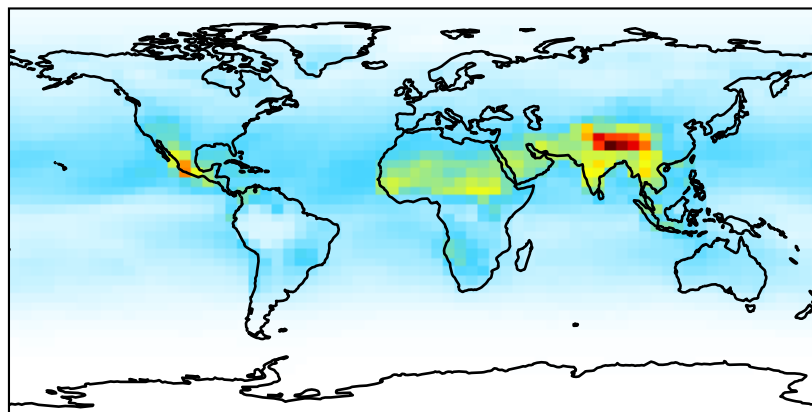


# SpeciesConc\_01D (Apr2019)

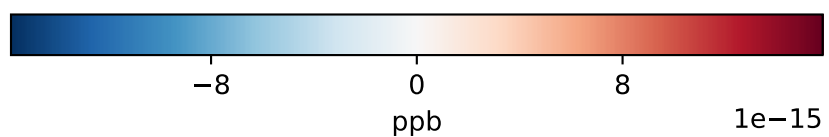
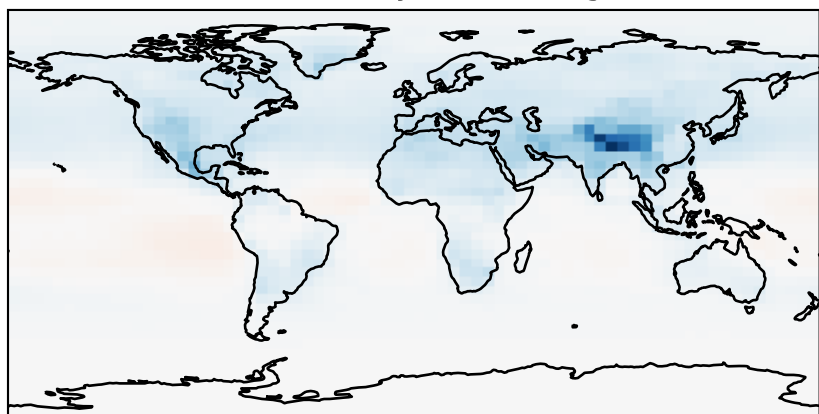
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



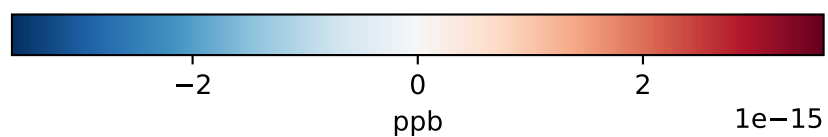
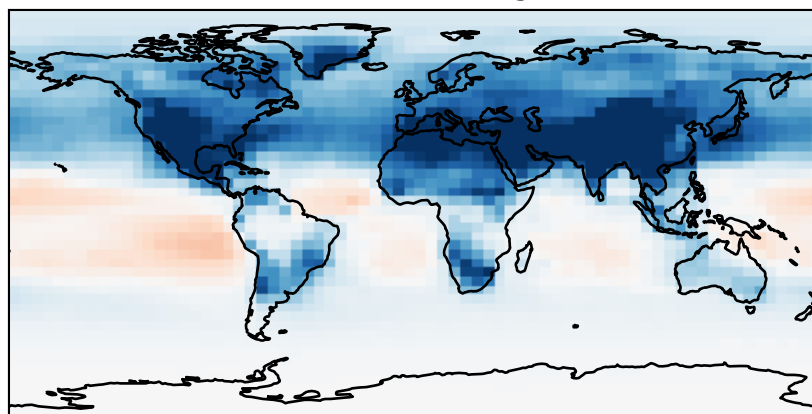
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



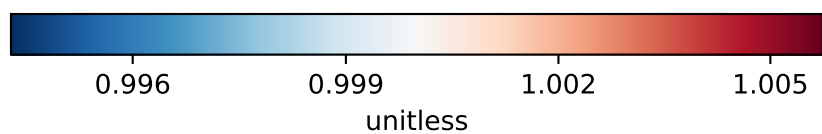
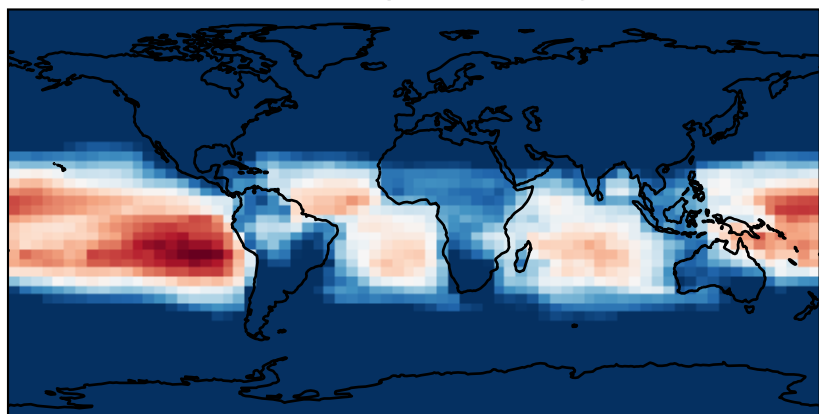
Difference  
Dev - Ref, Dynamic Range



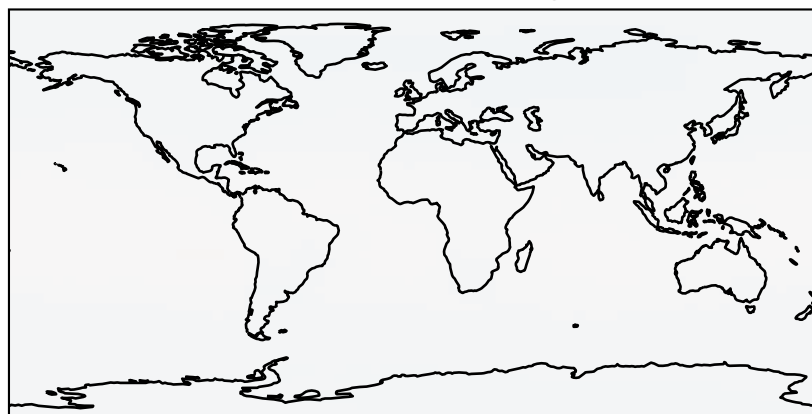
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

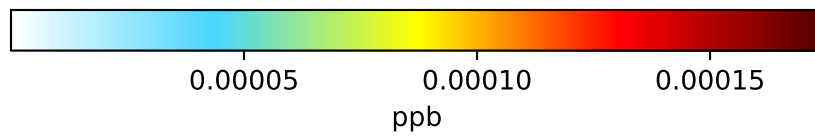
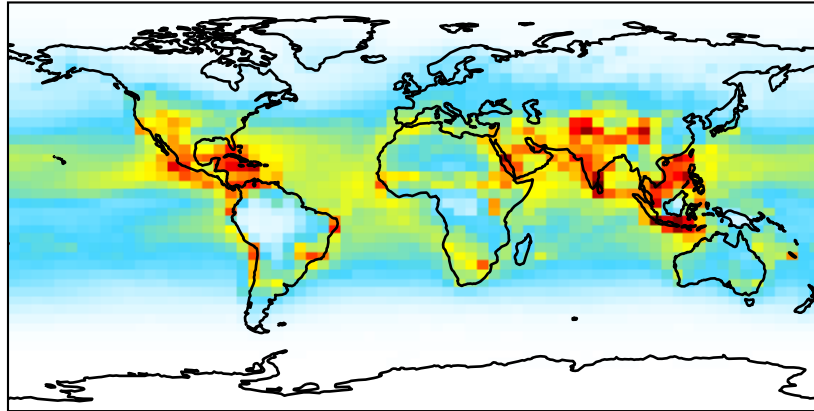


Ratio  
Dev/Ref, Fixed Range

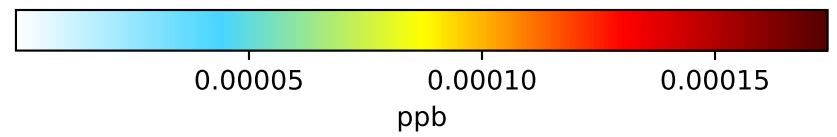
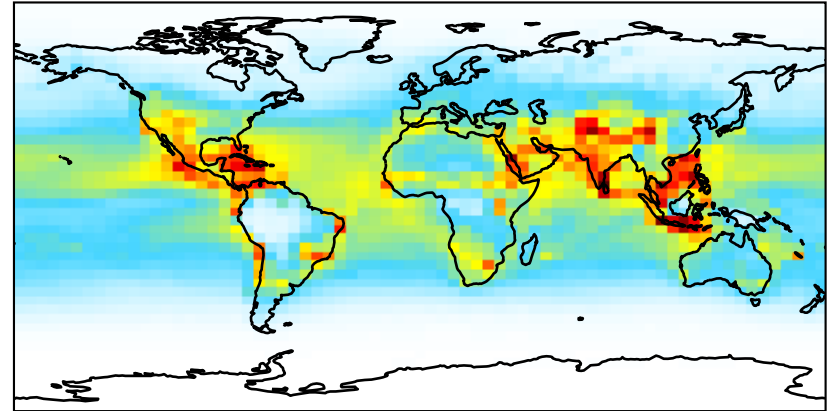


# SpeciesConc\_OH (Apr2019)

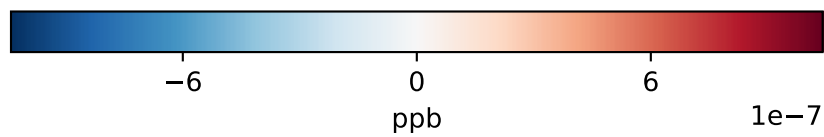
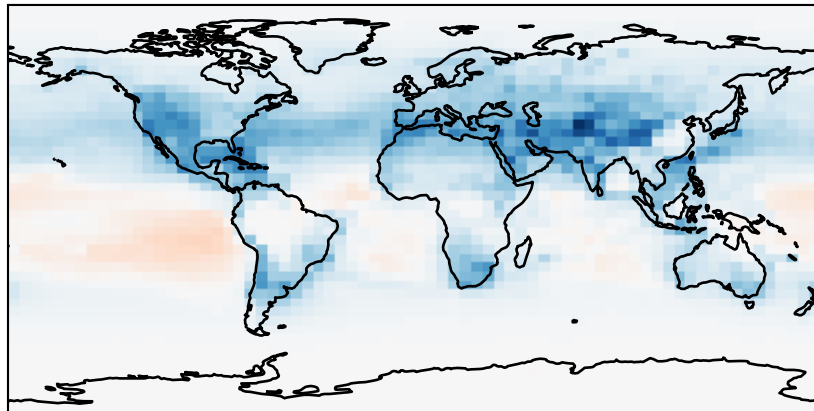
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



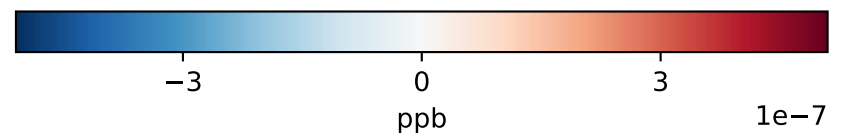
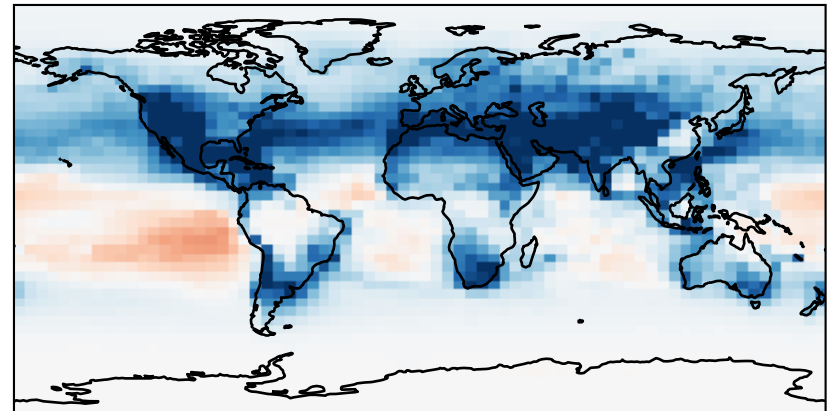
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



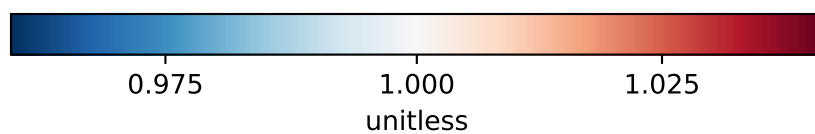
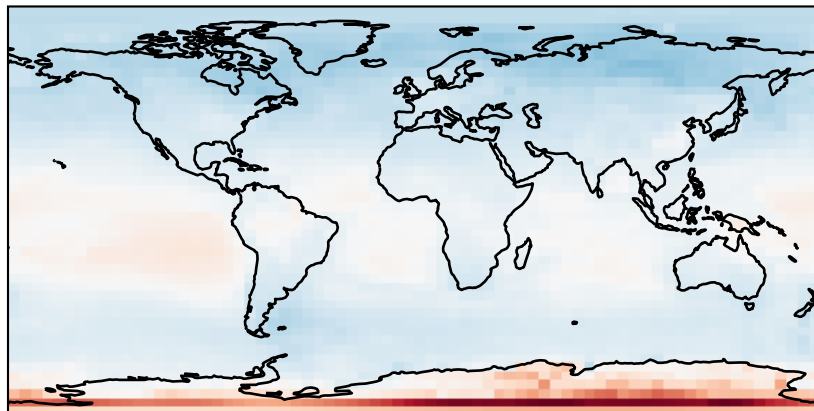
Difference  
Dev - Ref, Dynamic Range



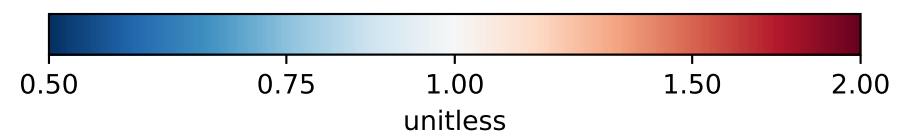
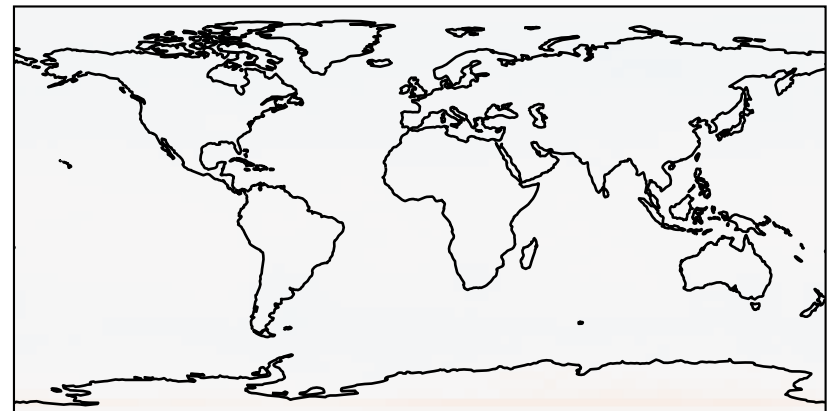
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

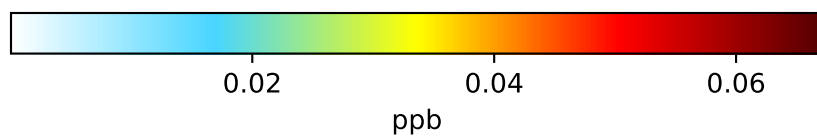
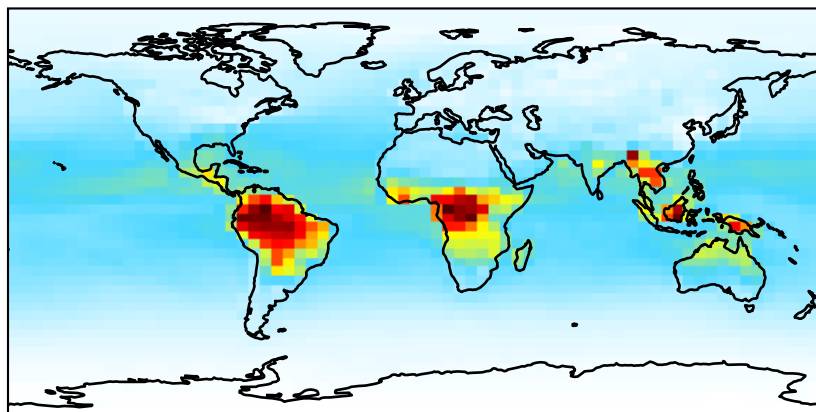


Ratio  
Dev/Ref, Fixed Range

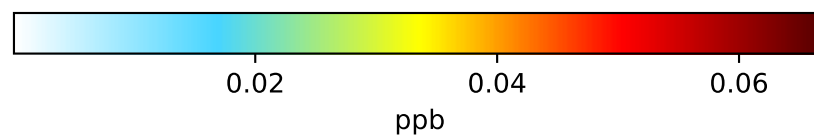
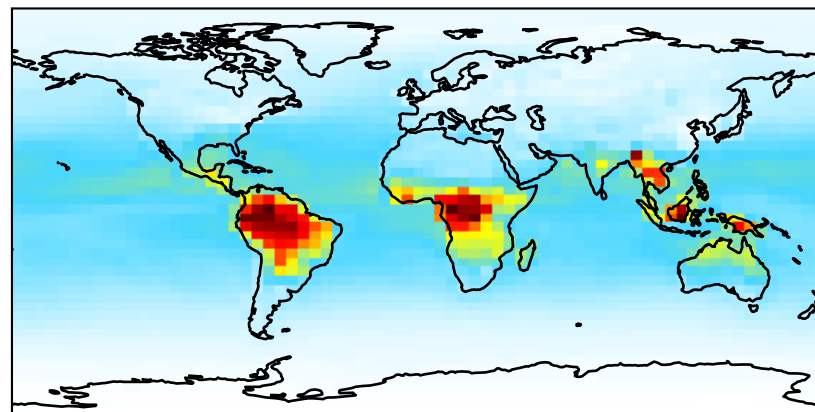


# SpeciesConc\_RO2 (Apr2019)

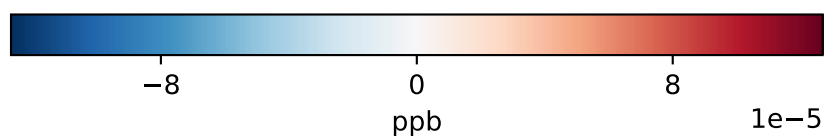
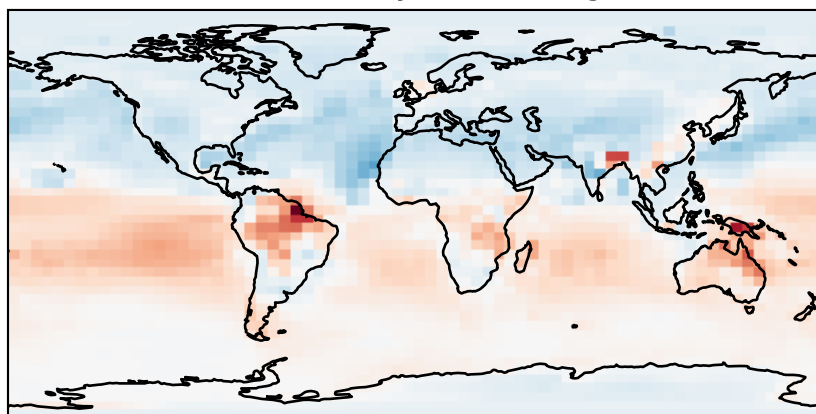
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



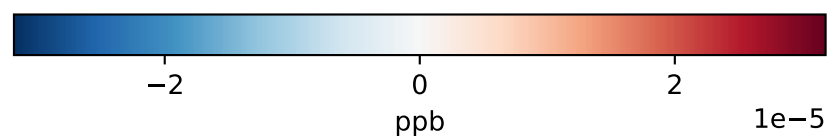
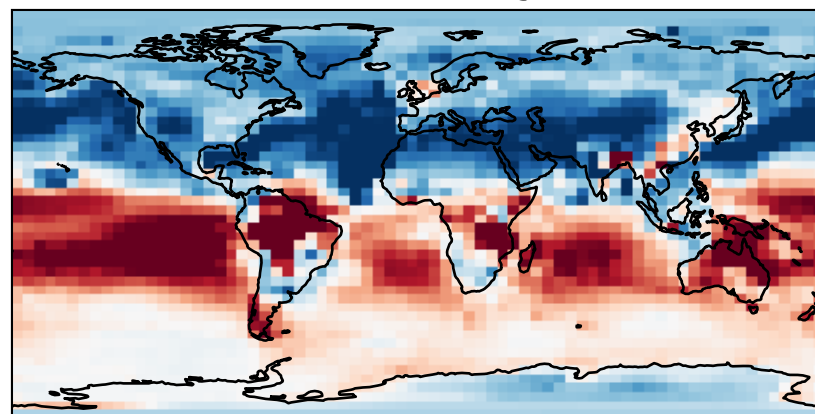
GCC\_14.0.0-rc.0 (Dev)  
4.0x5.0



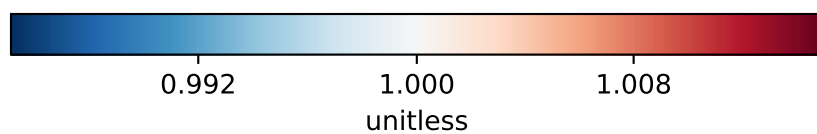
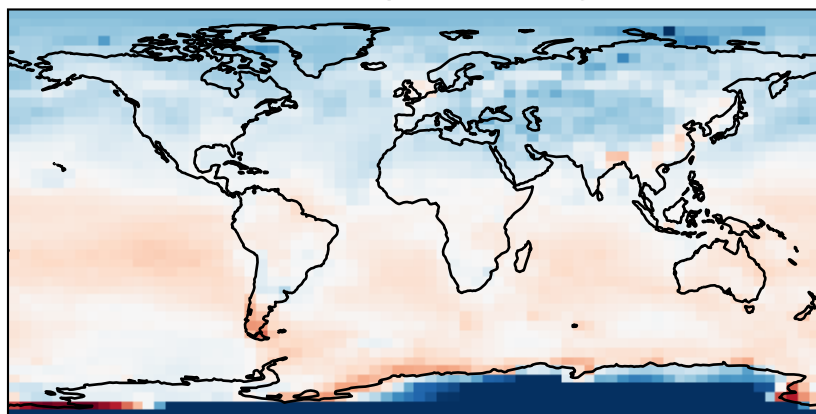
Difference  
Dev - Ref, Dynamic Range



Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

