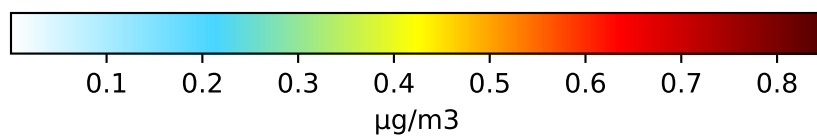
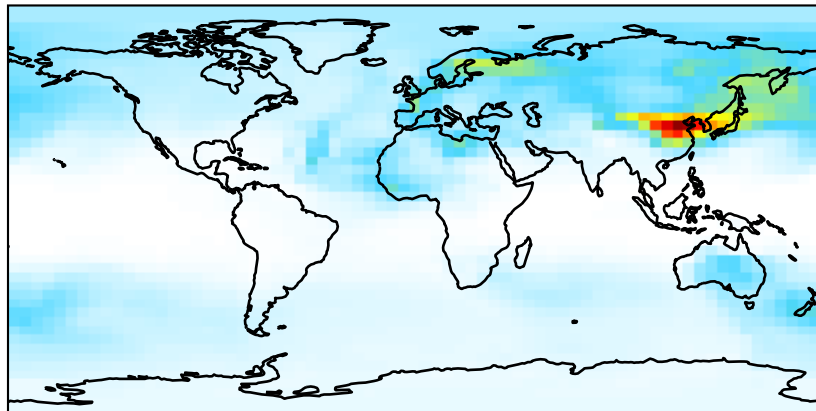
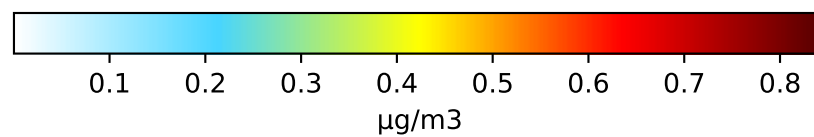
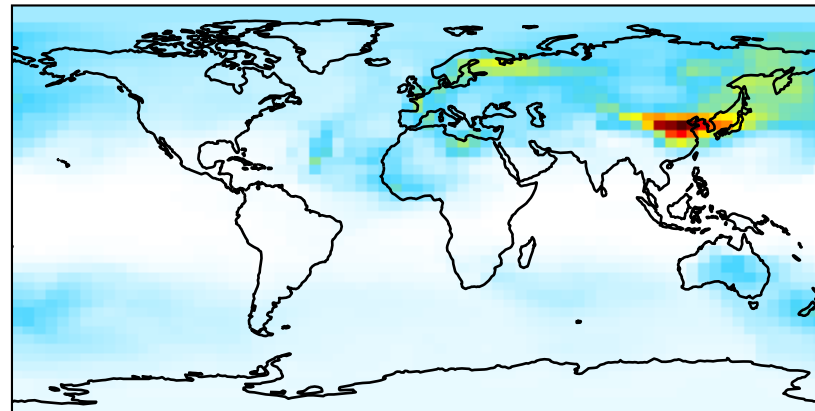


# SpeciesConc\_DST1 (Oct2019)

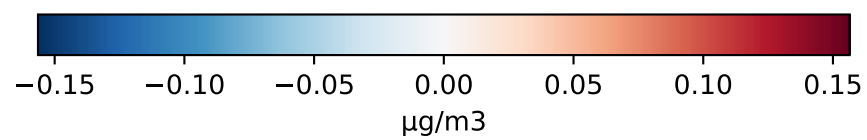
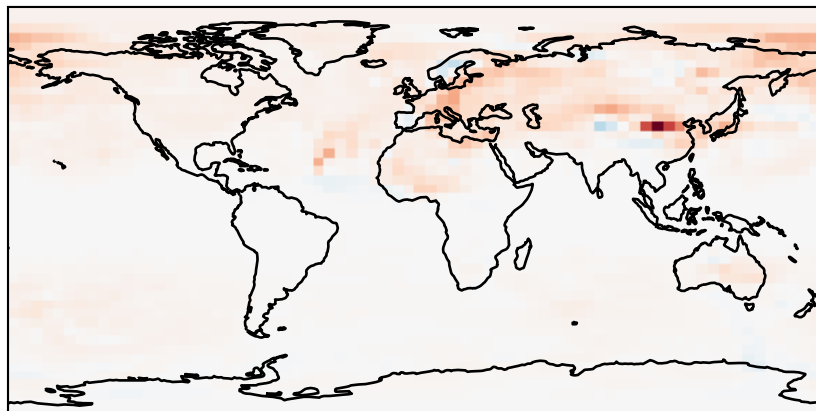
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



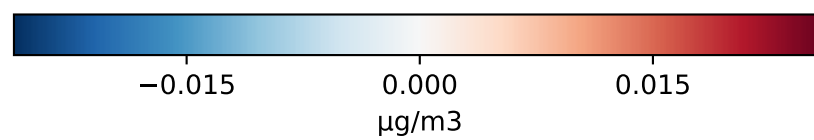
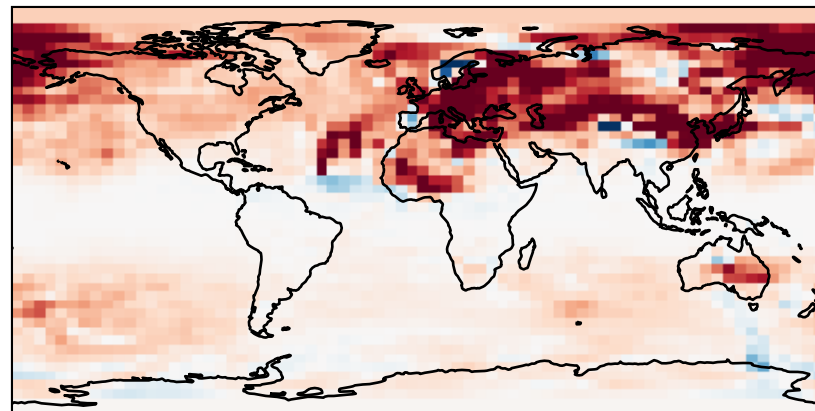
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



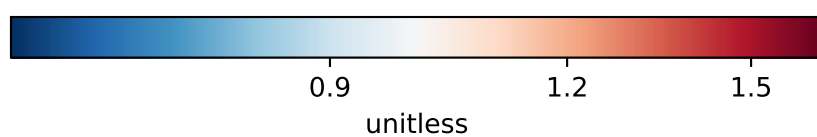
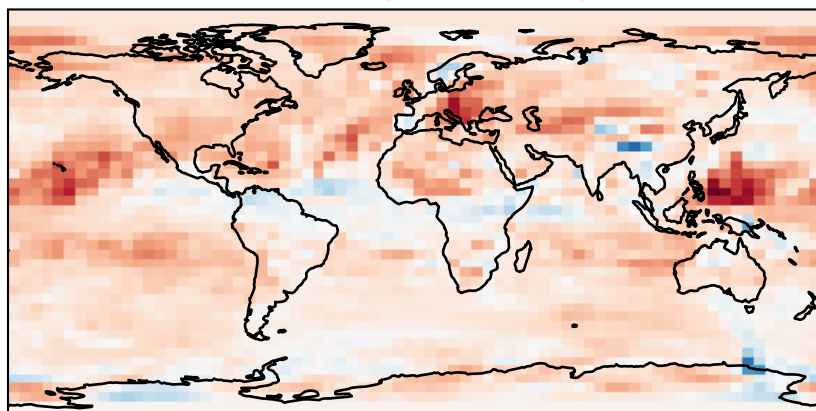
Difference  
Dev - Ref, Dynamic Range



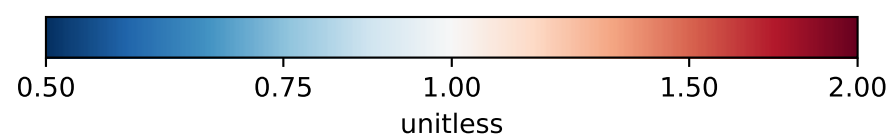
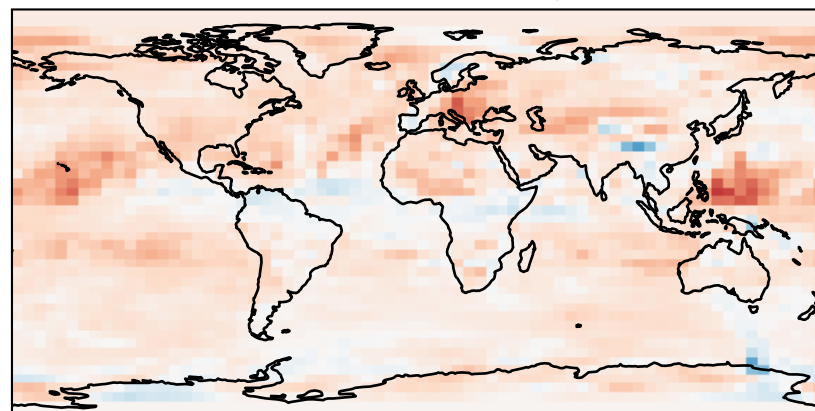
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

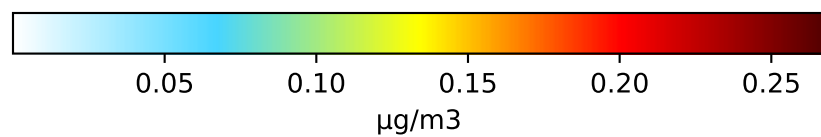
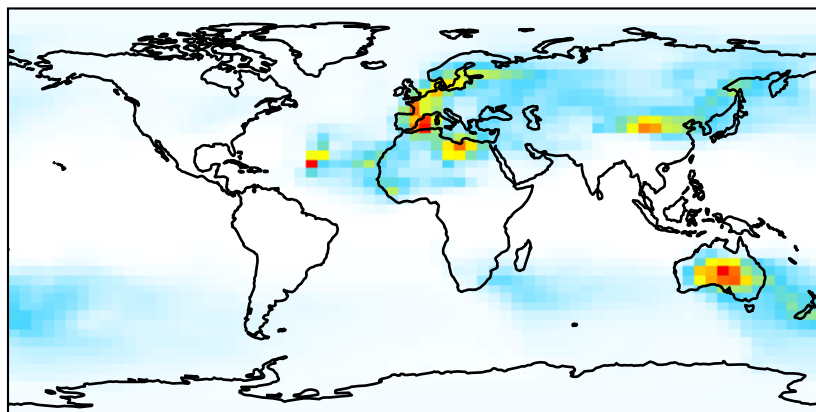


Ratio  
Dev/Ref, Fixed Range

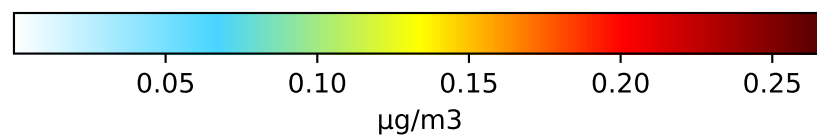
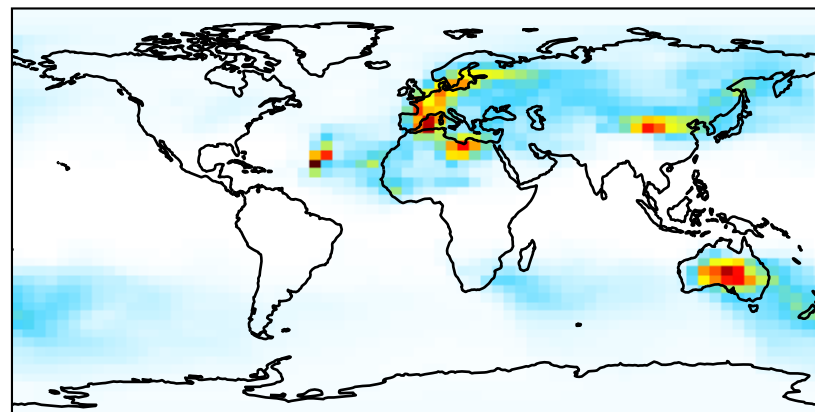


# SpeciesConc\_DST2 (Oct2019)

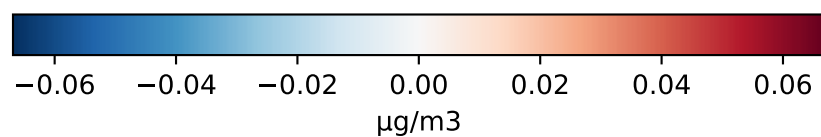
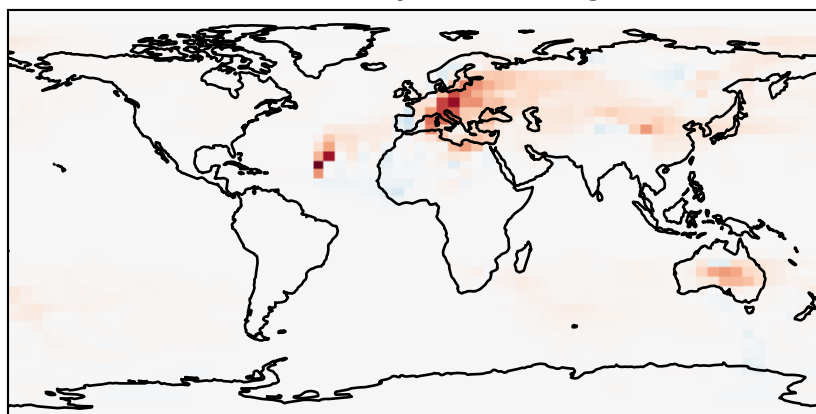
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



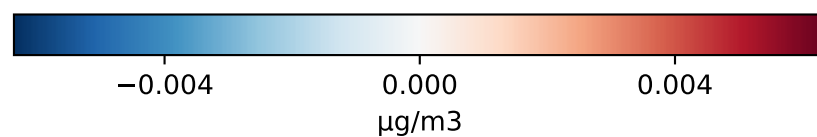
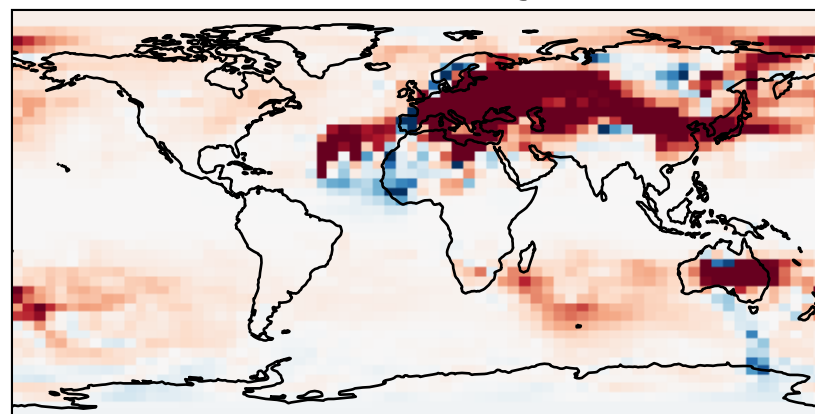
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



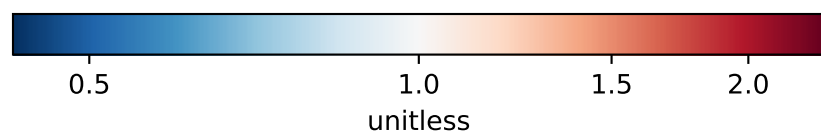
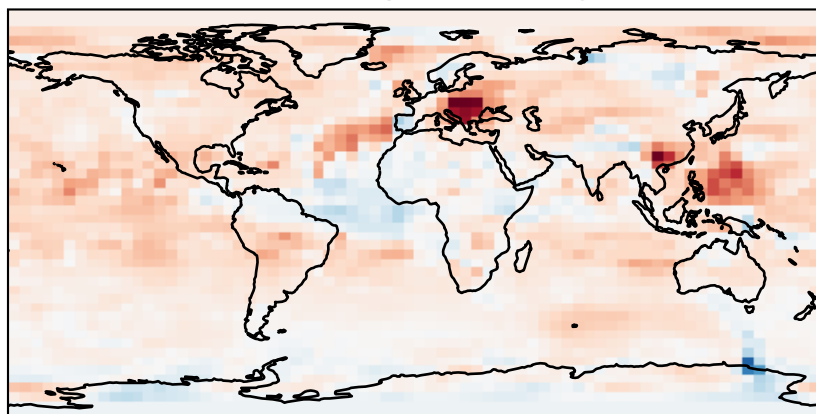
Difference  
Dev - Ref, Dynamic Range



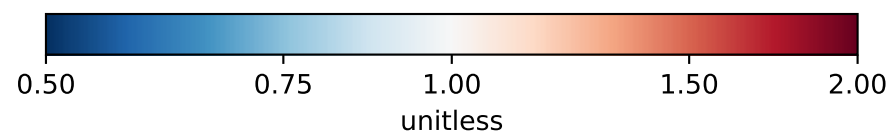
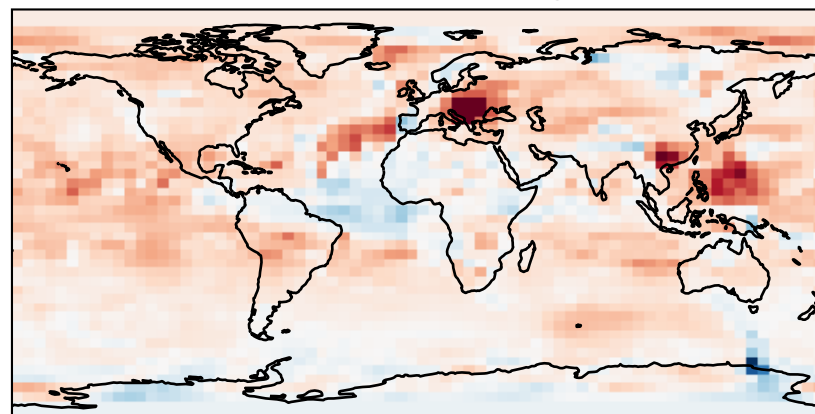
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

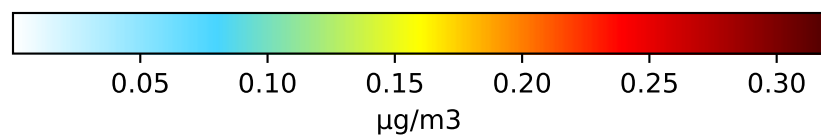
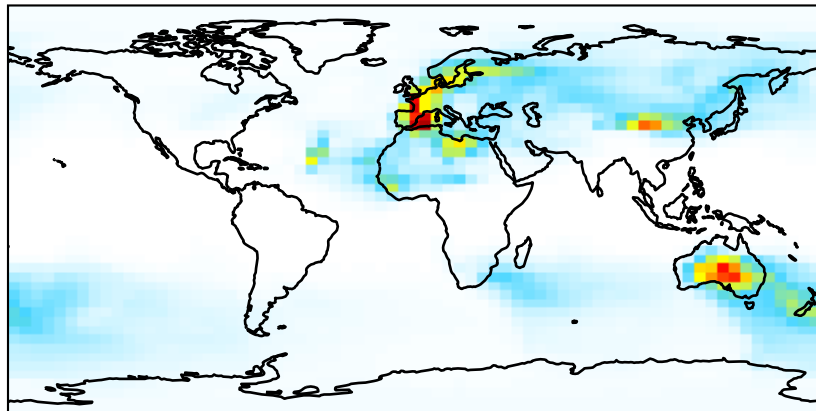


Ratio  
Dev/Ref, Fixed Range

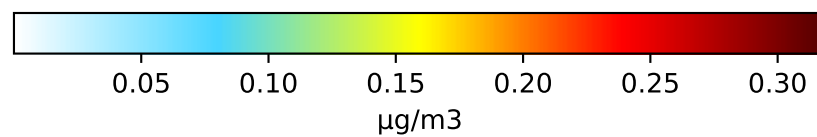
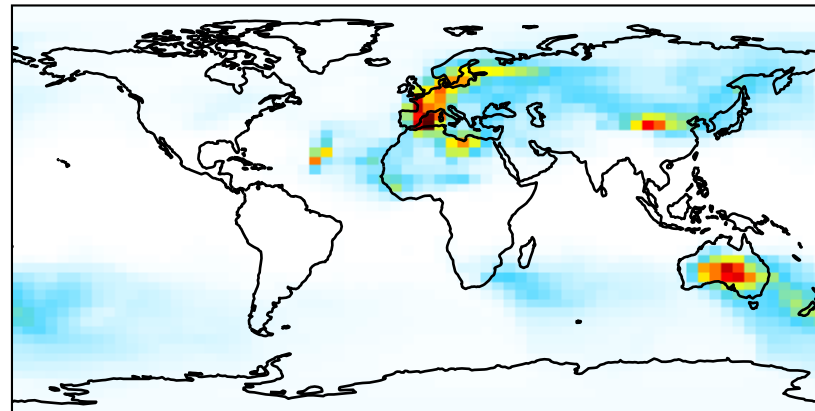


# SpeciesConc\_DST3 (Oct2019)

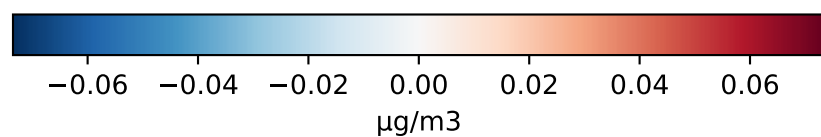
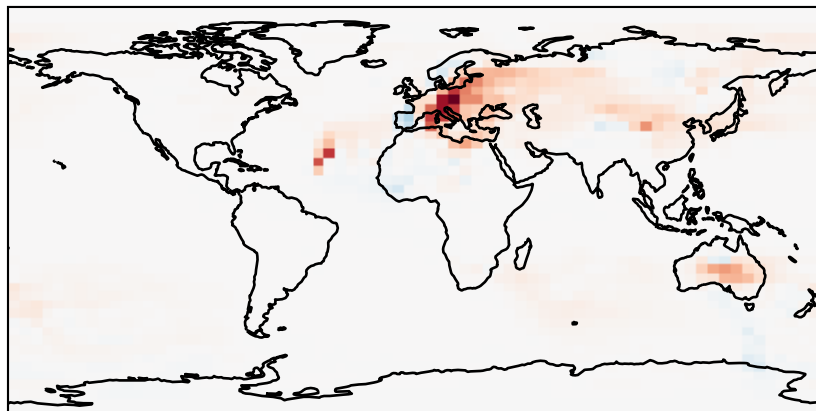
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



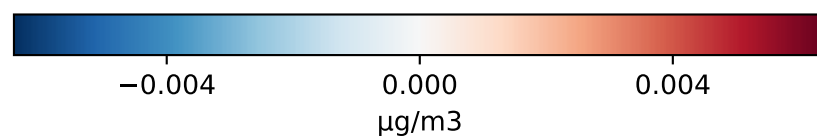
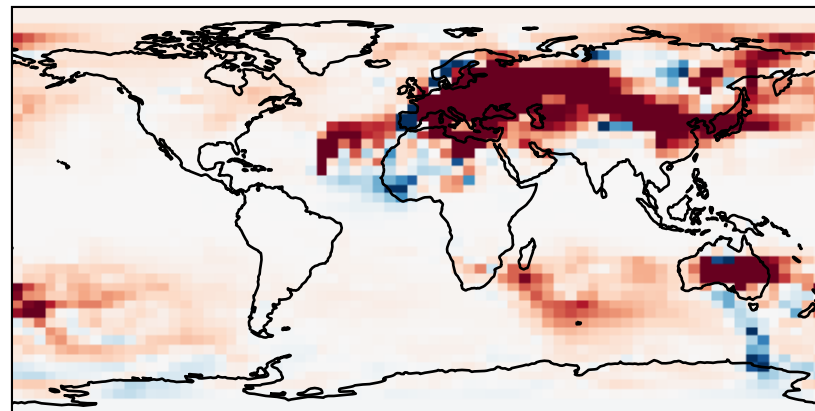
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



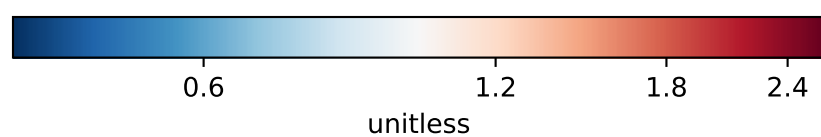
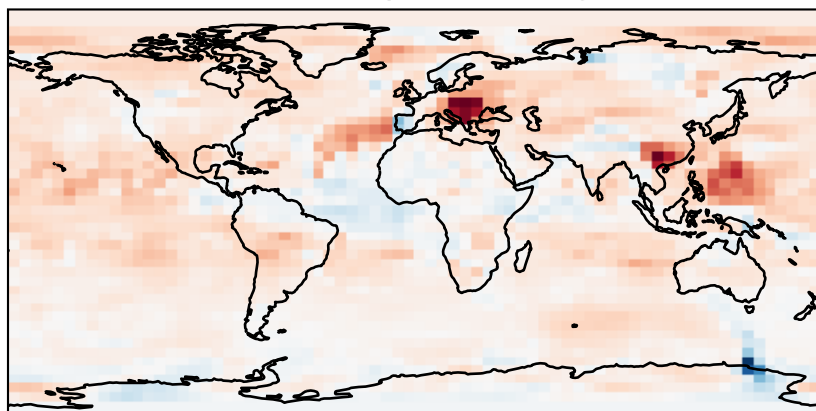
Difference  
Dev - Ref, Dynamic Range



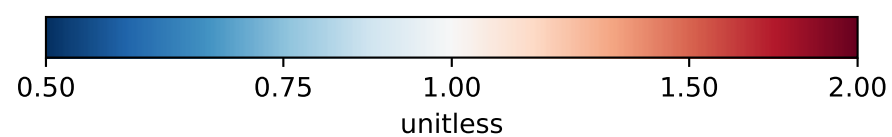
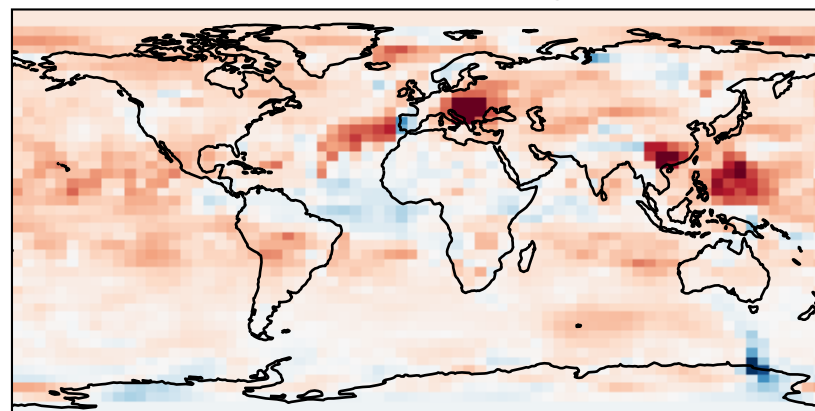
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

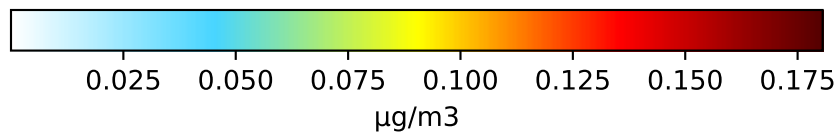
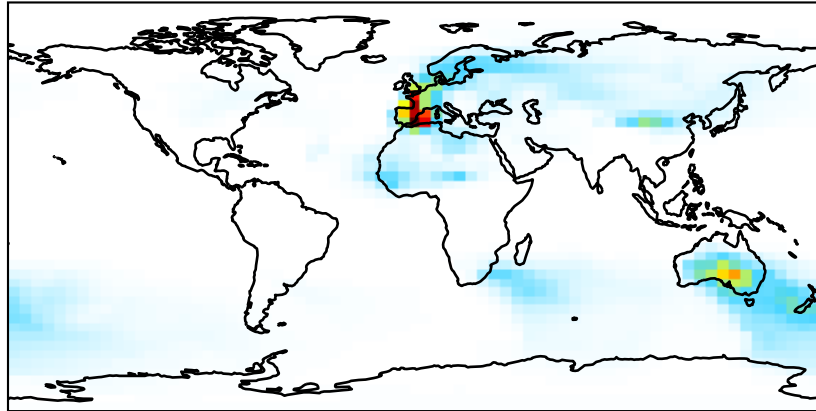


Ratio  
Dev/Ref, Fixed Range

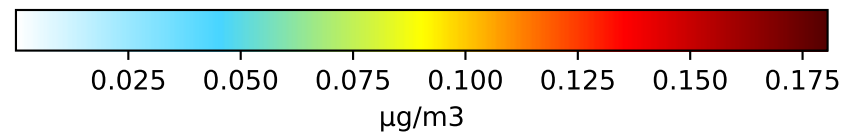
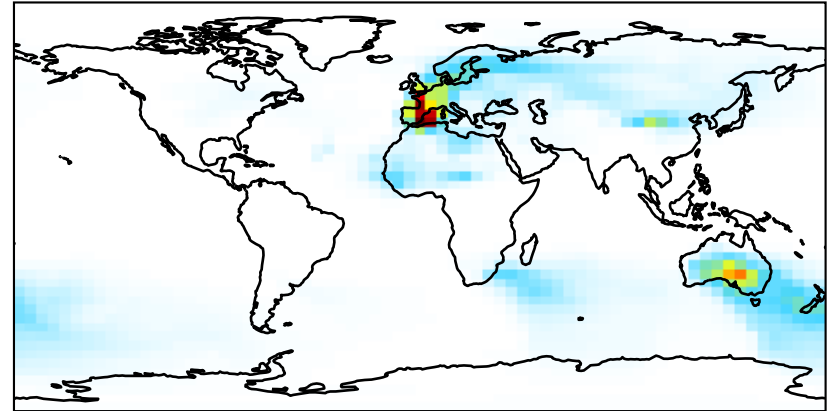


# SpeciesConc\_DST4 (Oct2019)

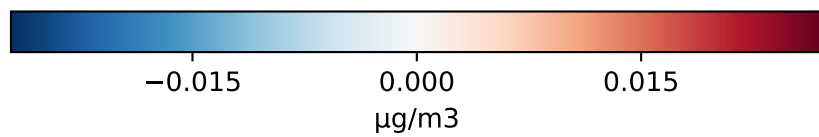
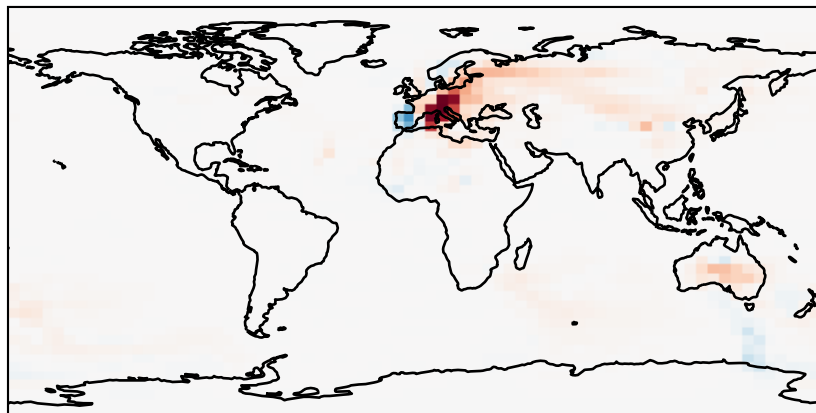
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



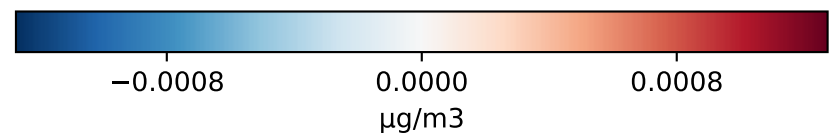
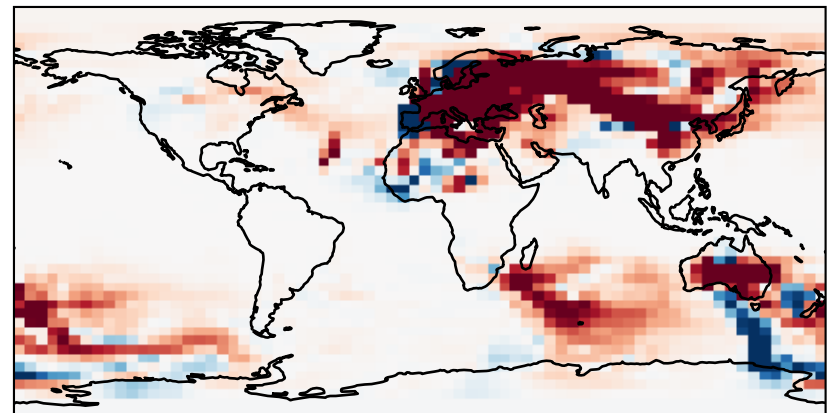
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



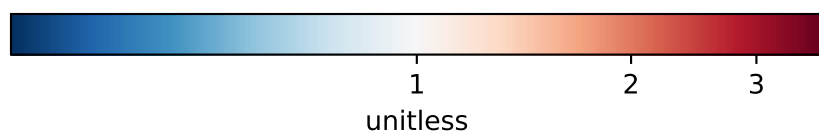
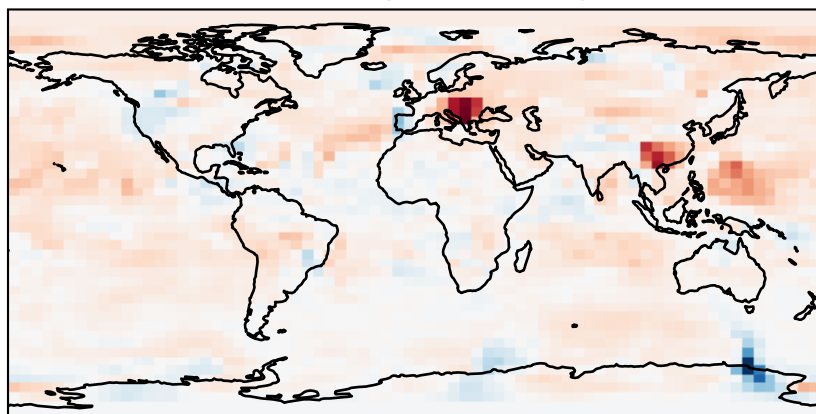
Difference  
Dev - Ref, Dynamic Range



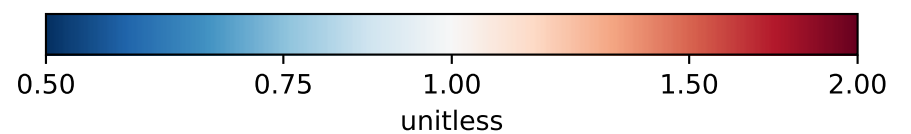
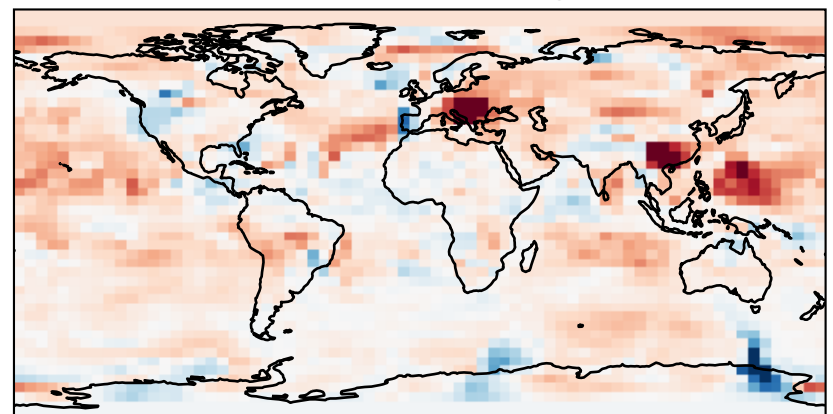
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

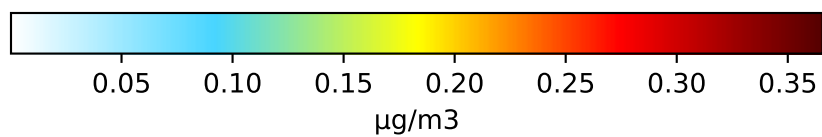
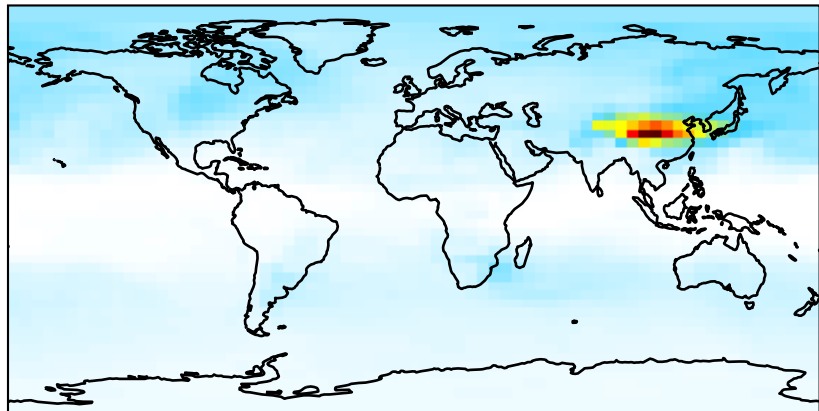


Ratio  
Dev/Ref, Fixed Range

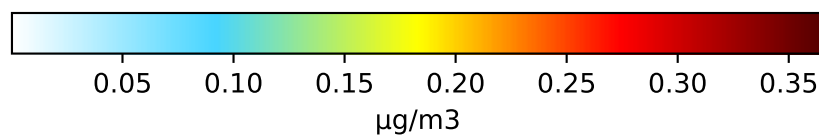
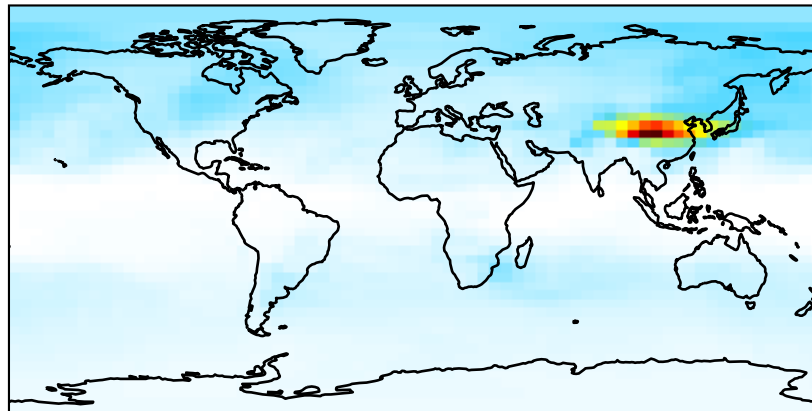


# SpeciesConc\_NH4 (Oct2019)

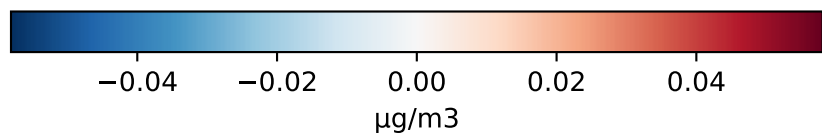
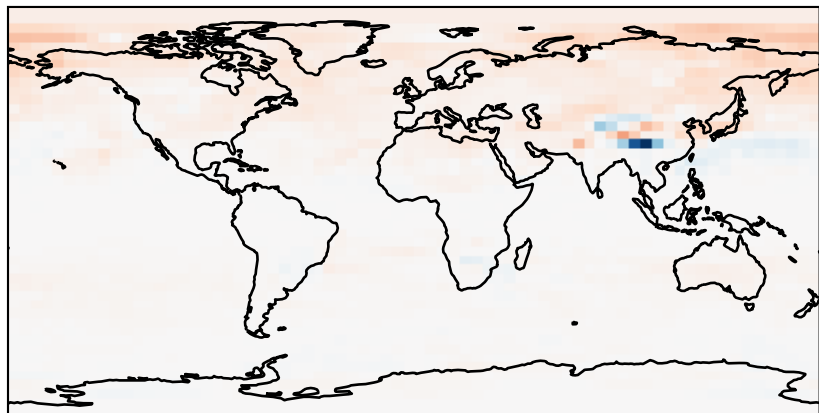
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



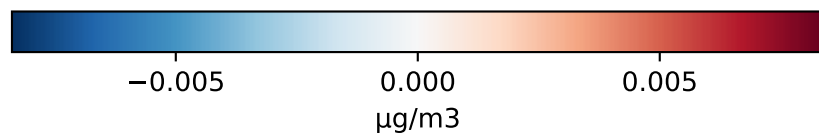
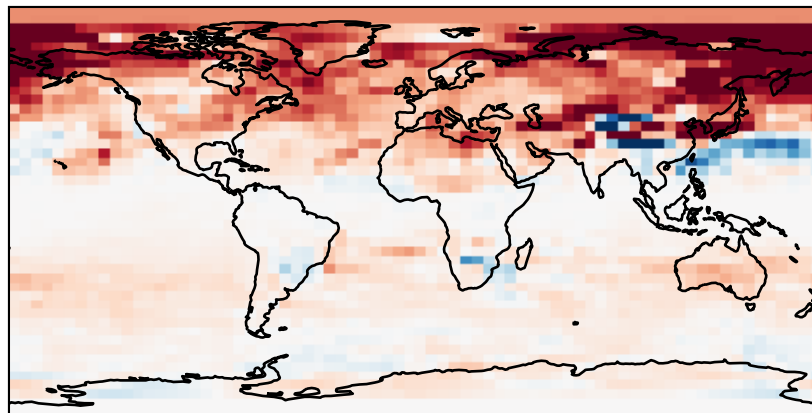
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



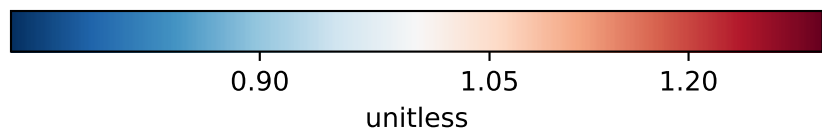
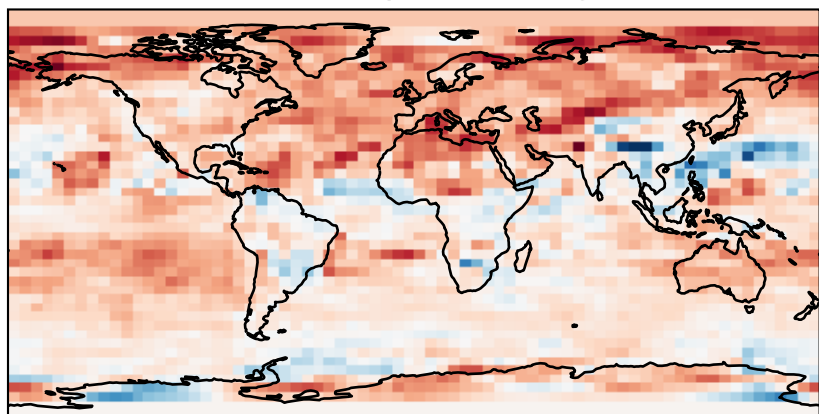
Difference  
Dev - Ref, Dynamic Range



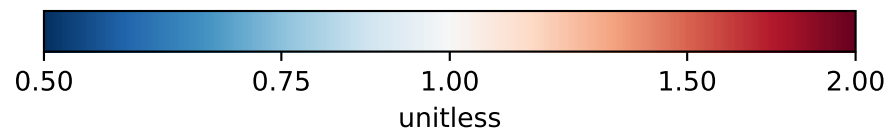
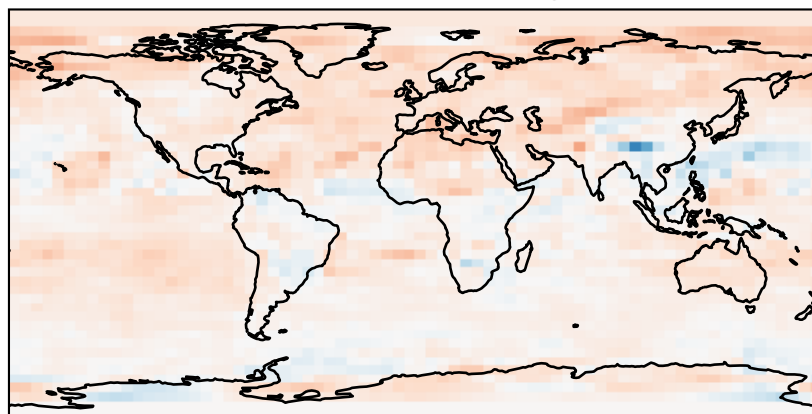
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

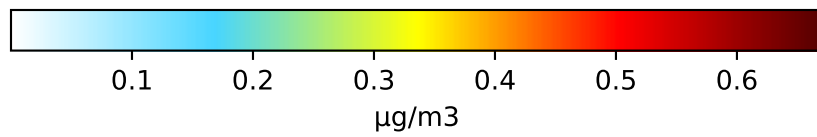
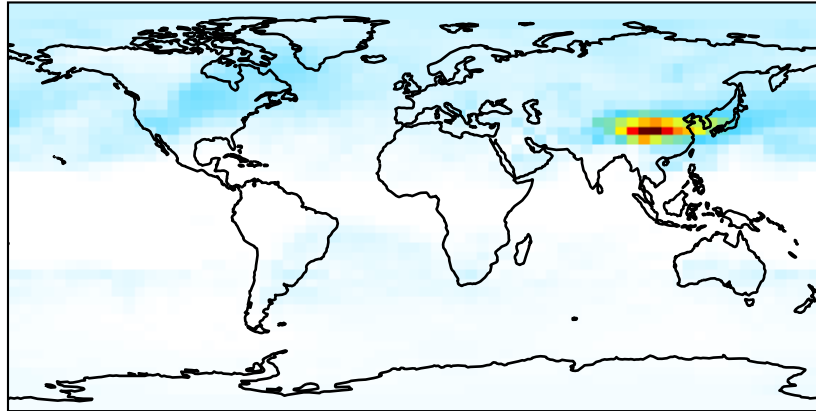


Ratio  
Dev/Ref, Fixed Range

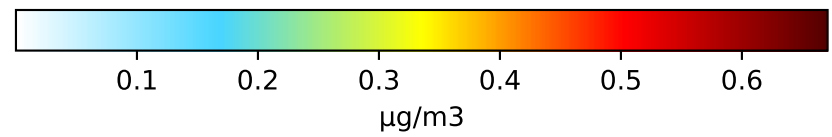
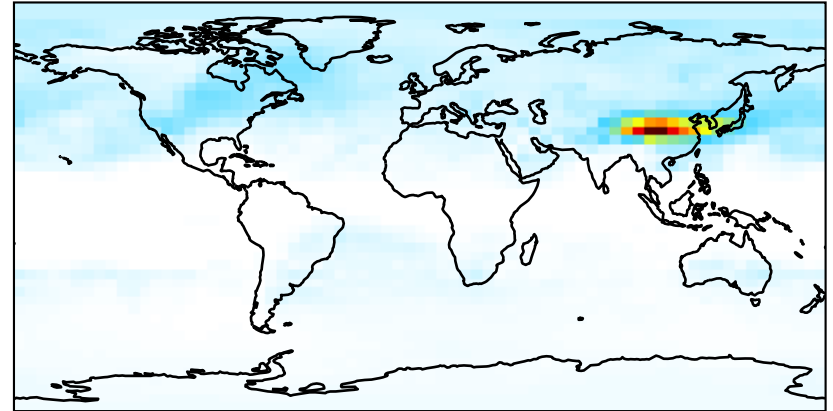


# SpeciesConc\_NIT (Oct2019)

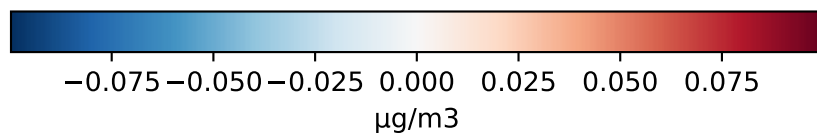
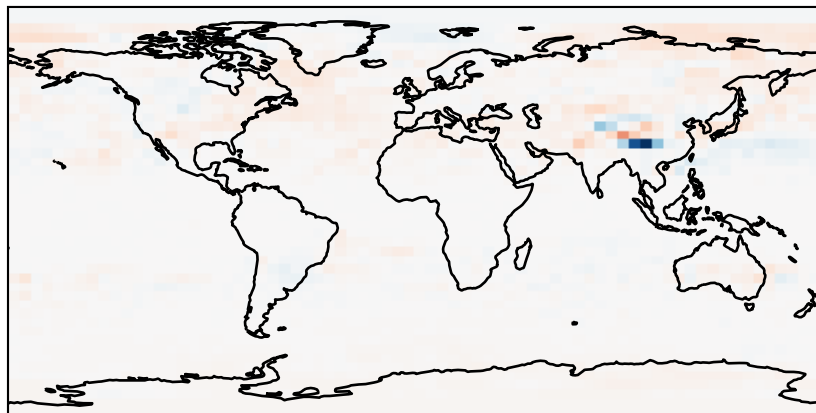
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



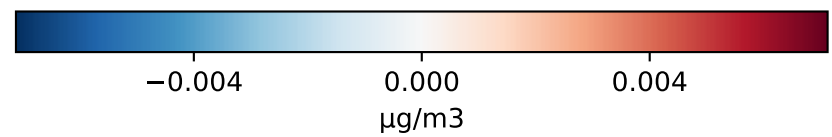
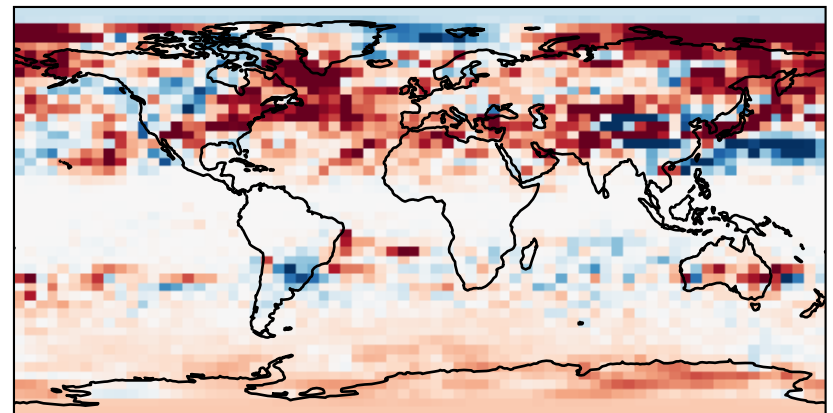
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



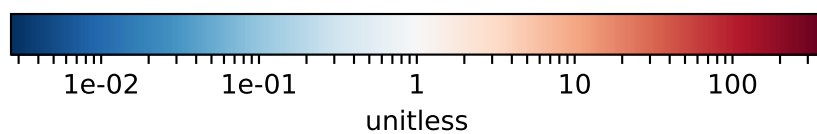
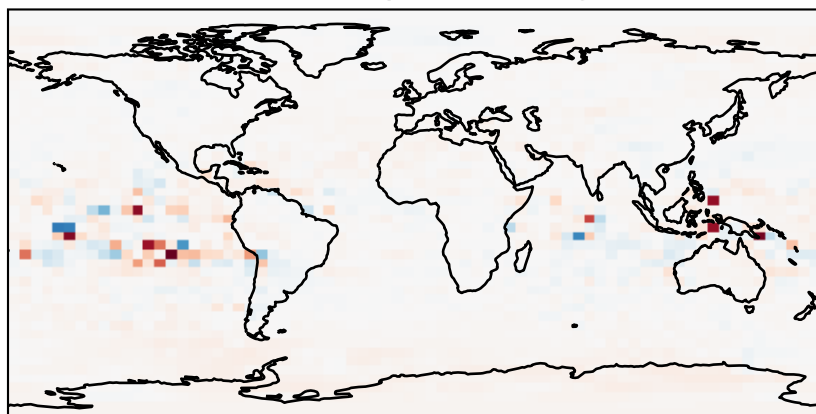
Difference  
Dev - Ref, Dynamic Range



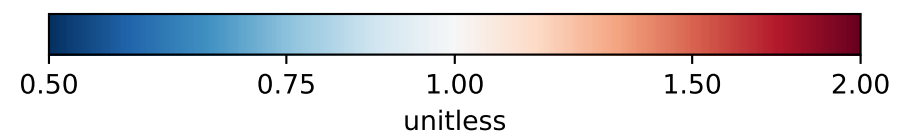
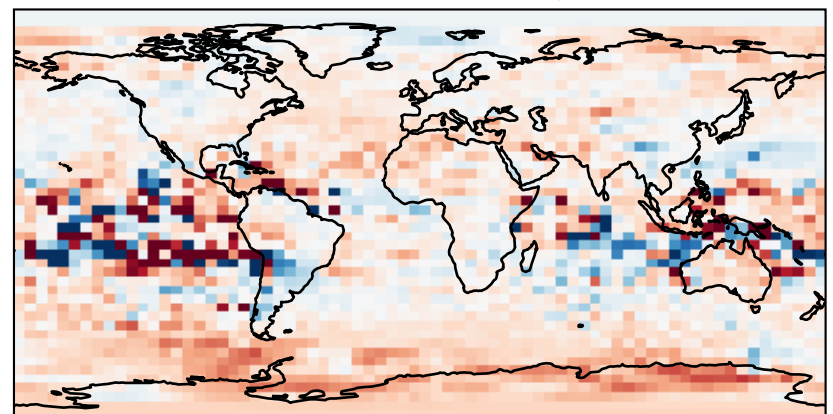
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

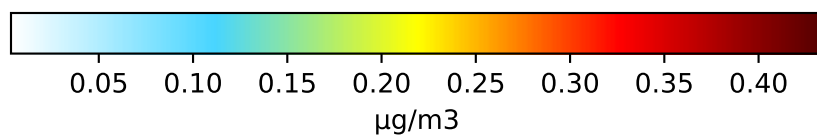
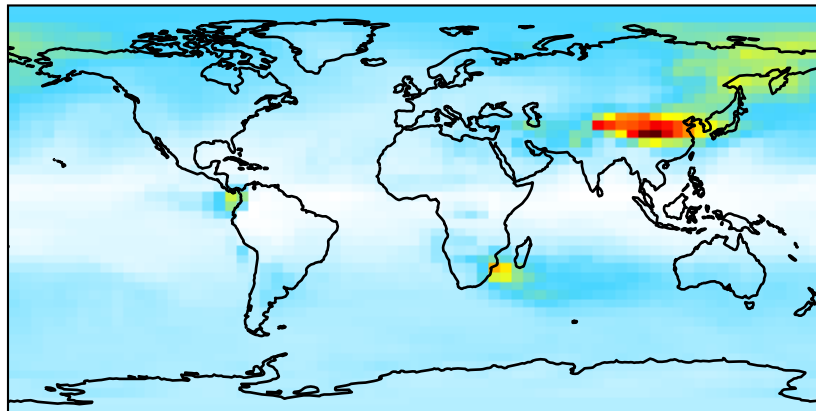


Ratio  
Dev/Ref, Fixed Range

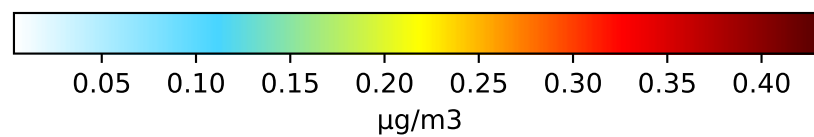
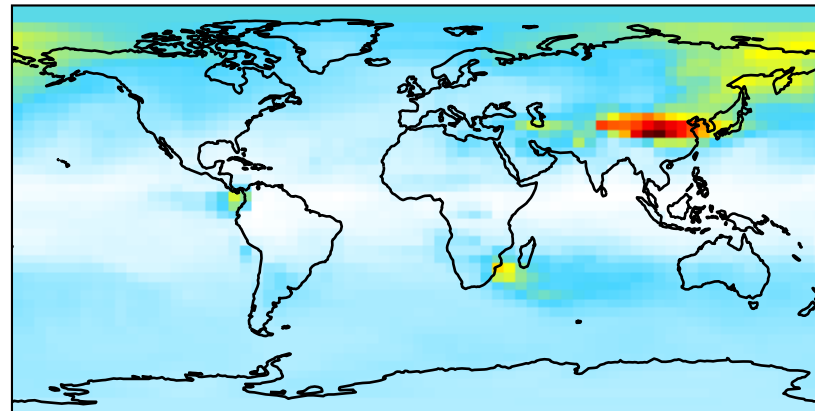


# SpeciesConc\_S04 (Oct2019)

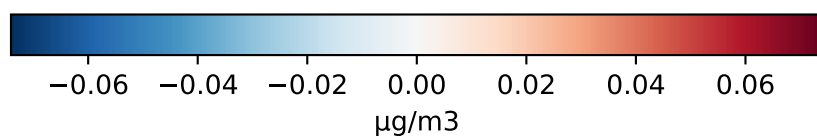
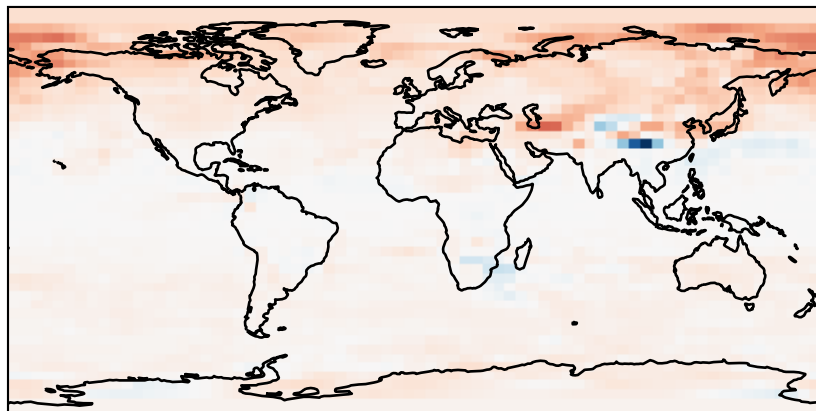
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



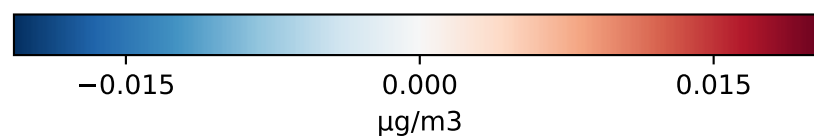
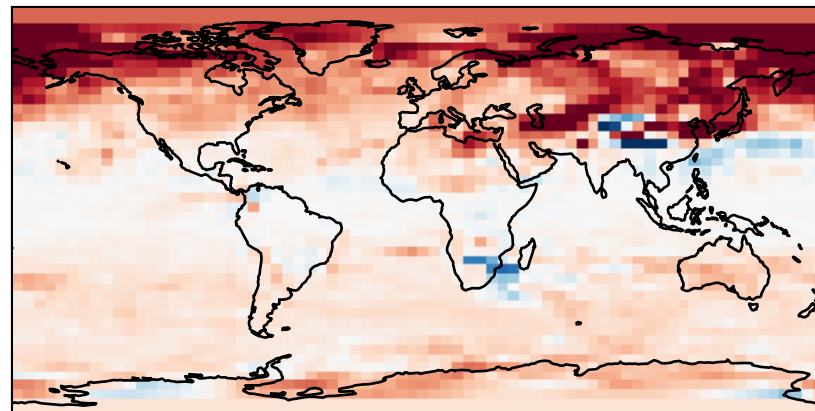
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



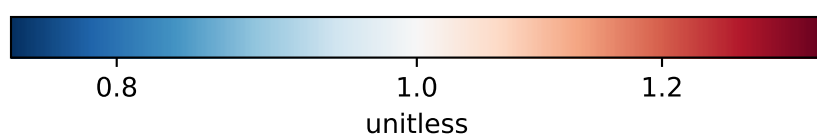
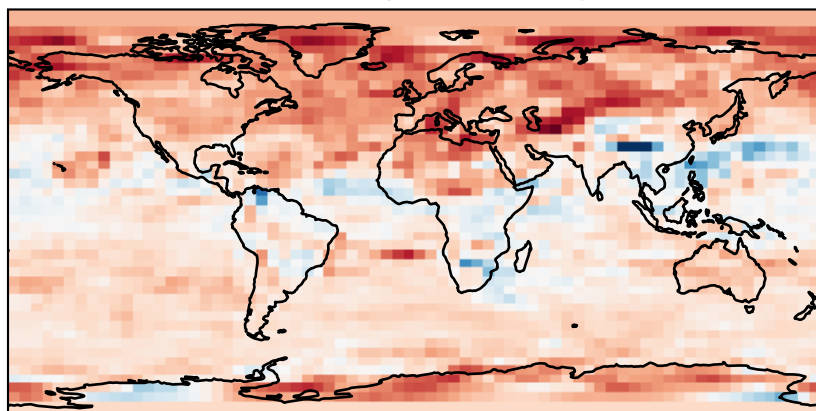
Difference  
Dev - Ref, Dynamic Range



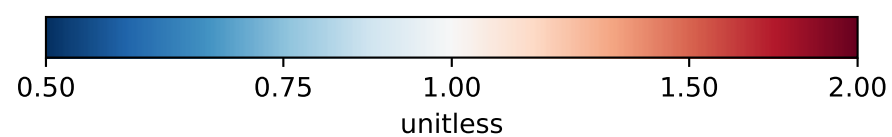
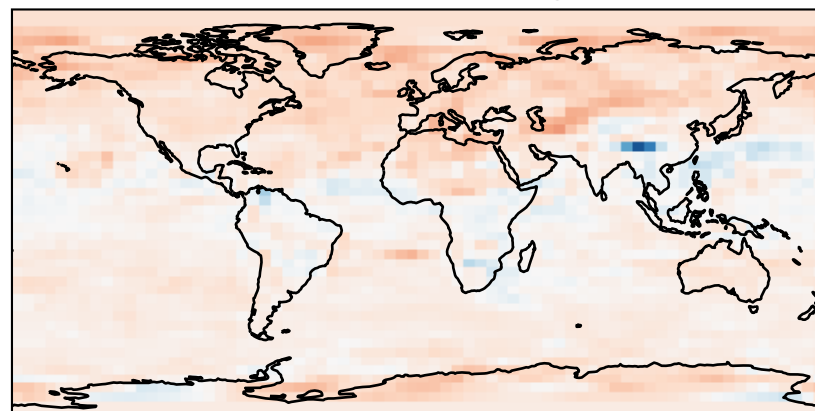
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

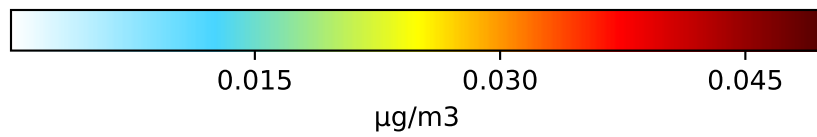
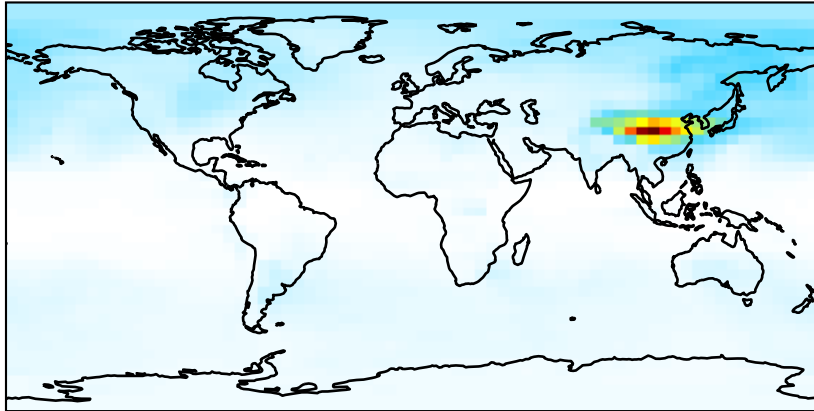


Ratio  
Dev/Ref, Fixed Range

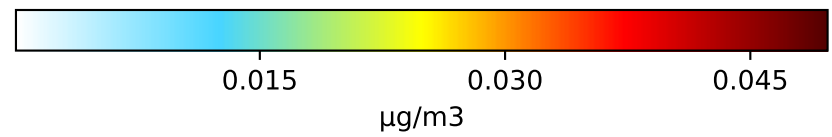
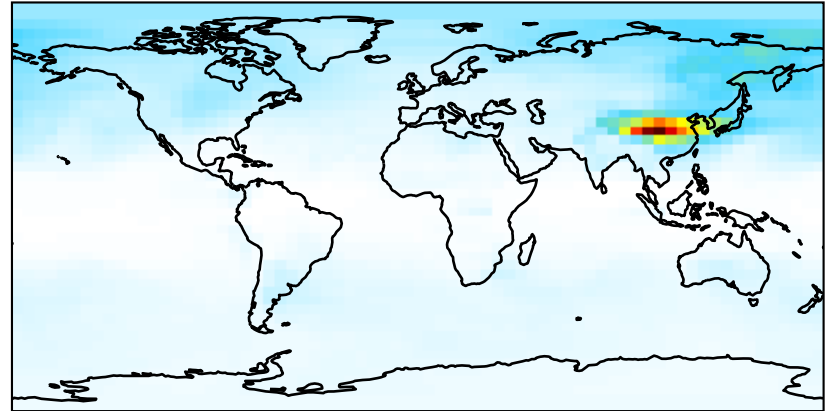


# SpeciesConc\_HMS (Oct2019)

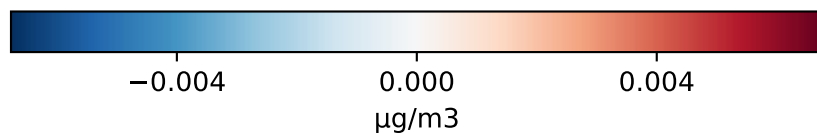
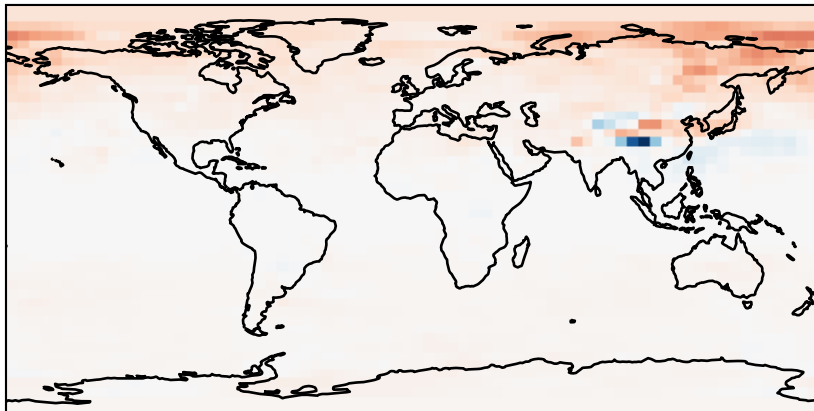
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



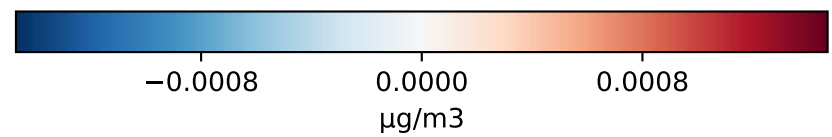
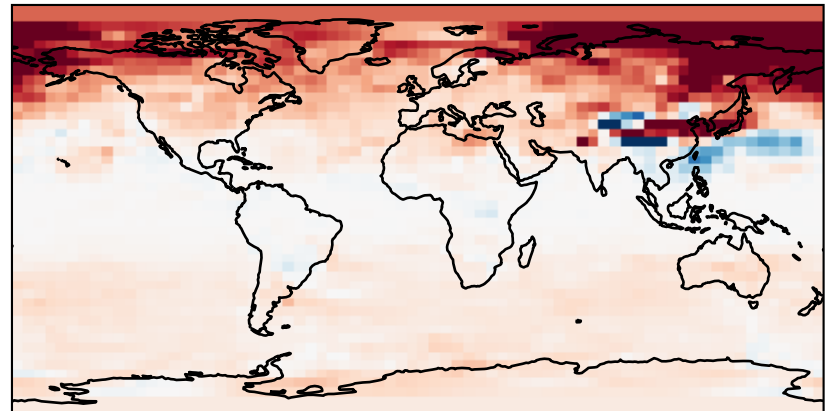
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



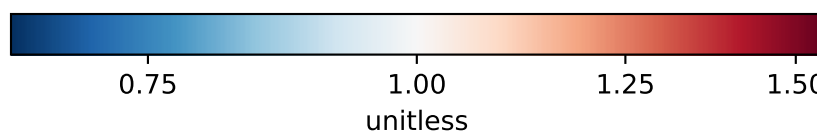
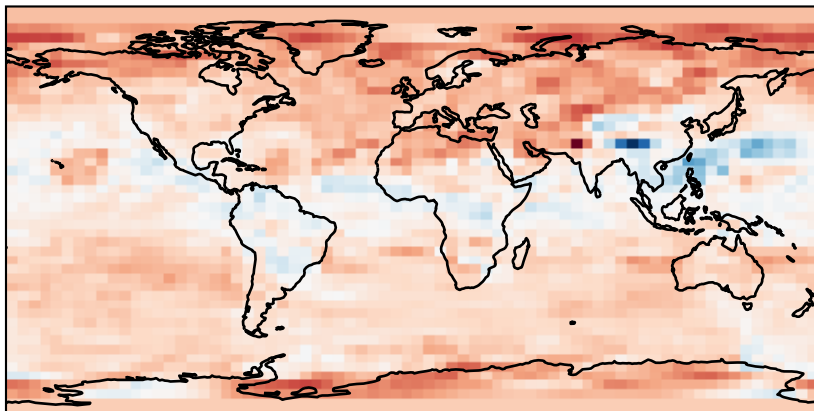
Difference  
Dev - Ref, Dynamic Range



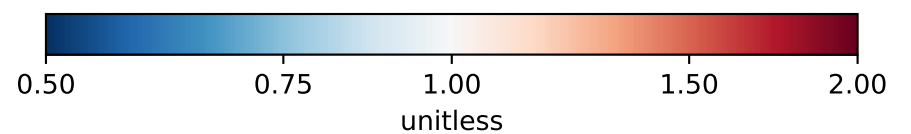
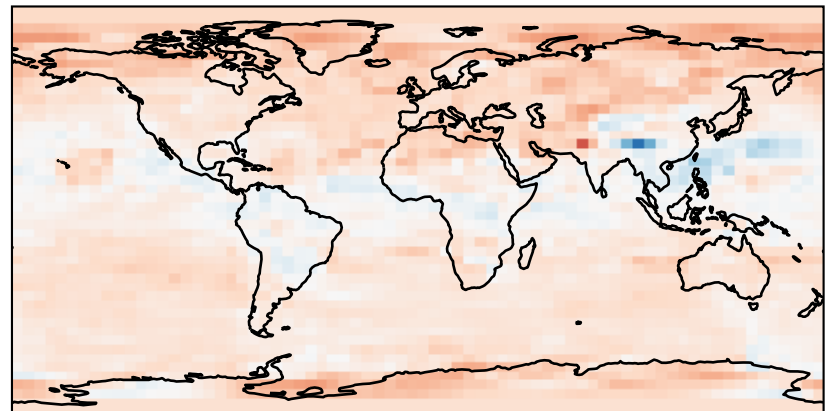
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



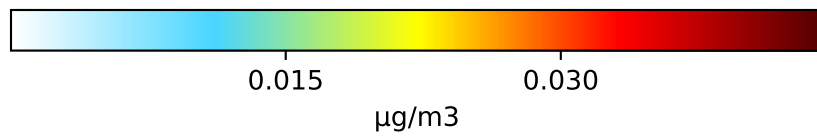
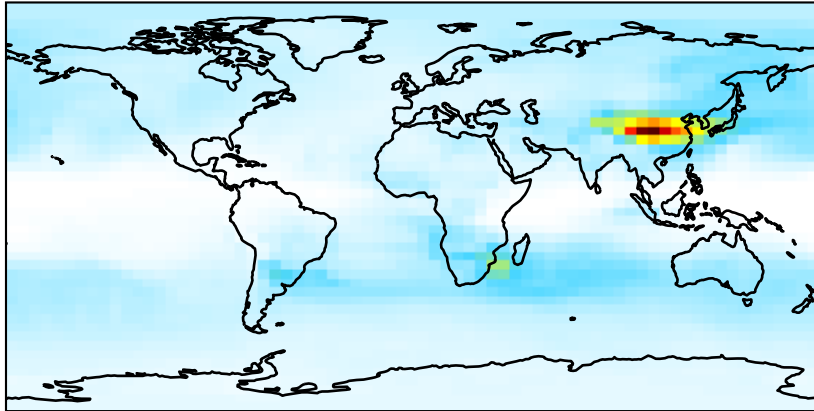
Ratio  
Dev/Ref, Fixed Range



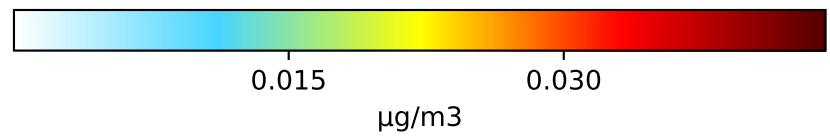
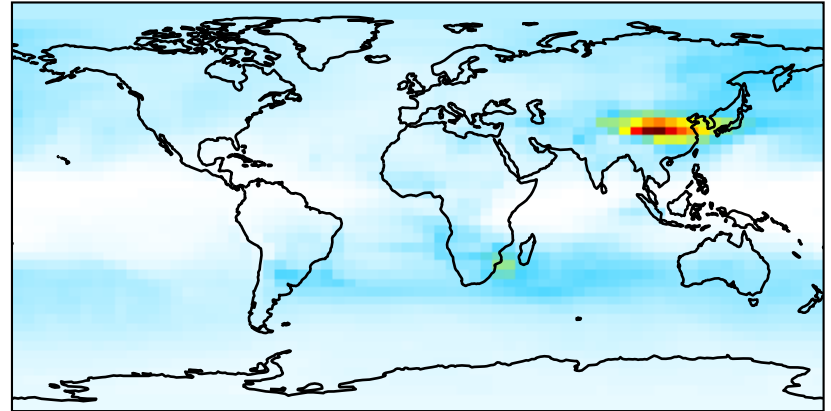


# SpeciesConc\_BCPI (Oct2019)

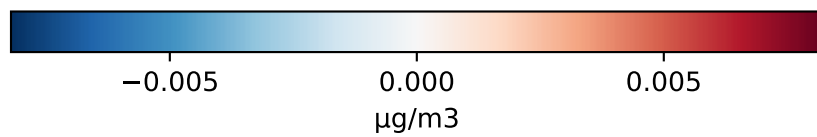
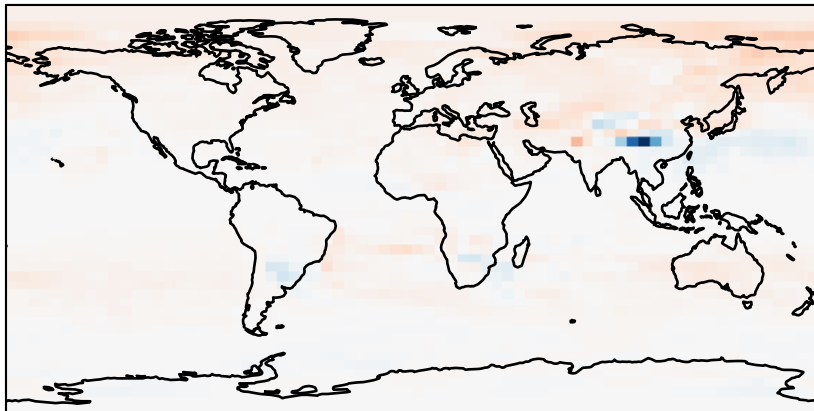
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



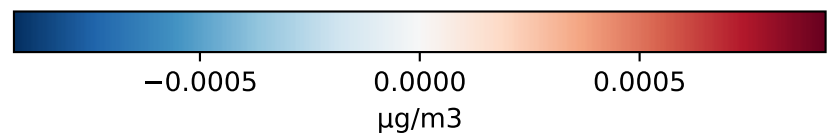
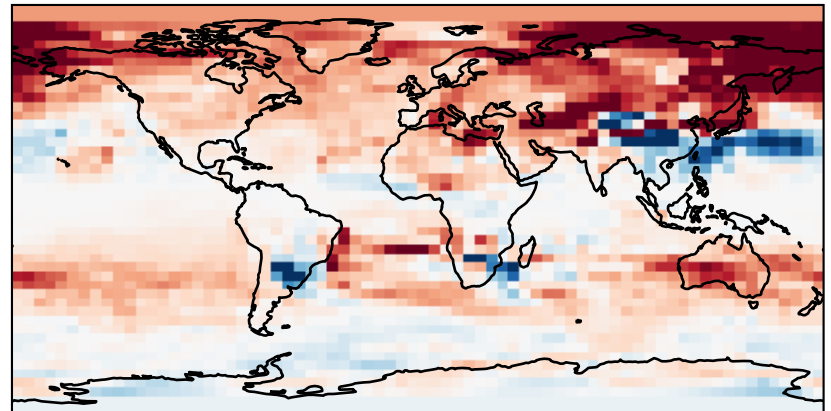
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



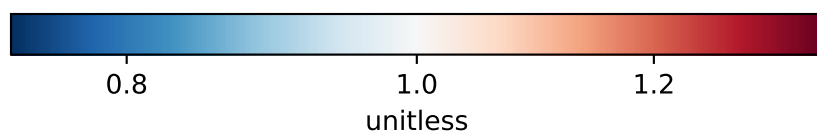
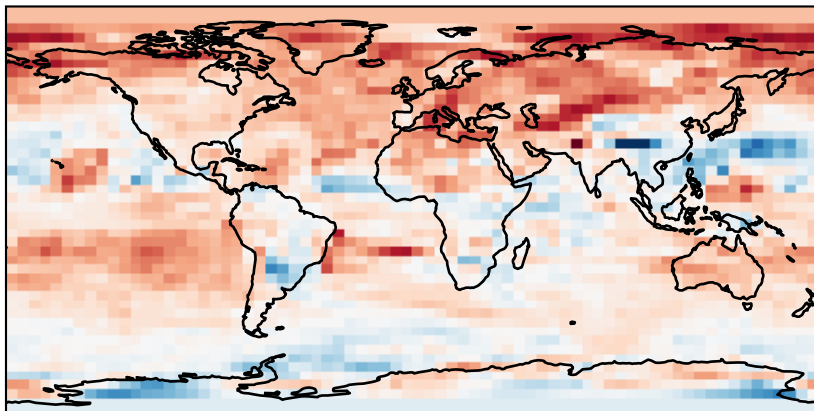
Difference  
Dev - Ref, Dynamic Range



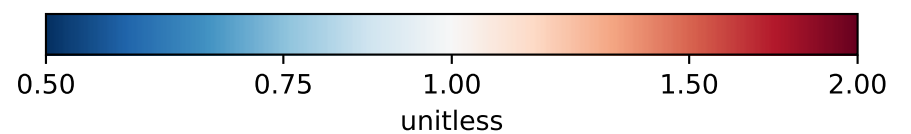
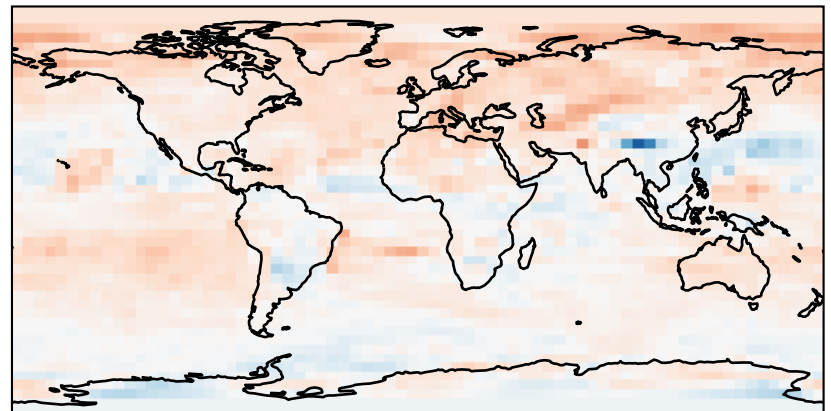
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

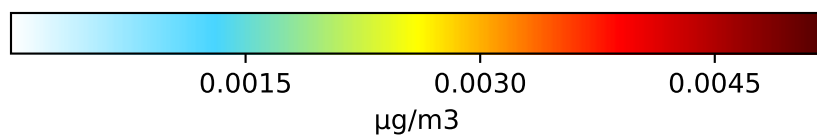
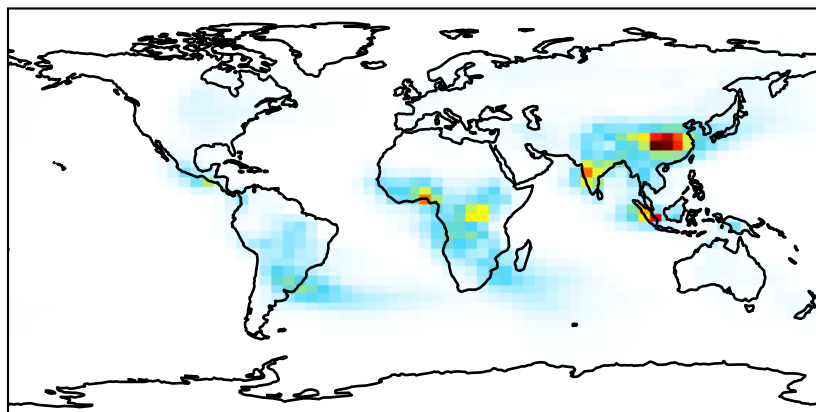


Ratio  
Dev/Ref, Fixed Range

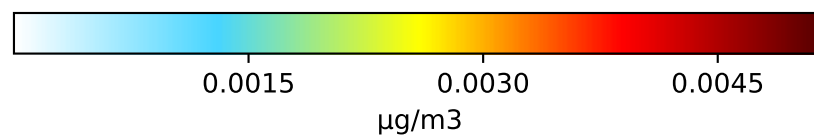
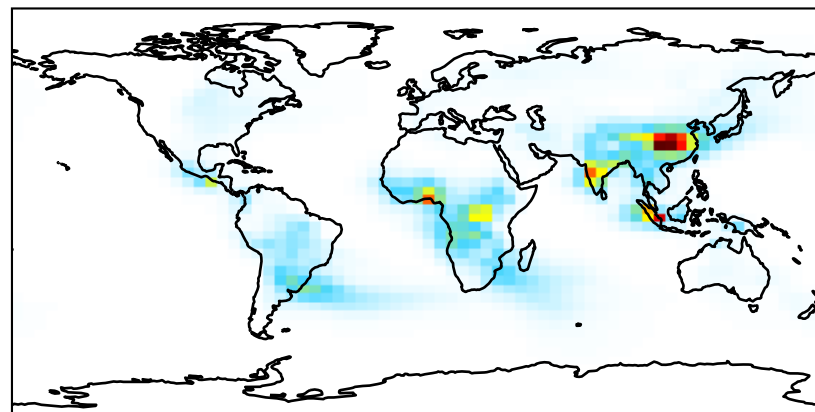


# SpeciesConc\_BCPO (Oct2019)

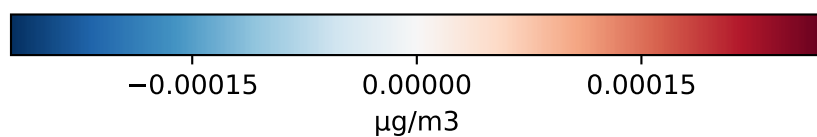
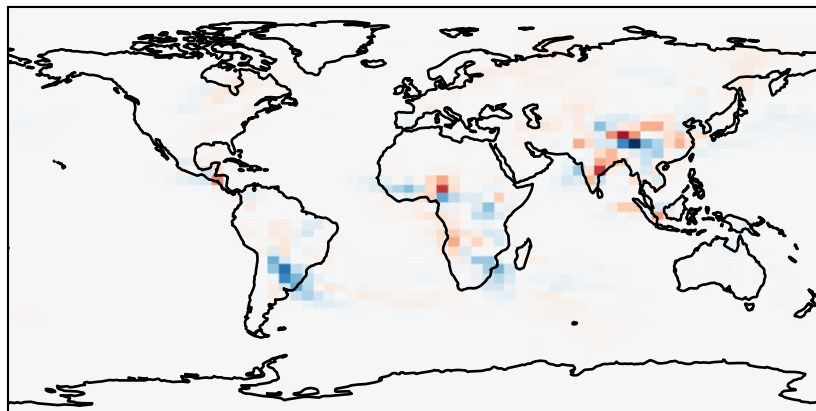
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



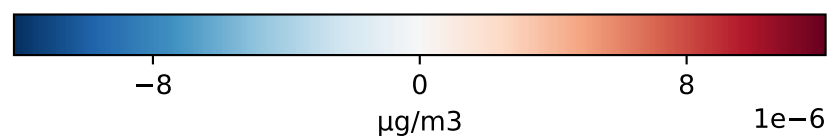
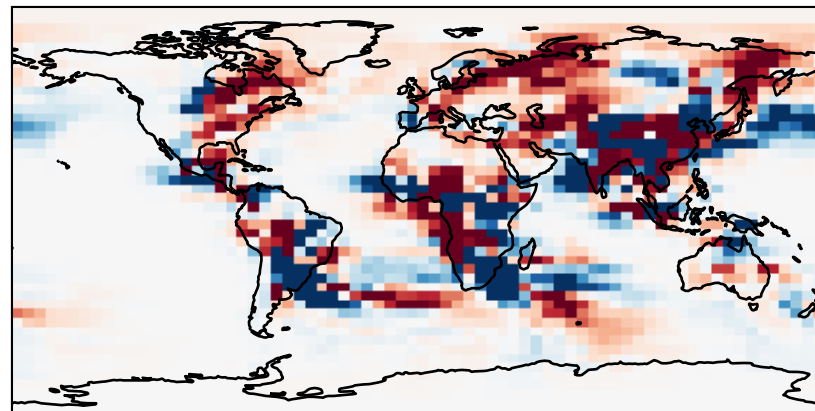
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



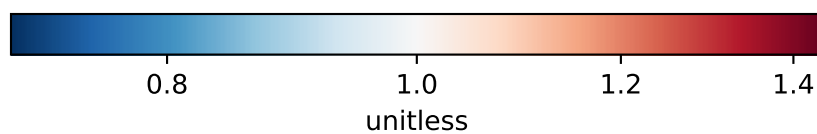
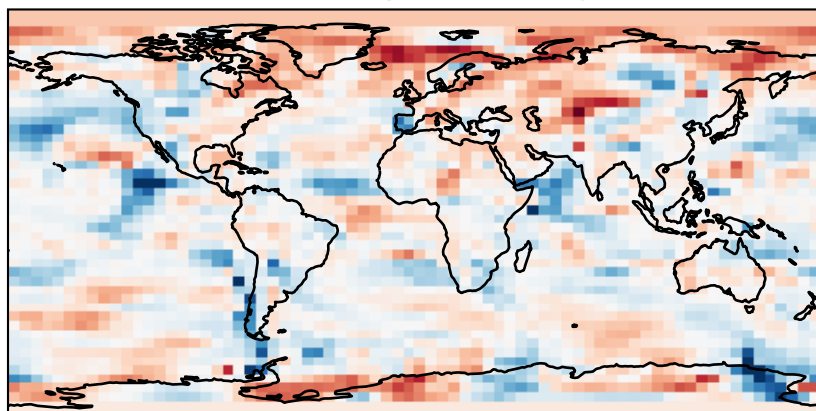
Difference  
Dev - Ref, Dynamic Range



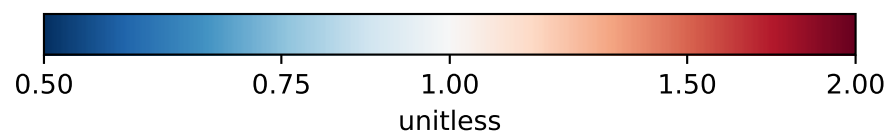
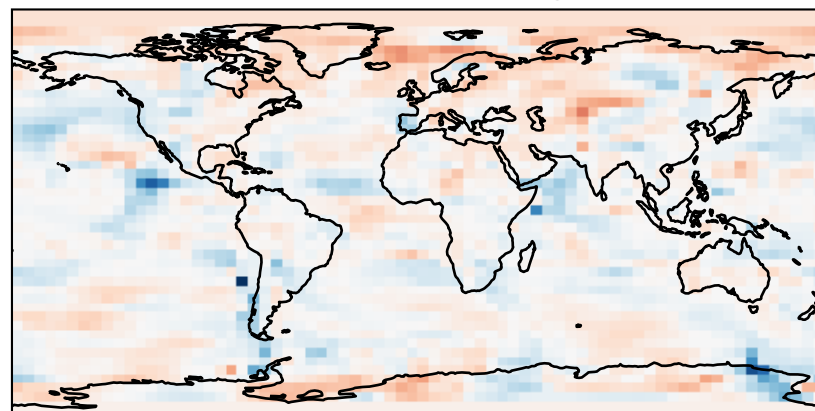
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

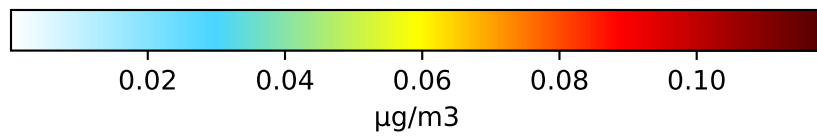
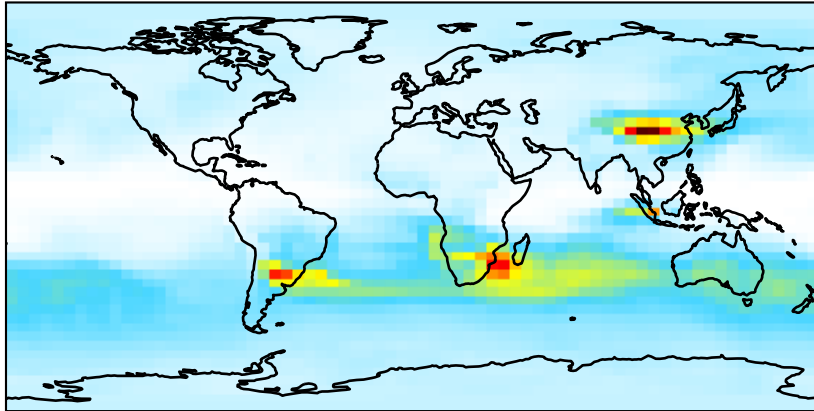


Ratio  
Dev/Ref, Fixed Range

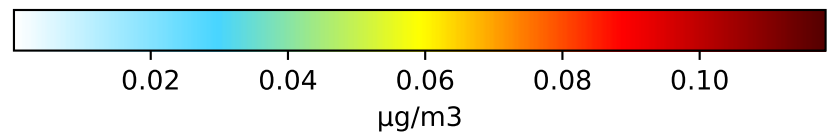
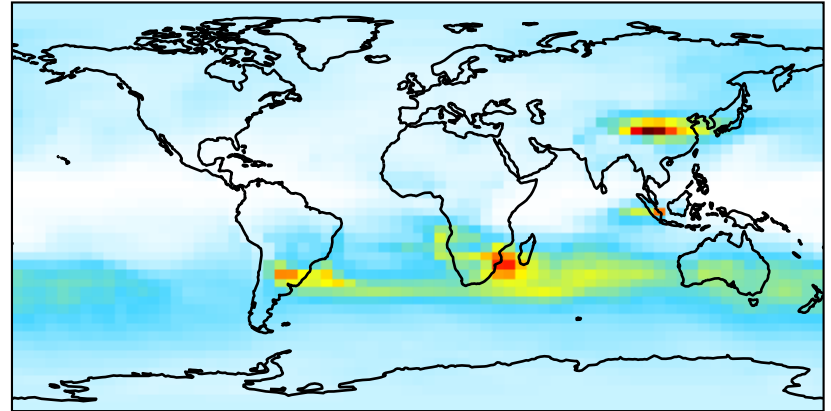


# SpeciesConc\_OCPI (Oct2019)

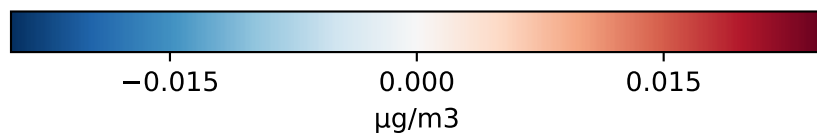
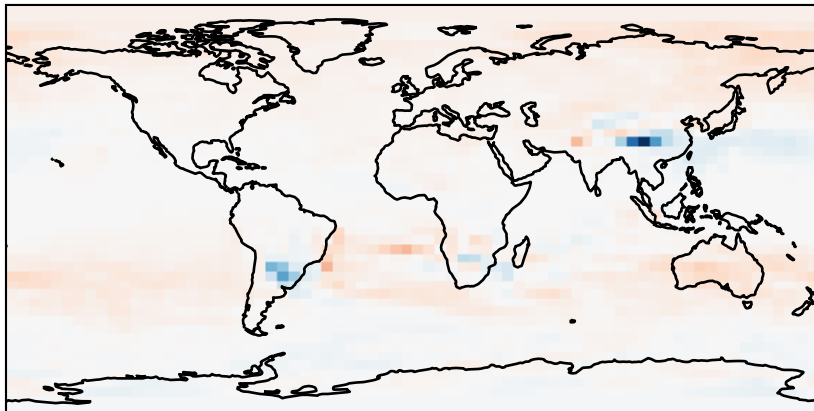
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



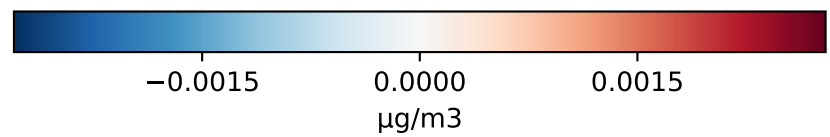
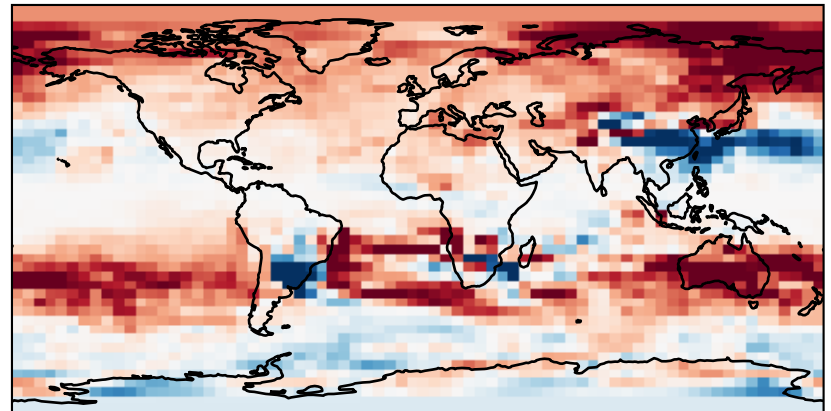
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



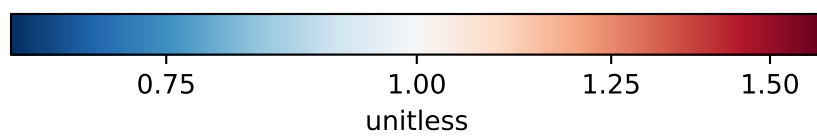
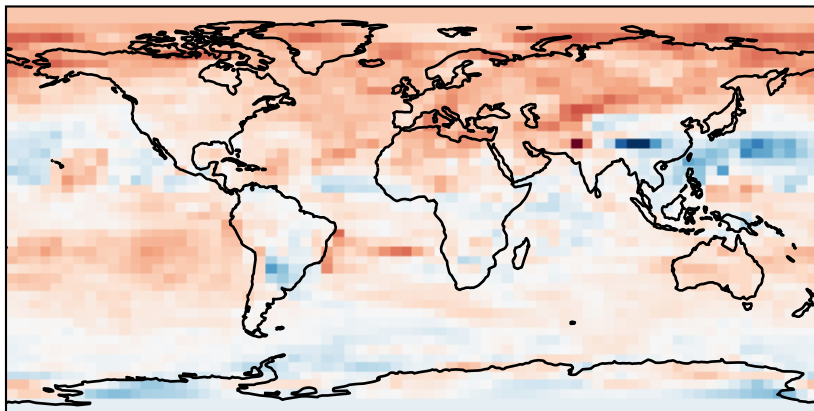
Difference  
Dev - Ref, Dynamic Range



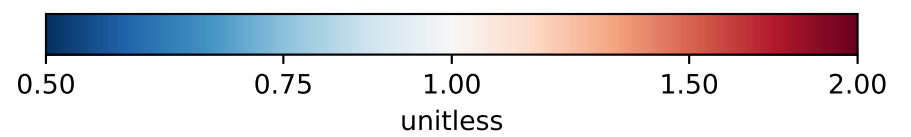
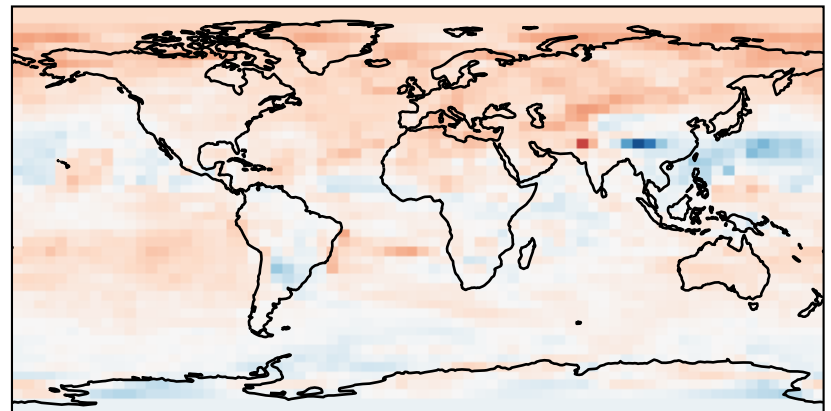
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

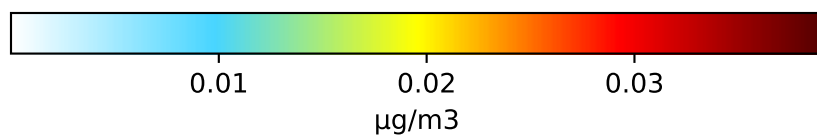
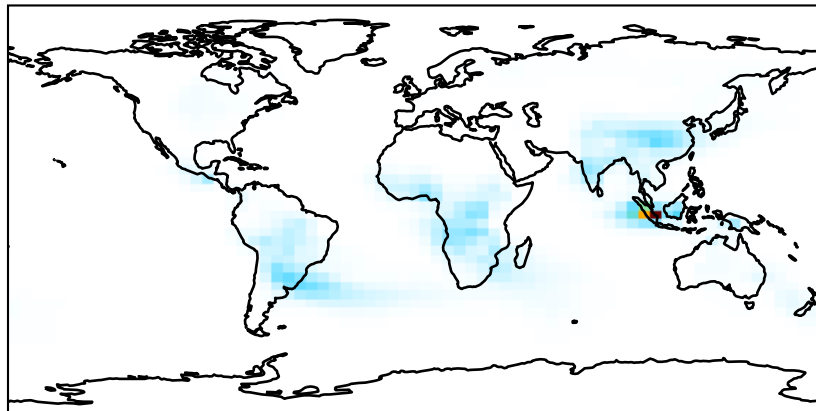


Ratio  
Dev/Ref, Fixed Range

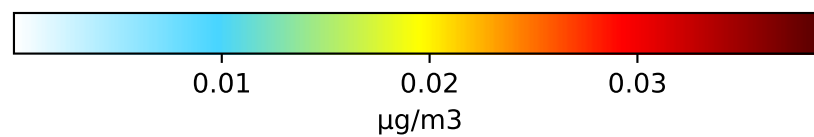
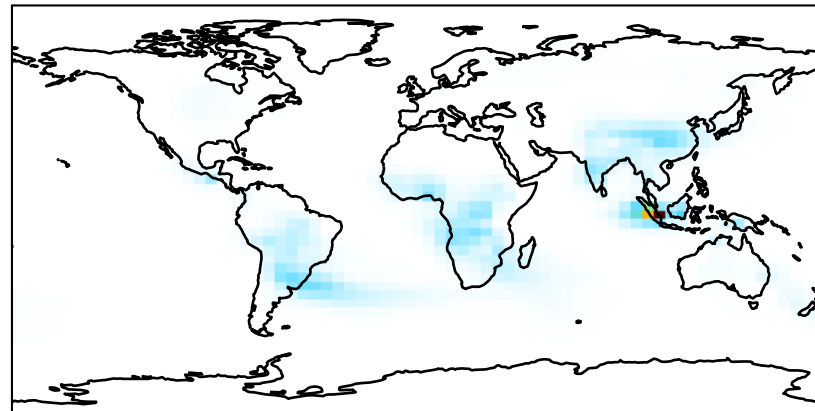


# SpeciesConc\_OCPO (Oct2019)

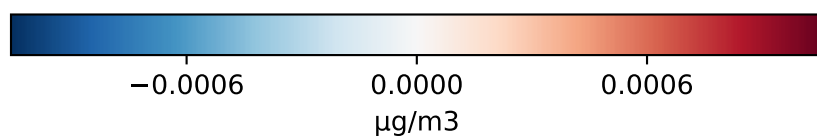
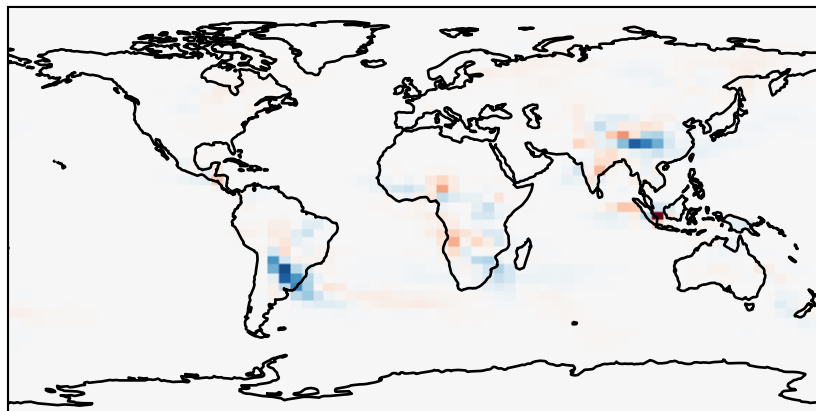
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



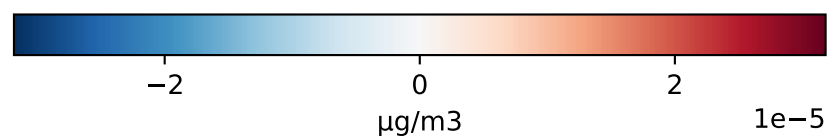
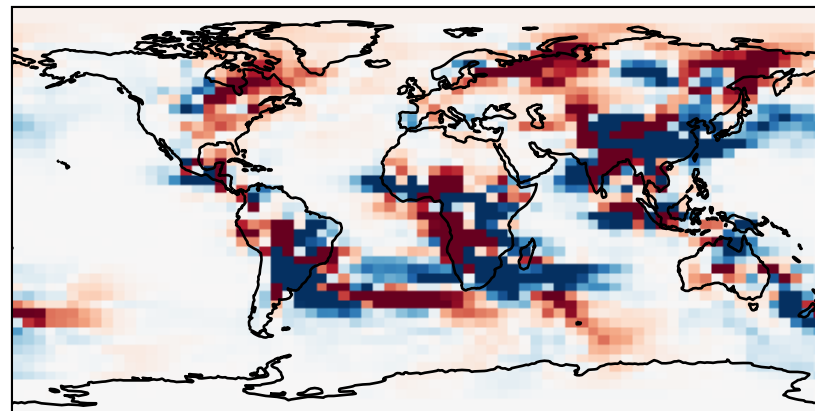
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



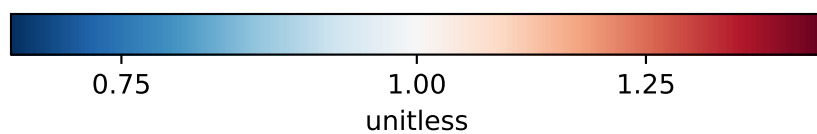
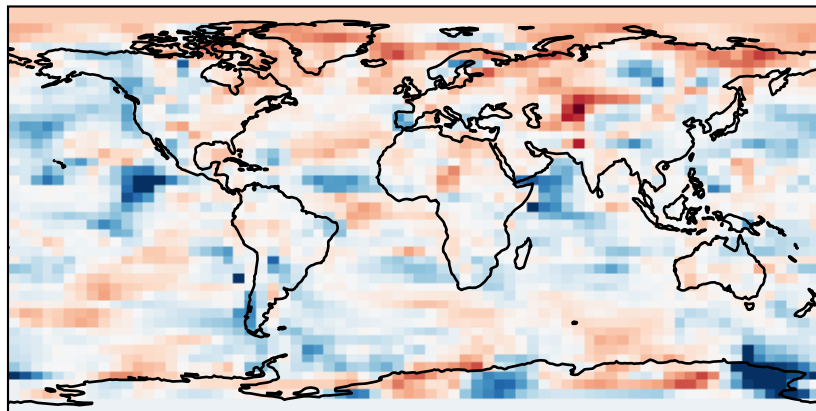
Difference  
Dev - Ref, Dynamic Range



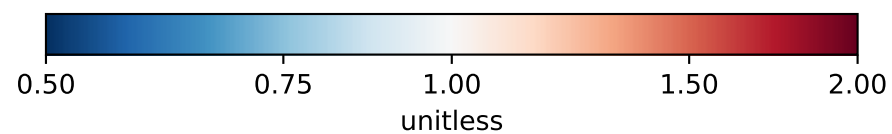
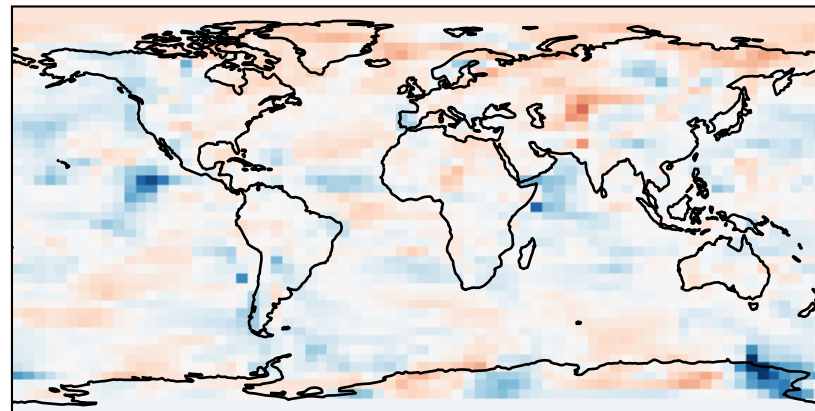
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

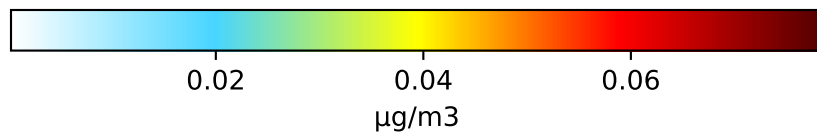
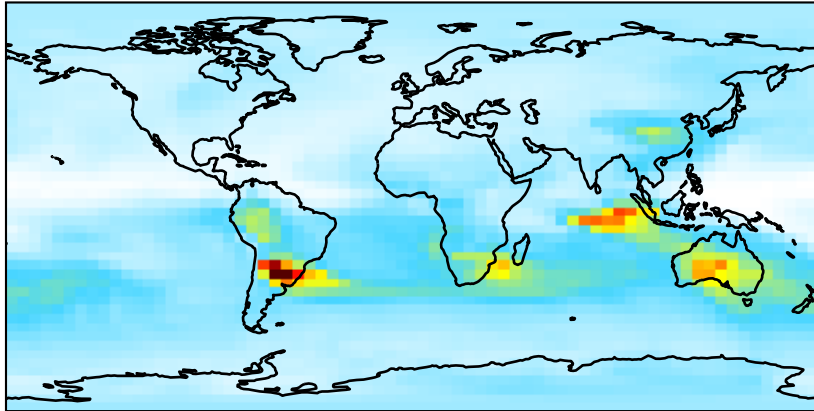


Ratio  
Dev/Ref, Fixed Range

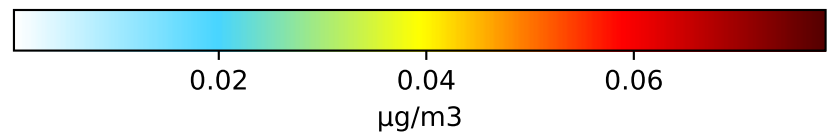
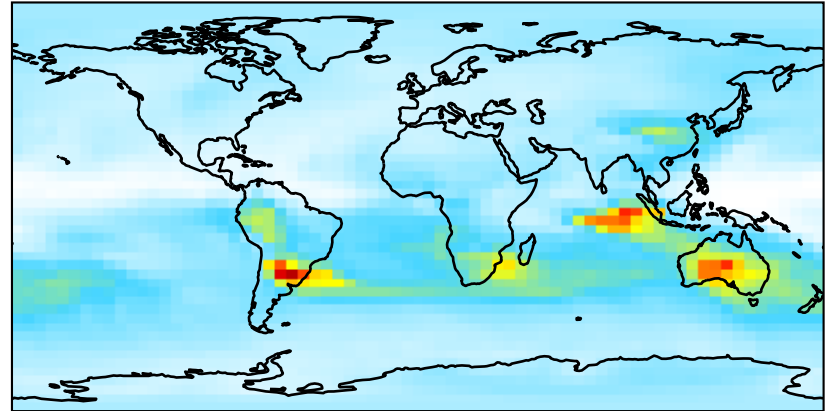


# SpeciesConc\_Complex\_SOA (Oct2019)

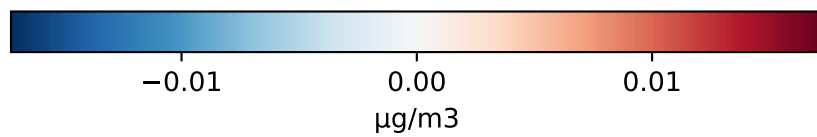
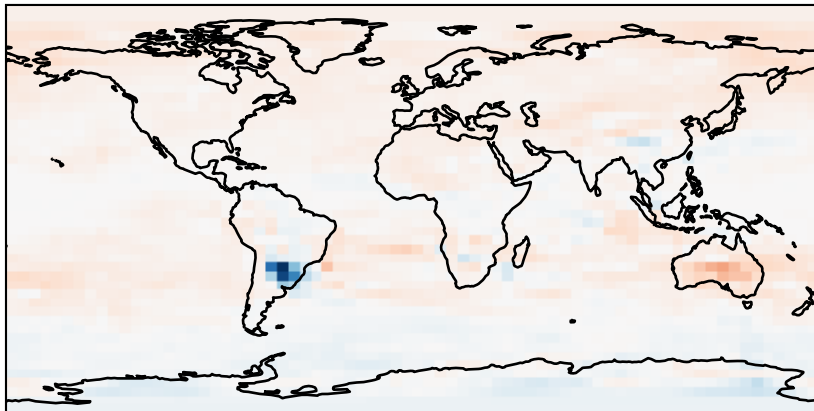
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



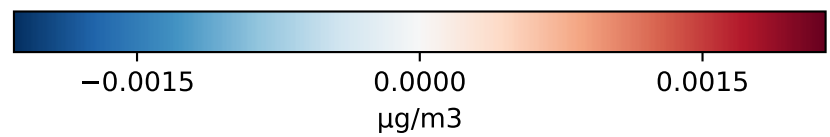
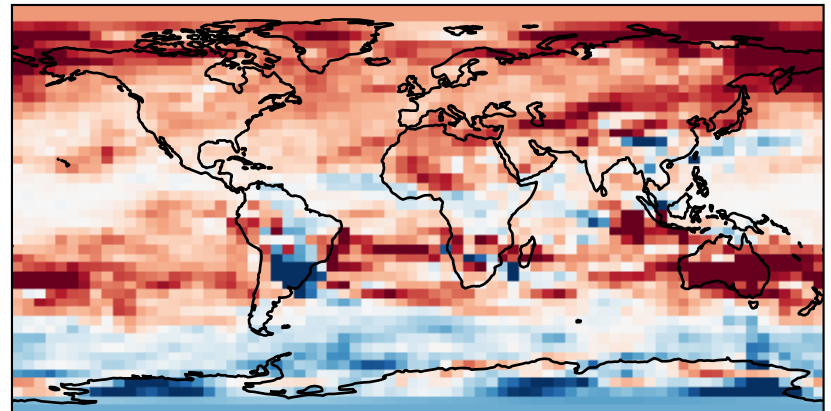
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



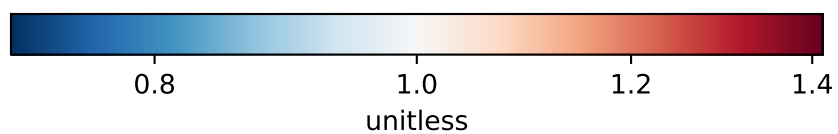
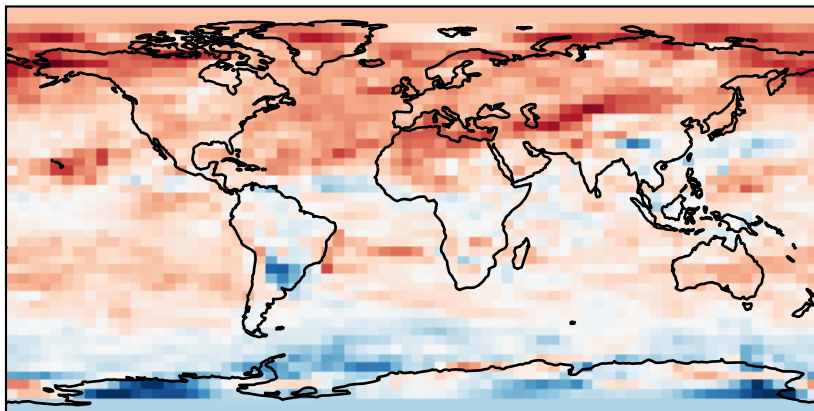
Difference  
Dev - Ref, Dynamic Range



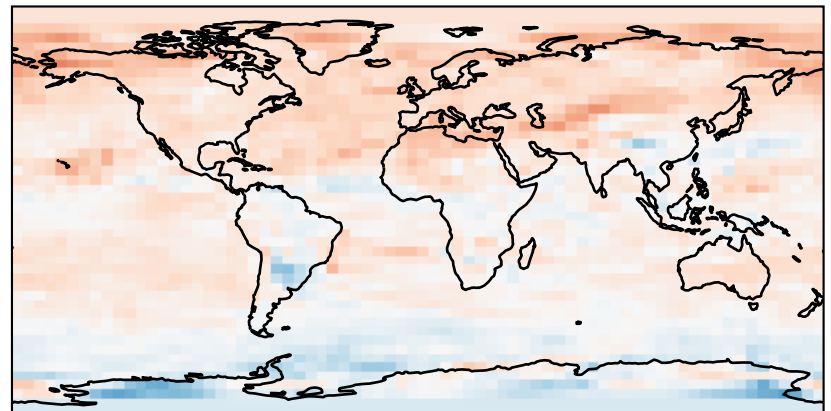
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

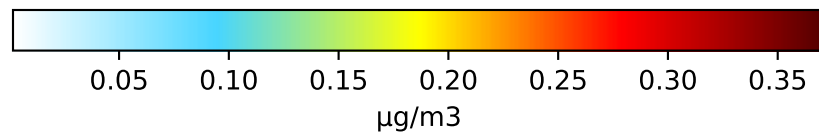
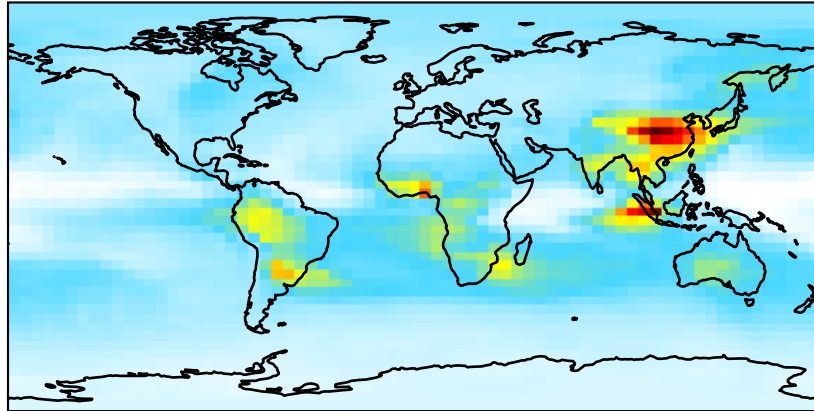


Ratio  
Dev/Ref, Fixed Range

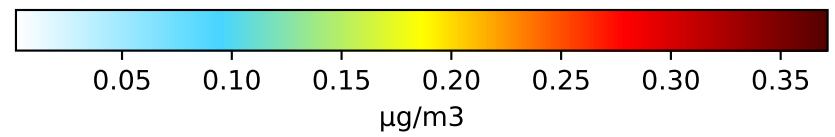
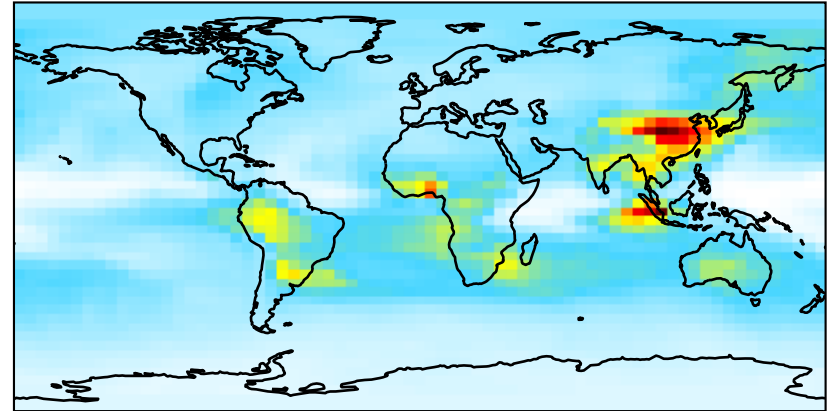


# SpeciesConc\_Simple\_SOA (Oct2019)

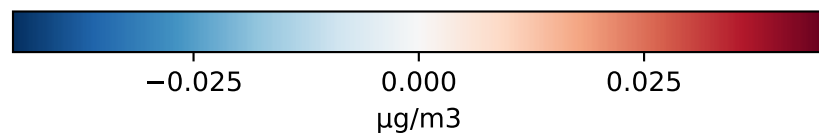
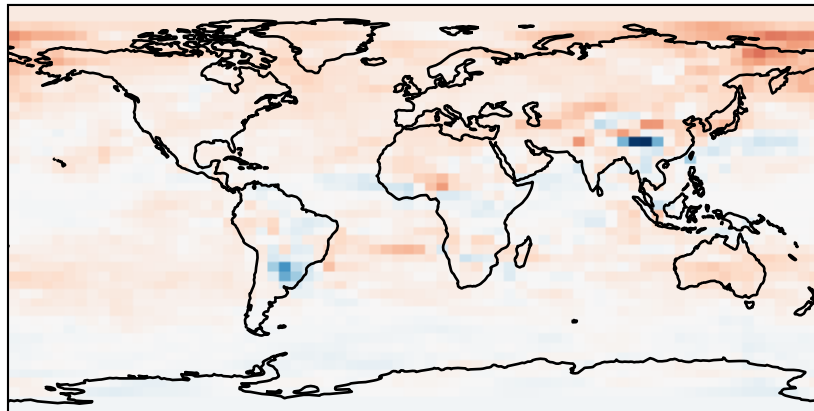
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



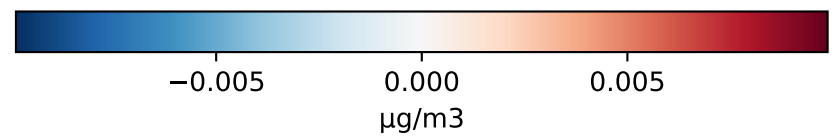
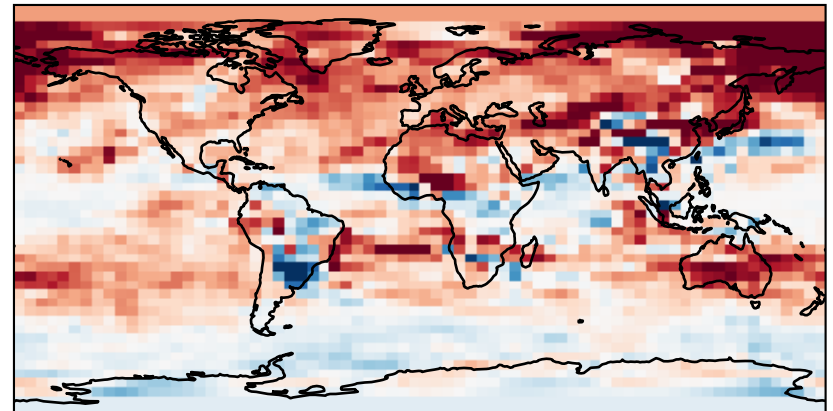
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



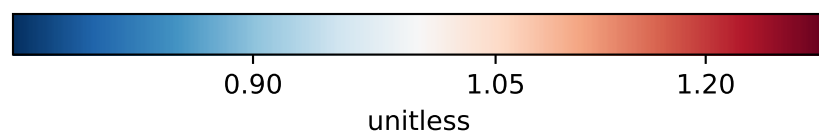
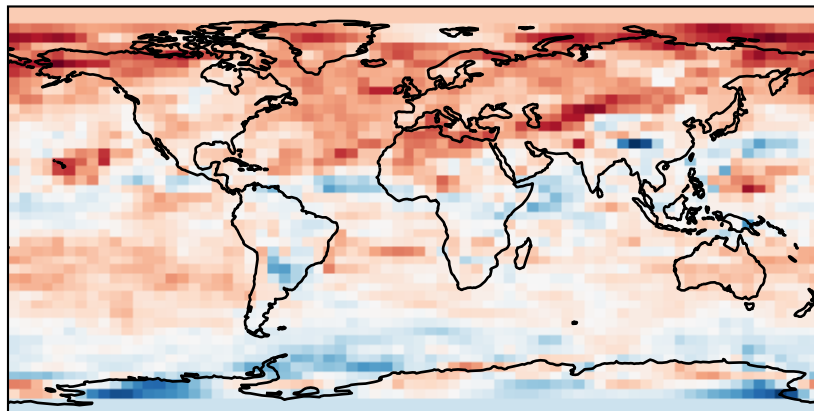
Difference  
Dev - Ref, Dynamic Range



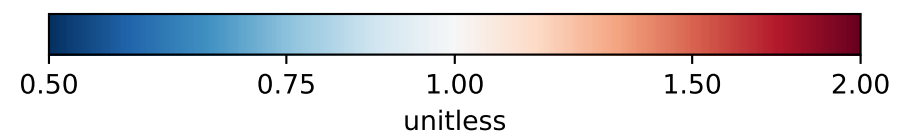
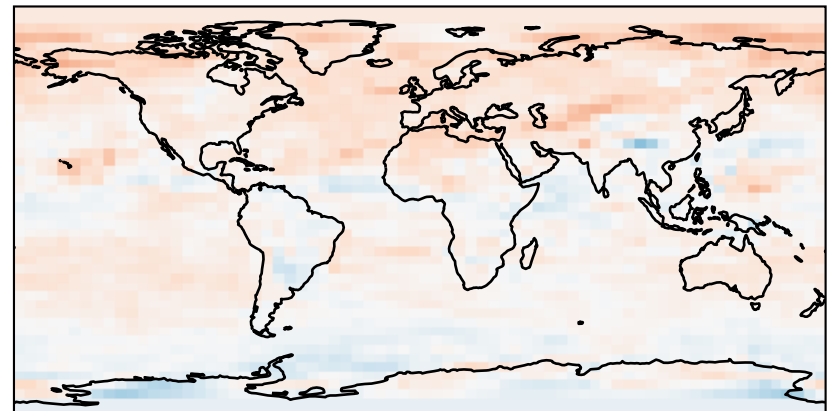
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

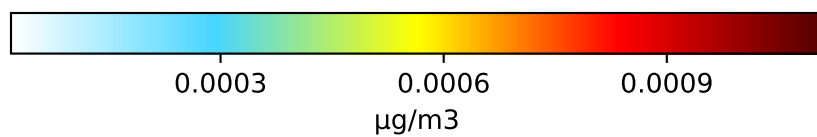
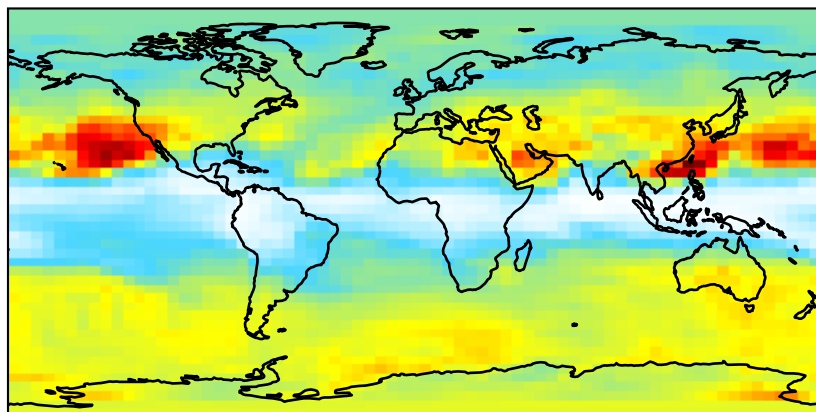


Ratio  
Dev/Ref, Fixed Range

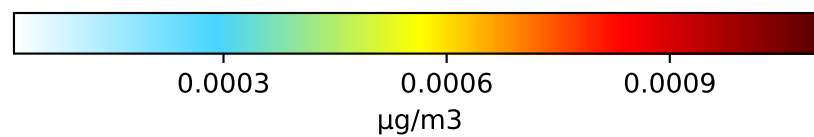
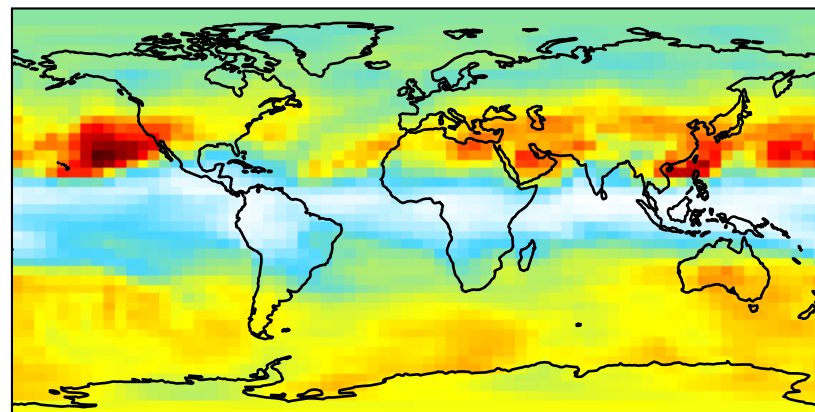


# SpeciesConc\_AERI (Oct2019)

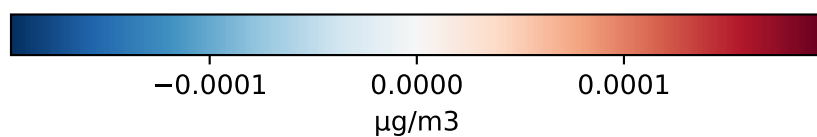
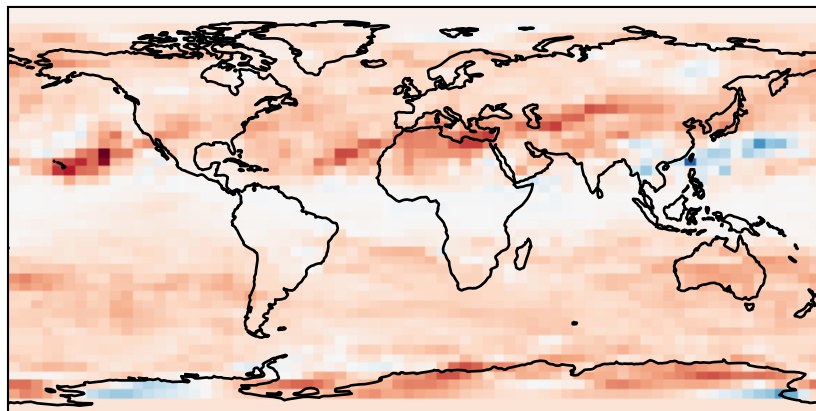
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



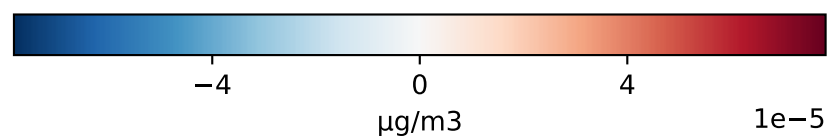
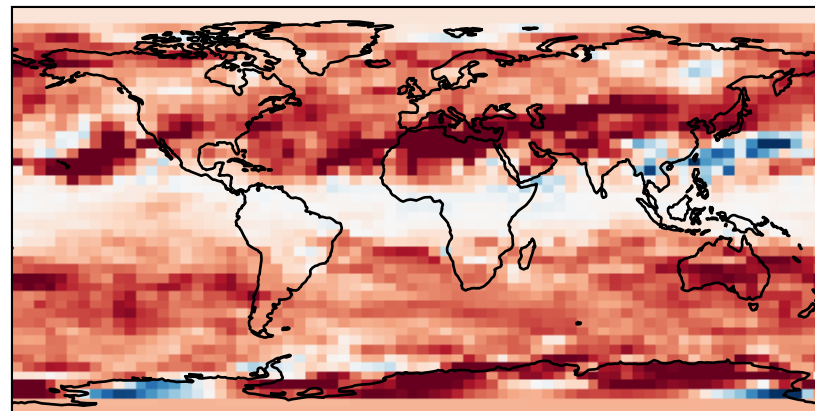
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



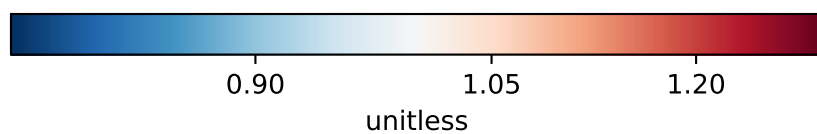
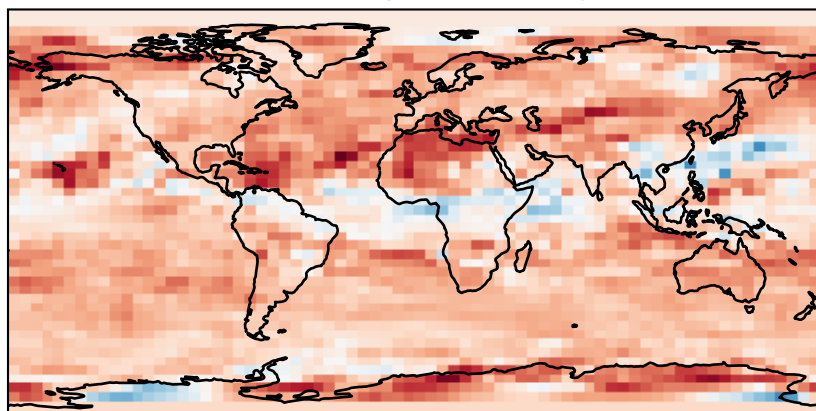
Difference  
Dev - Ref, Dynamic Range



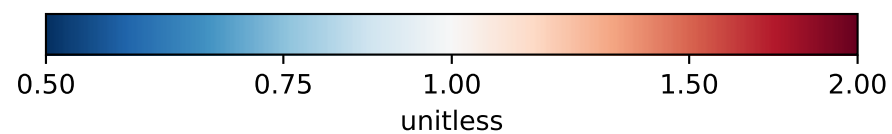
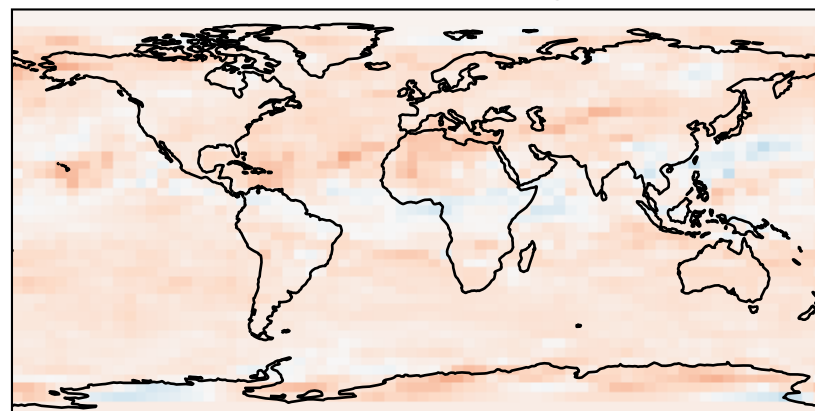
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

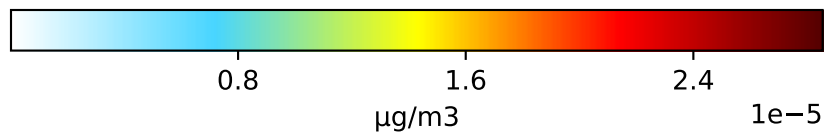
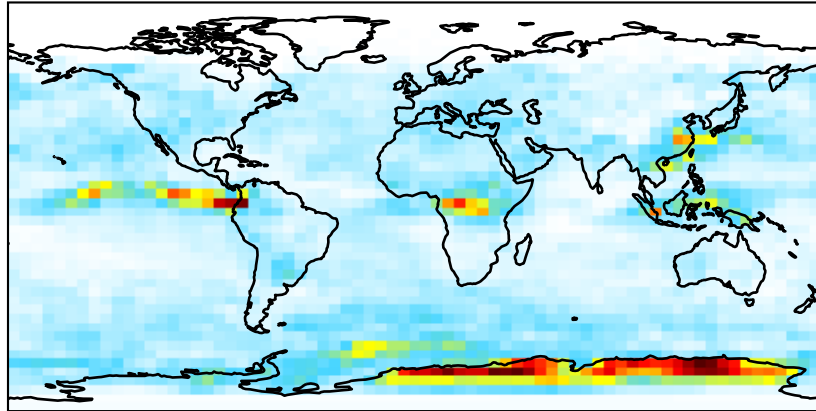


Ratio  
Dev/Ref, Fixed Range

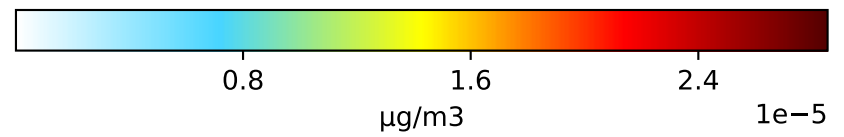
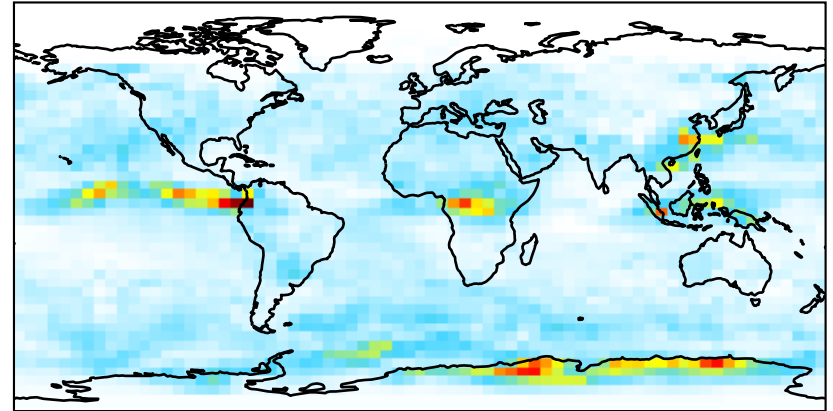


# SpeciesConc\_BrSALA (Oct2019)

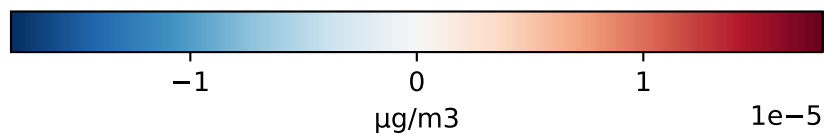
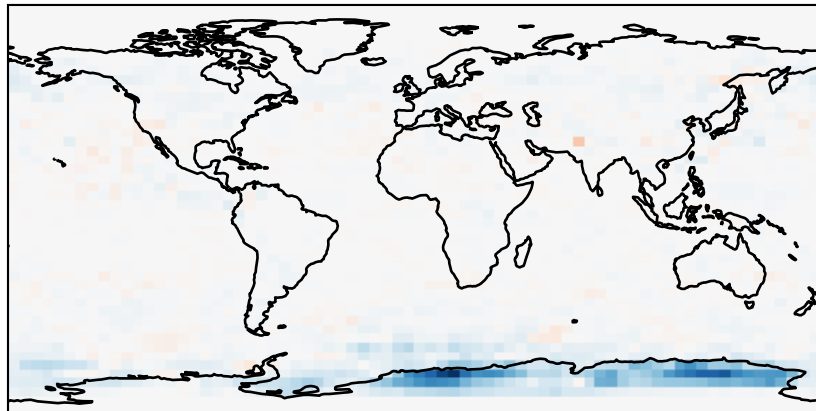
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



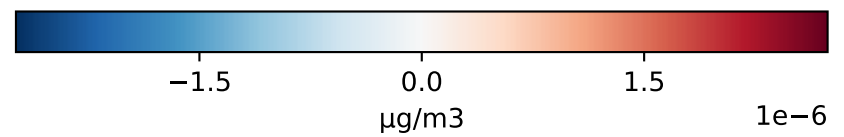
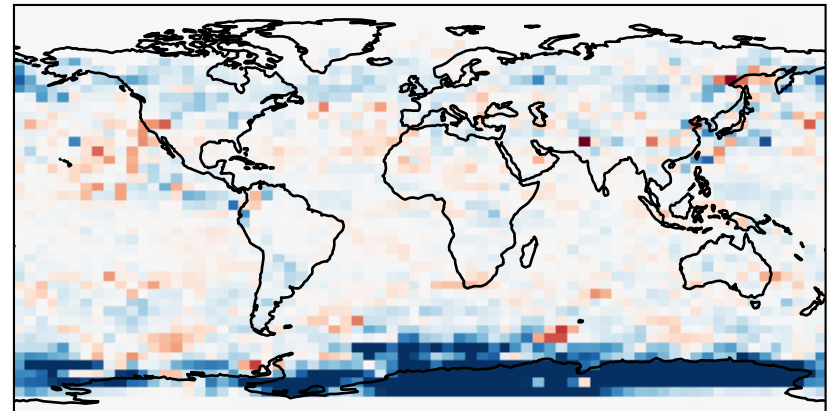
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



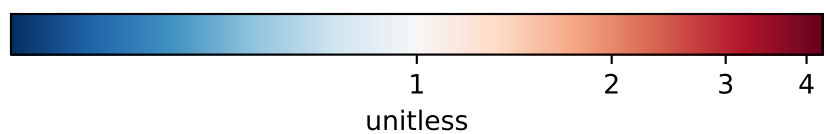
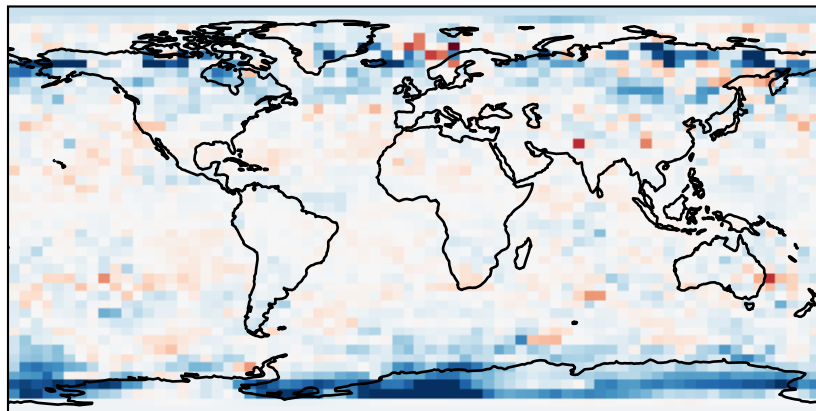
Difference  
Dev - Ref, Dynamic Range



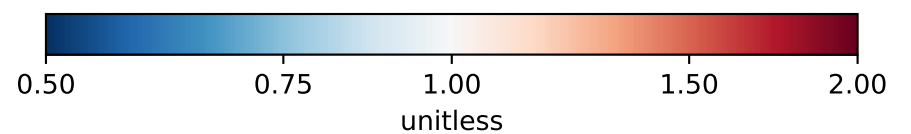
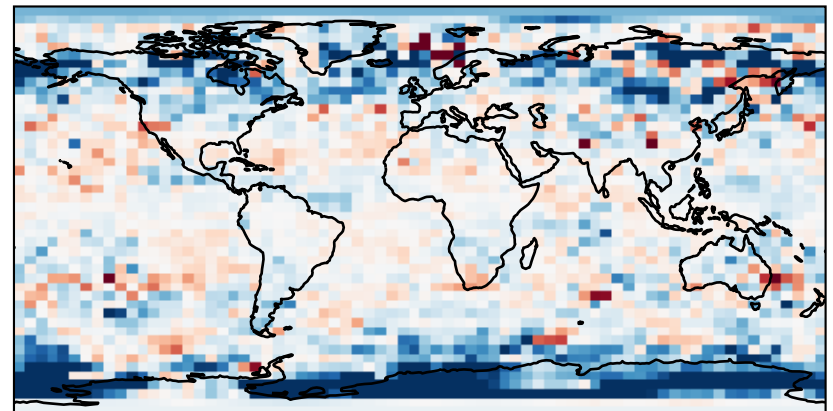
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



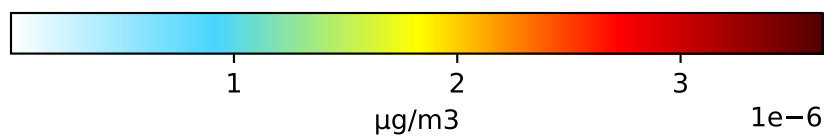
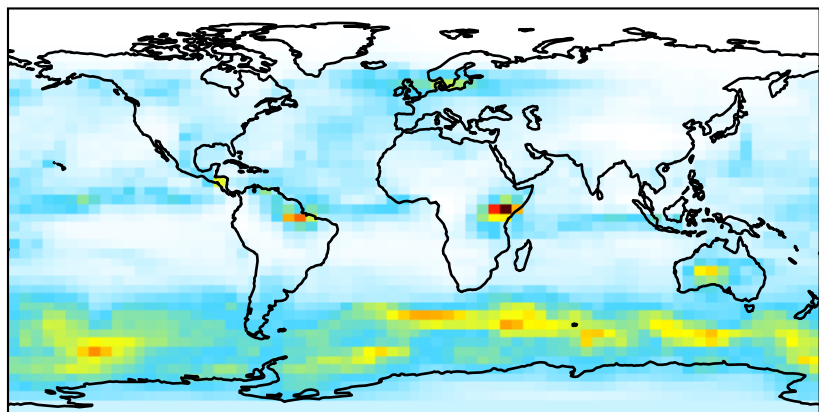
Ratio  
Dev/Ref, Fixed Range



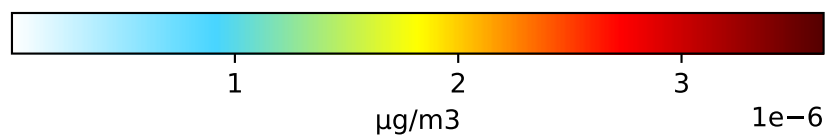
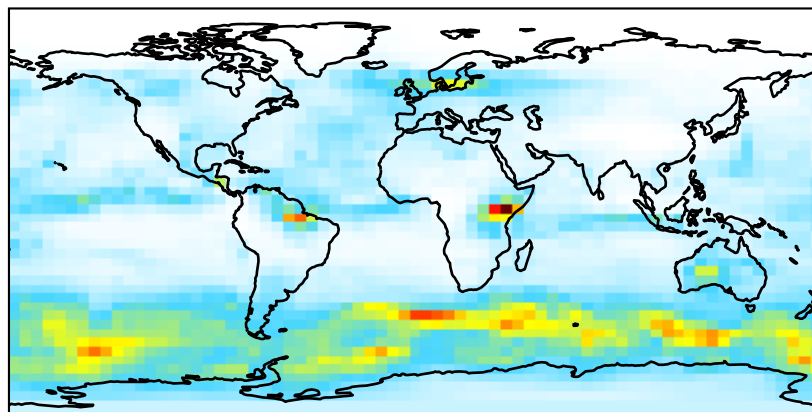


# SpeciesConc\_BrSALC (Oct2019)

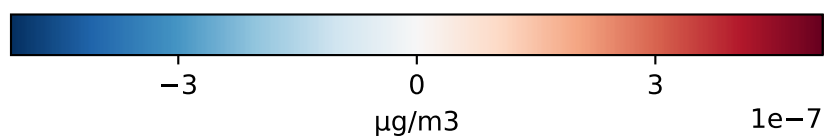
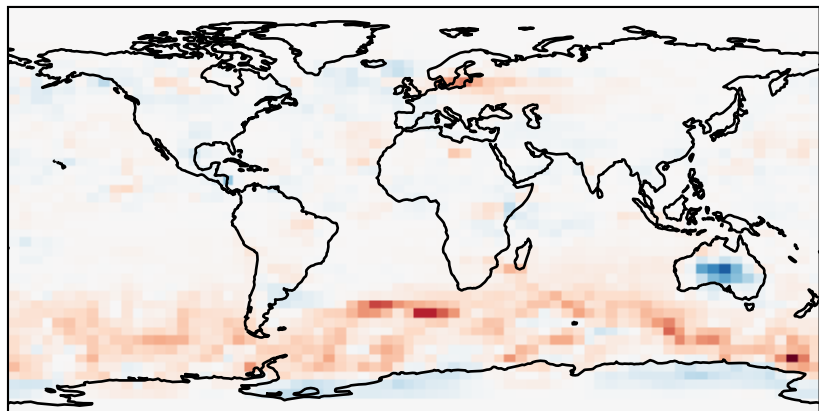
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



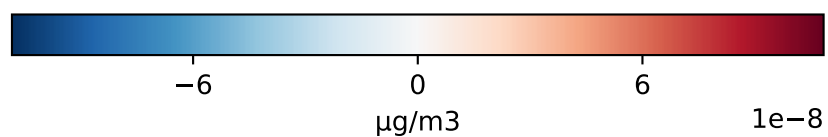
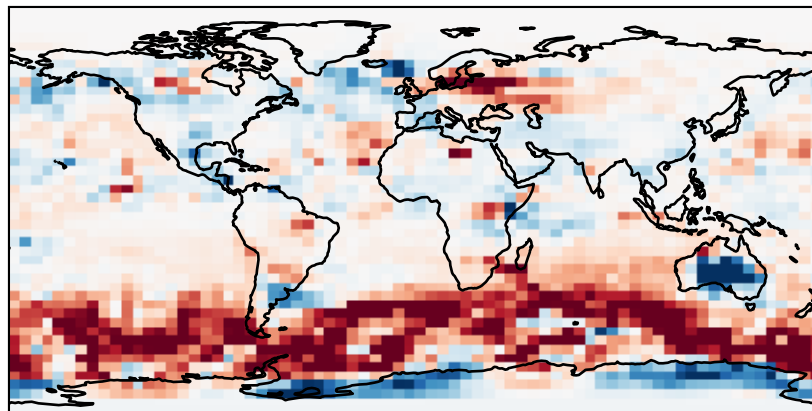
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



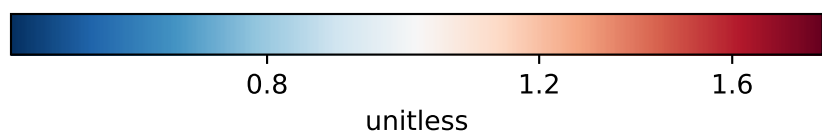
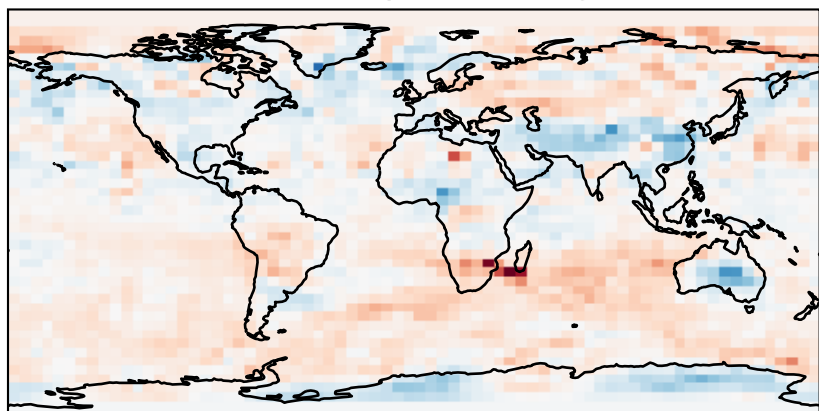
Difference  
Dev - Ref, Dynamic Range



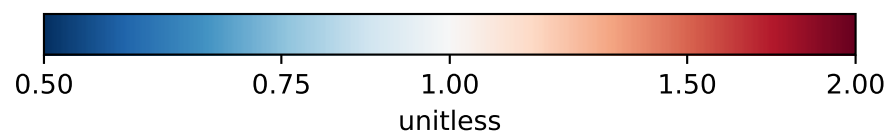
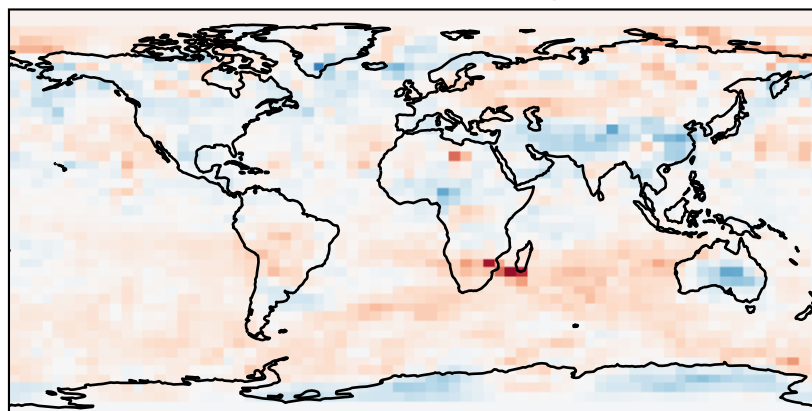
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

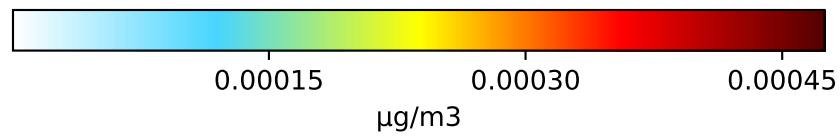
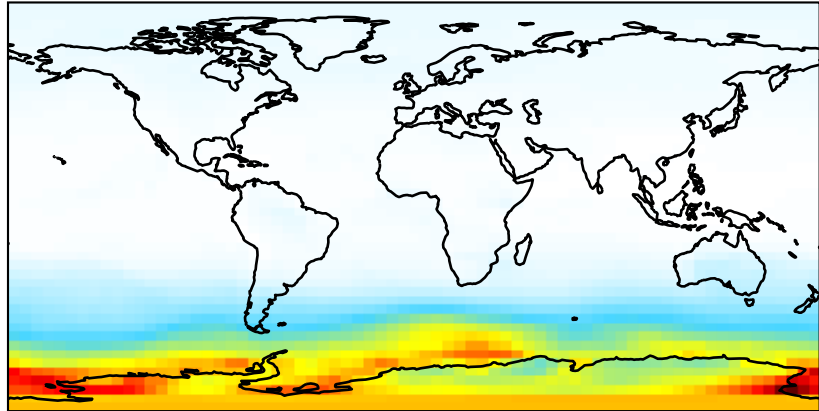


Ratio  
Dev/Ref, Fixed Range

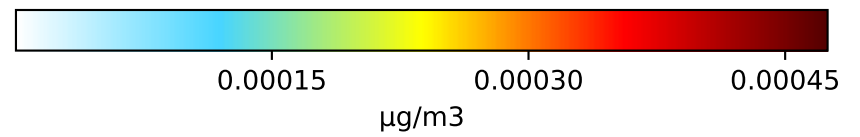
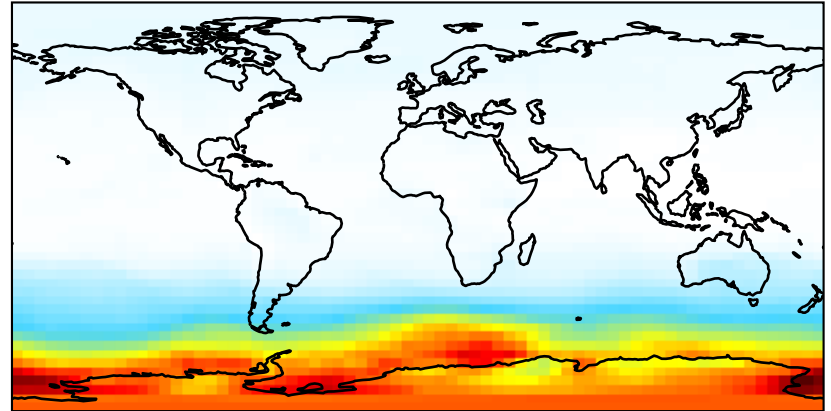


# SpeciesConc\_ISALA (Oct2019)

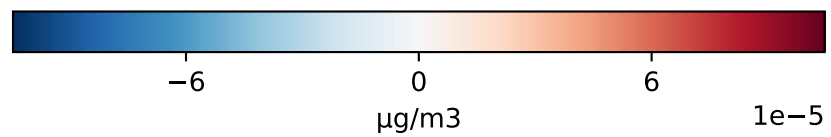
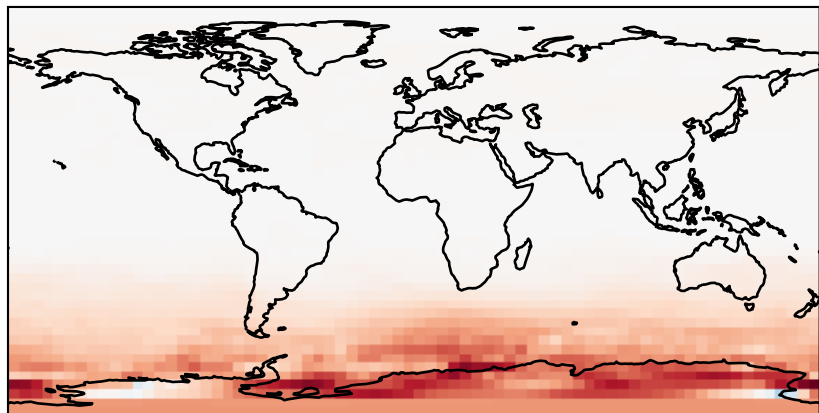
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



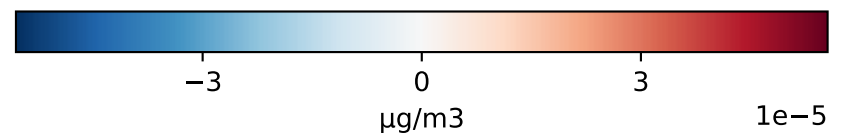
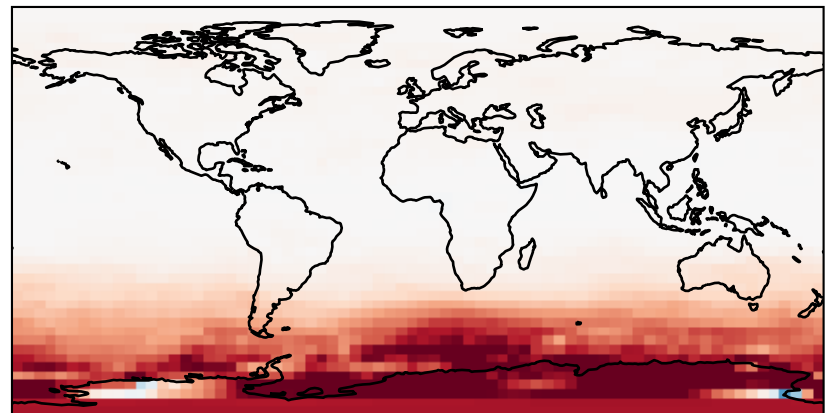
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



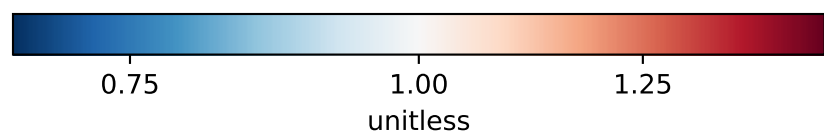
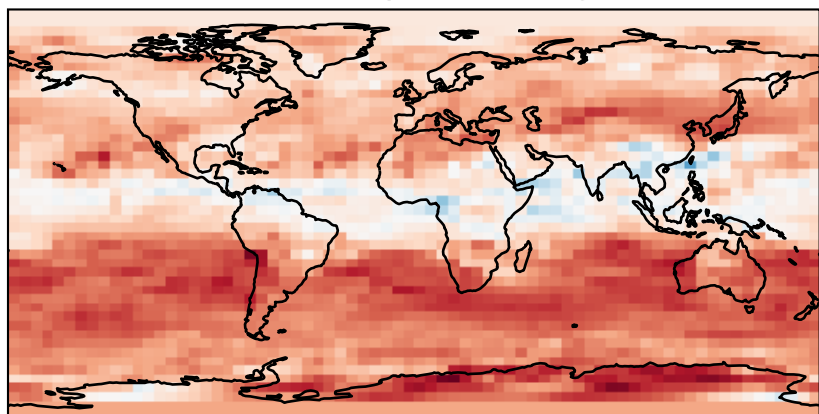
Difference  
Dev - Ref, Dynamic Range



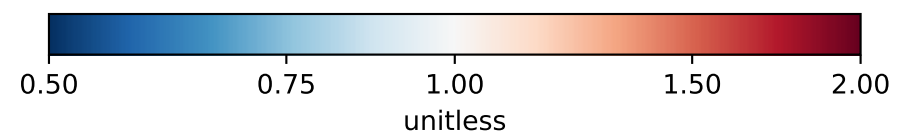
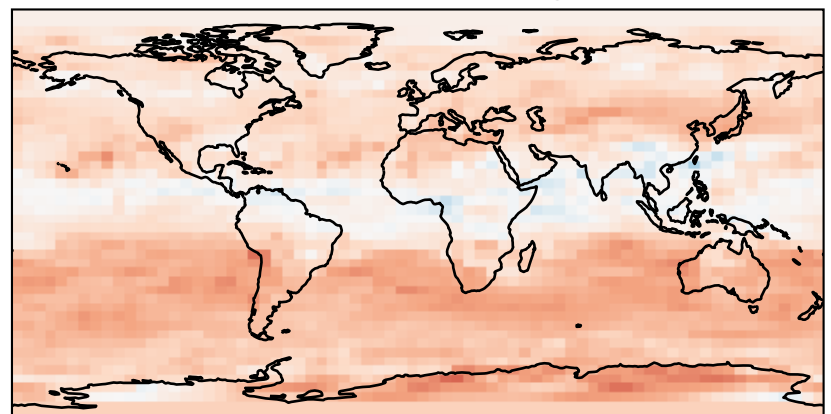
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

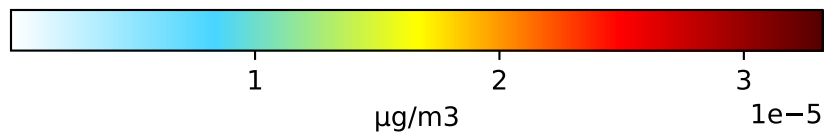
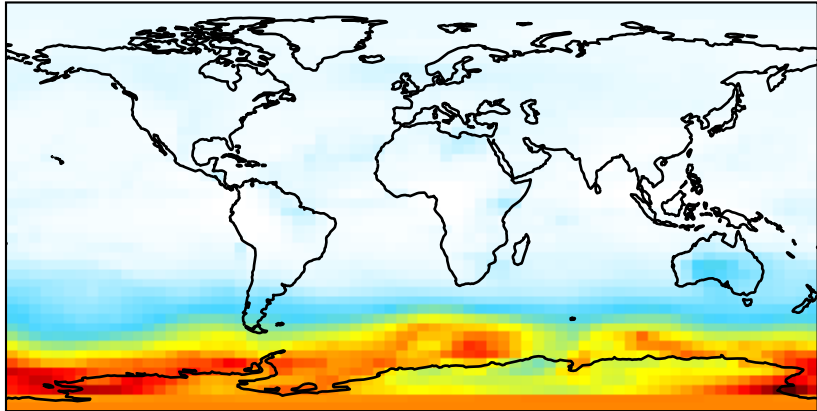


Ratio  
Dev/Ref, Fixed Range

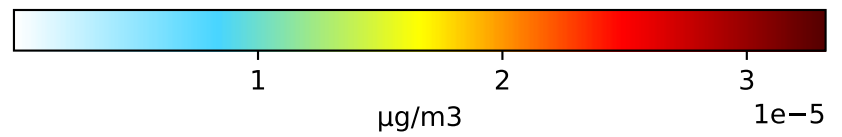
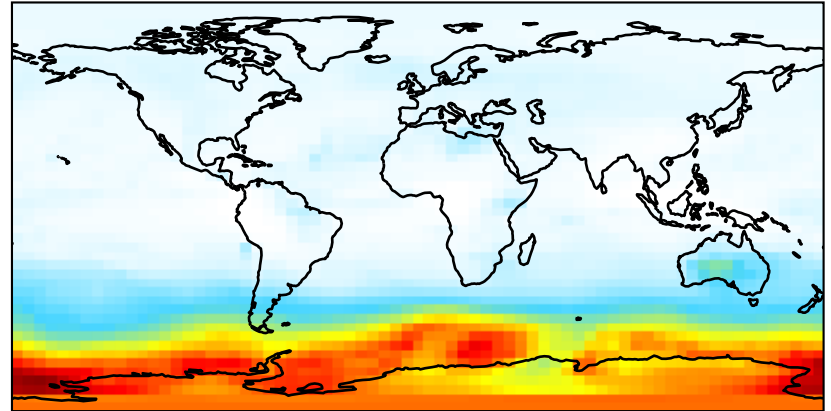


# SpeciesConc\_ISALC (Oct2019)

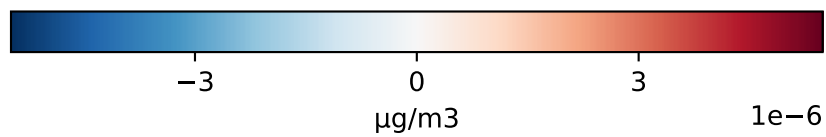
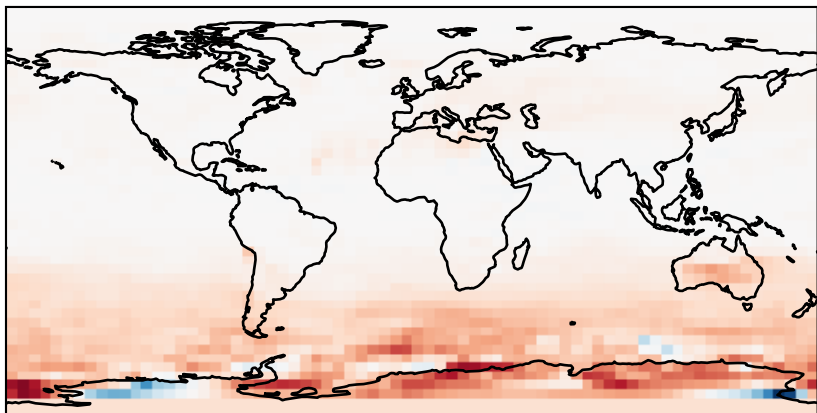
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



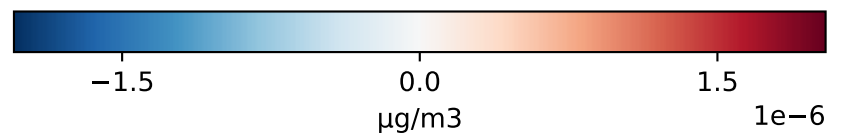
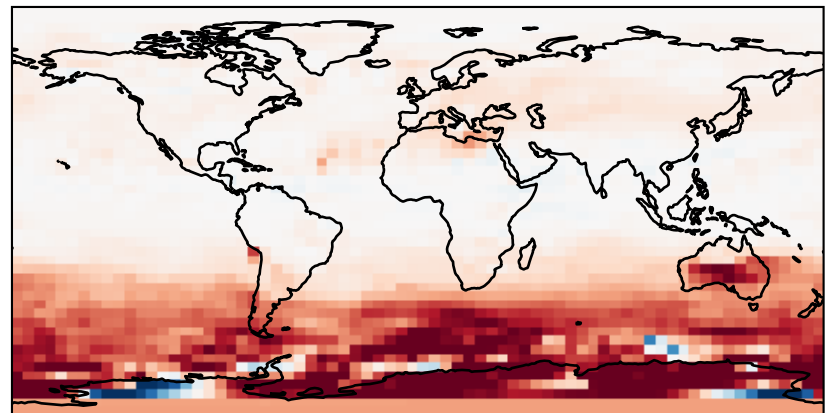
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



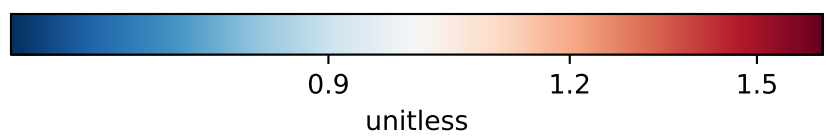
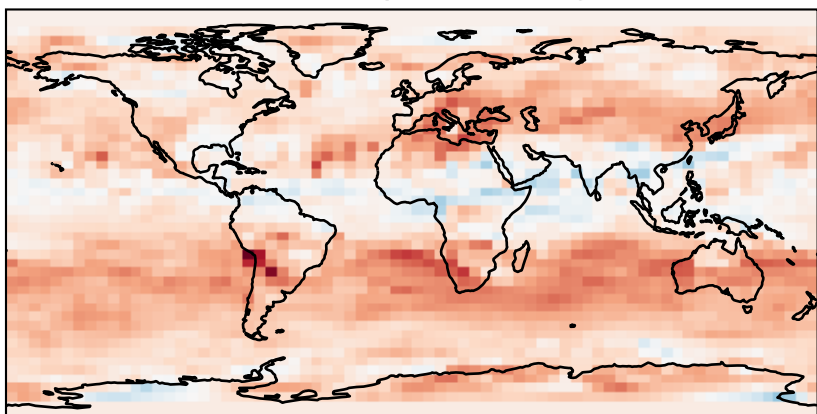
Difference  
Dev - Ref, Dynamic Range



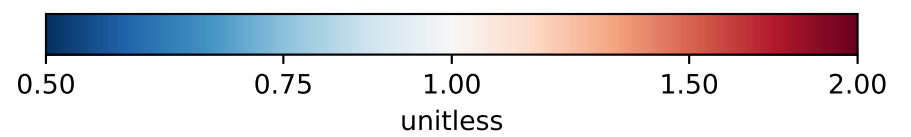
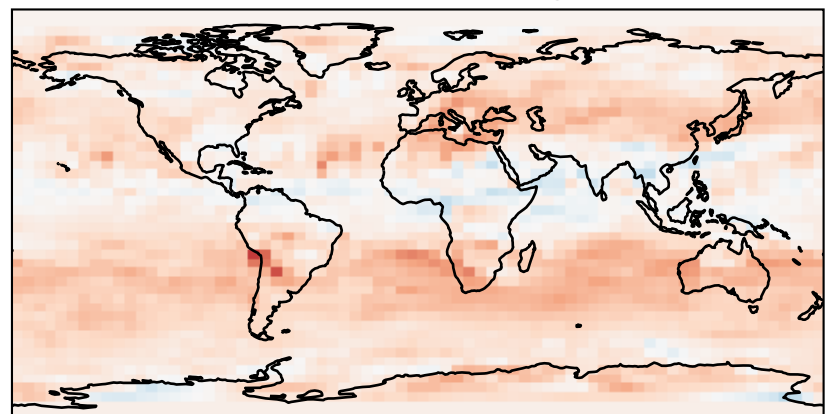
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

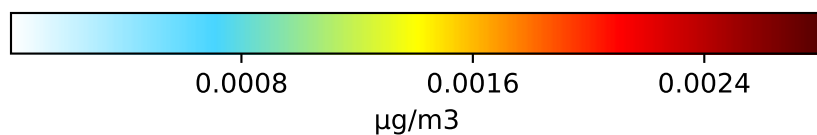
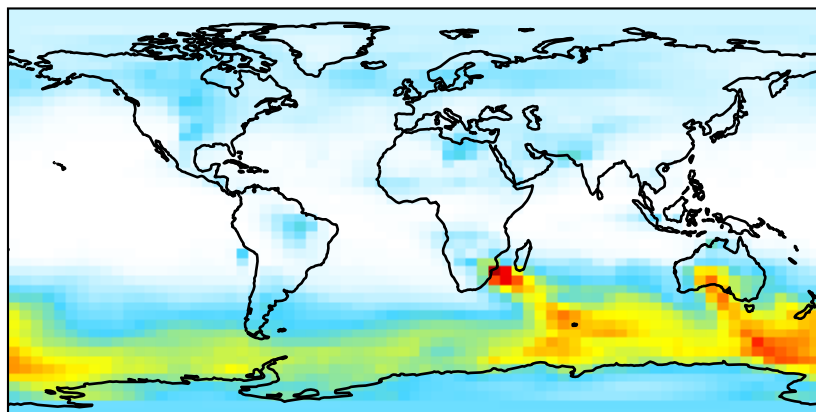


Ratio  
Dev/Ref, Fixed Range

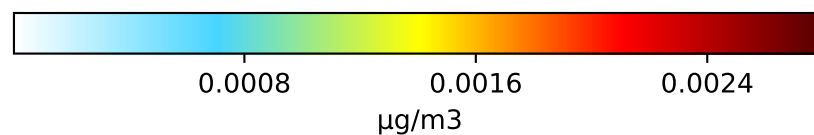
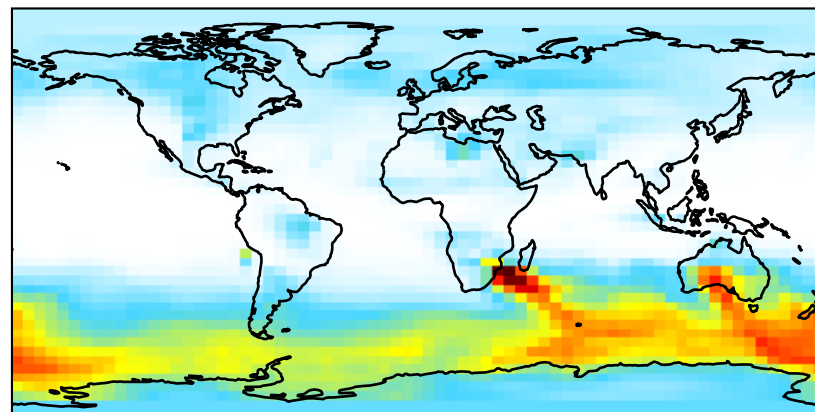


# SpeciesConc\_NITs (Oct2019)

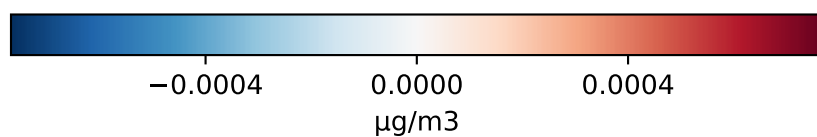
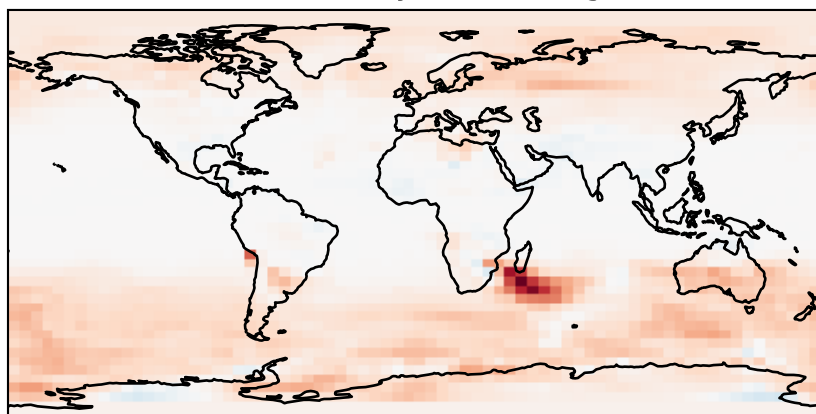
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



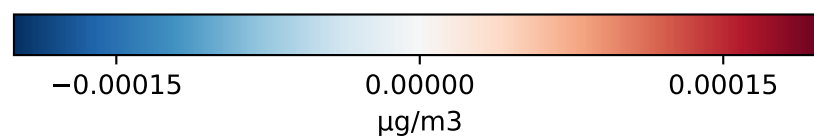
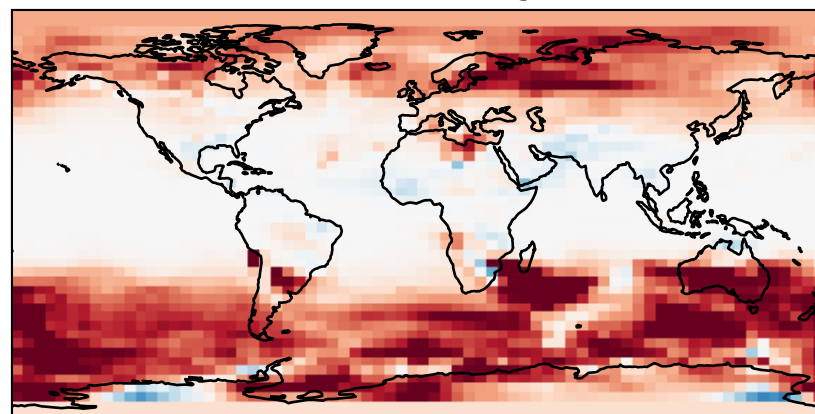
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



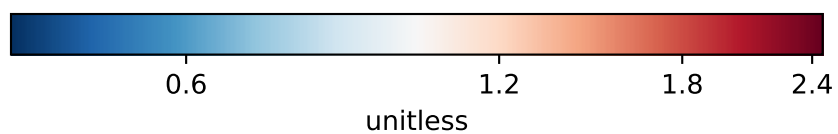
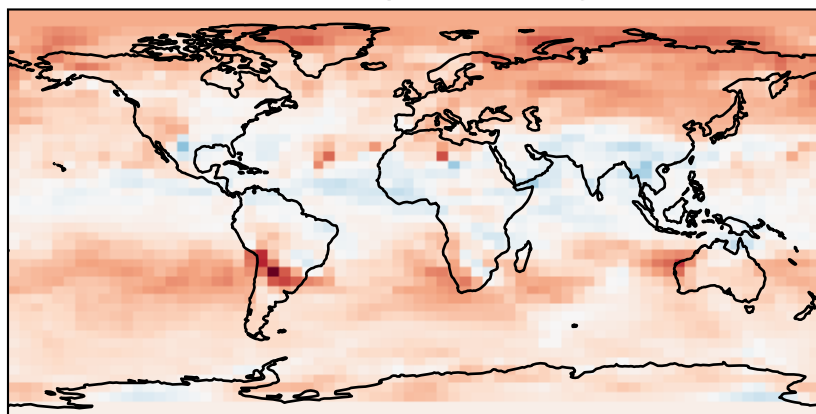
Difference  
Dev - Ref, Dynamic Range



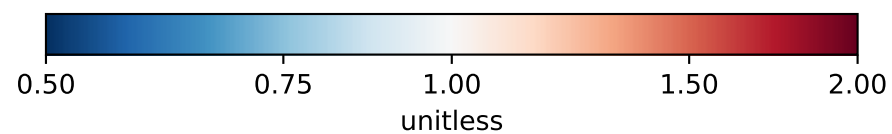
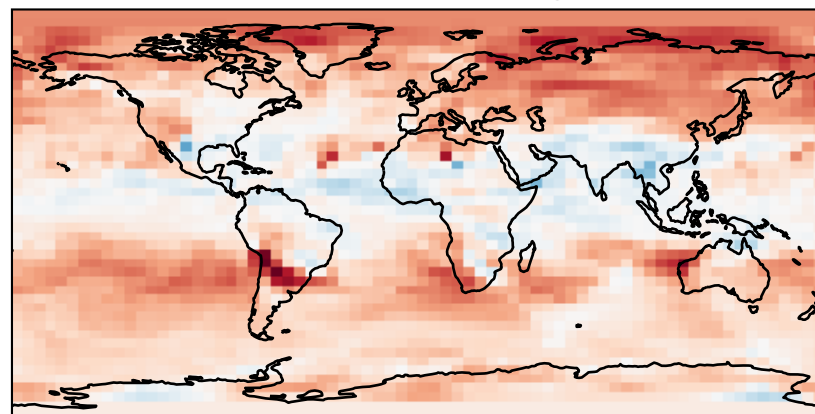
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

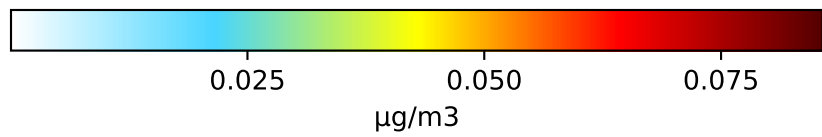
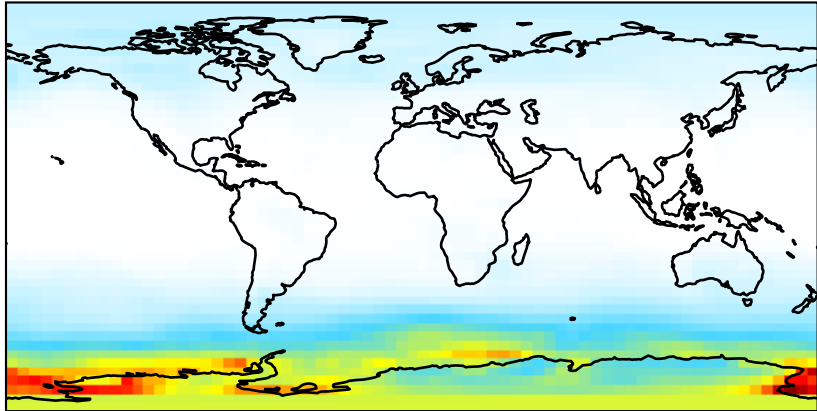


Ratio  
Dev/Ref, Fixed Range

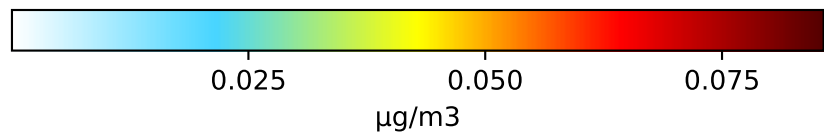
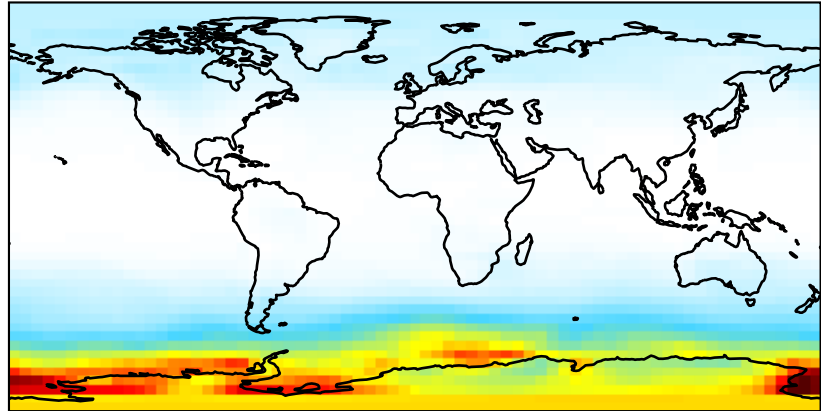


# SpeciesConc\_SALA (Oct2019)

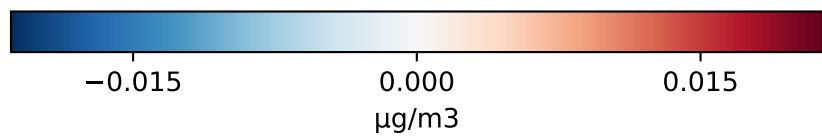
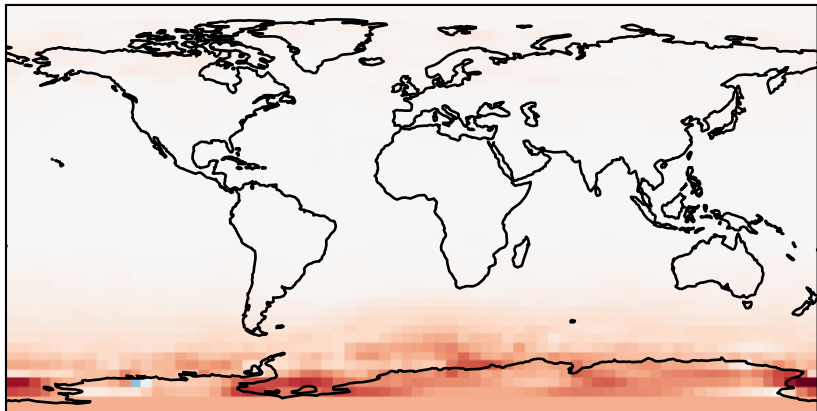
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



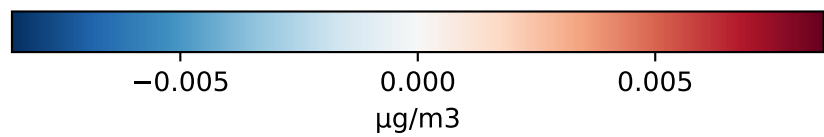
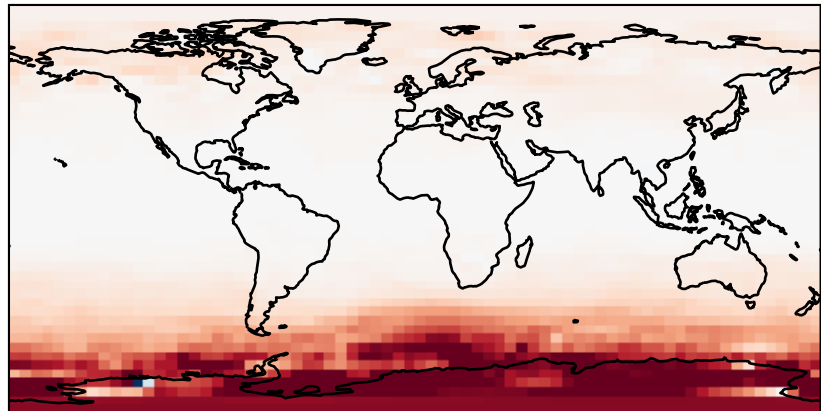
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



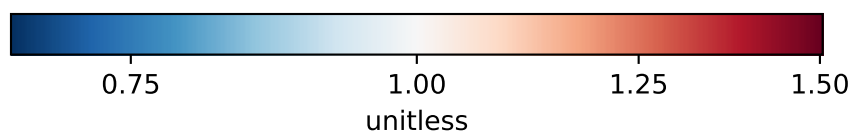
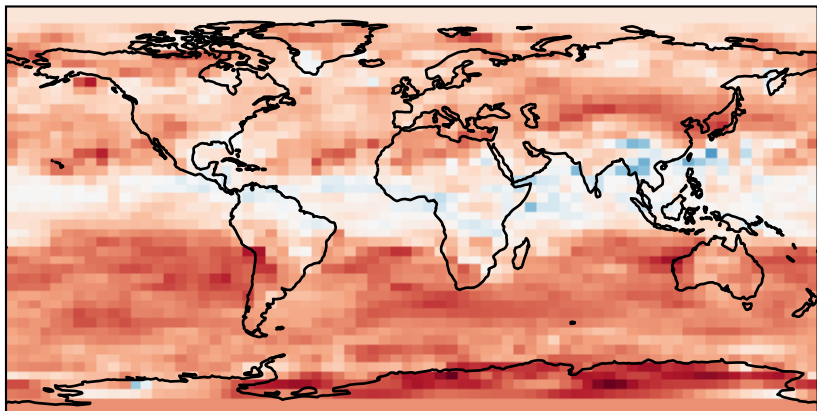
Difference  
Dev - Ref, Dynamic Range



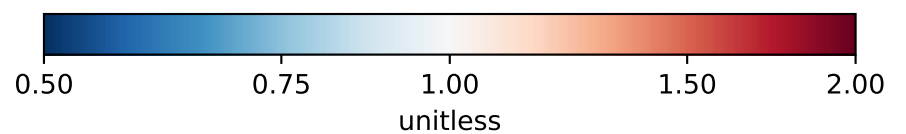
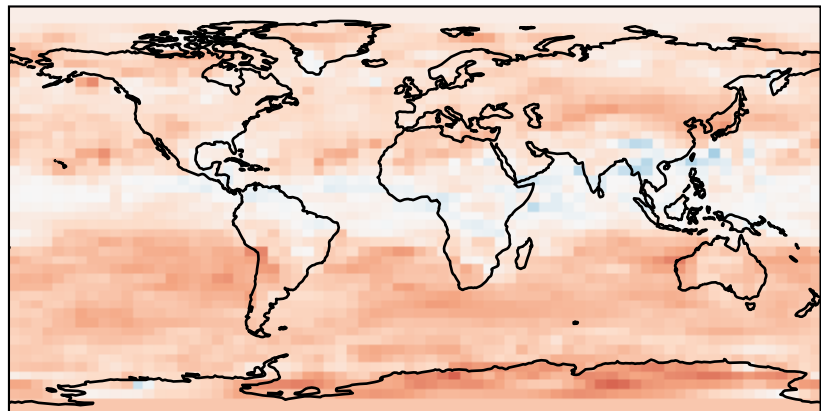
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

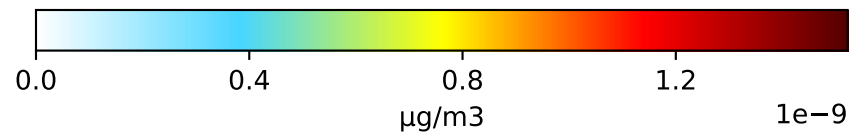
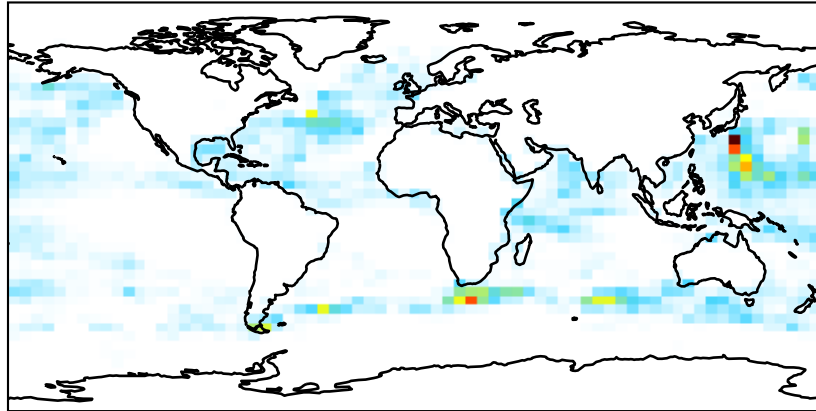


Ratio  
Dev/Ref, Fixed Range

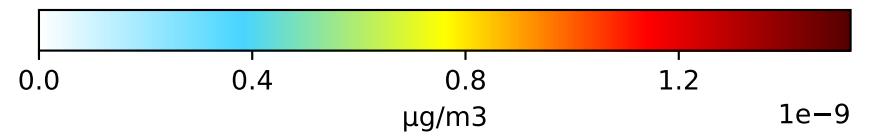
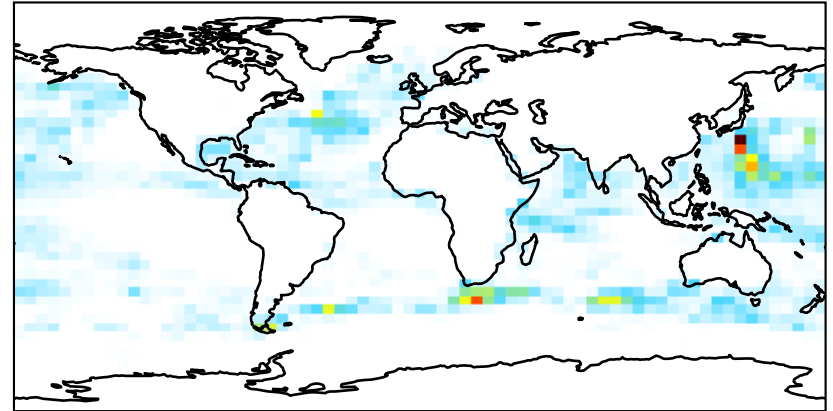


# SpeciesConc\_SALAAL (Oct2019)

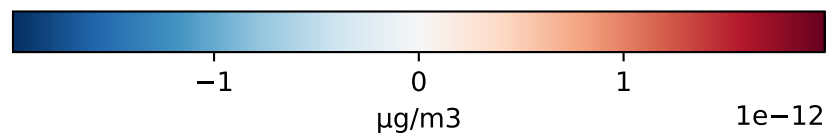
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



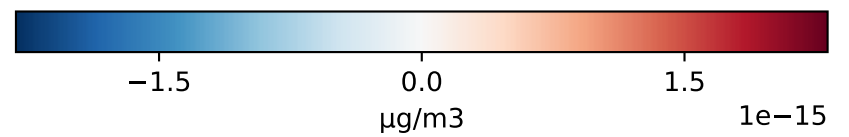
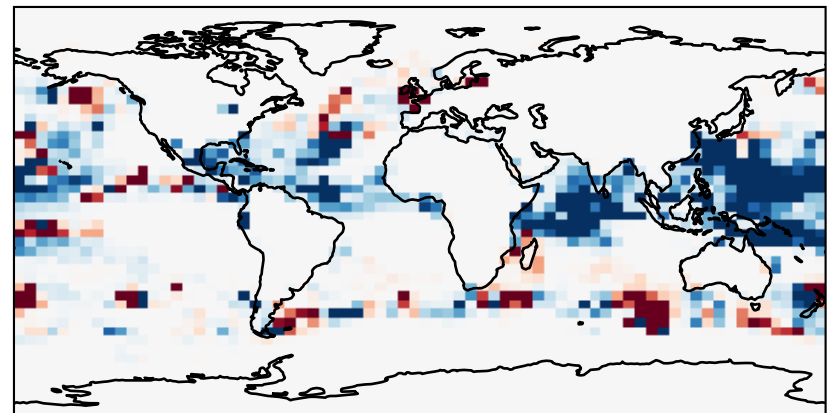
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



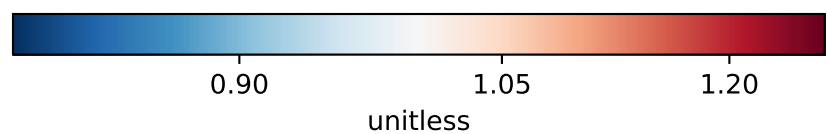
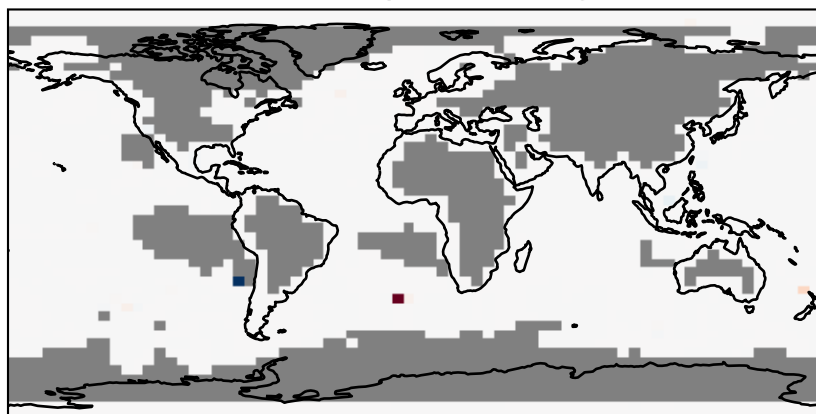
Difference  
Dev - Ref, Dynamic Range



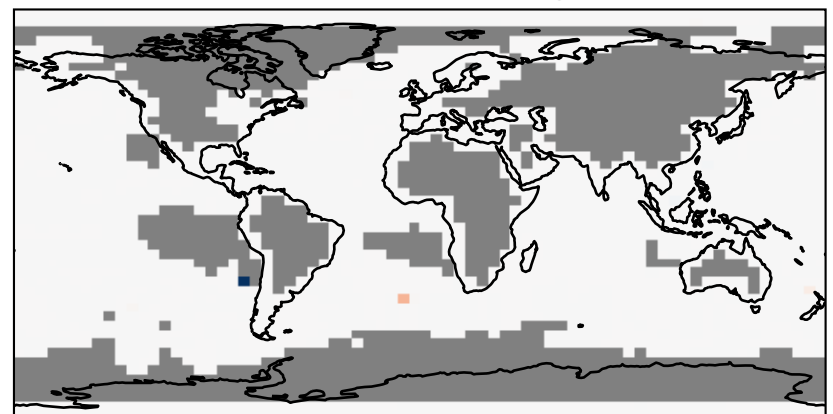
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

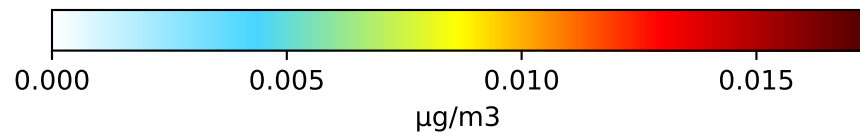
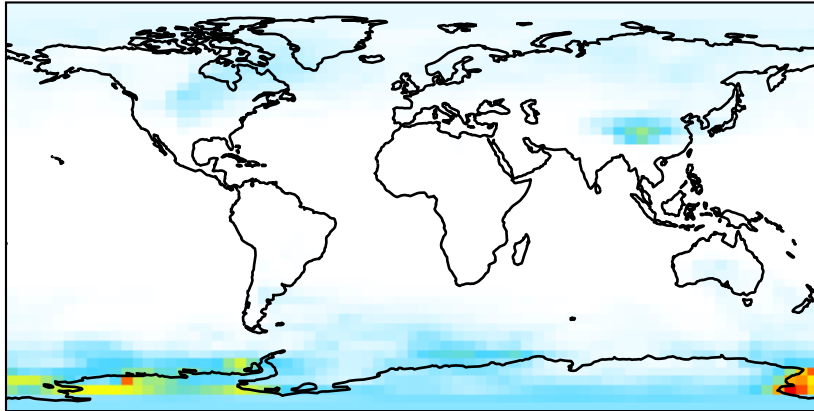


Ratio  
Dev/Ref, Fixed Range

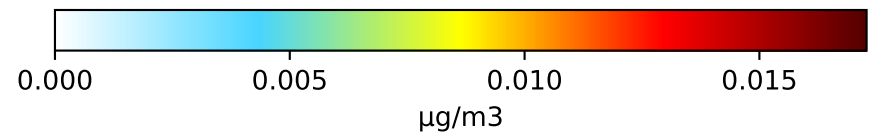
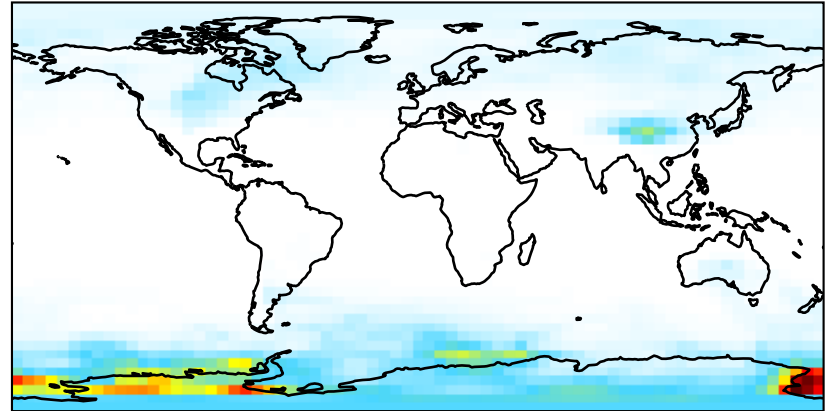


# SpeciesConc\_SALACL (Oct2019)

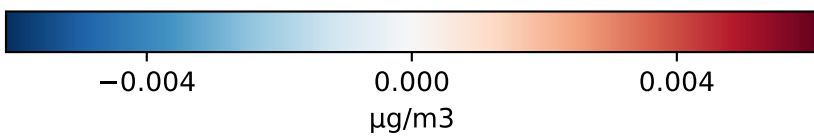
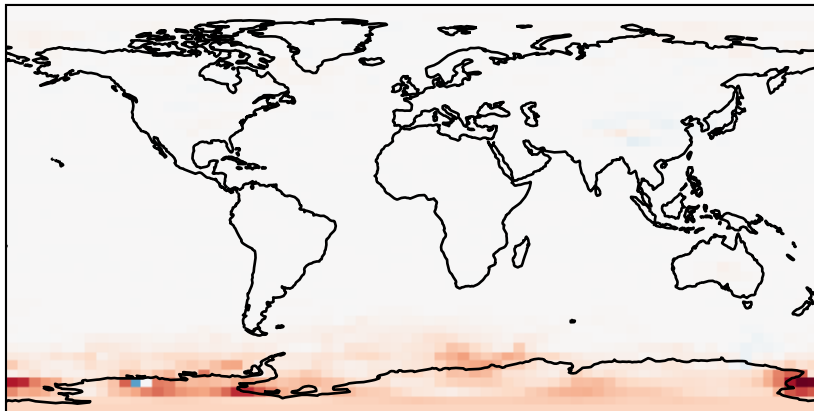
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



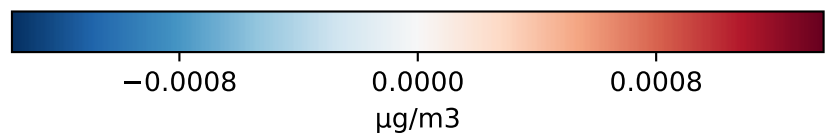
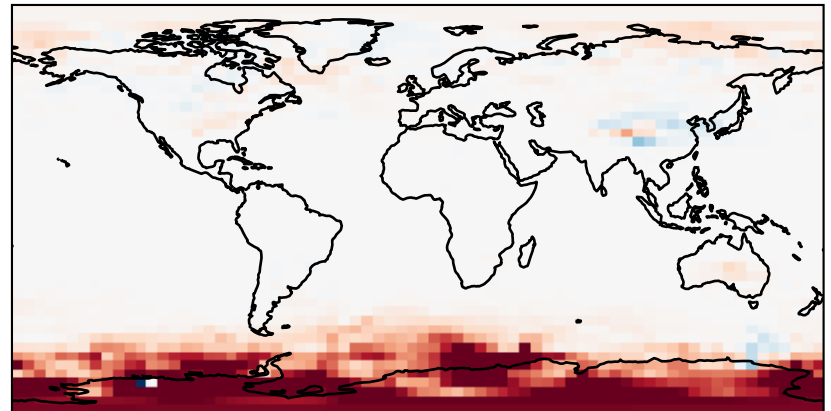
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



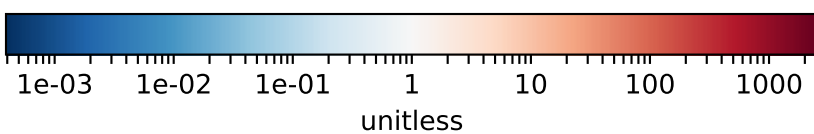
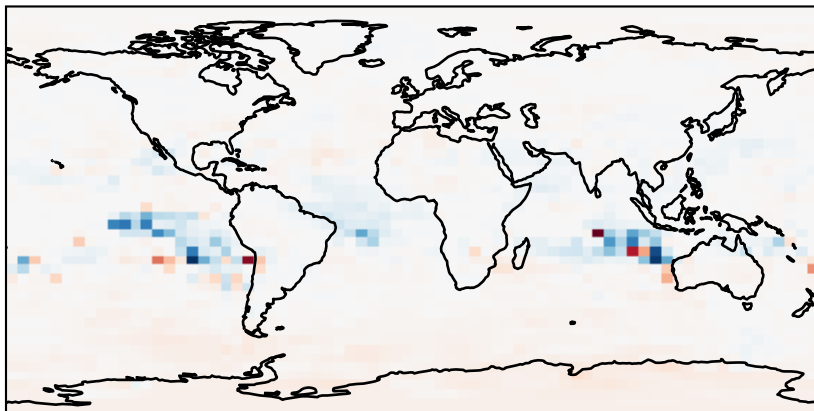
Difference  
Dev - Ref, Dynamic Range



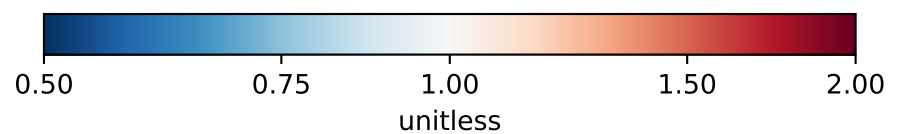
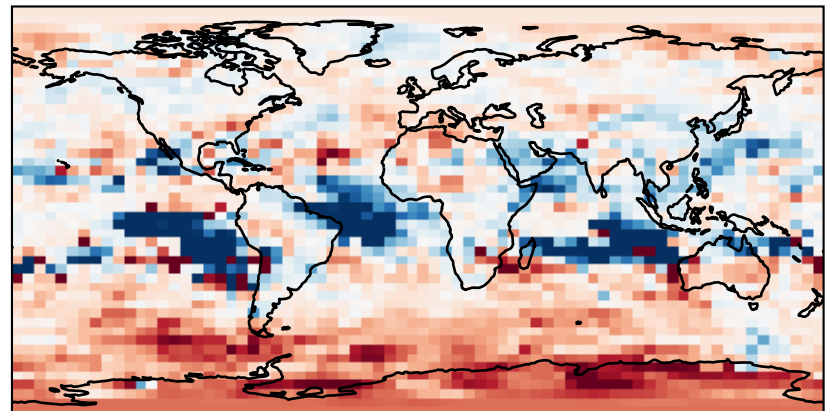
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

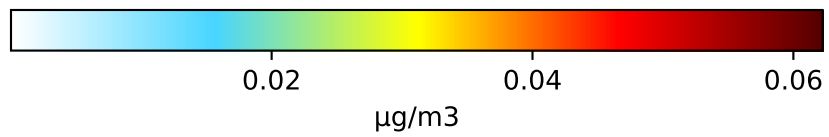
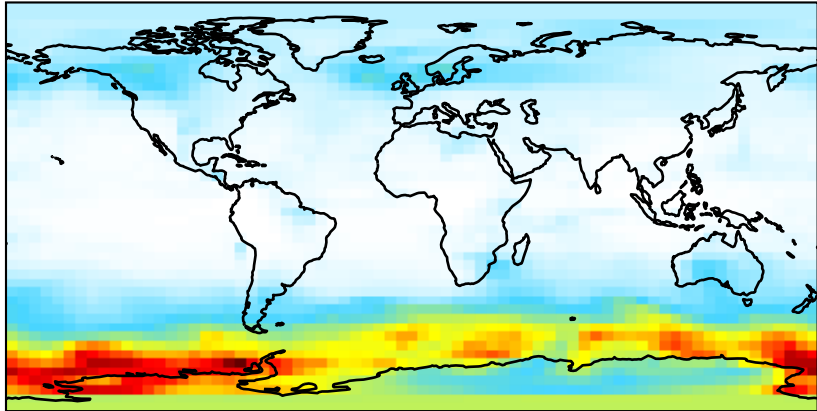


Ratio  
Dev/Ref, Fixed Range

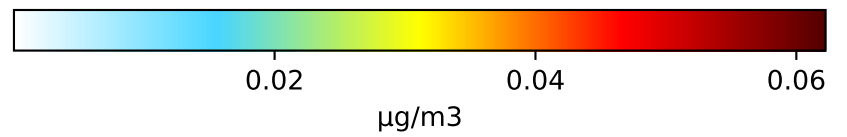
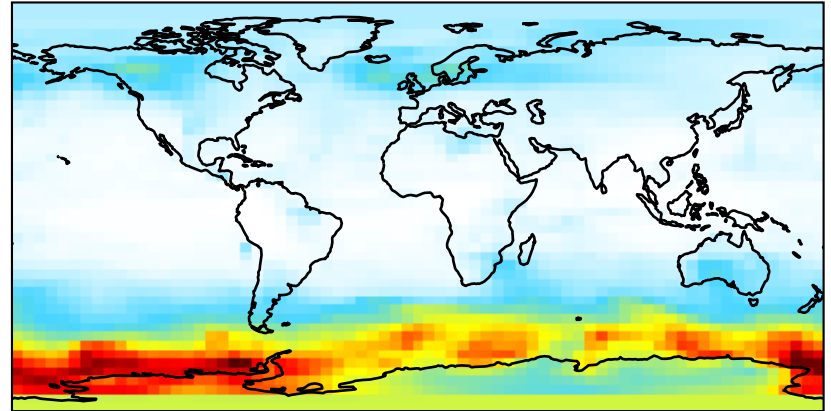


# SpeciesConc\_SALC (Oct2019)

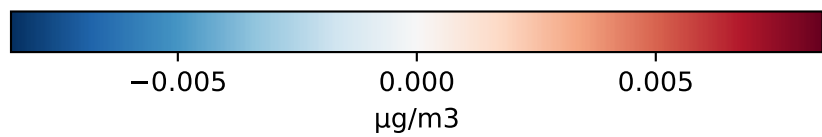
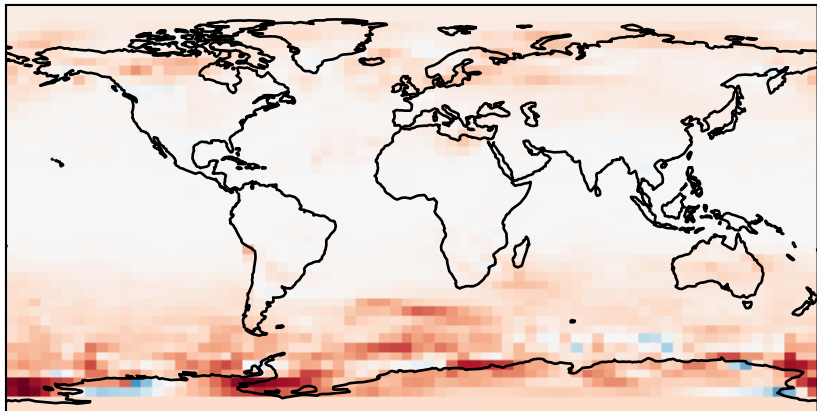
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



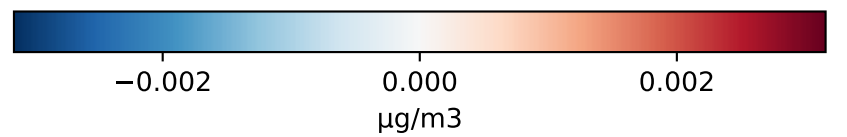
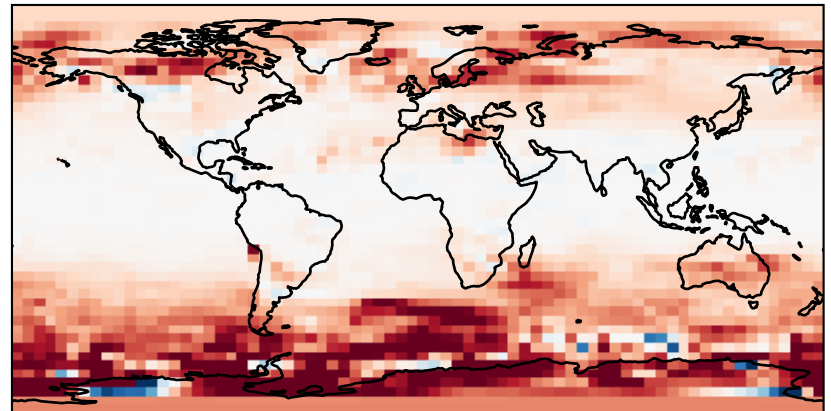
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



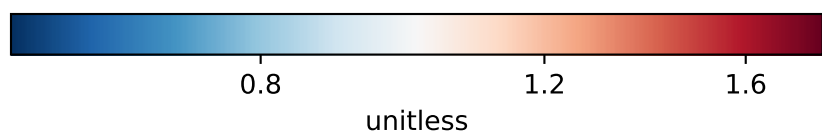
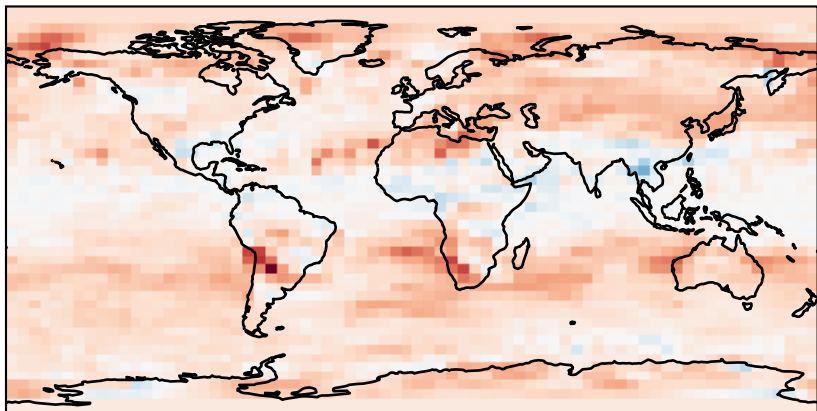
Difference  
Dev - Ref, Dynamic Range



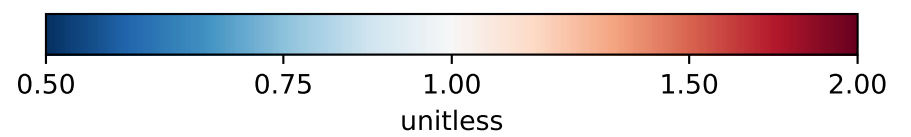
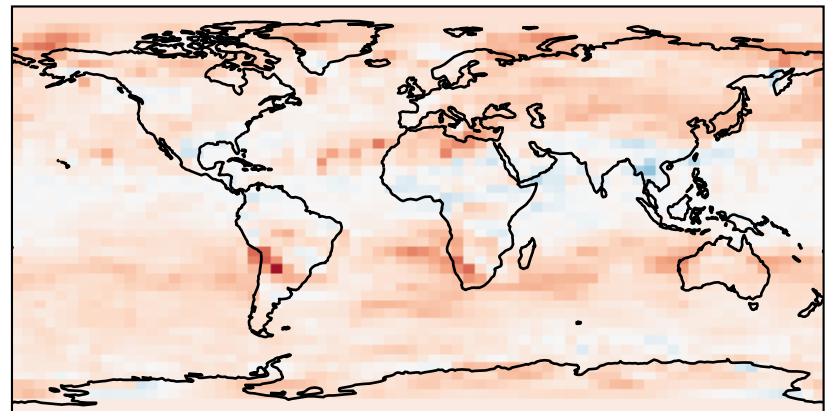
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



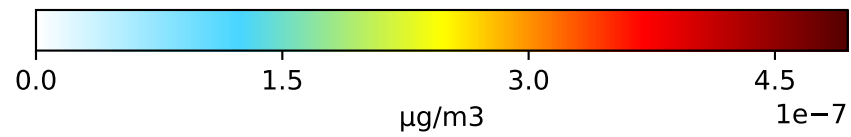
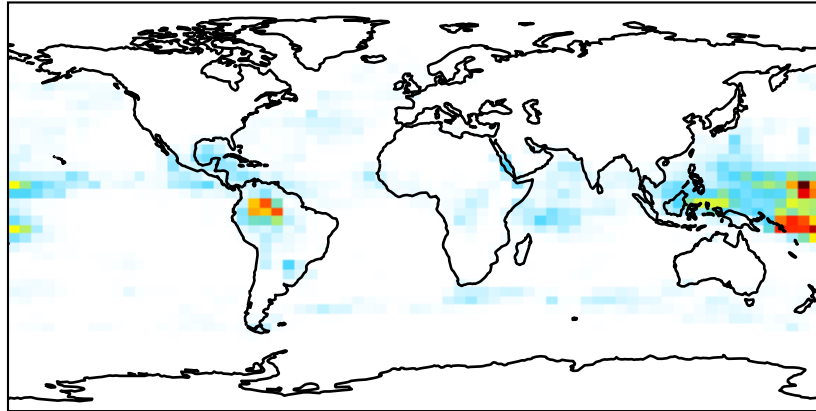
Ratio  
Dev/Ref, Fixed Range



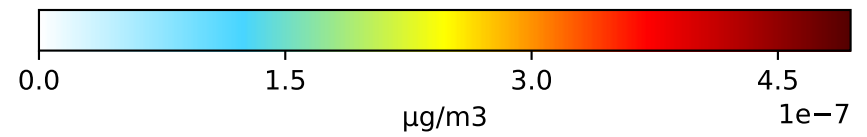
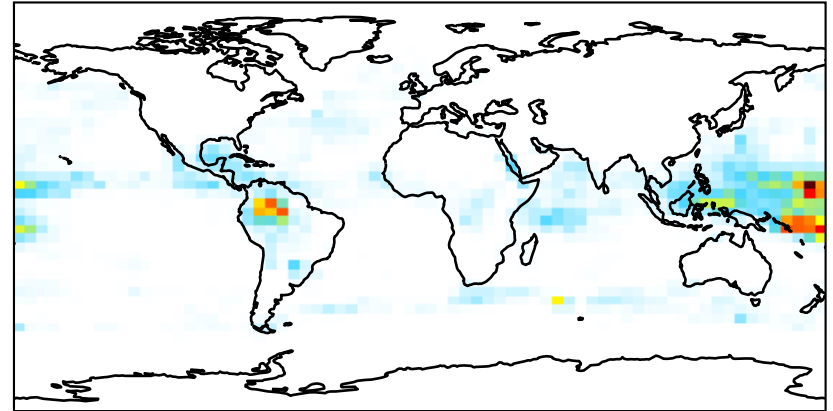


# SpeciesConc\_SALCAL (Oct2019)

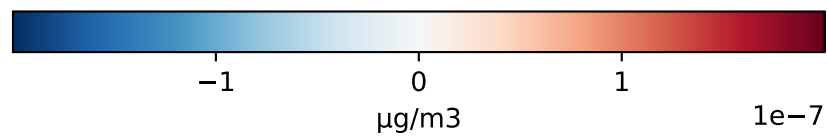
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



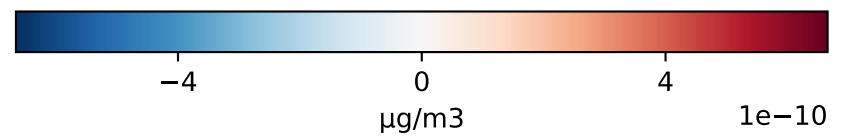
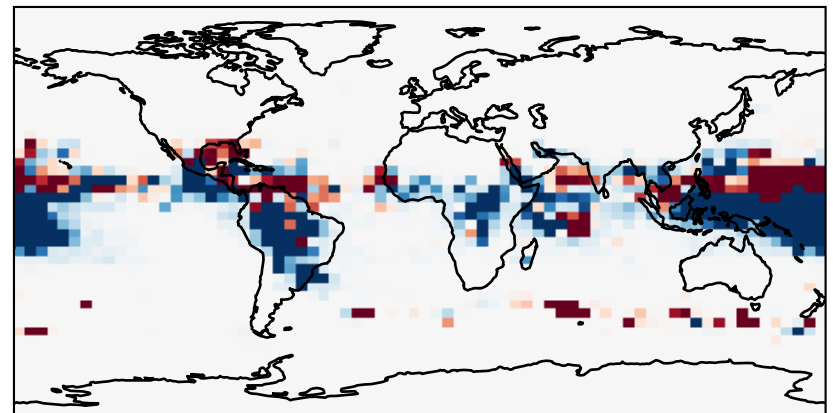
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



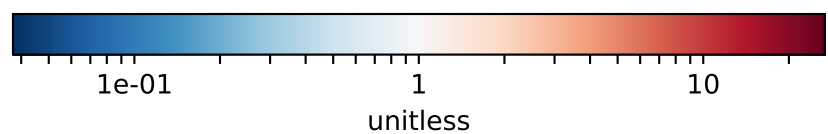
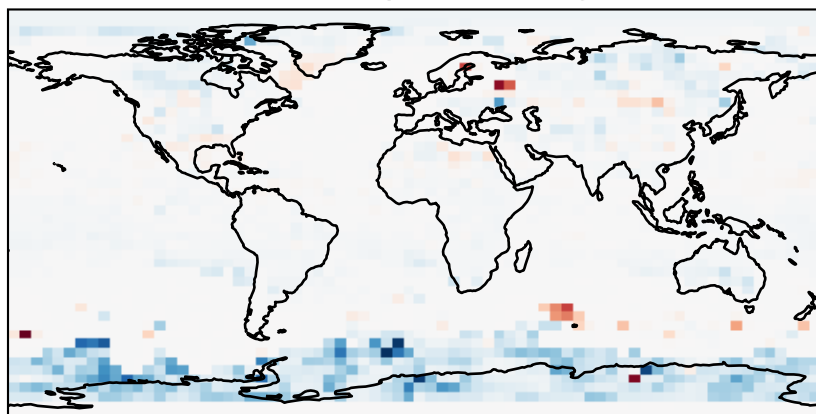
Difference  
Dev - Ref, Dynamic Range



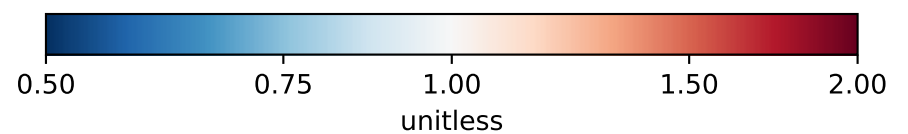
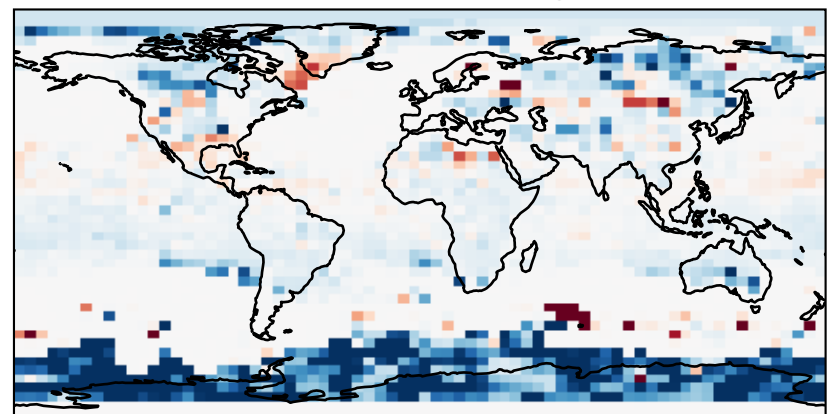
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

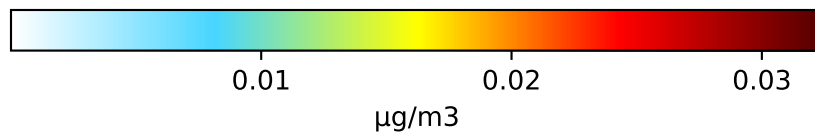
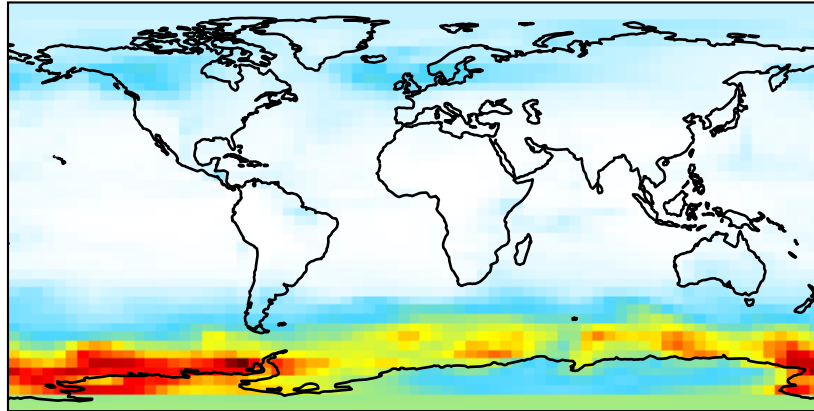


Ratio  
Dev/Ref, Fixed Range

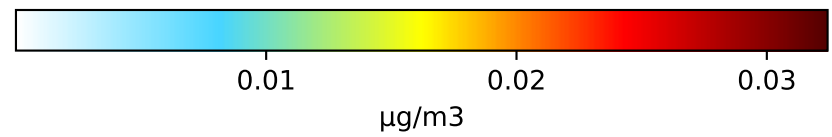
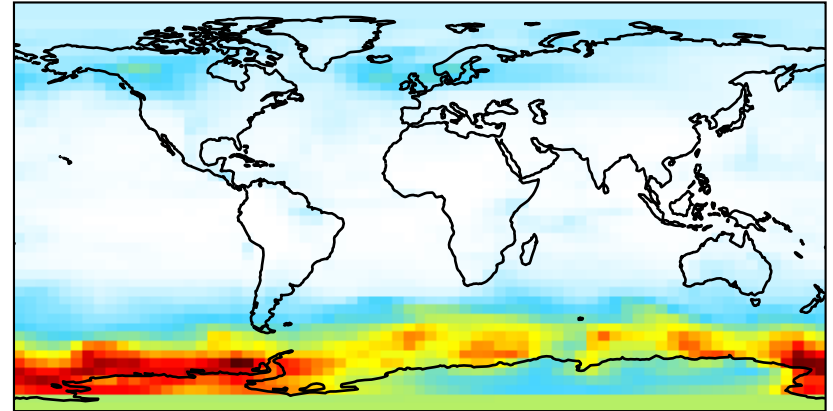


# SpeciesConc\_SALCCL (Oct2019)

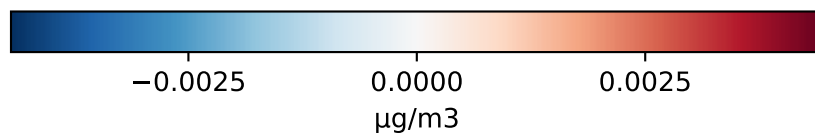
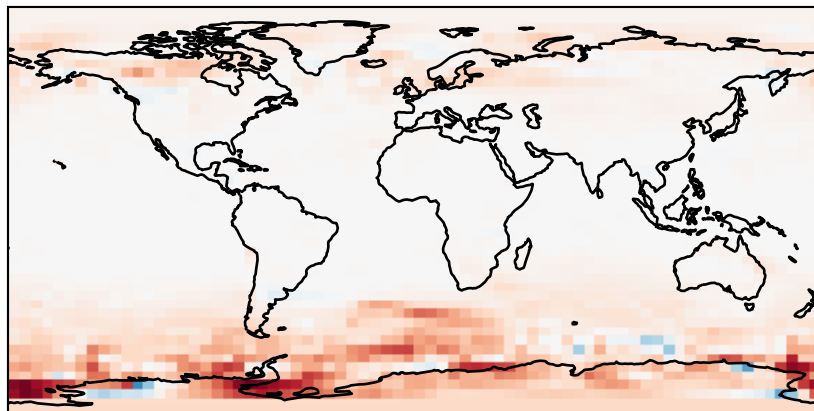
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



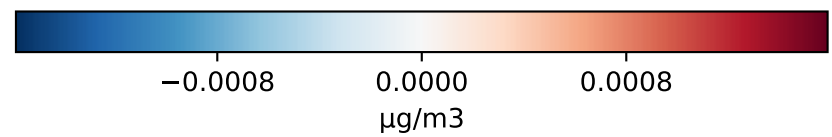
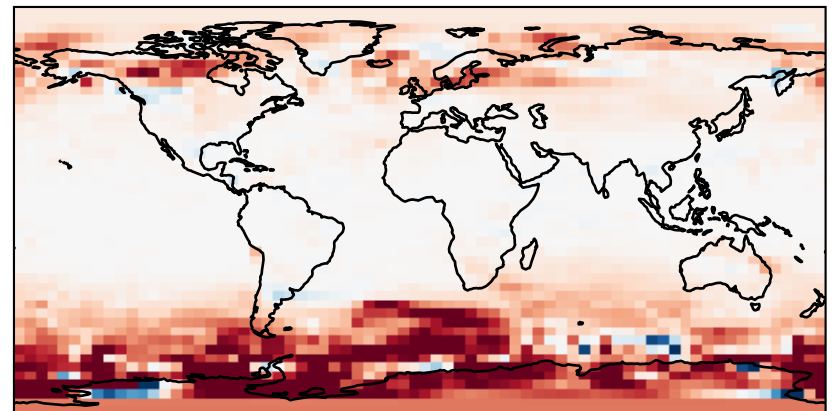
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



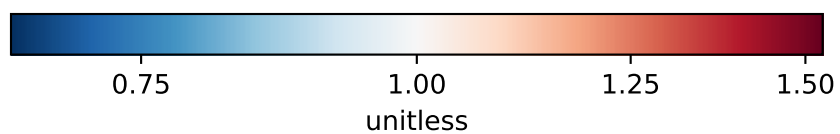
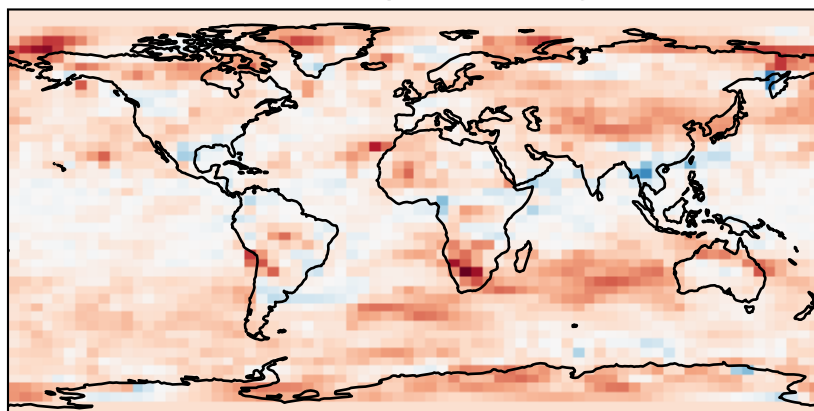
Difference  
Dev - Ref, Dynamic Range



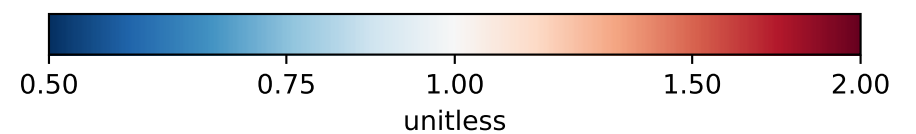
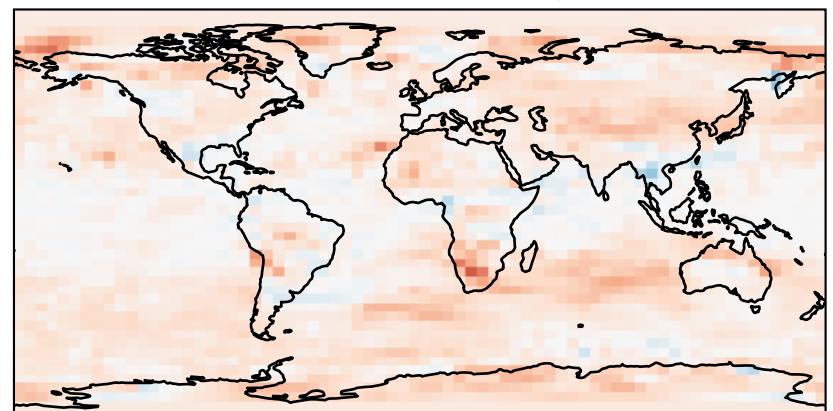
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

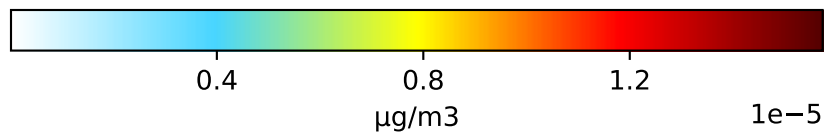
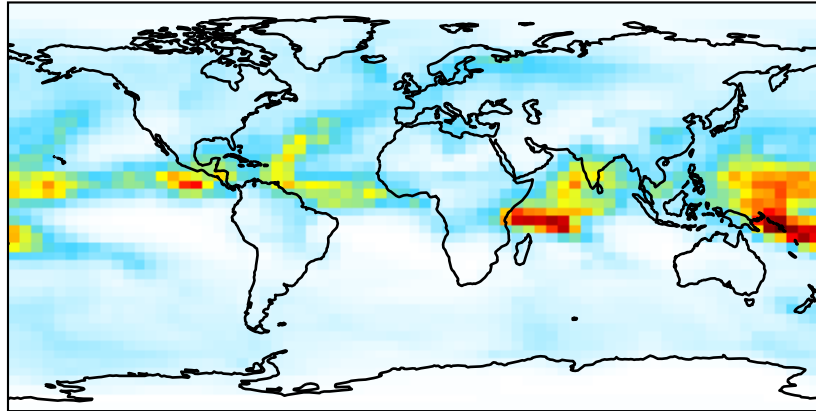


Ratio  
Dev/Ref, Fixed Range

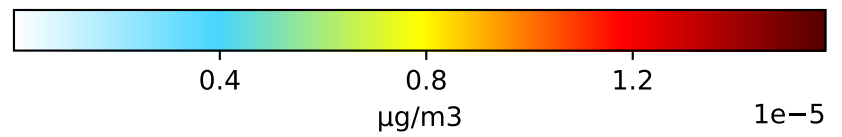
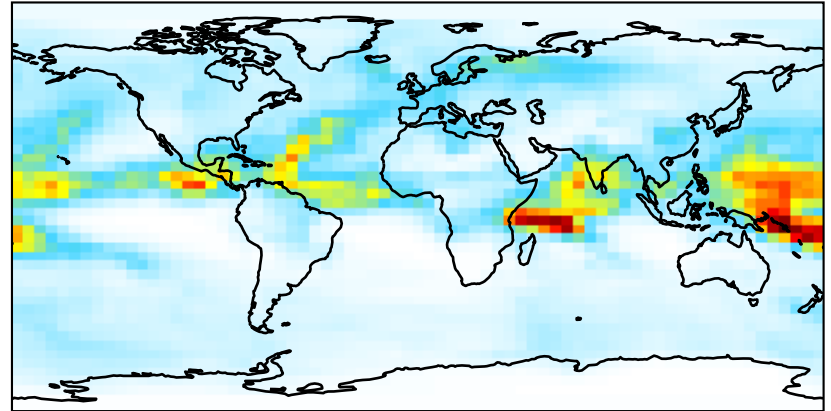


# SpeciesConc\_S04s (Oct2019)

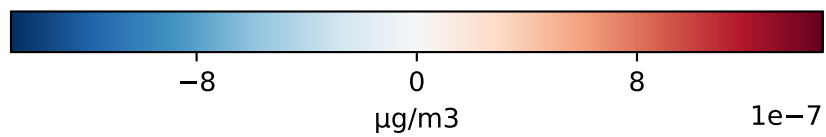
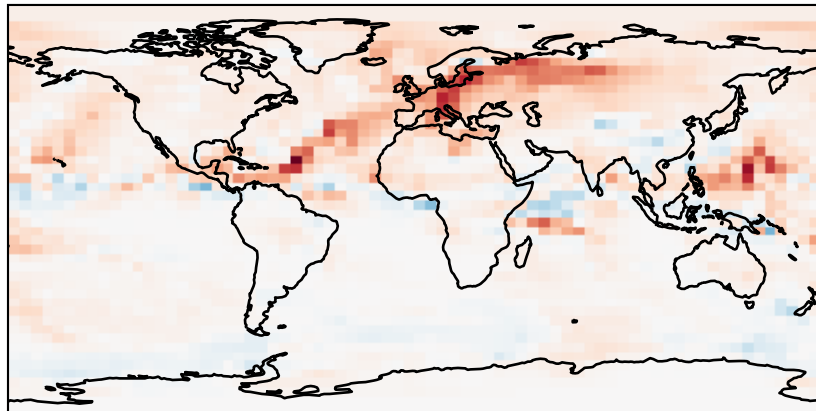
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



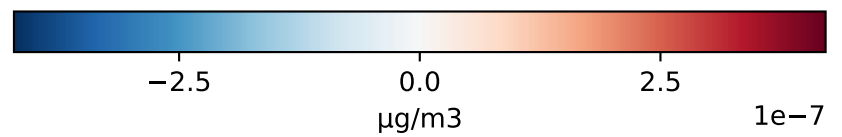
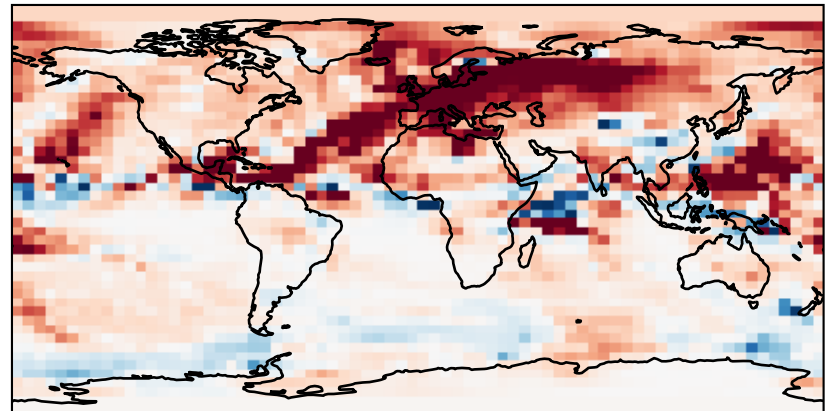
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



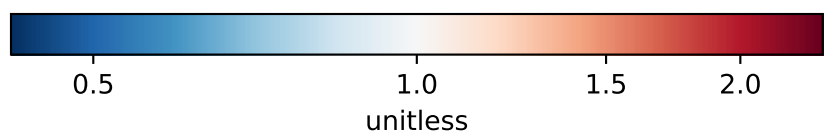
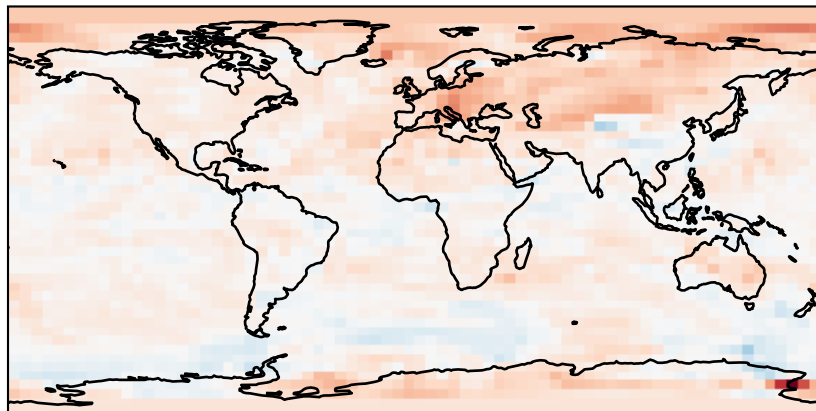
Difference  
Dev - Ref, Dynamic Range



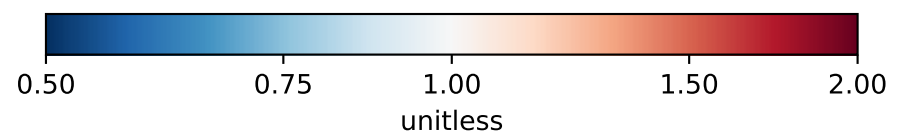
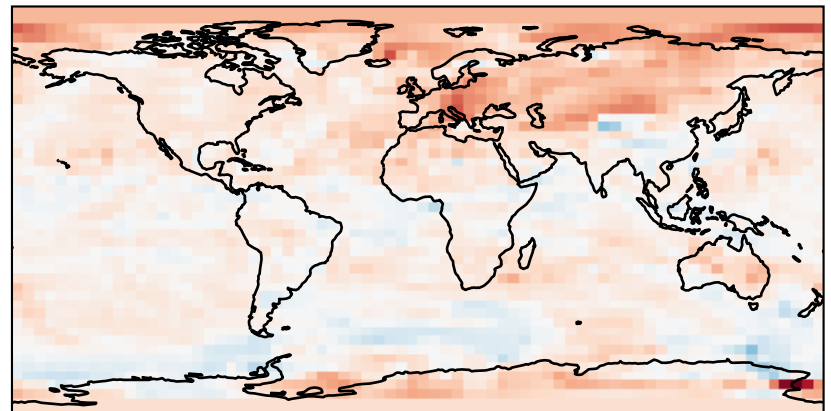
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

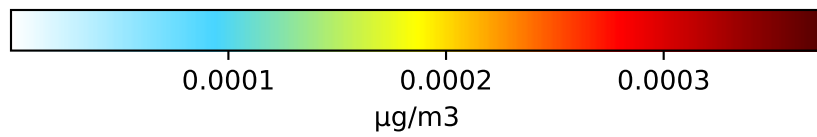
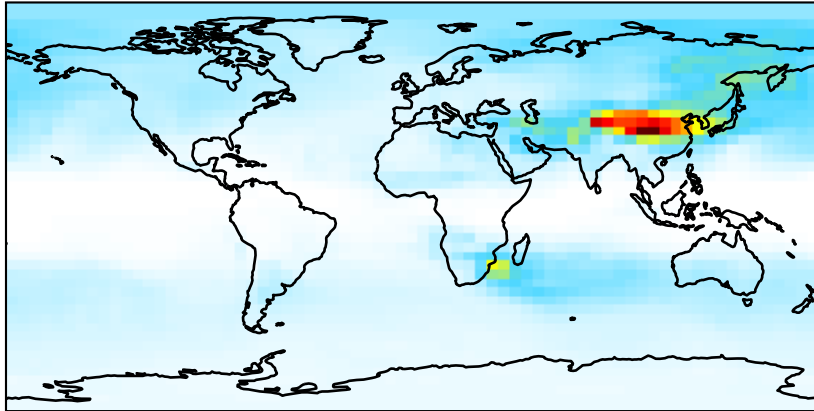


Ratio  
Dev/Ref, Fixed Range

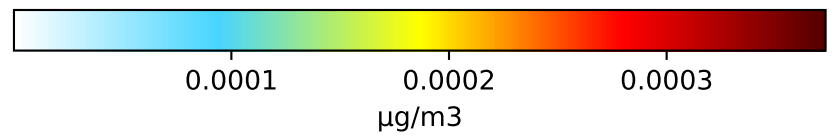
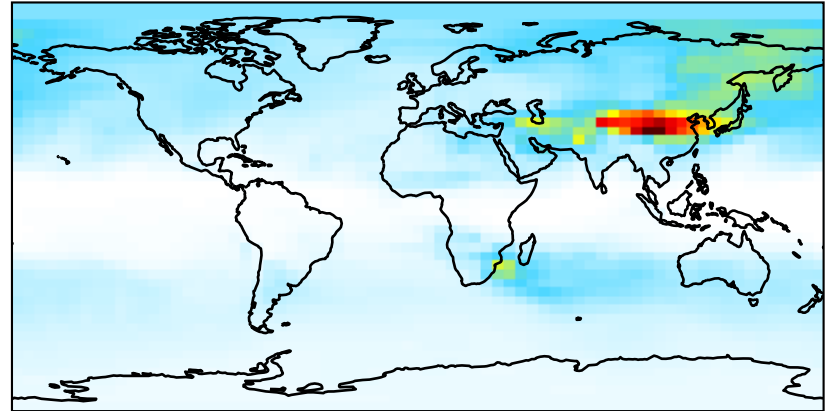


# SpeciesConc\_pFe (Oct2019)

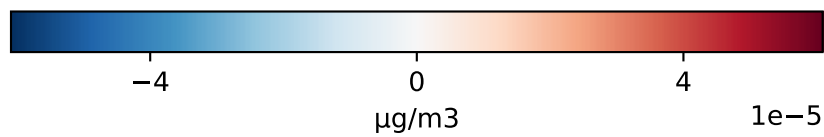
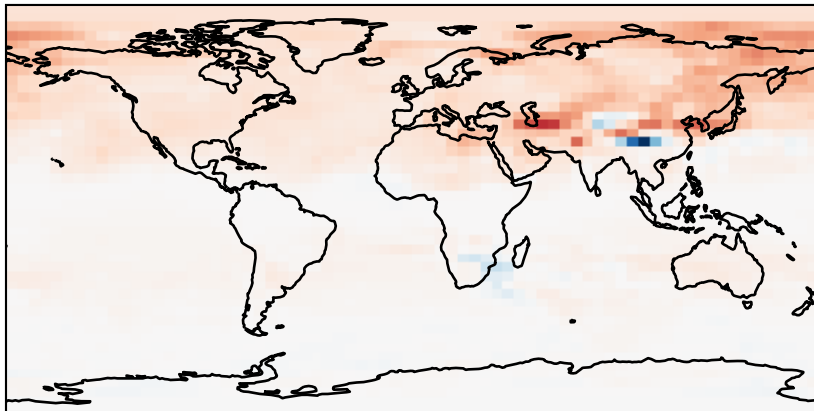
GCC\_13.4.0-rc.4 (Ref)  
4.0x5.0



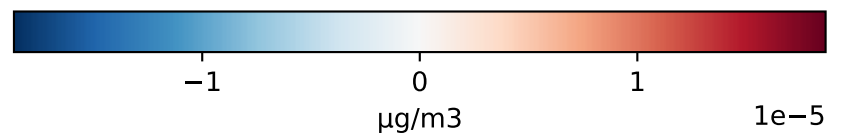
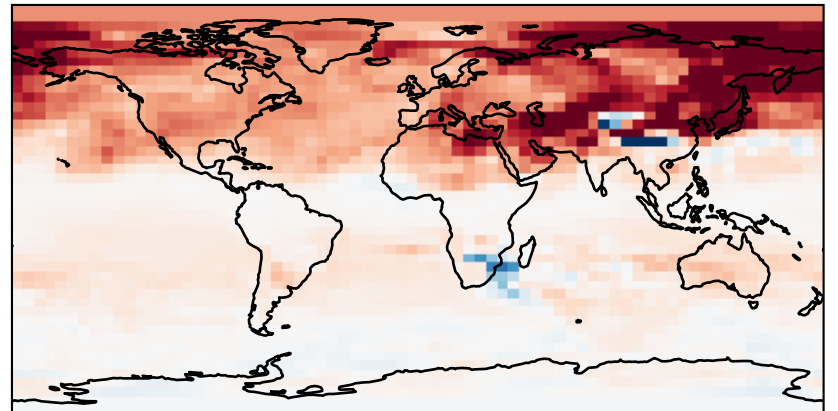
GCC\_14.0.0-rc.3 (Dev)  
4.0x5.0



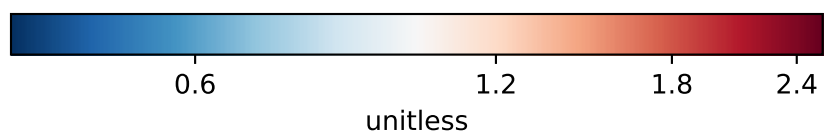
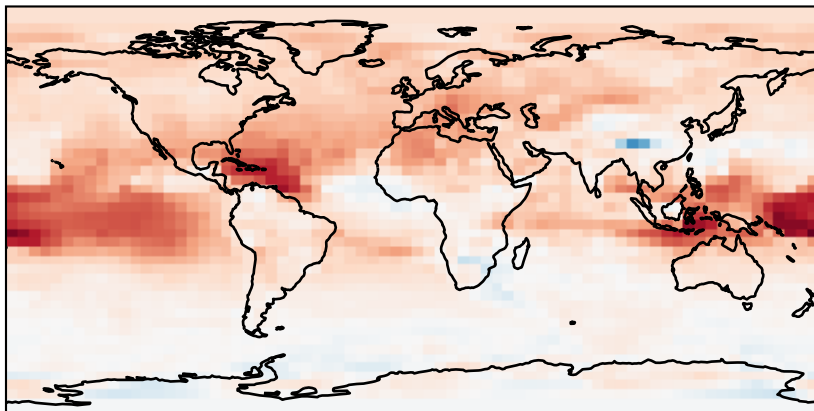
Difference  
Dev - Ref, Dynamic Range



Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

