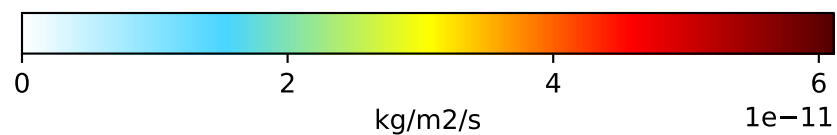
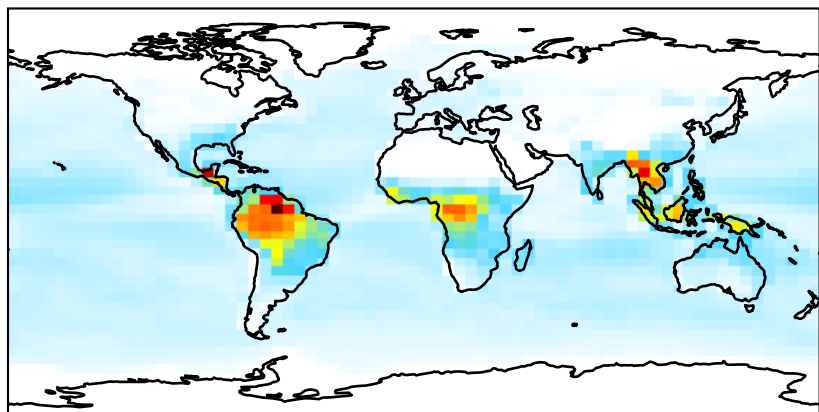
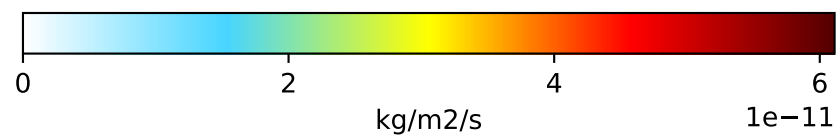
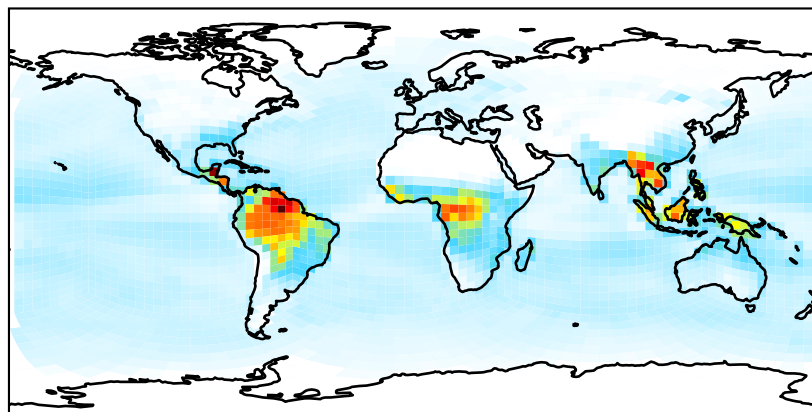


# EmisACET\_Total (Apr2019)

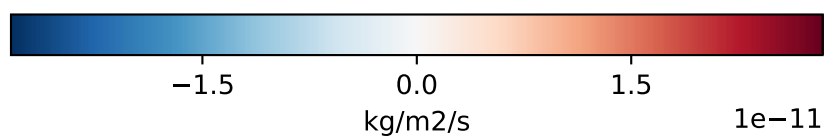
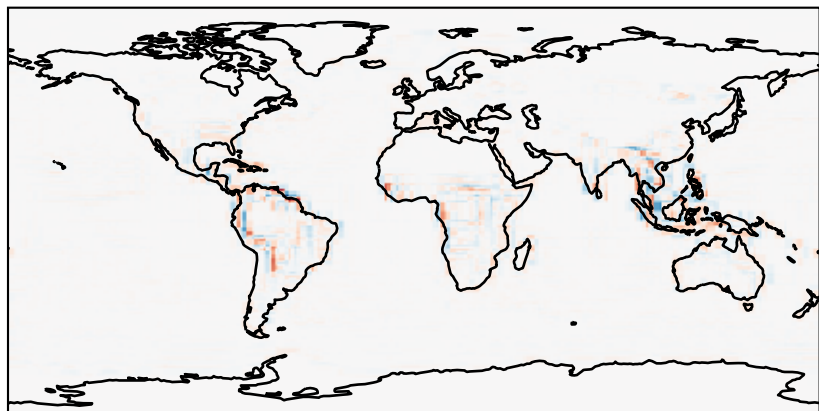
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



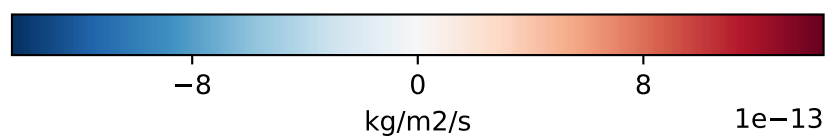
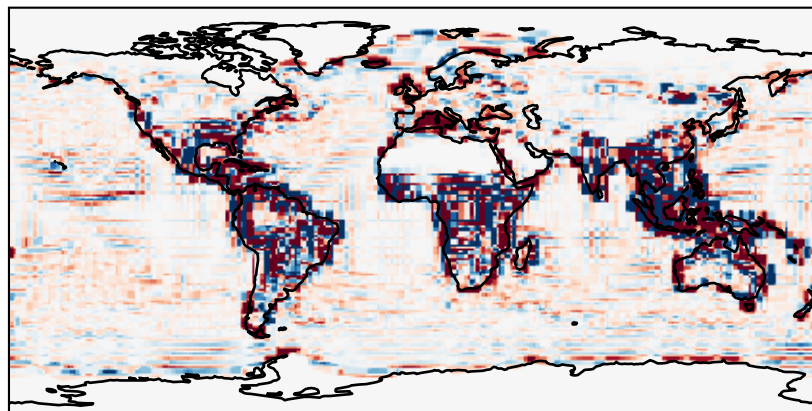
GCHP\_14.0.0-rc.3 (Dev)  
c24



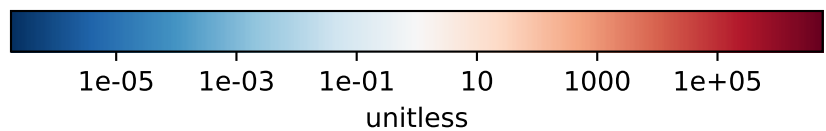
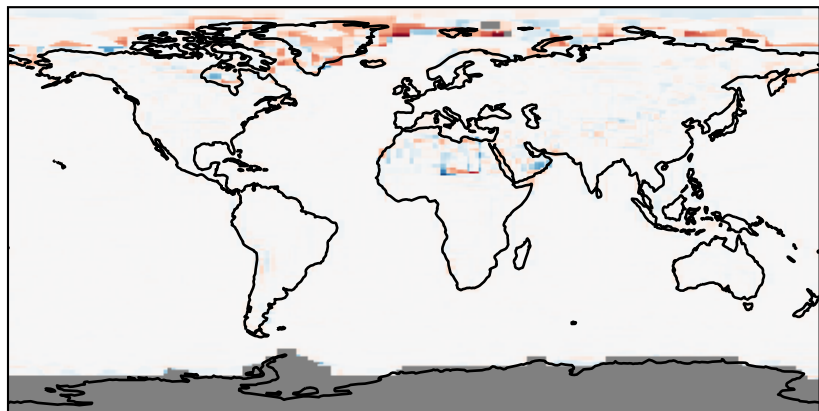
Difference (1x1.25)  
Dev - Ref, Dynamic Range



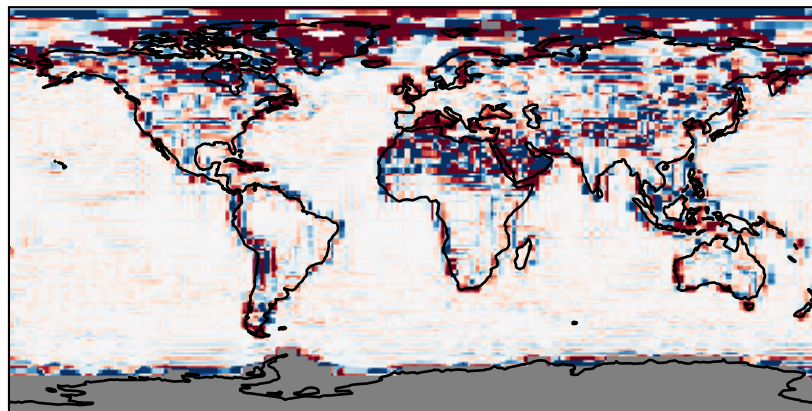
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

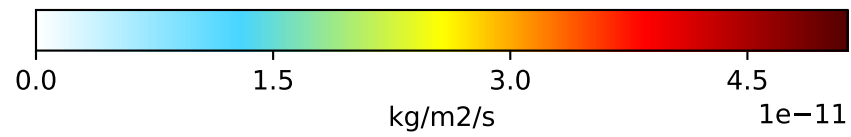
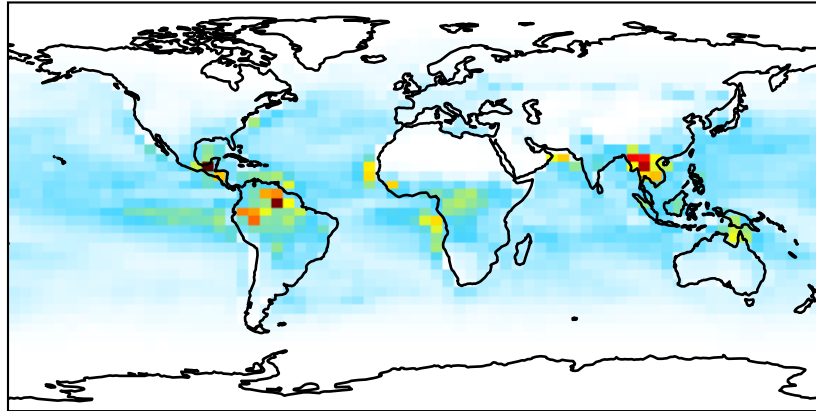


Ratio (1x1.25)  
Dev/Ref, Fixed Range

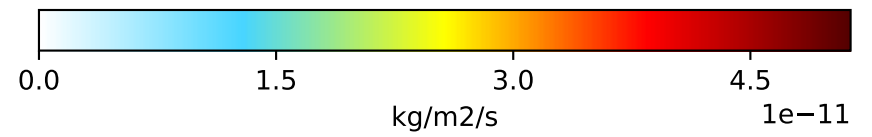
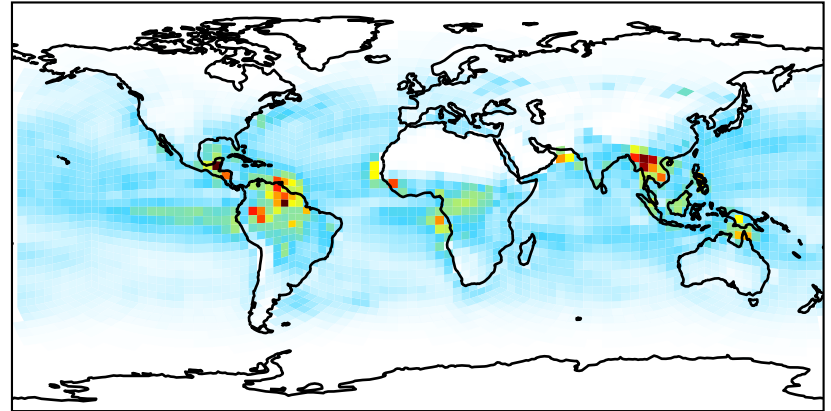


# EmisALD2\_Total (Apr2019)

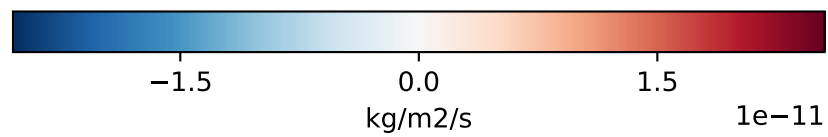
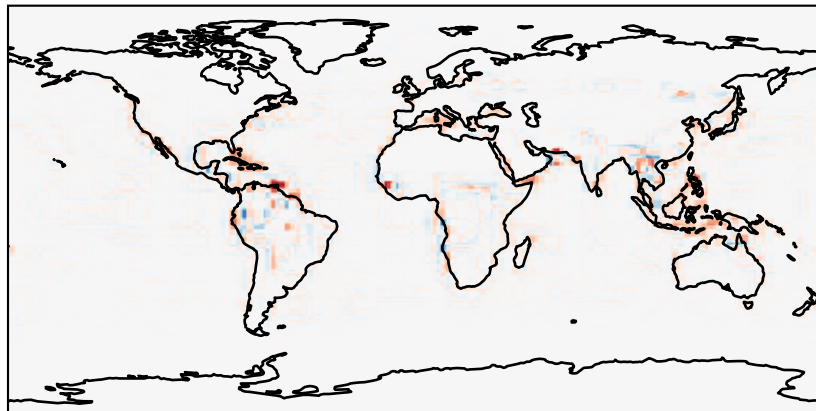
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



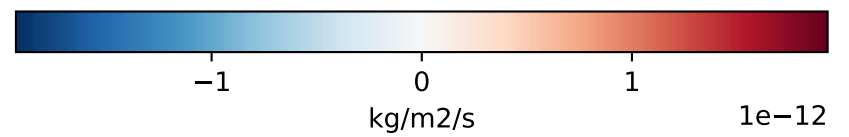
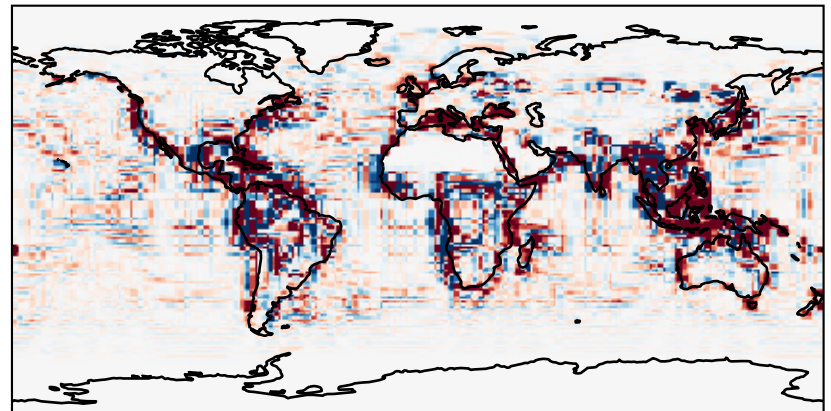
GCHP\_14.0.0-rc.3 (Dev)  
c24



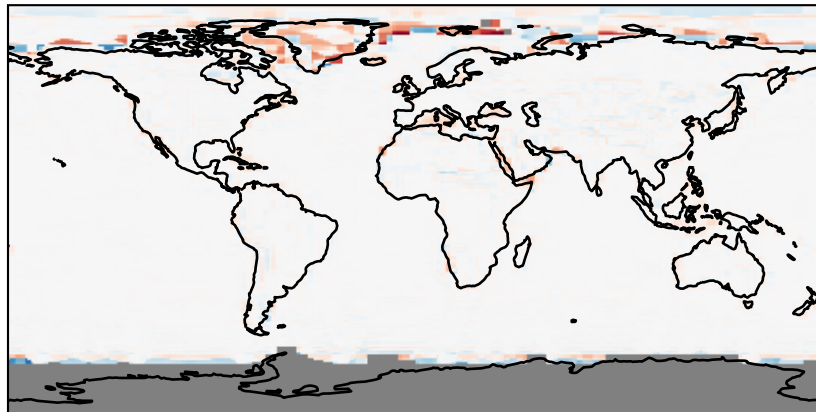
Difference (1x1.25)  
Dev - Ref, Dynamic Range



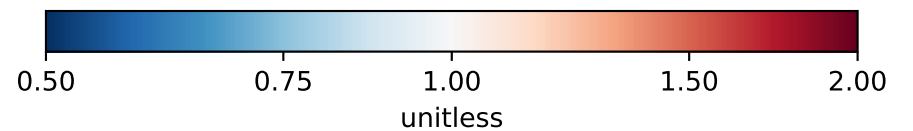
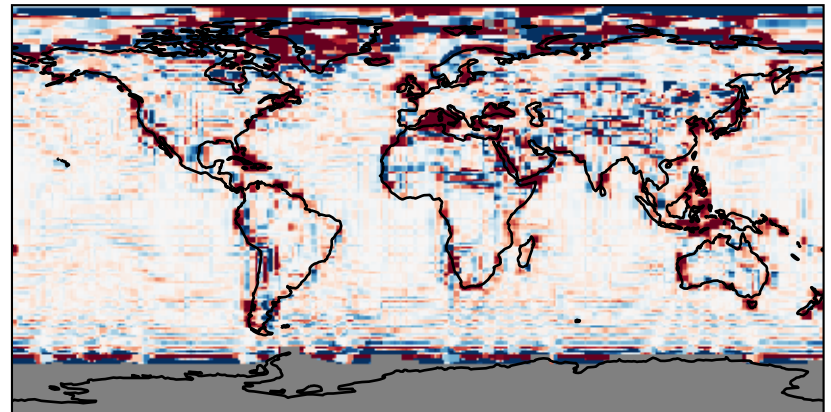
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

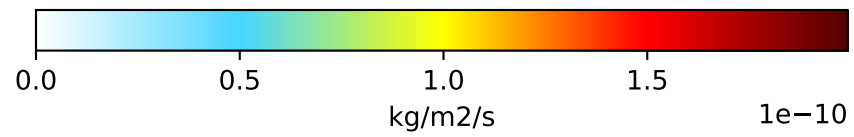
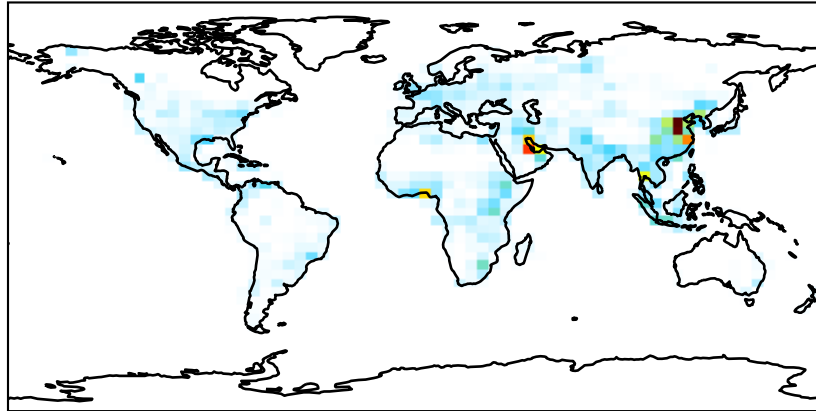


Ratio (1x1.25)  
Dev/Ref, Fixed Range

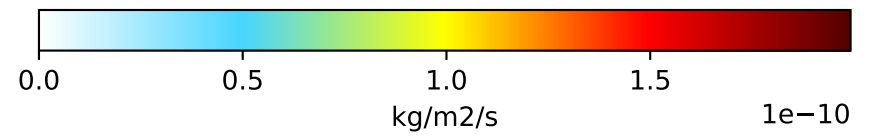
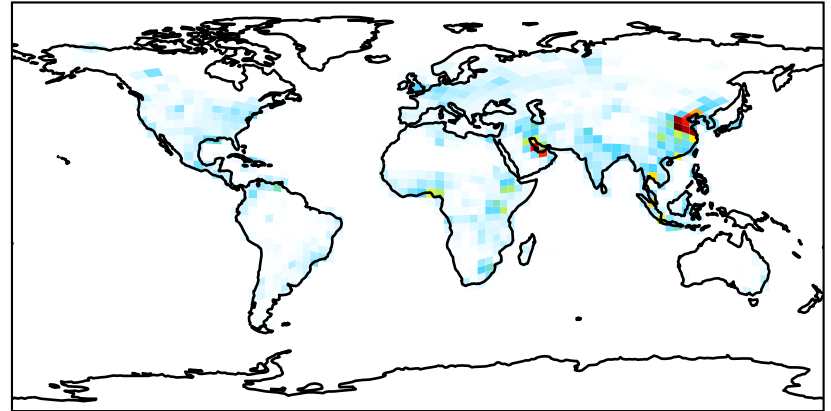


# EmisALK4\_Total (Apr2019)

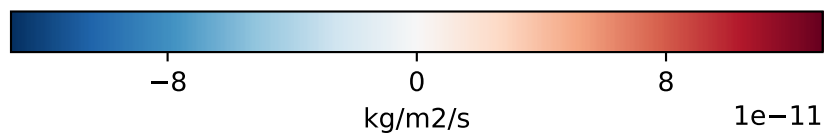
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



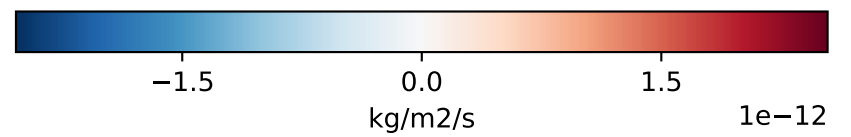
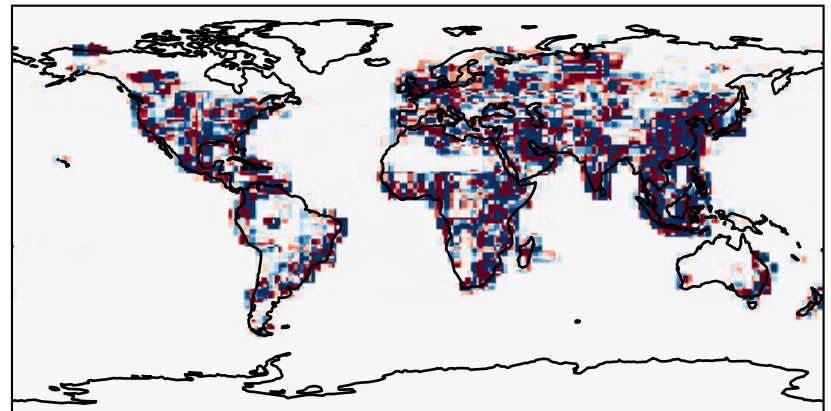
GCHP\_14.0.0-rc.3 (Dev)  
c24



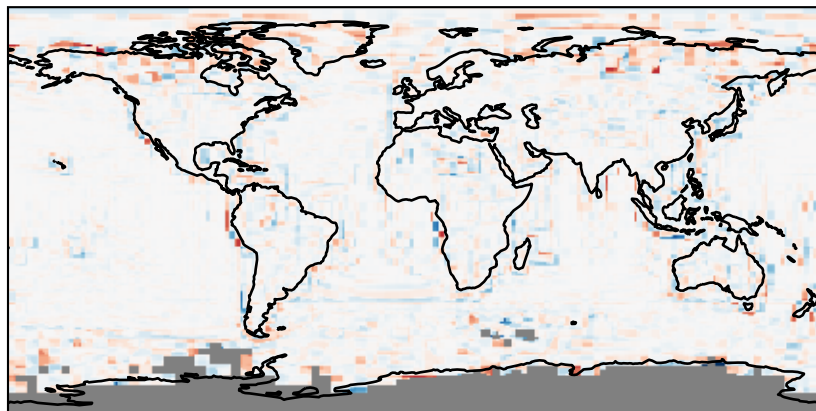
Difference (1x1.25)  
Dev - Ref, Dynamic Range



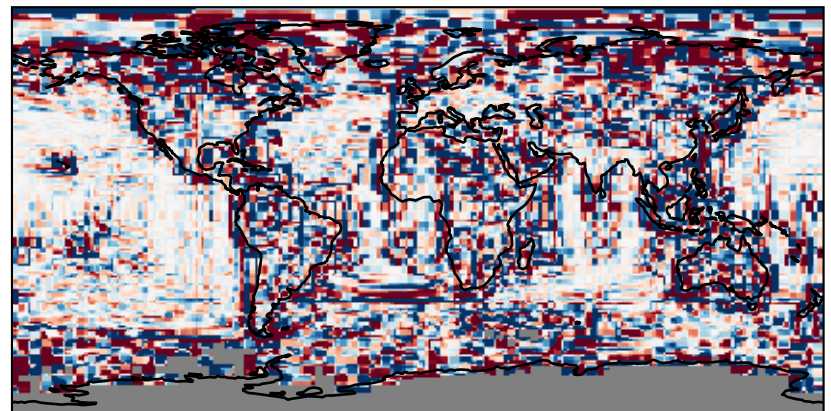
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

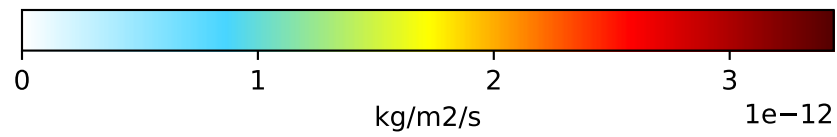
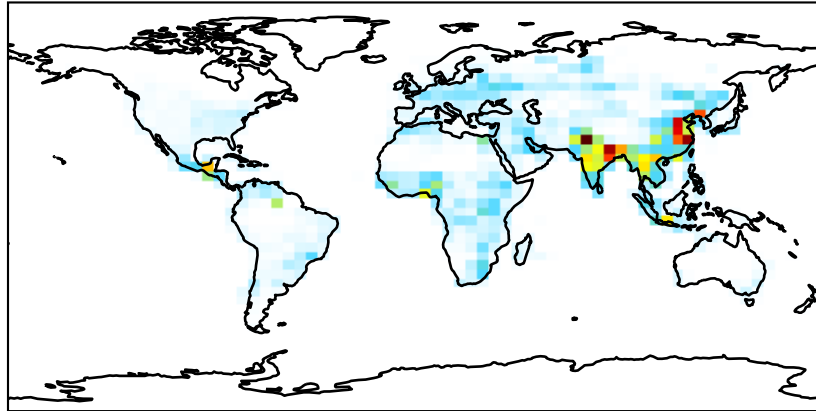


Ratio (1x1.25)  
Dev/Ref, Fixed Range

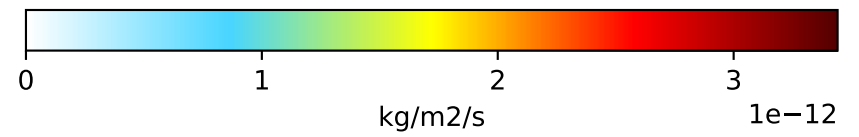
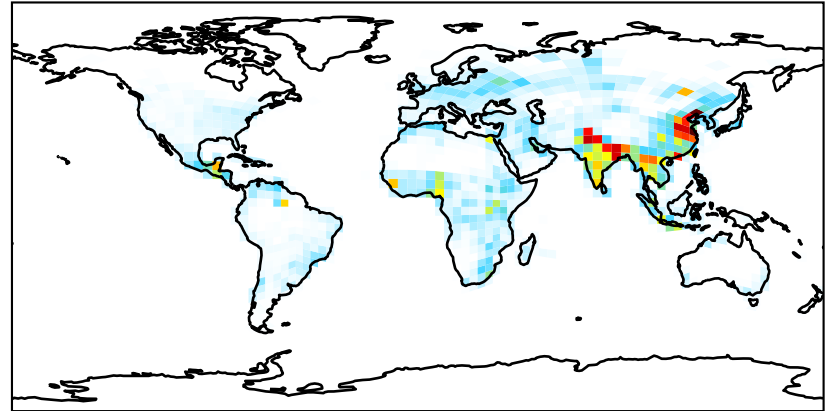


# EmisBCPI\_Total (Apr2019)

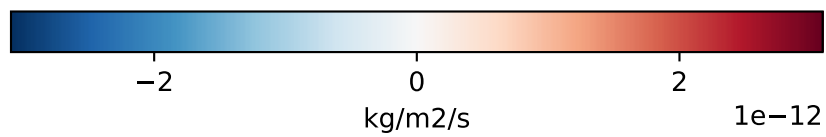
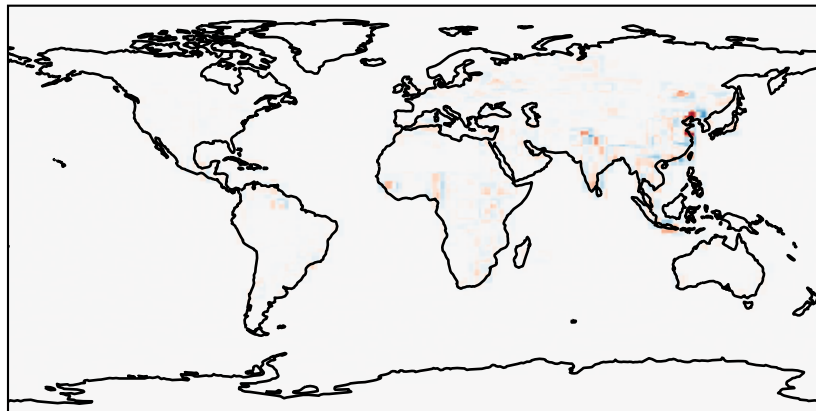
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



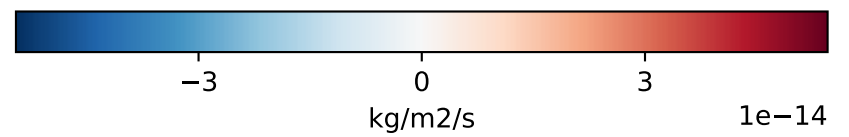
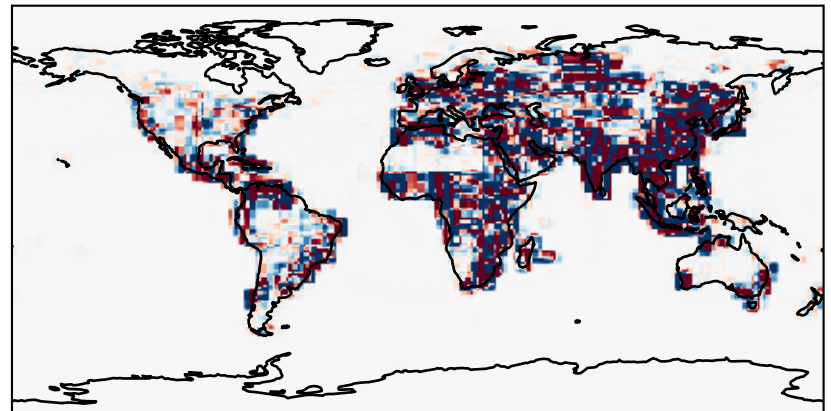
GCHP\_14.0.0-rc.3 (Dev)  
c24



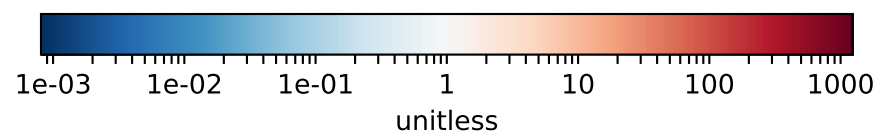
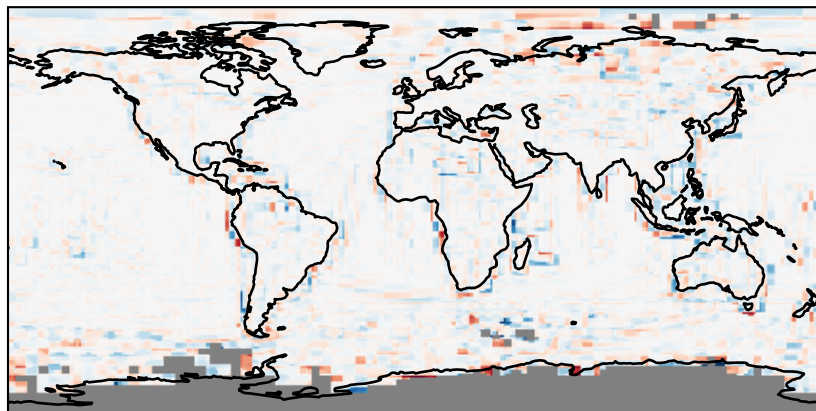
Difference (1x1.25)  
Dev - Ref, Dynamic Range



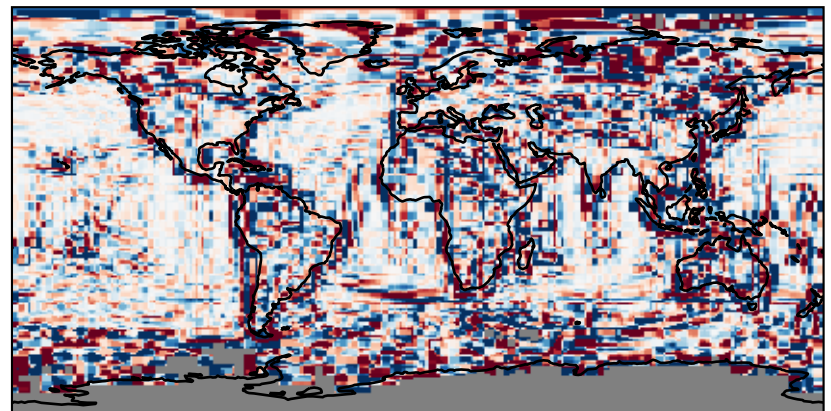
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

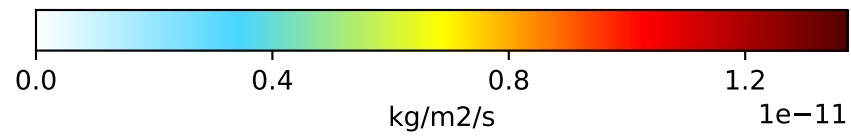
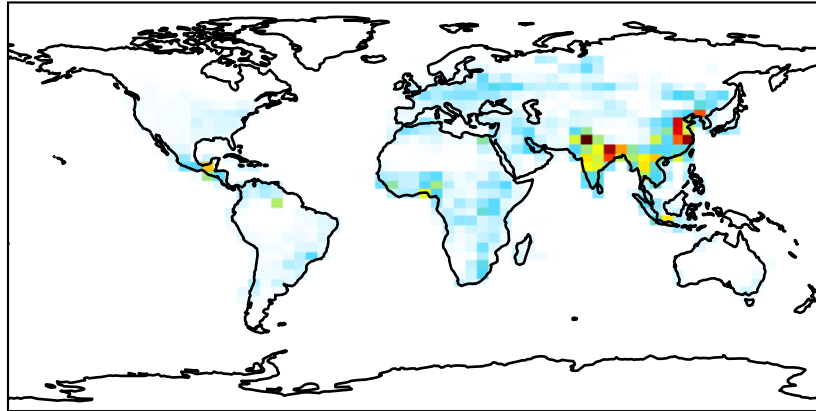


Ratio (1x1.25)  
Dev/Ref, Fixed Range

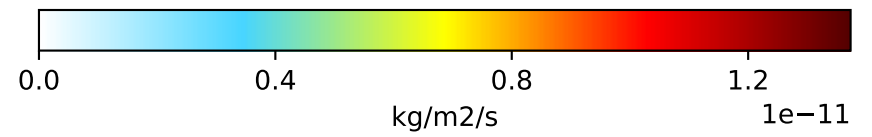
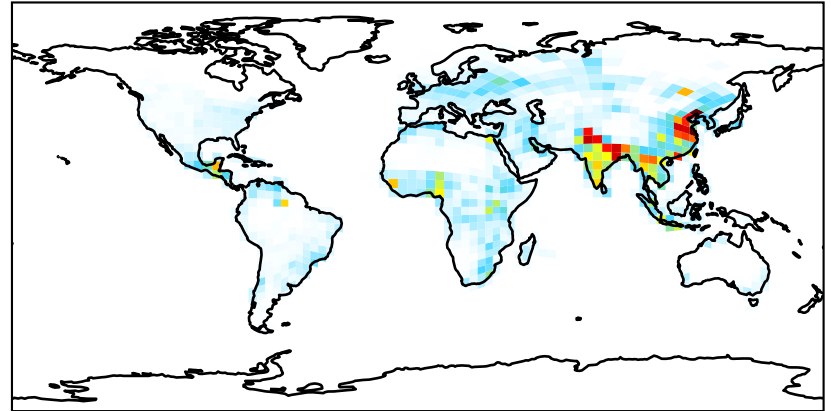


# EmisBCPO\_Total (Apr2019)

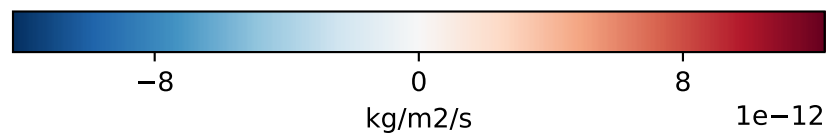
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



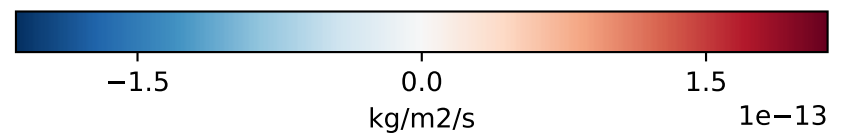
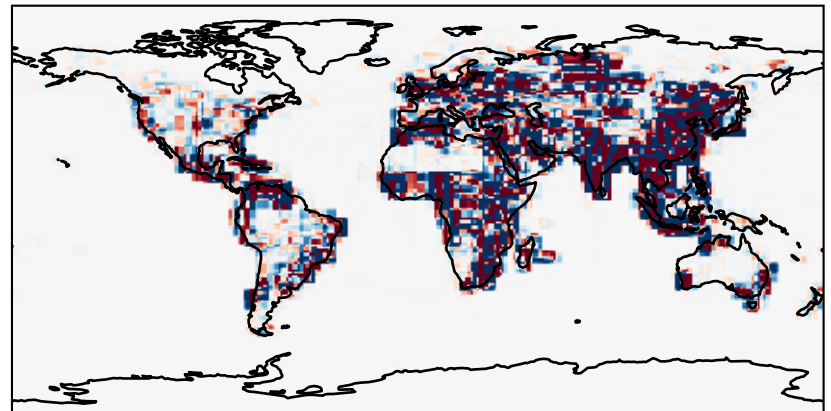
GCHP\_14.0.0-rc.3 (Dev)  
c24



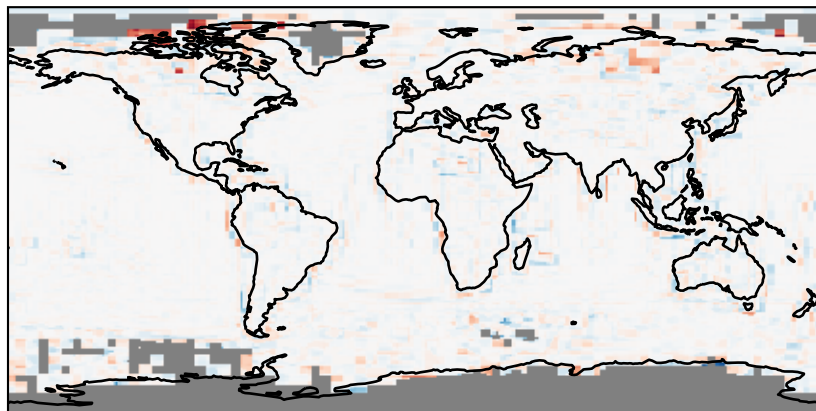
Difference (1x1.25)  
Dev - Ref, Dynamic Range



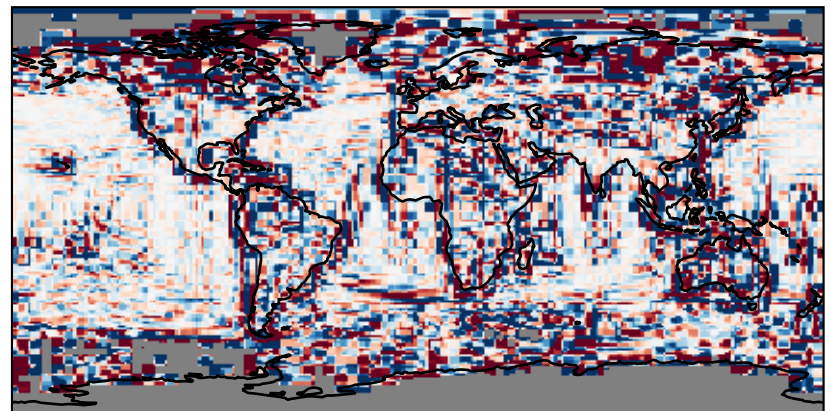
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

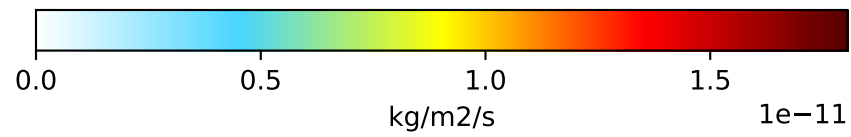
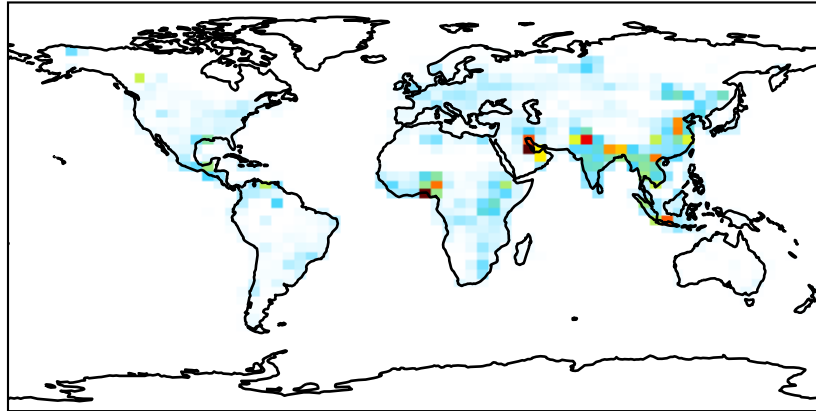


Ratio (1x1.25)  
Dev/Ref, Fixed Range

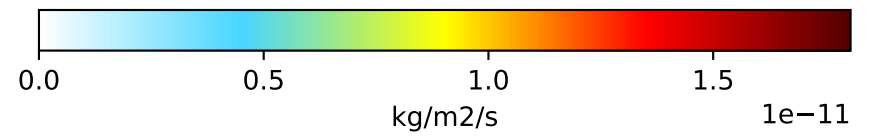
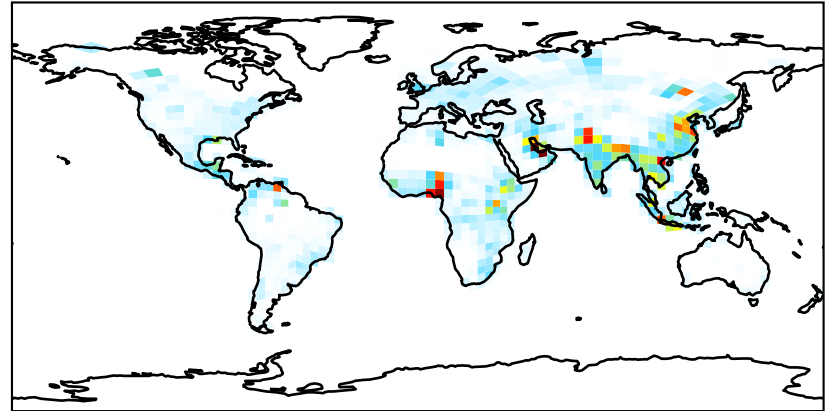


# EmisBENZ\_Total (Apr2019)

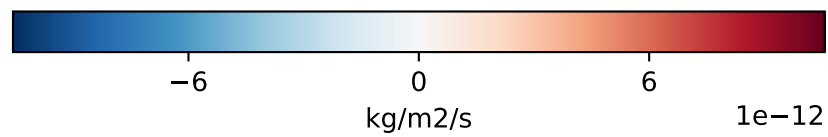
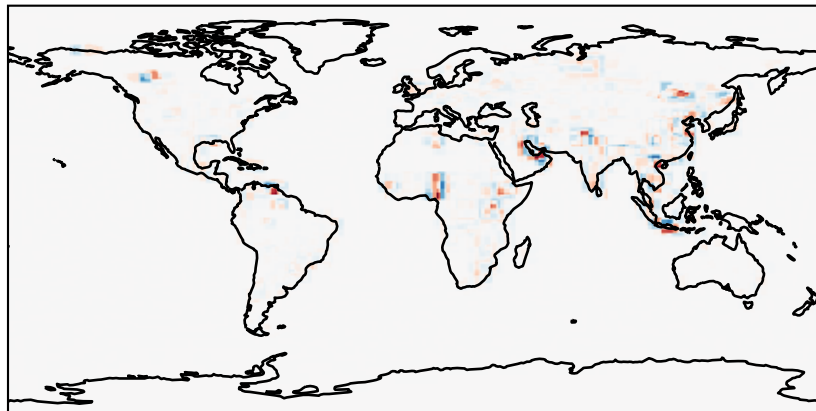
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



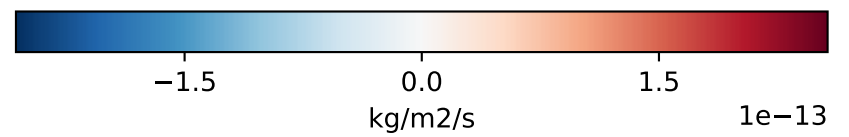
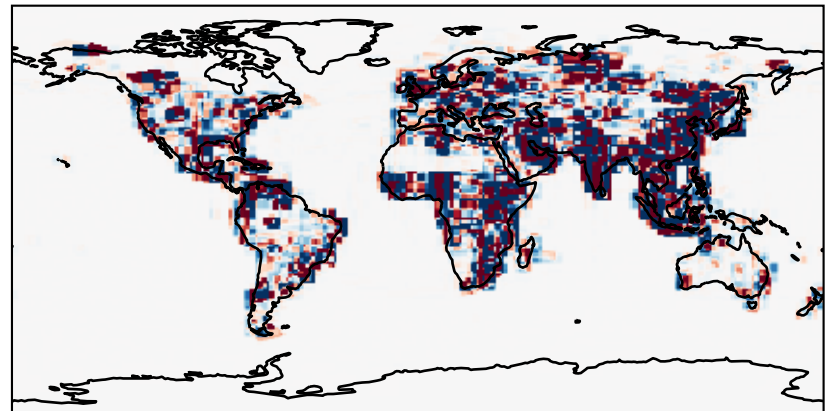
GCHP\_14.0.0-rc.3 (Dev)  
c24



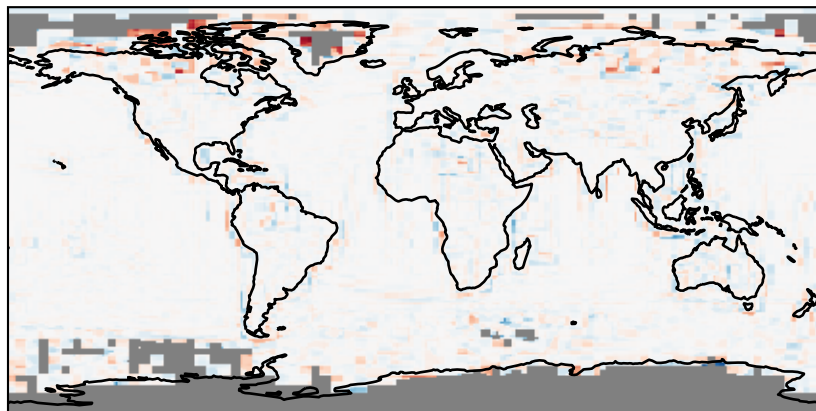
Difference (1x1.25)  
Dev - Ref, Dynamic Range



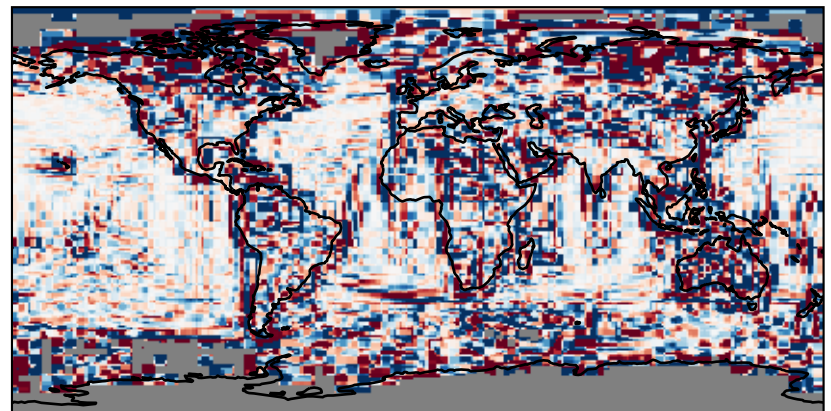
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

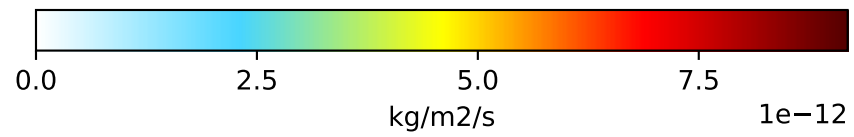
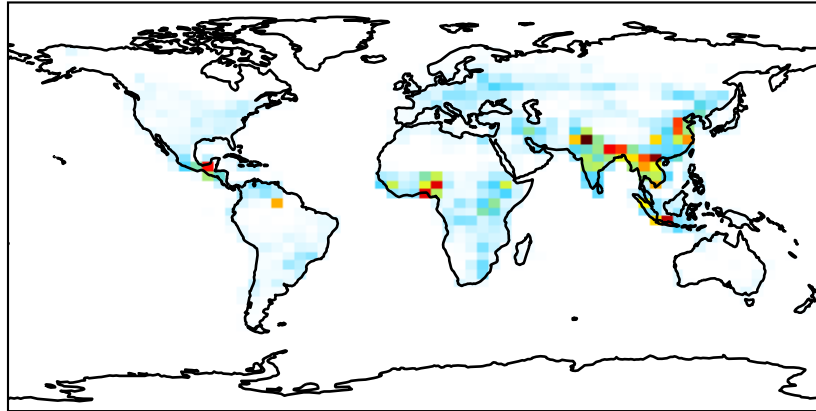


Ratio (1x1.25)  
Dev/Ref, Fixed Range

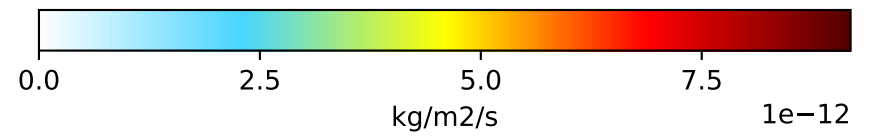
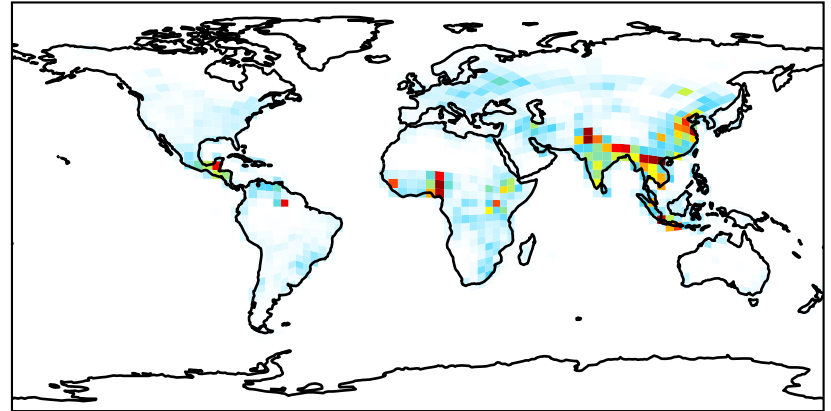


# EmisC2H2\_Total (Apr2019)

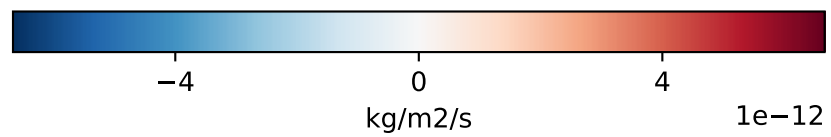
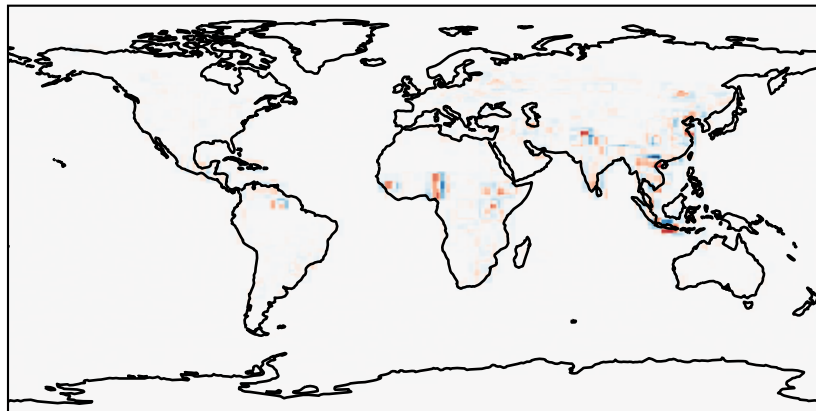
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



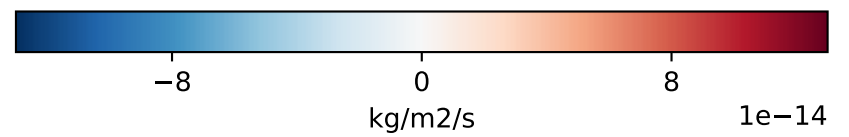
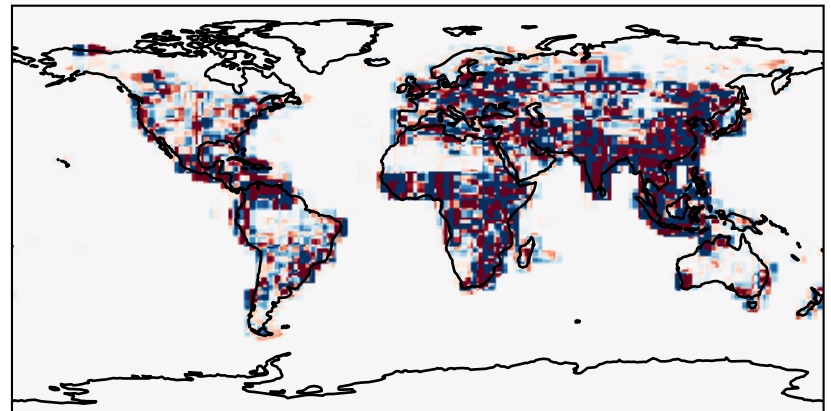
GCHP\_14.0.0-rc.3 (Dev)  
c24



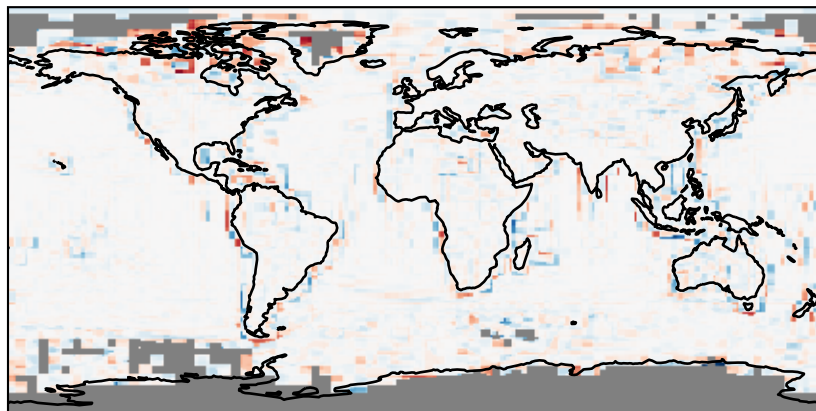
Difference (1x1.25)  
Dev - Ref, Dynamic Range



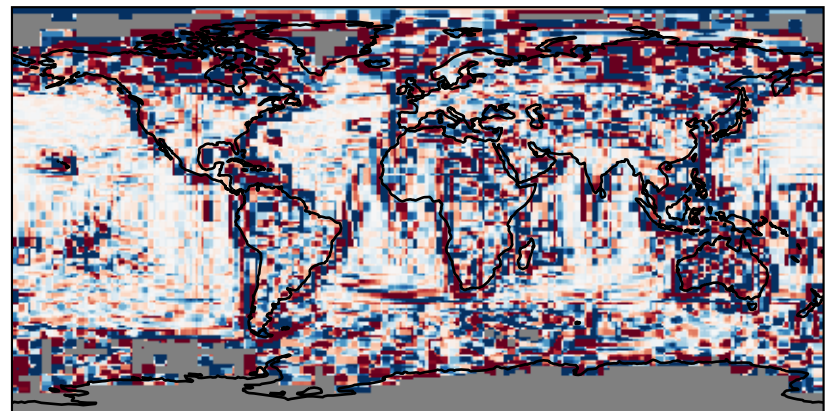
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

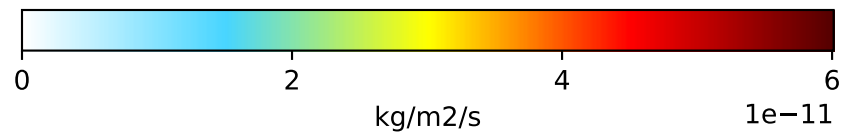
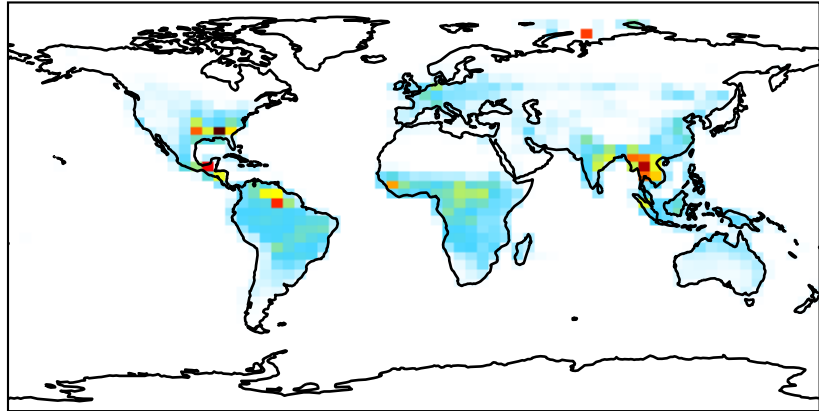


Ratio (1x1.25)  
Dev/Ref, Fixed Range

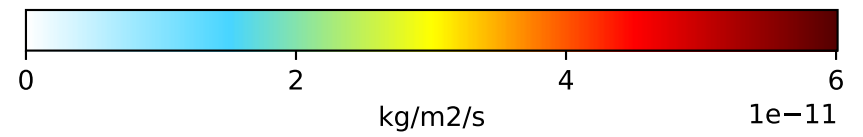
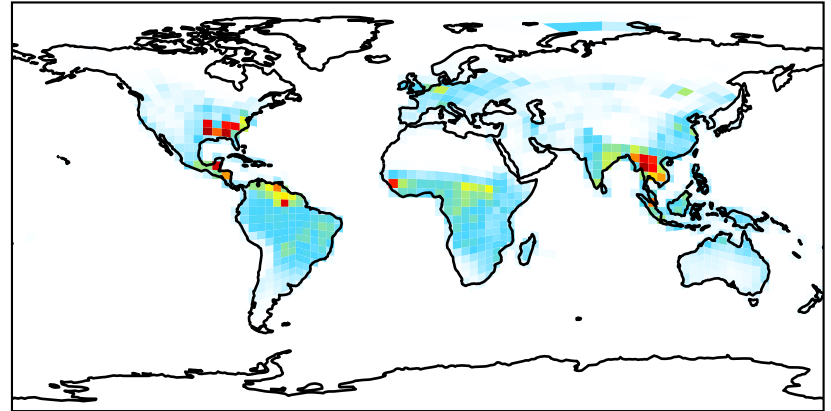


# EmisC2H4\_Total (Apr2019)

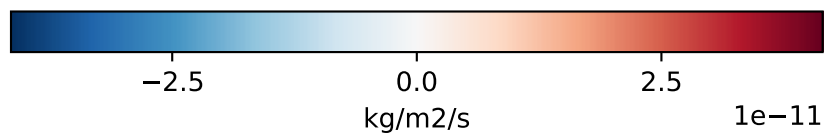
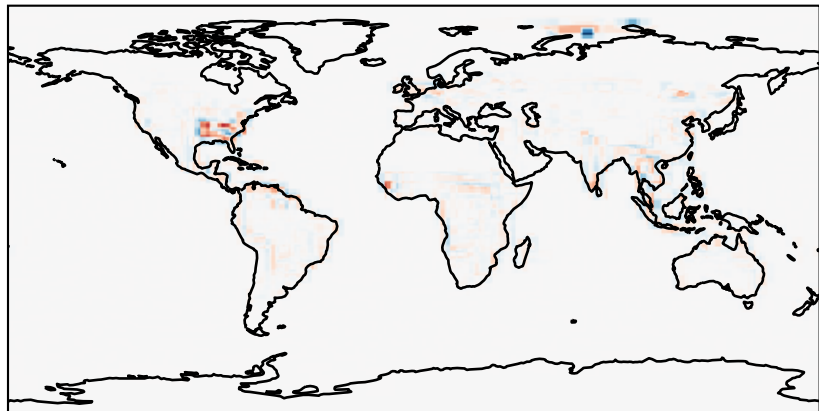
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



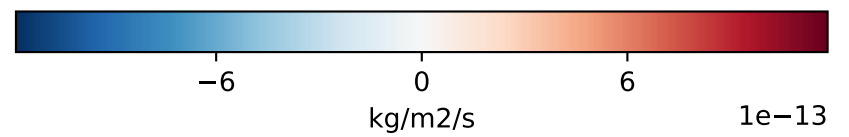
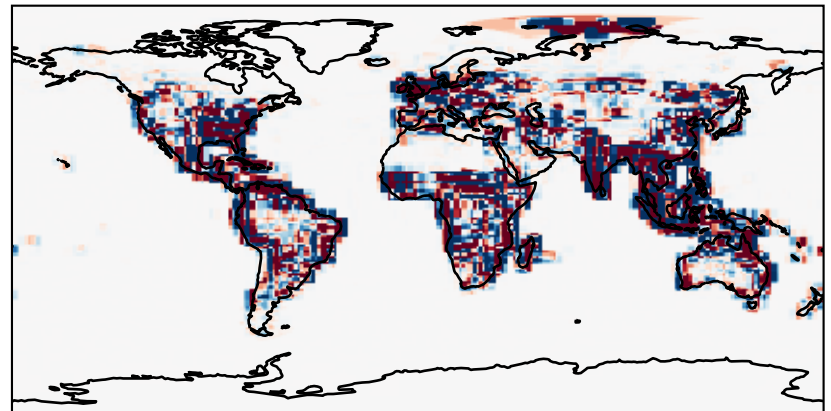
GCHP\_14.0.0-rc.3 (Dev)  
c24



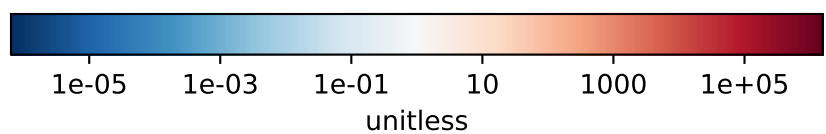
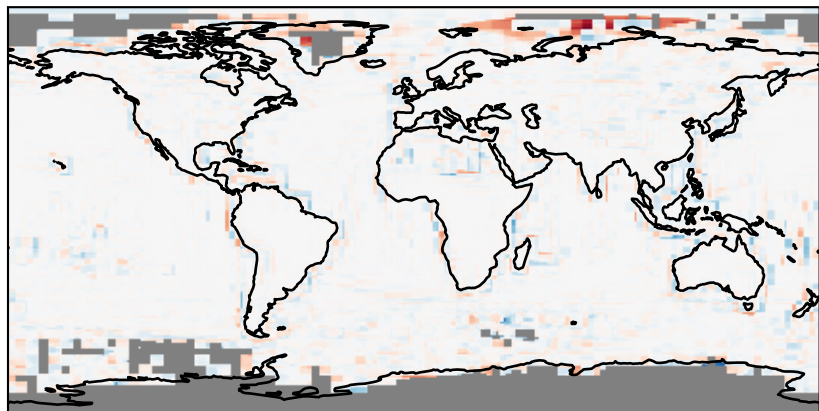
Difference (1x1.25)  
Dev - Ref, Dynamic Range



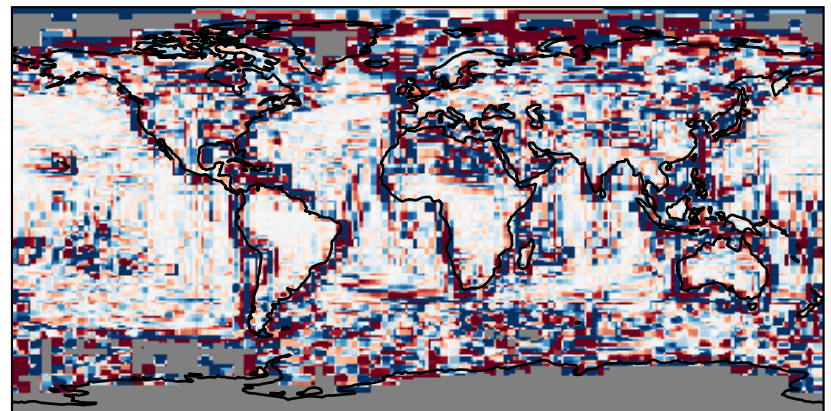
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



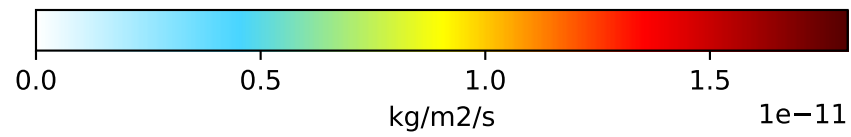
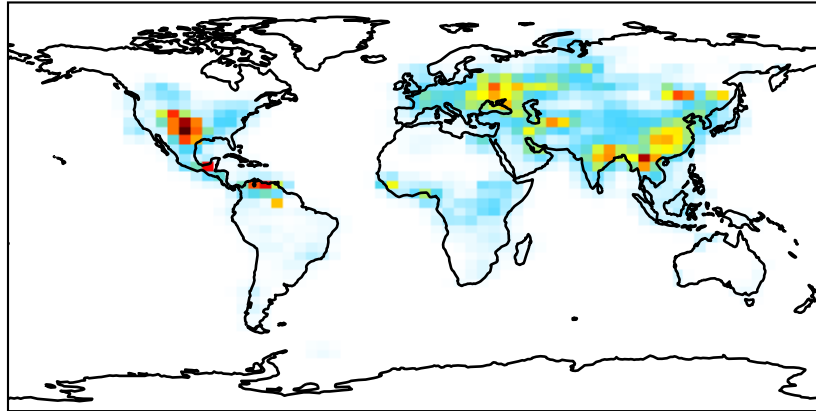
Ratio (1x1.25)  
Dev/Ref, Fixed Range



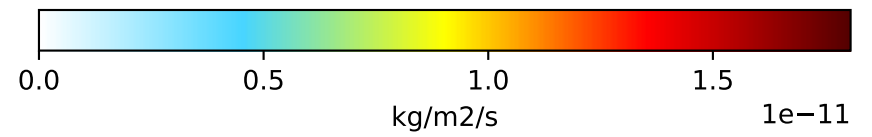
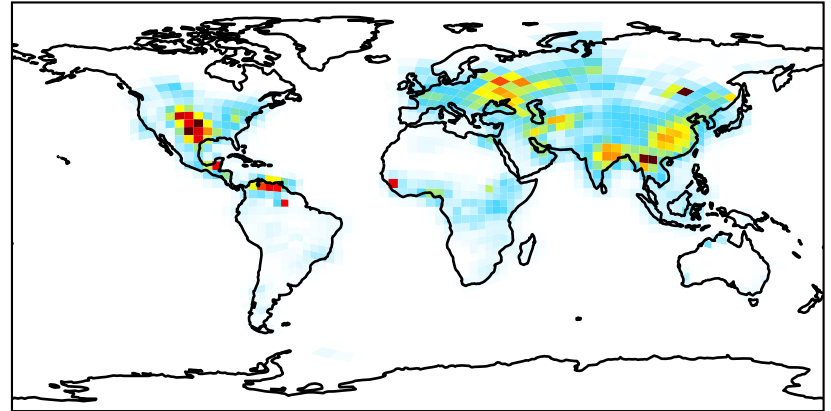


# EmisC2H6\_Total (Apr2019)

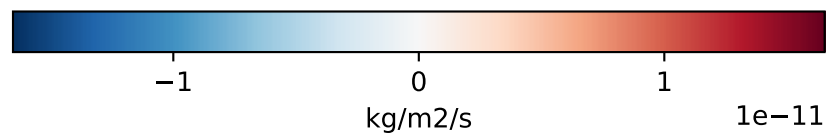
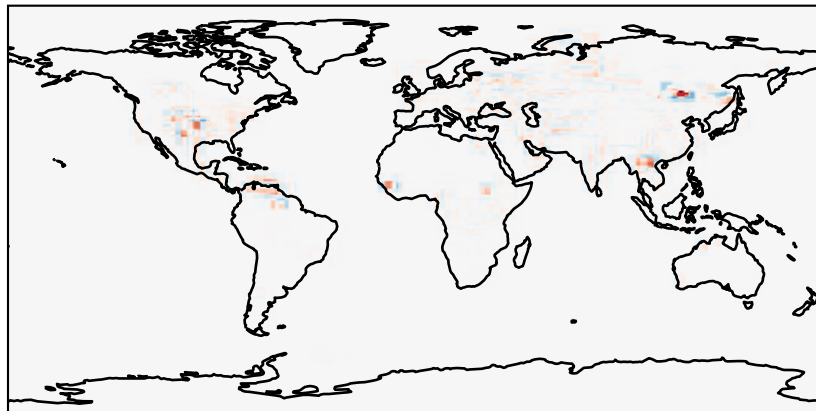
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



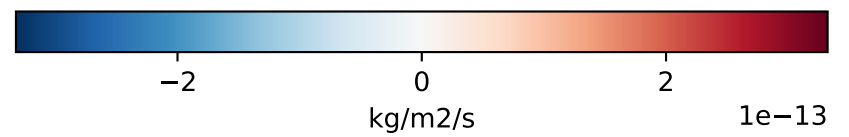
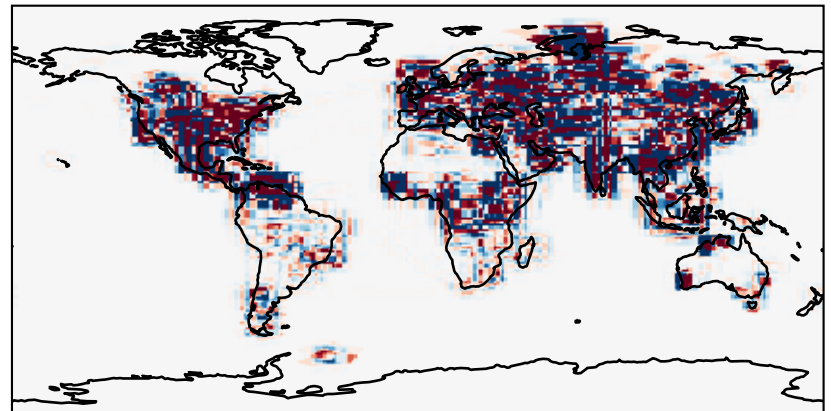
GCHP\_14.0.0-rc.3 (Dev)  
c24



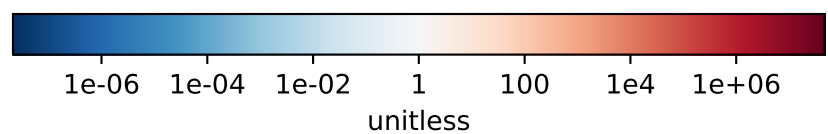
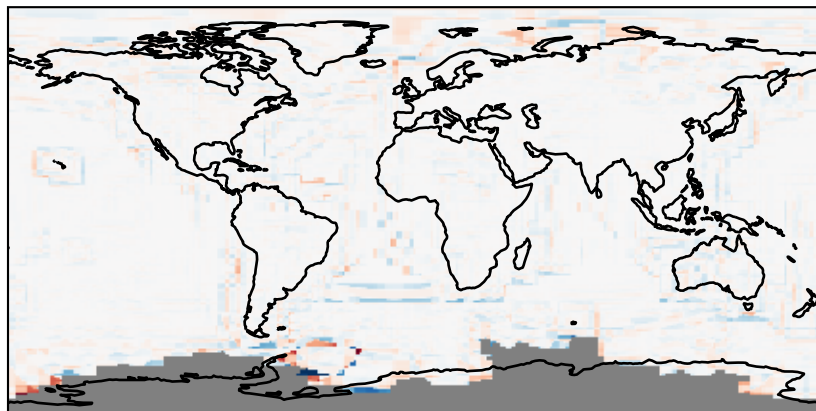
Difference (1x1.25)  
Dev - Ref, Dynamic Range



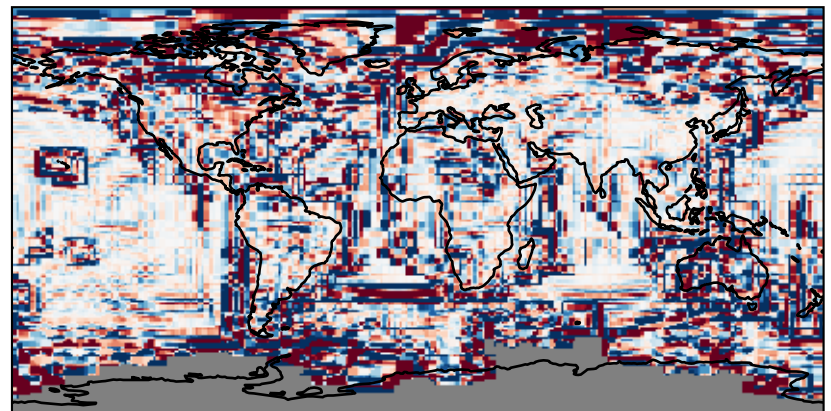
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

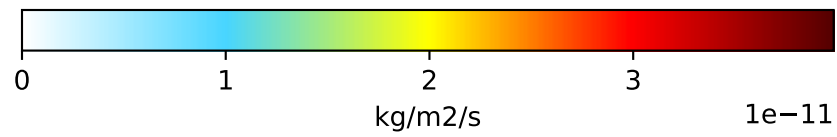
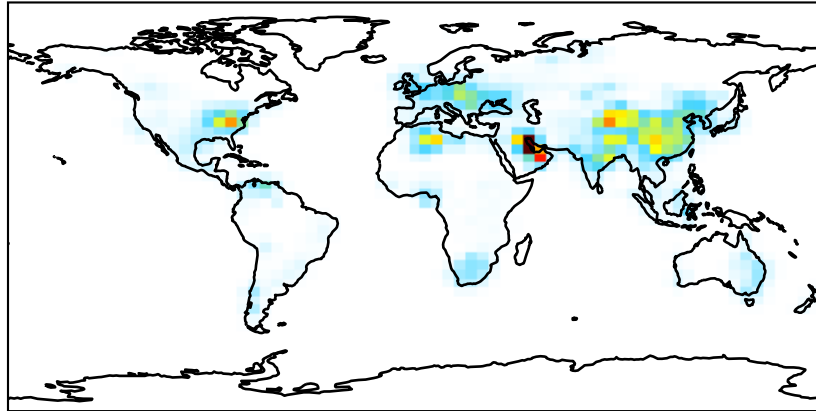


Ratio (1x1.25)  
Dev/Ref, Fixed Range

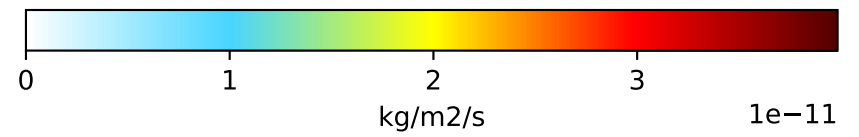
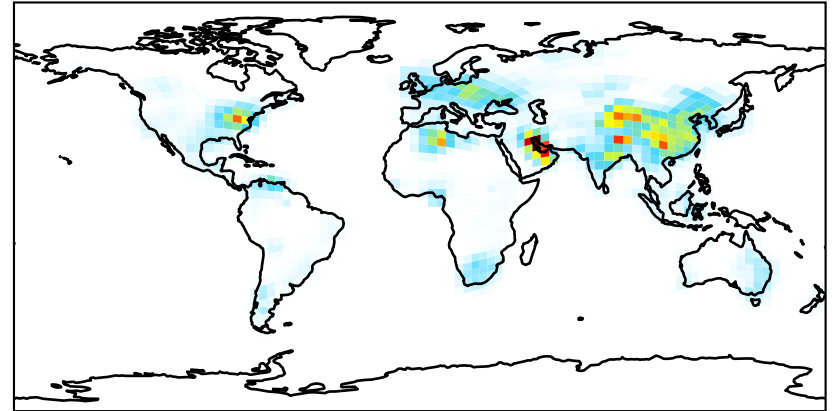


# EmisC3H8\_Total (Apr2019)

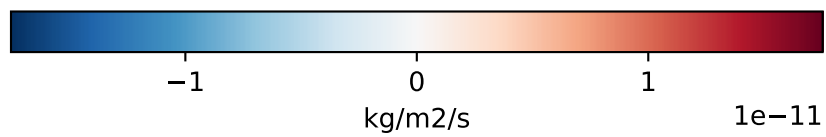
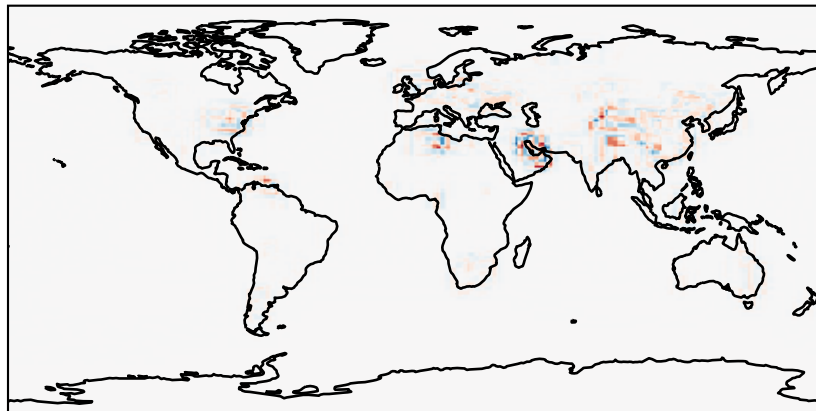
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



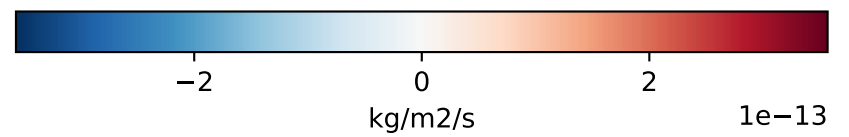
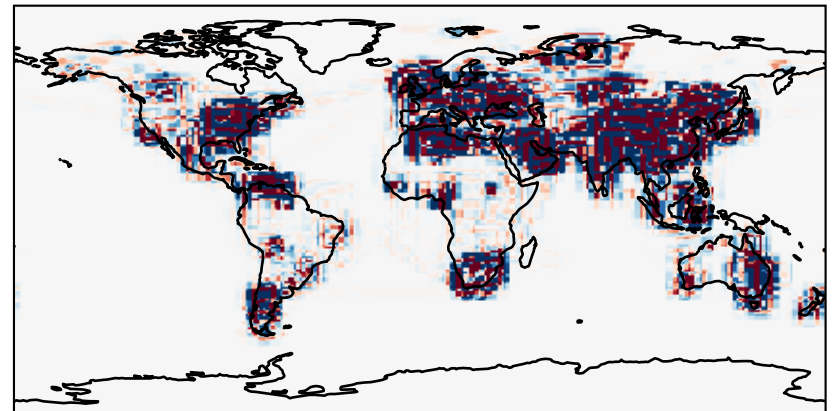
GCHP\_14.0.0-rc.3 (Dev)  
c24



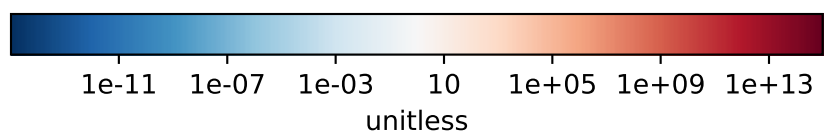
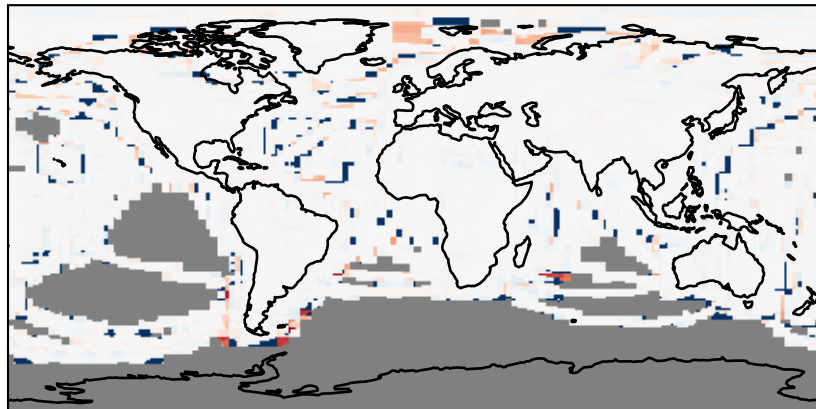
Difference (1x1.25)  
Dev - Ref, Dynamic Range



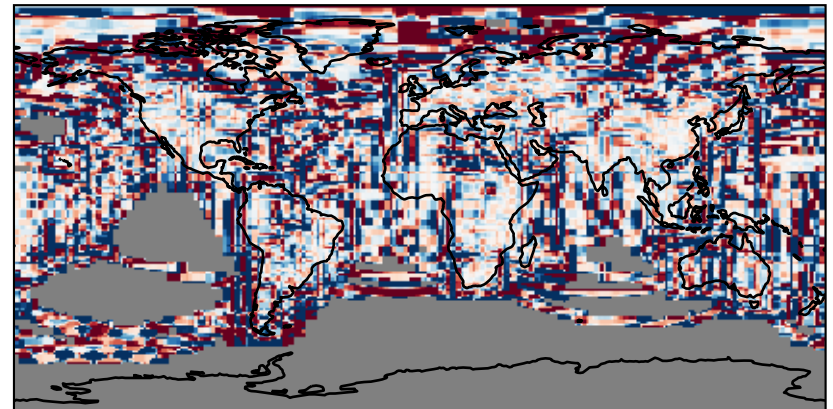
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

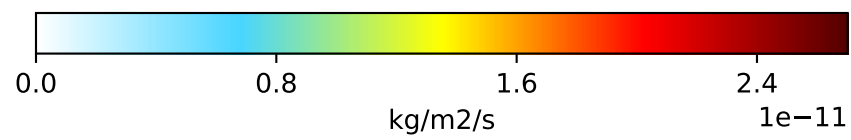
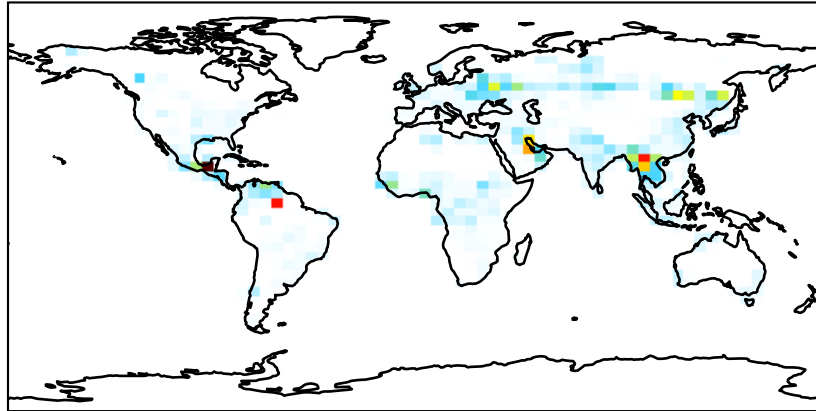


Ratio (1x1.25)  
Dev/Ref, Fixed Range

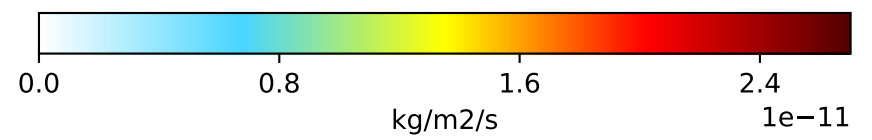
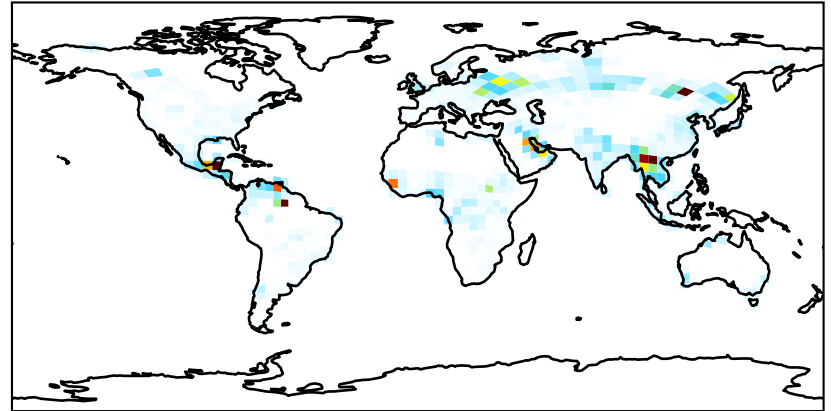


# EmisCH2O\_Total (Apr2019)

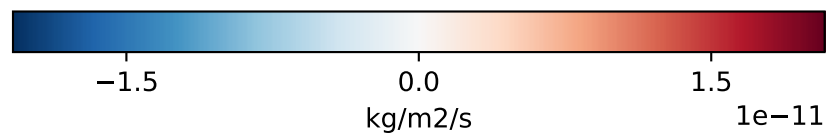
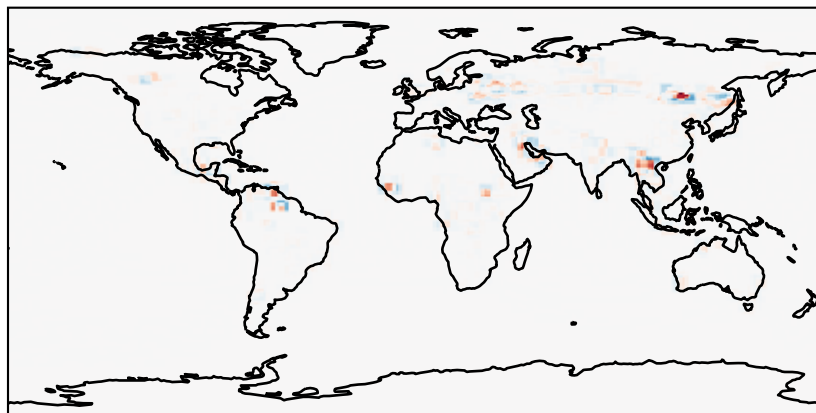
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



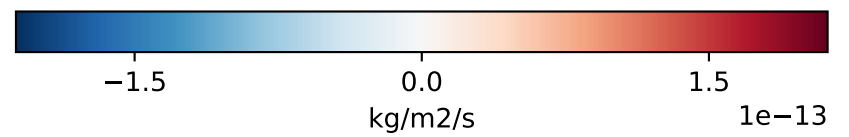
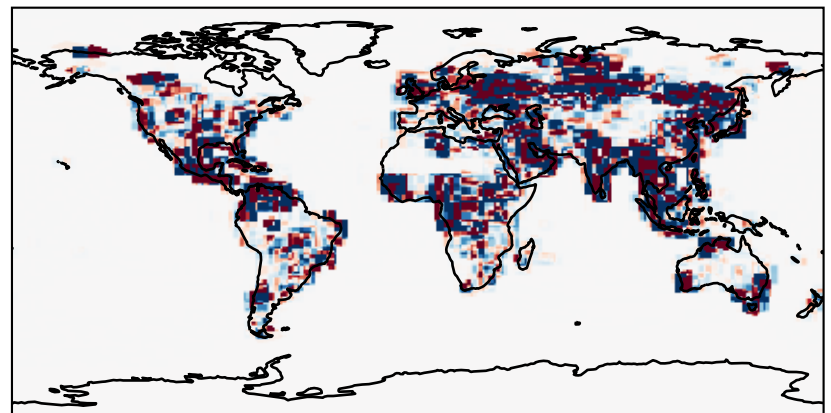
GCHP\_14.0.0-rc.3 (Dev)  
c24



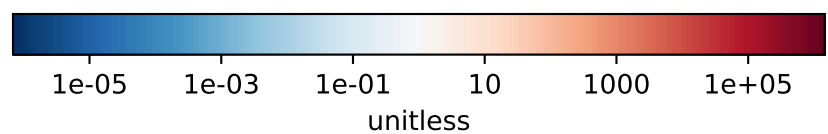
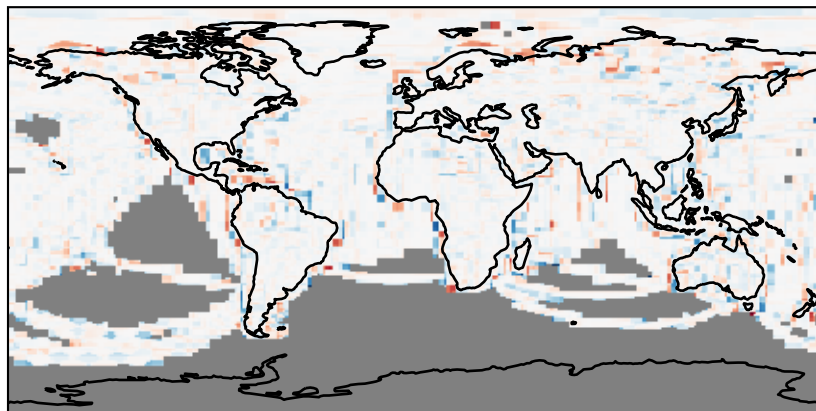
Difference (1x1.25)  
Dev - Ref, Dynamic Range



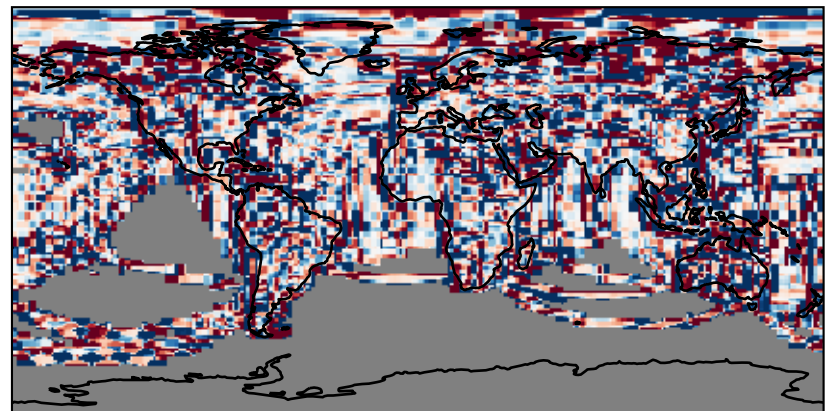
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

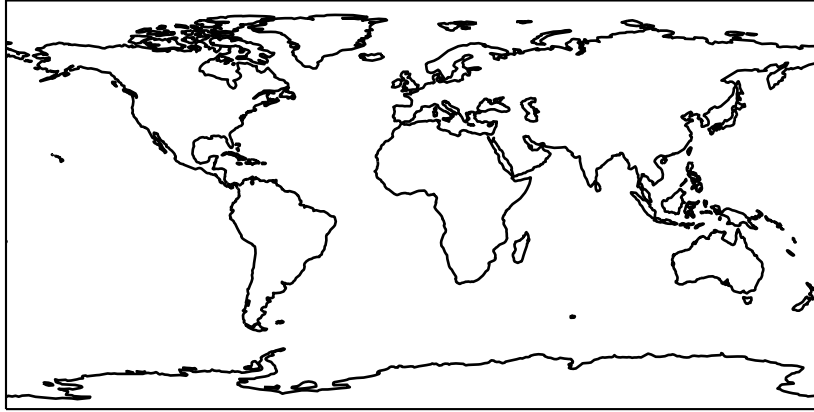


Ratio (1x1.25)  
Dev/Ref, Fixed Range



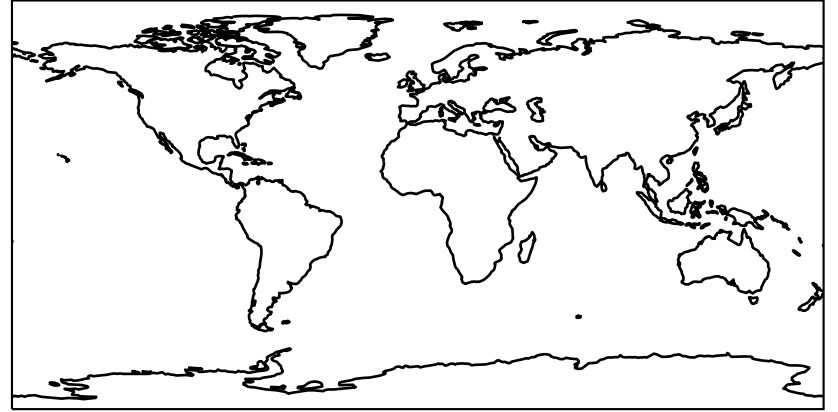
# EmisCO2\_Total (Apr2019)

GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



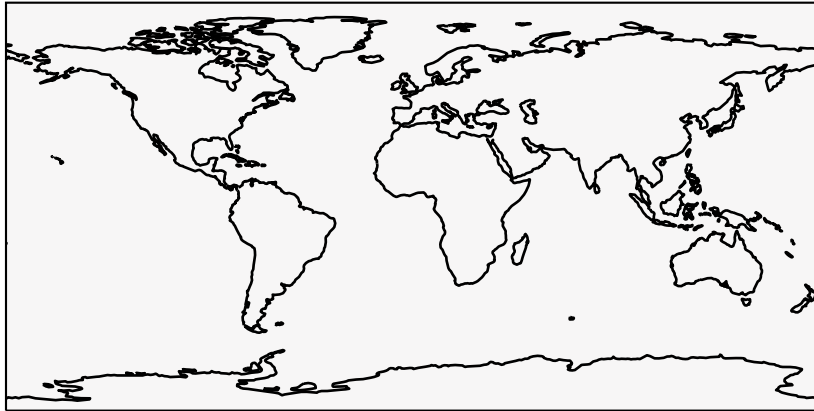
Zero throughout domain  
kg/m2/s

GCHP\_14.0.0-rc.3 (Dev)  
c24



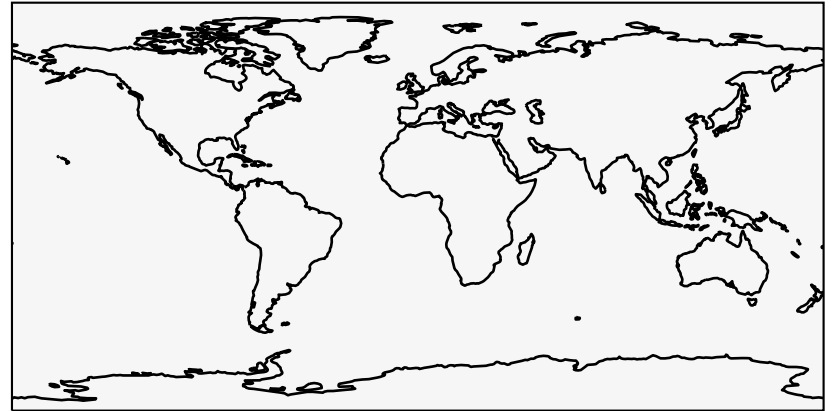
Zero throughout domain  
kg/m2/s

Difference (1x1.25)  
Dev - Ref, Dynamic Range



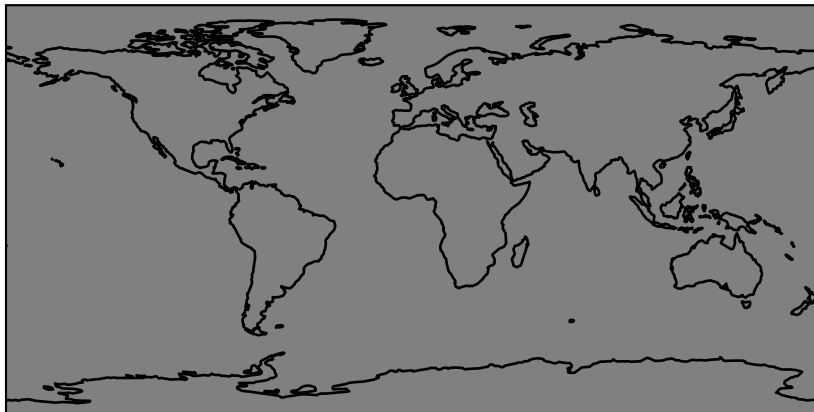
Zero throughout domain  
kg/m2/s

Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



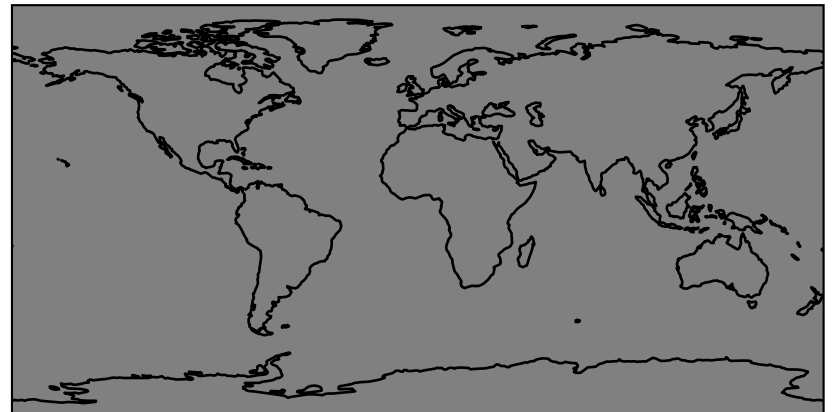
Zero throughout domain  
kg/m2/s

Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

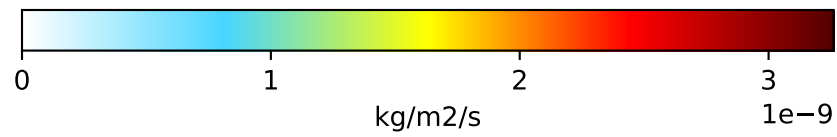
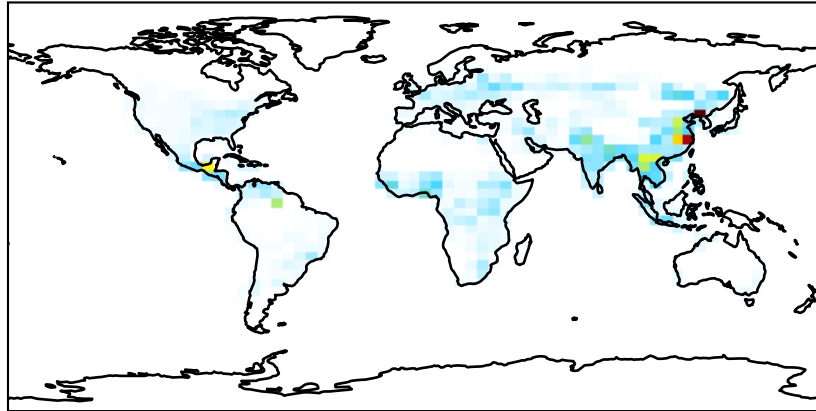
Ratio (1x1.25)  
Dev/Ref, Fixed Range



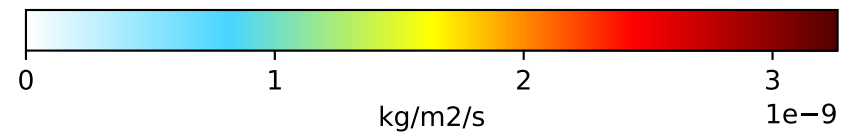
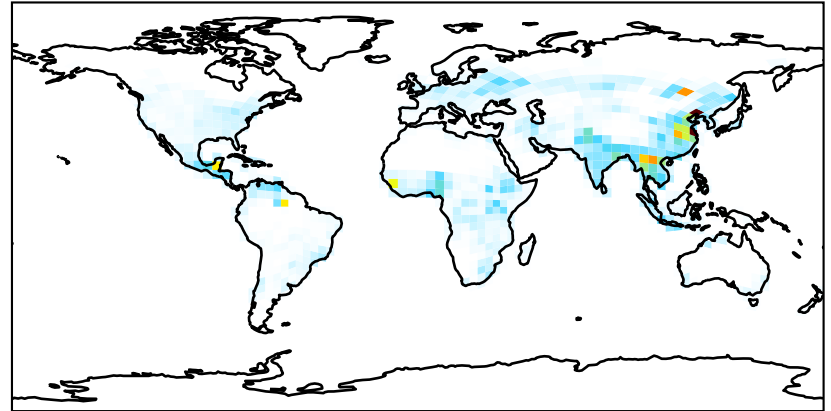
Undefined throughout domain  
unitless

# EmisCO\_Total (Apr2019)

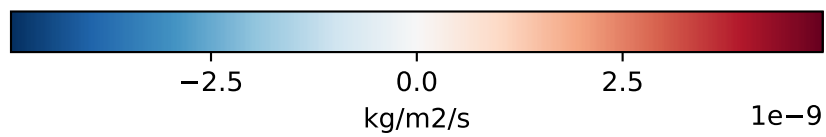
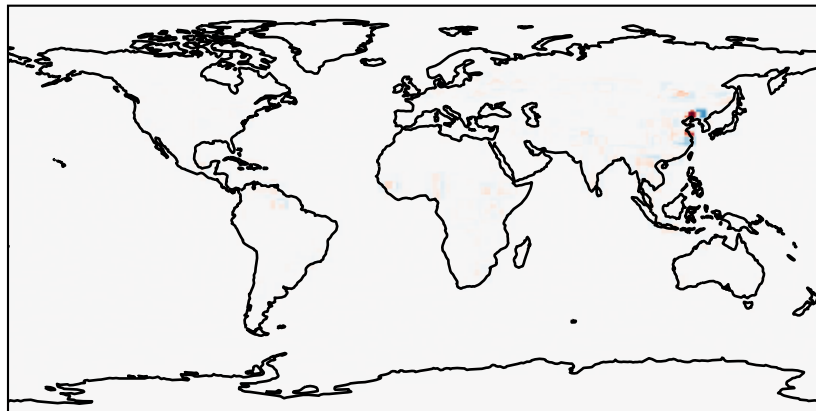
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



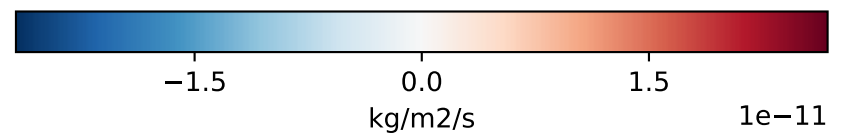
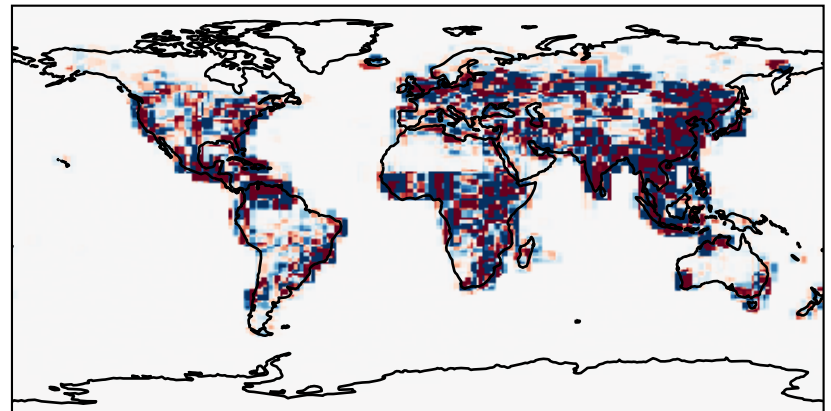
GCHP\_14.0.0-rc.3 (Dev)  
c24



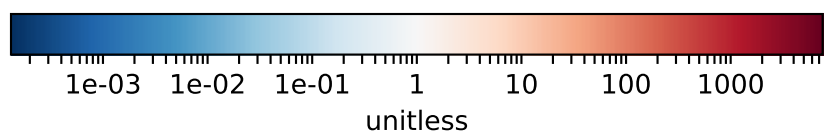
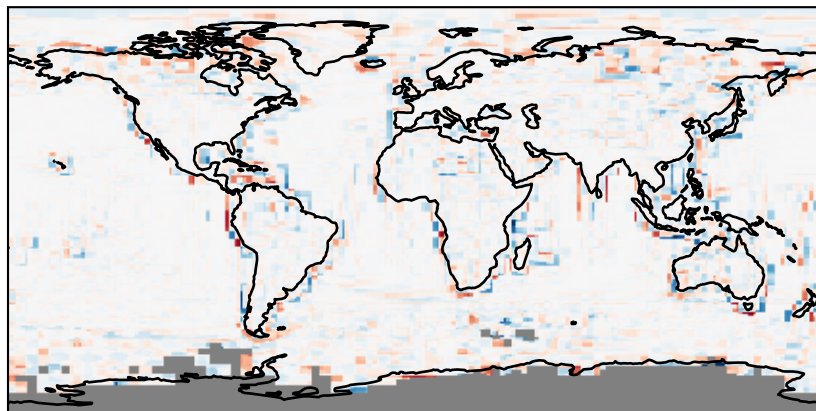
Difference (1x1.25)  
Dev - Ref, Dynamic Range



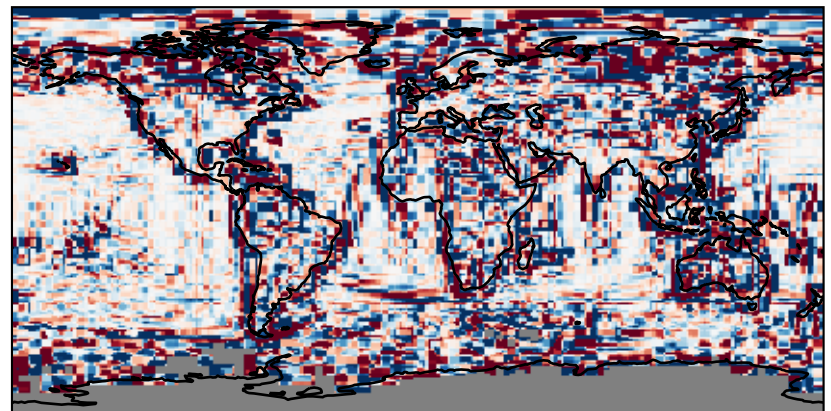
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

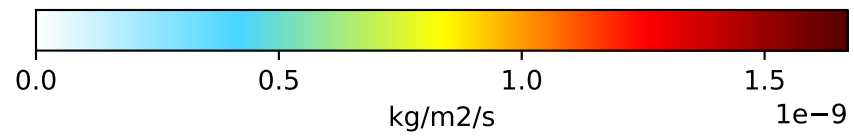
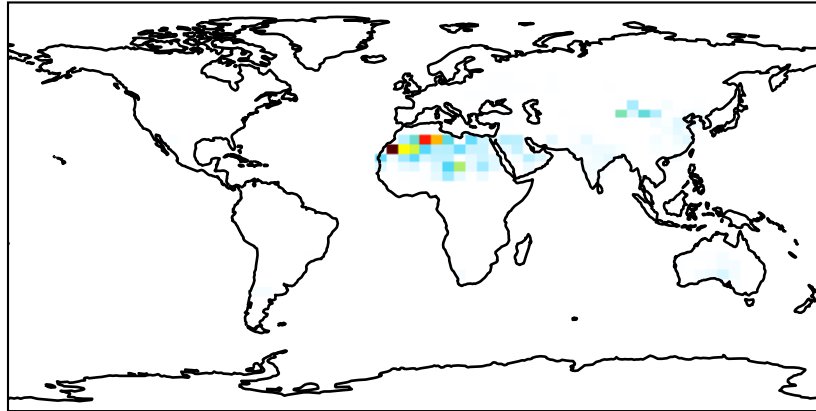


Ratio (1x1.25)  
Dev/Ref, Fixed Range

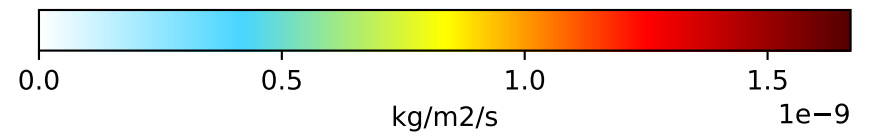
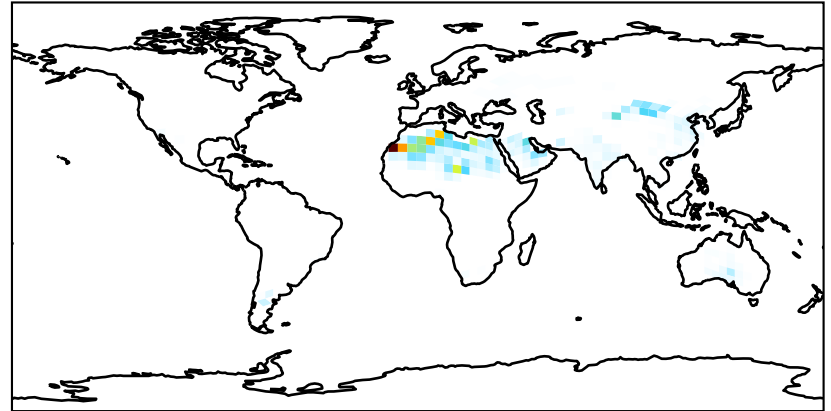


# EmisDST1\_Total (Apr2019)

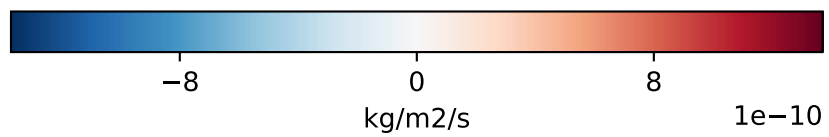
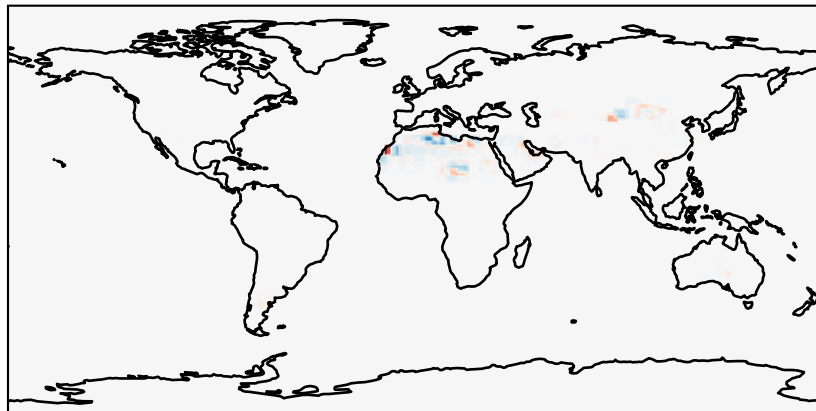
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



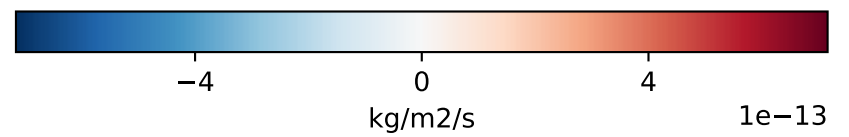
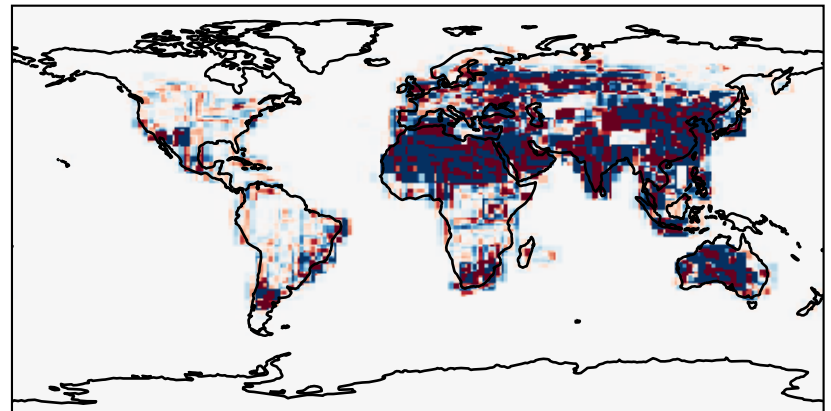
GCHP\_14.0.0-rc.3 (Dev)  
c24



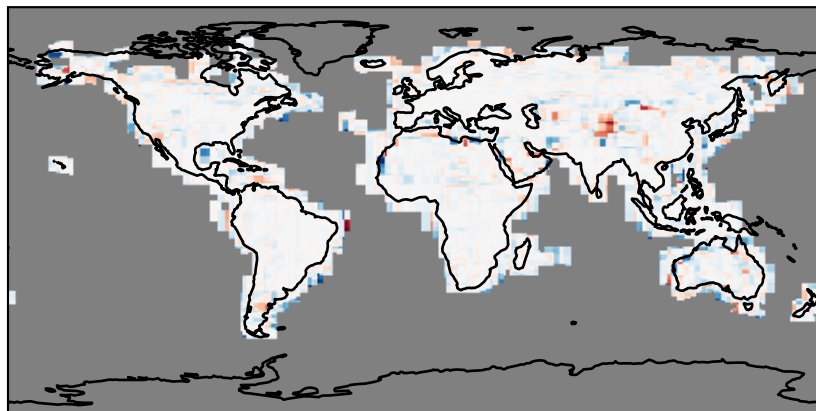
Difference (1x1.25)  
Dev - Ref, Dynamic Range



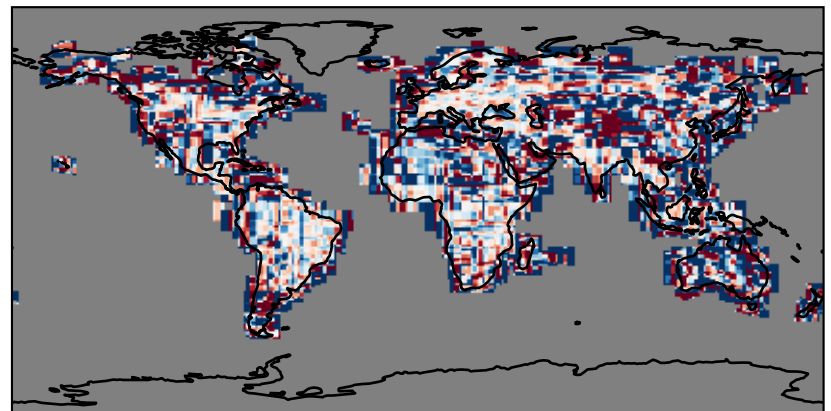
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

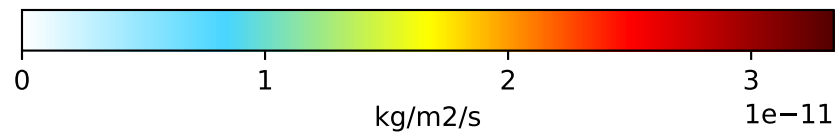
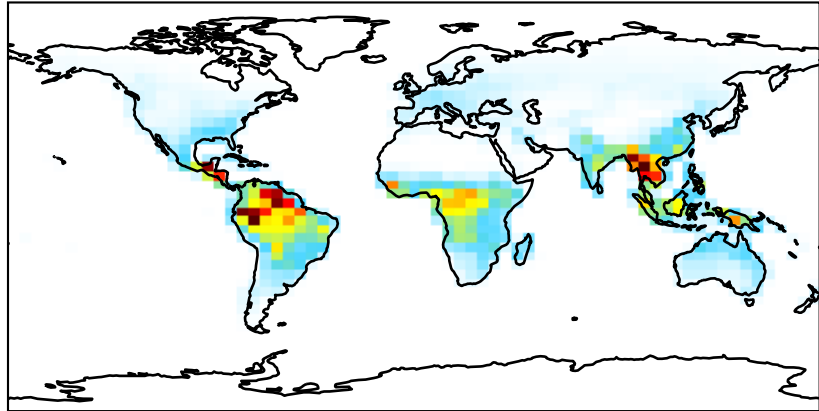


Ratio (1x1.25)  
Dev/Ref, Fixed Range

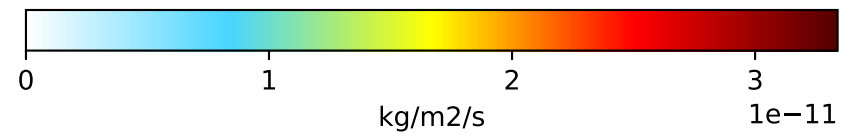
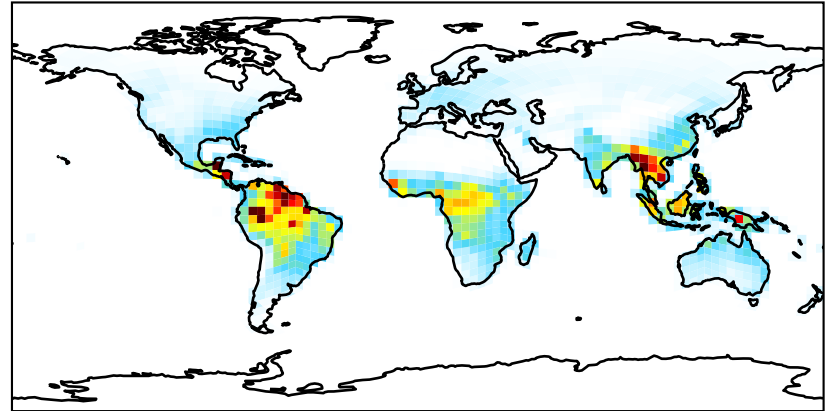


# EmisEOH\_Total (Apr2019)

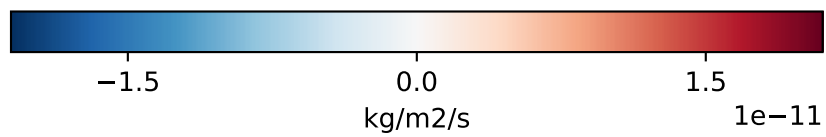
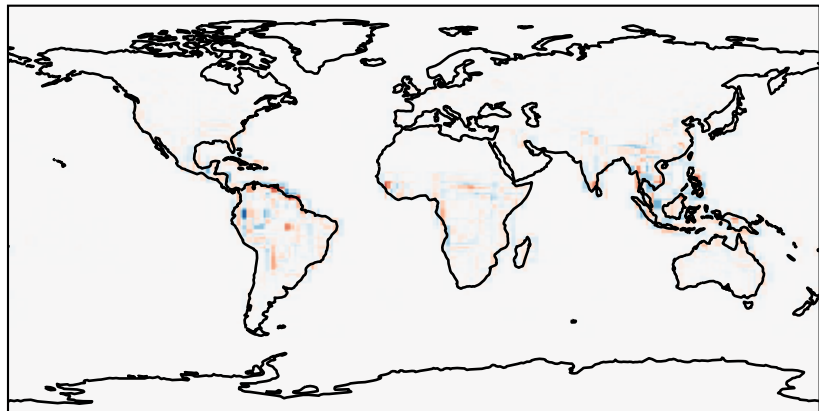
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



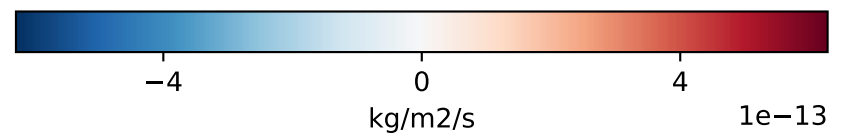
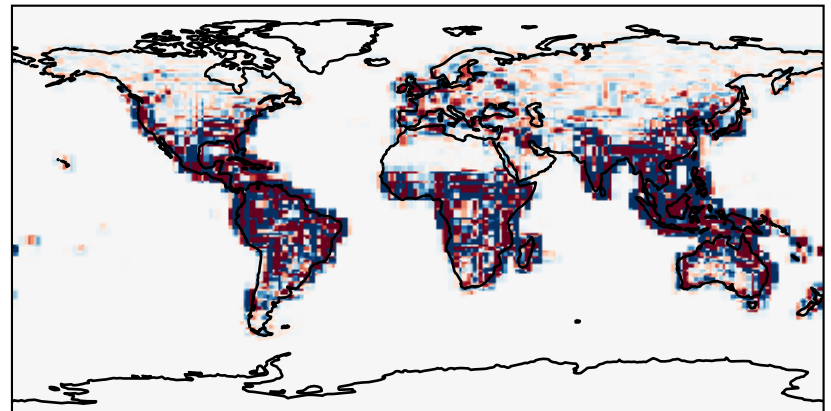
GCHP\_14.0.0-rc.3 (Dev)  
c24



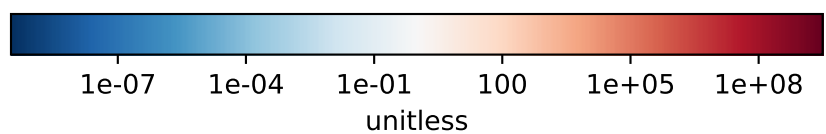
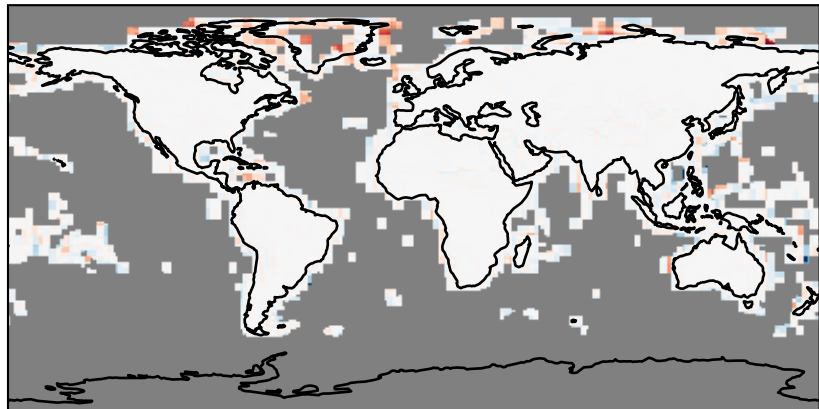
Difference (1x1.25)  
Dev - Ref, Dynamic Range



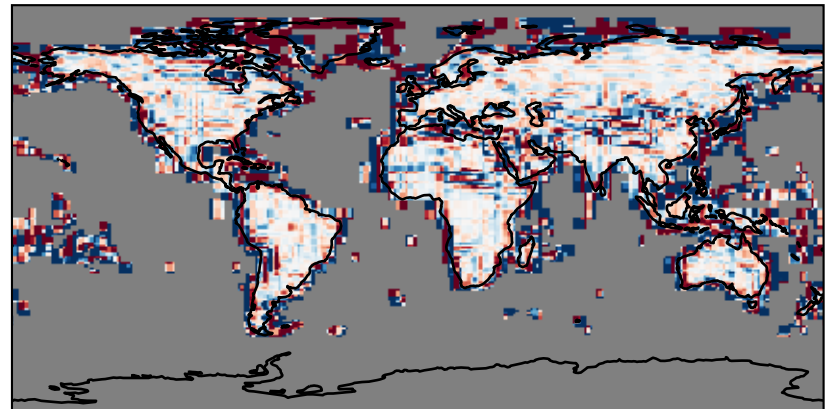
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

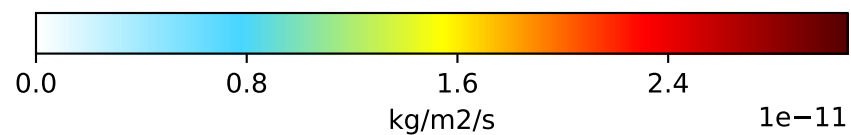
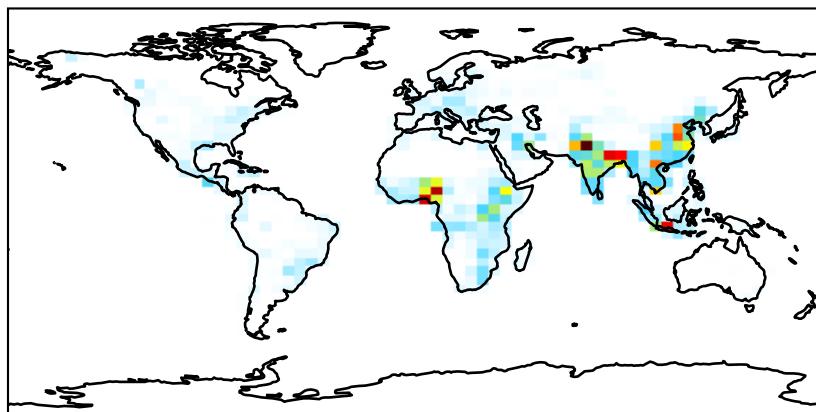


Ratio (1x1.25)  
Dev/Ref, Fixed Range

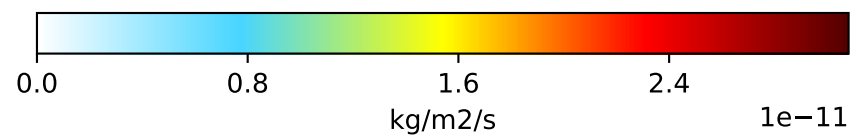
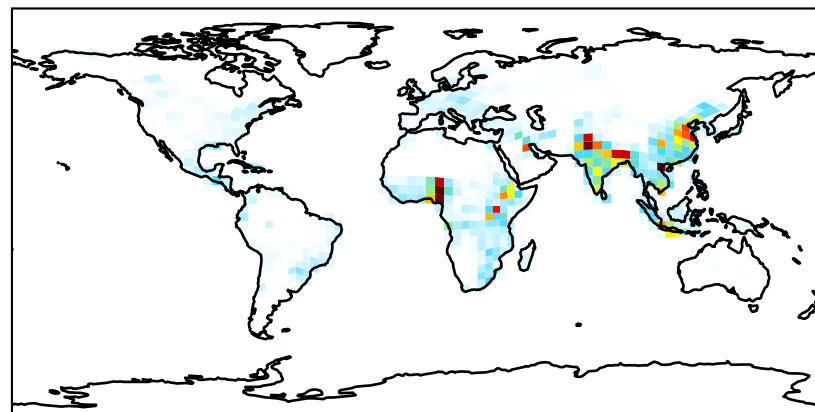


# EmisHCOOH\_Total (Apr2019)

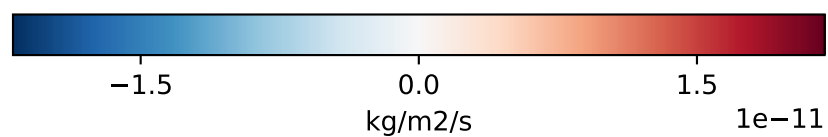
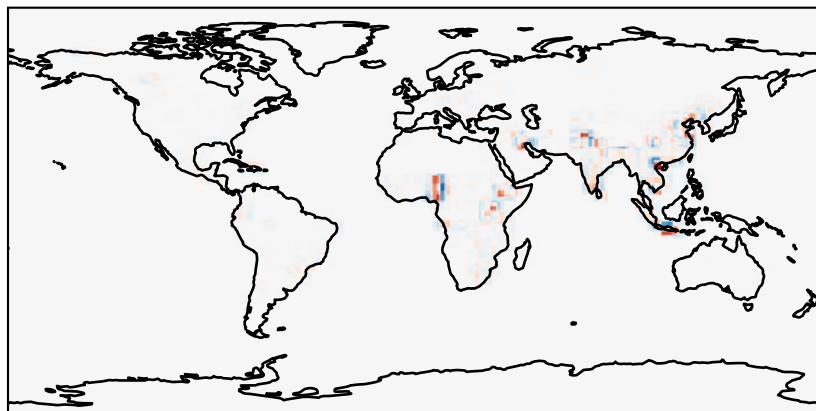
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



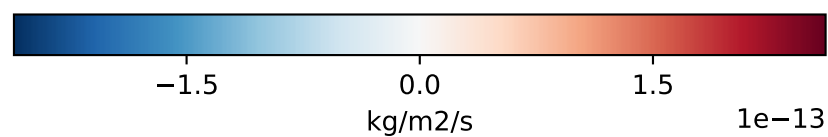
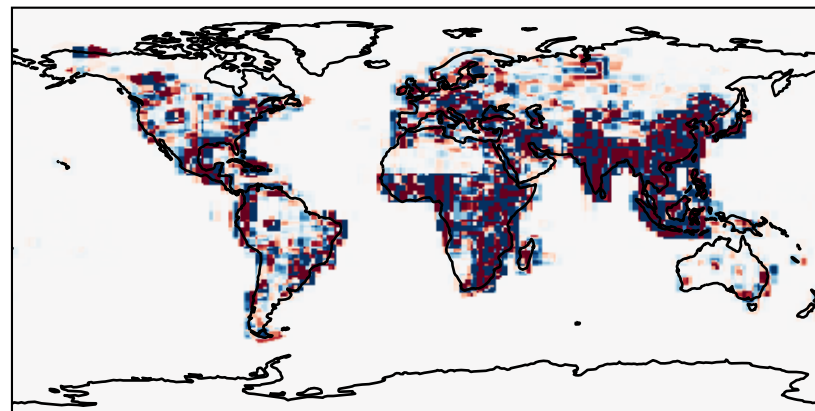
GCHP\_14.0.0-rc.3 (Dev)  
c24



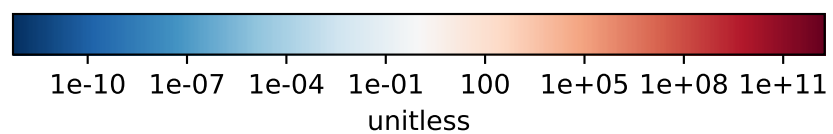
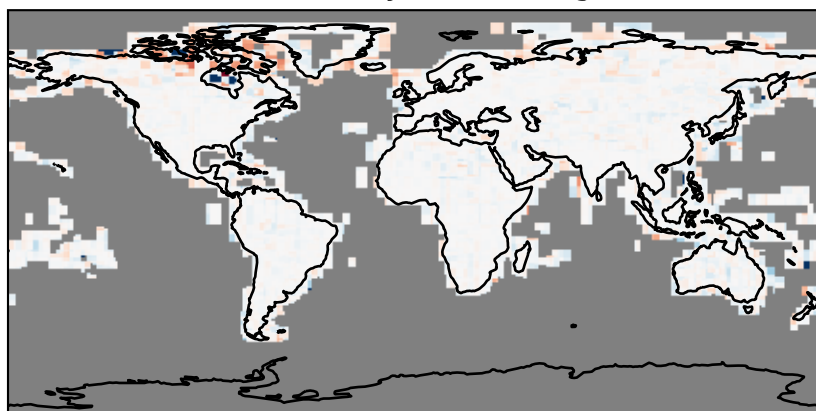
Difference (1x1.25)  
Dev - Ref, Dynamic Range



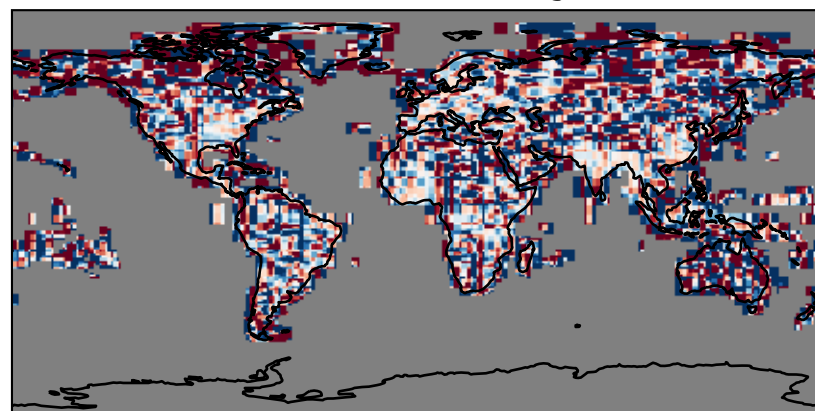
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



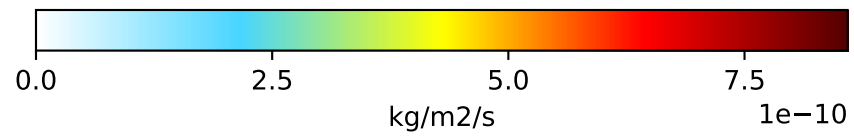
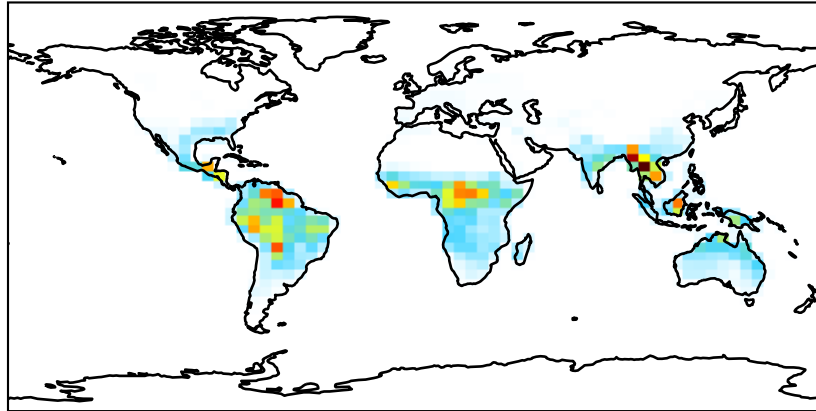
Ratio (1x1.25)  
Dev/Ref, Fixed Range



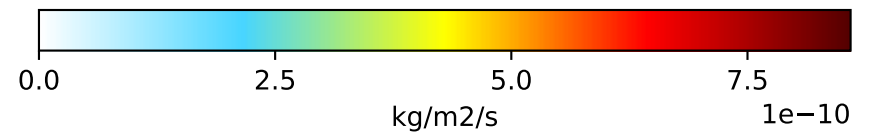
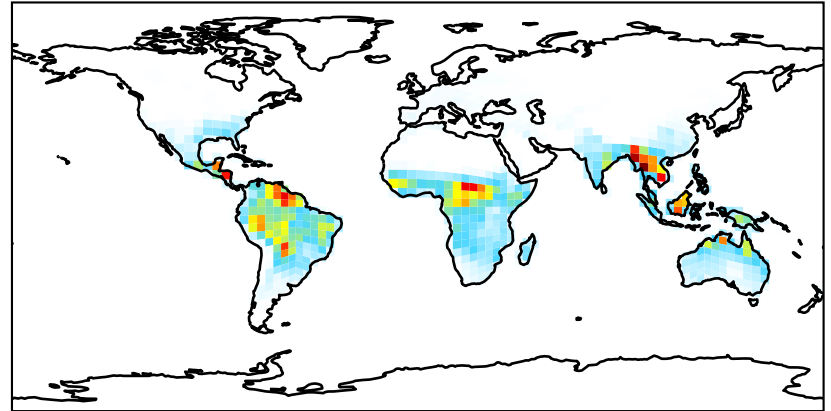


# EmisISOP\_Total (Apr2019)

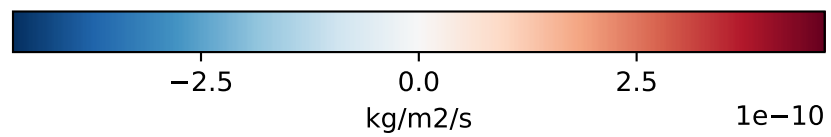
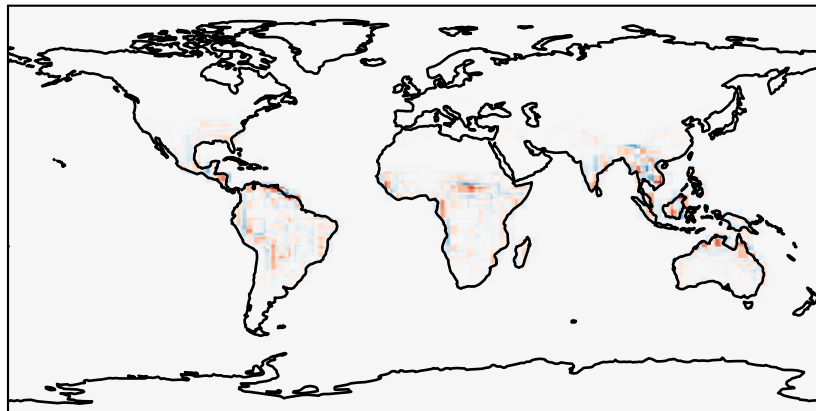
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



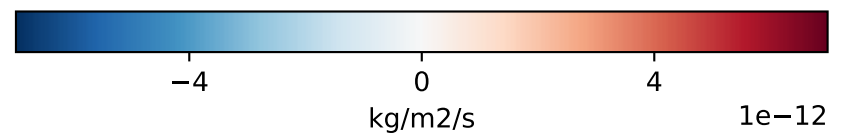
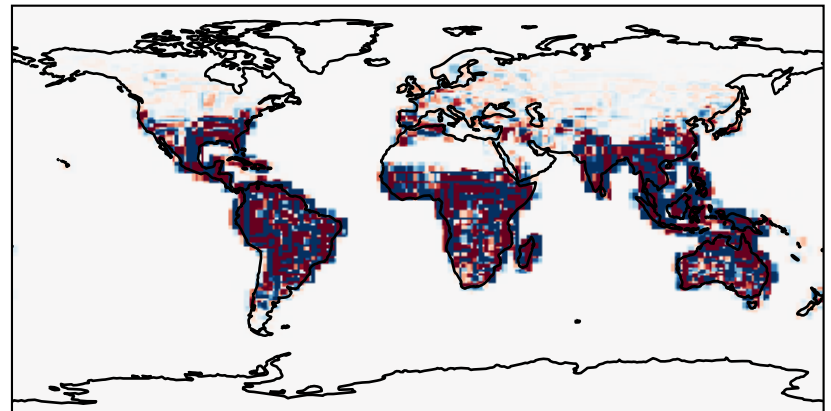
GCHP\_14.0.0-rc.3 (Dev)  
c24



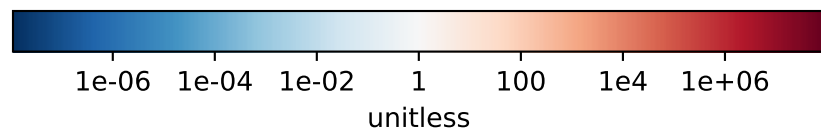
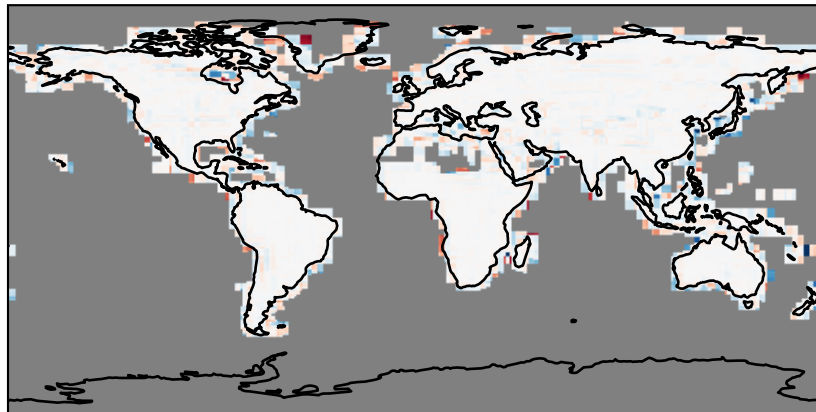
Difference (1x1.25)  
Dev - Ref, Dynamic Range



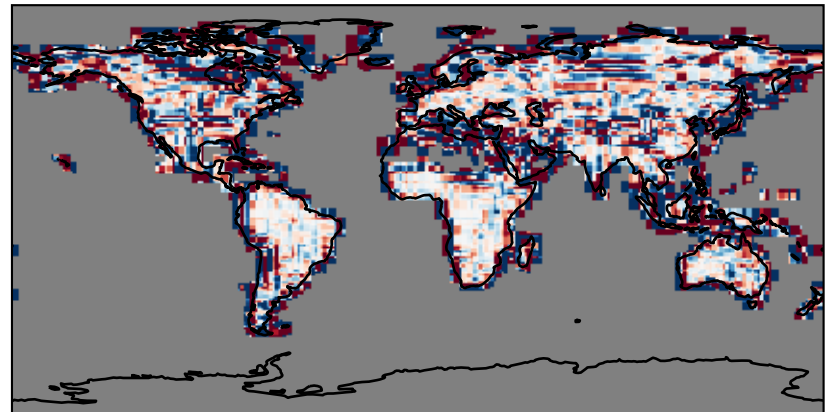
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

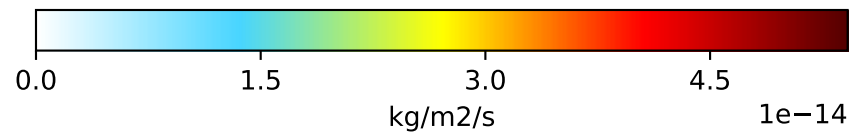
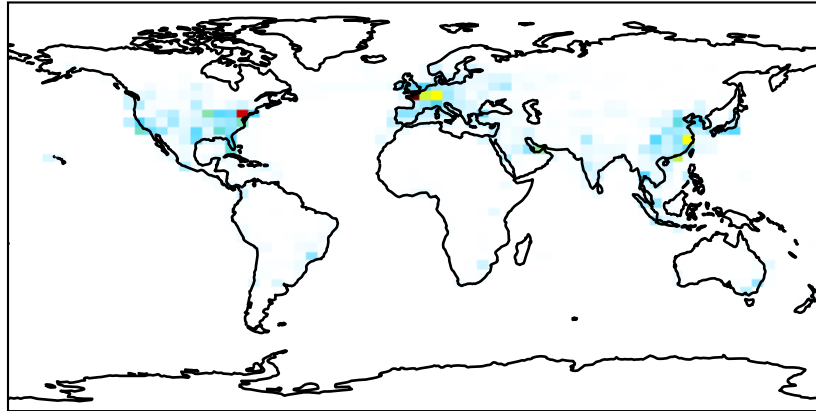


Ratio (1x1.25)  
Dev/Ref, Fixed Range

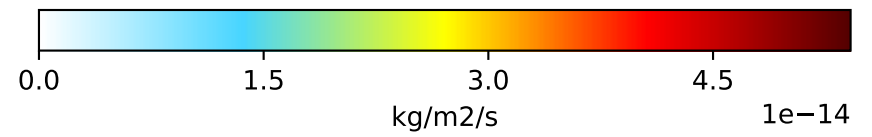
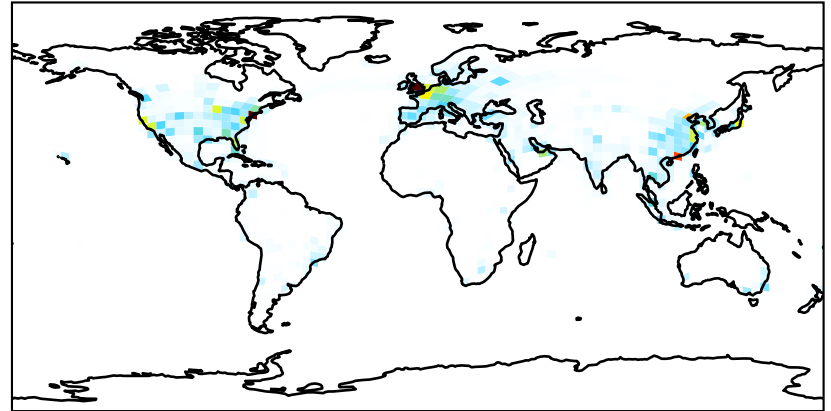


# EmisMACR\_Total (Apr2019)

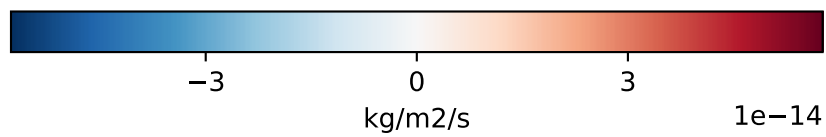
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



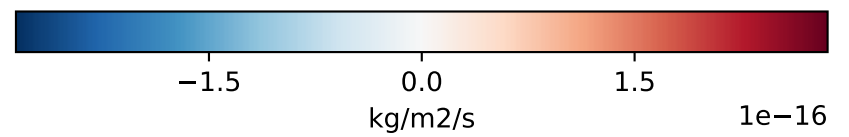
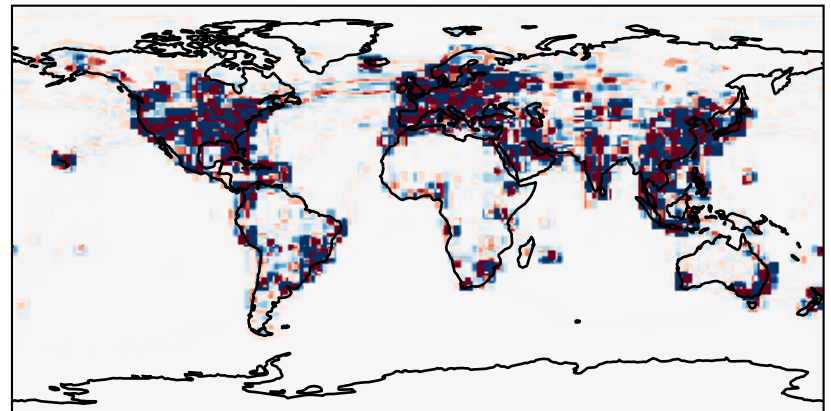
GCHP\_14.0.0-rc.3 (Dev)  
c24



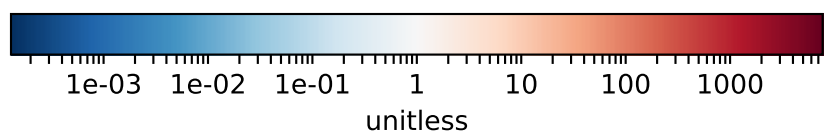
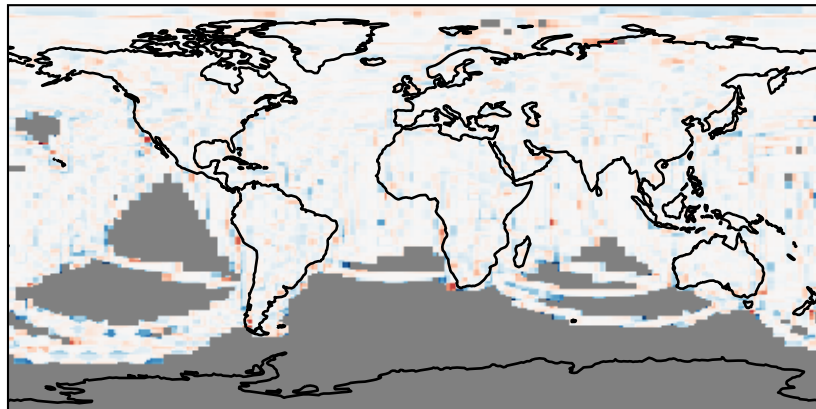
Difference (1x1.25)  
Dev - Ref, Dynamic Range



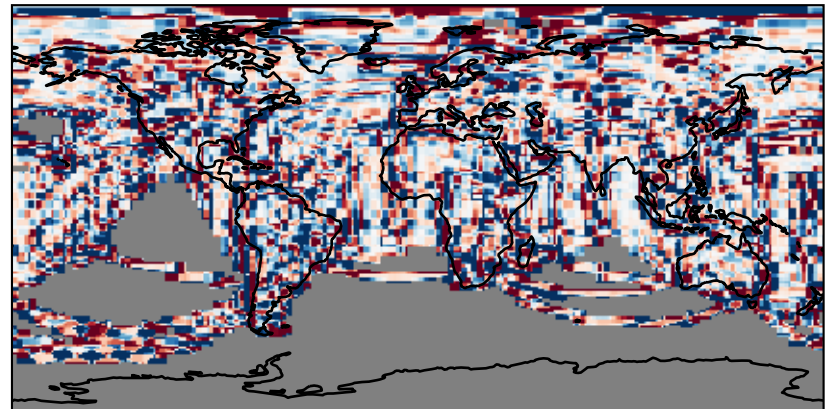
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

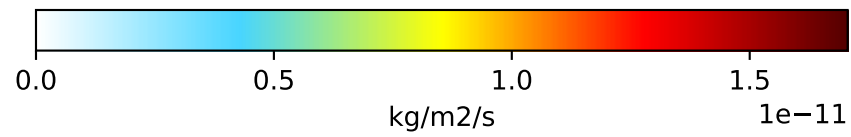
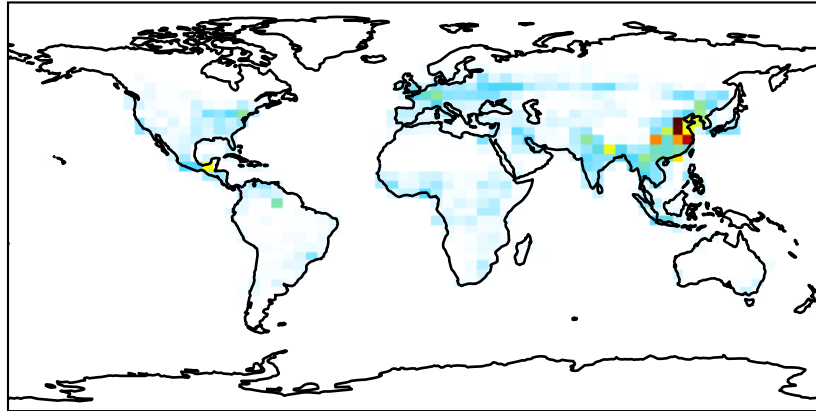


Ratio (1x1.25)  
Dev/Ref, Fixed Range

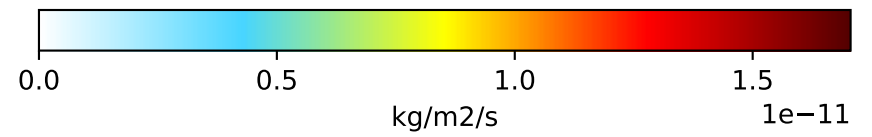
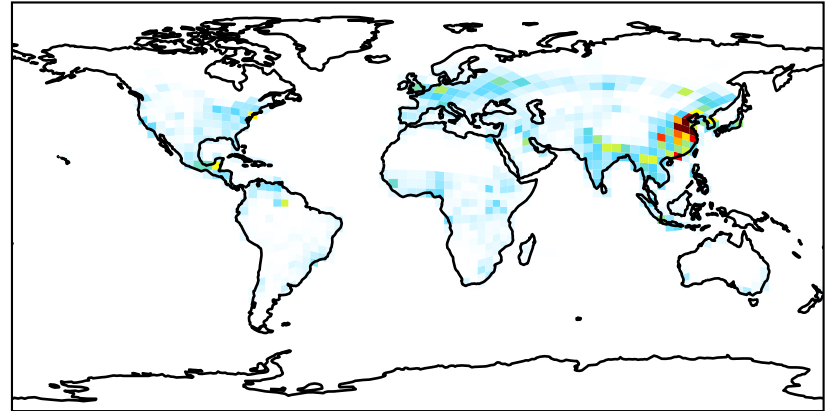


# EmisMEK\_Total (Apr2019)

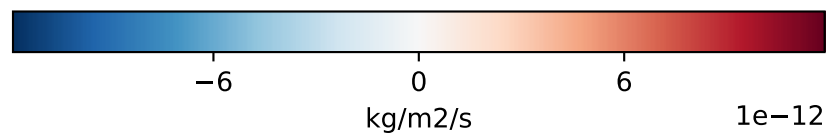
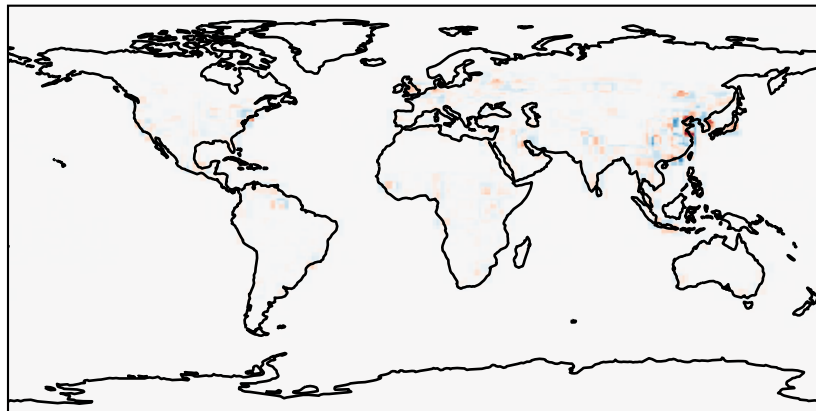
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



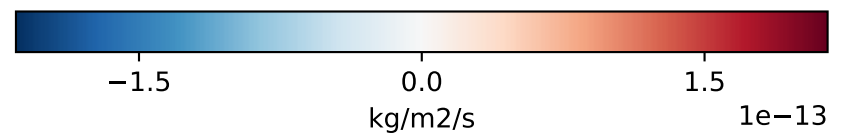
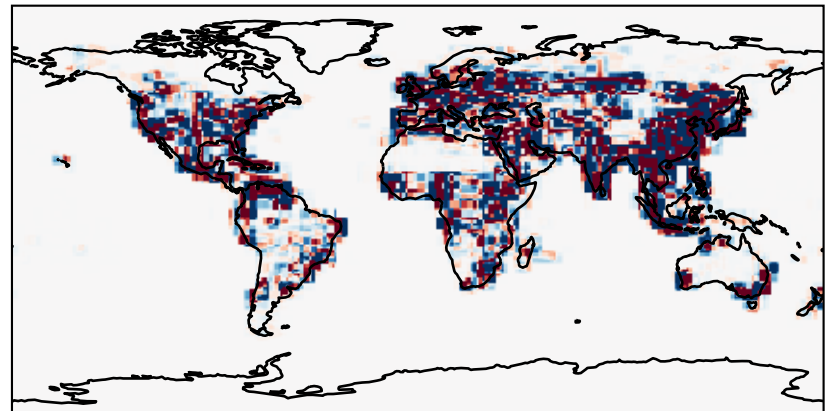
GCHP\_14.0.0-rc.3 (Dev)  
c24



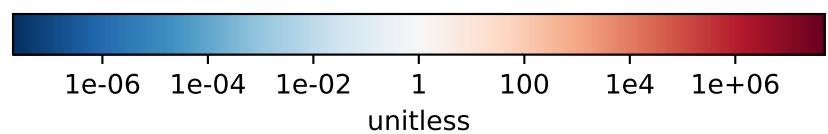
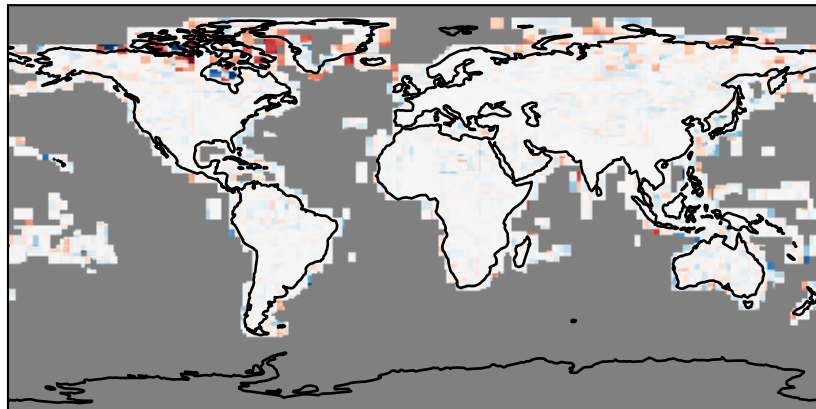
Difference (1x1.25)  
Dev - Ref, Dynamic Range



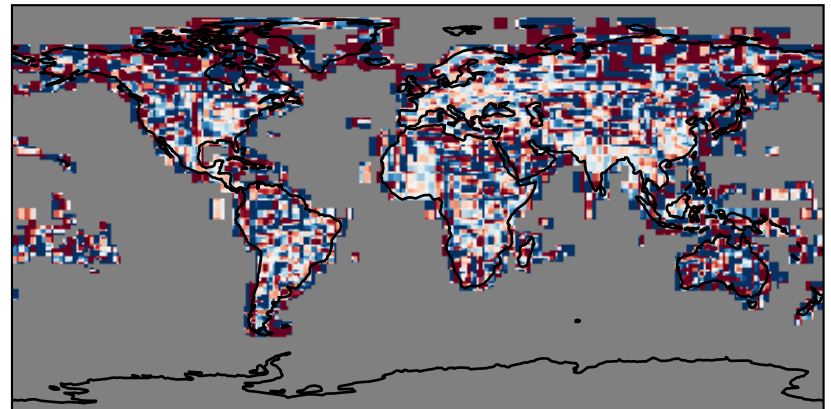
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

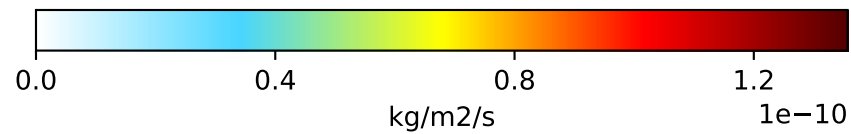
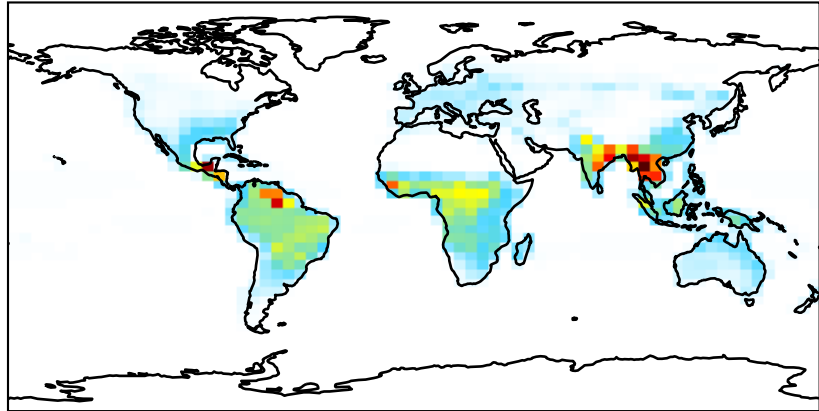


Ratio (1x1.25)  
Dev/Ref, Fixed Range

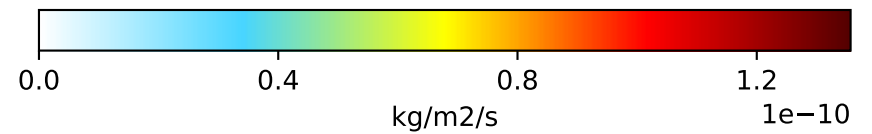
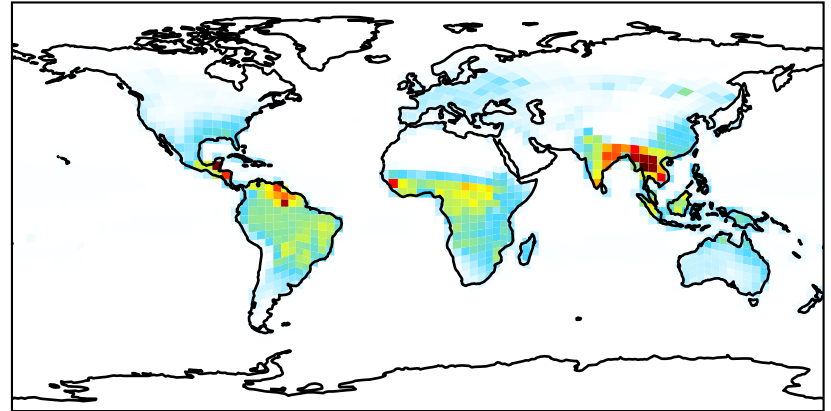


# EmisMOH\_Total (Apr2019)

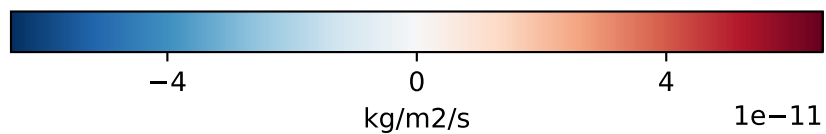
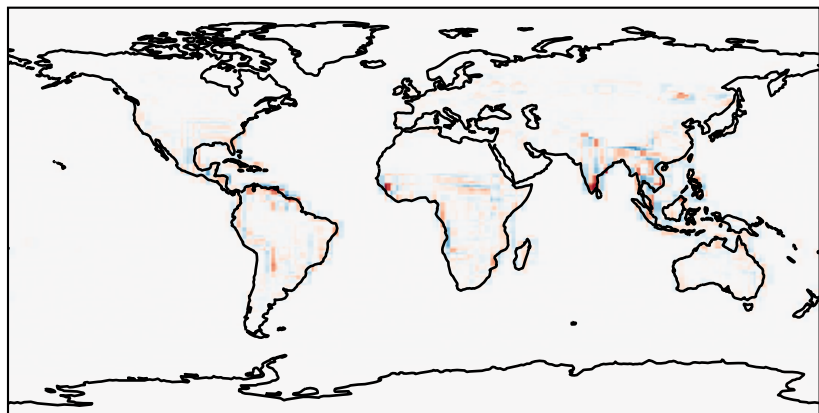
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



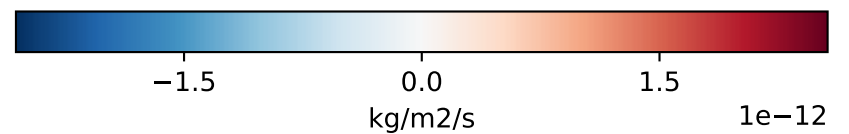
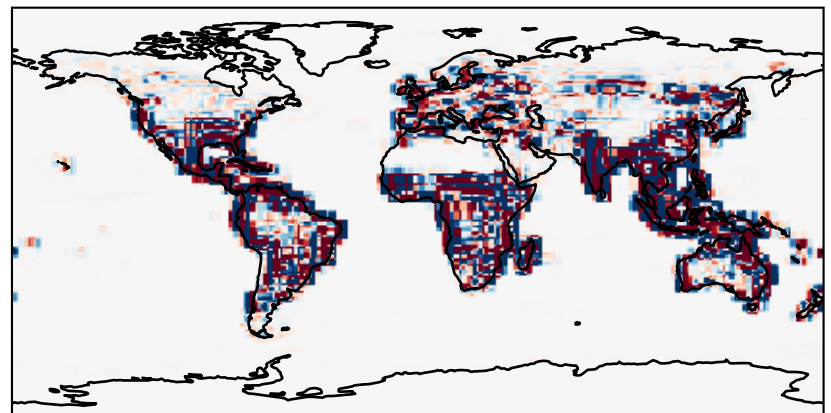
GCHP\_14.0.0-rc.3 (Dev)  
c24



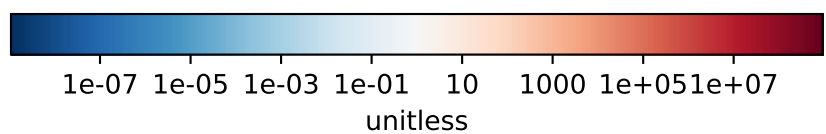
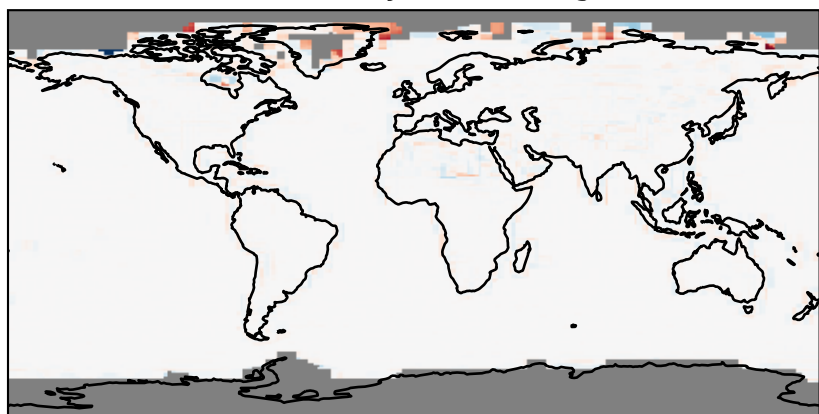
Difference (1x1.25)  
Dev - Ref, Dynamic Range



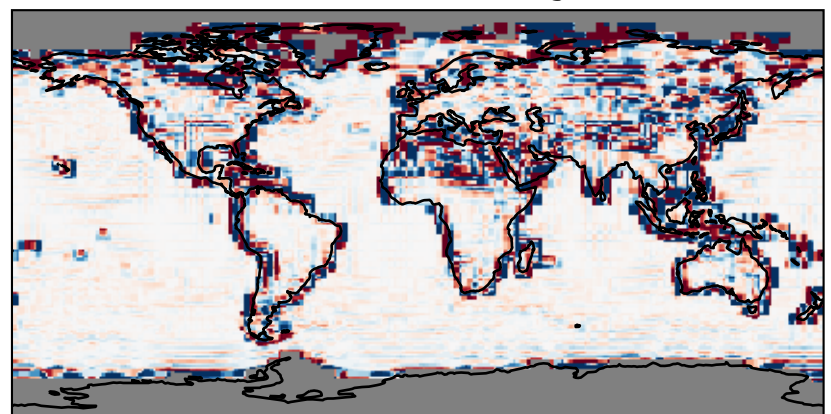
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

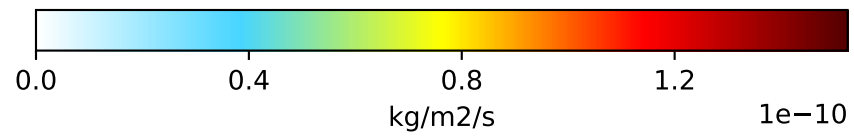
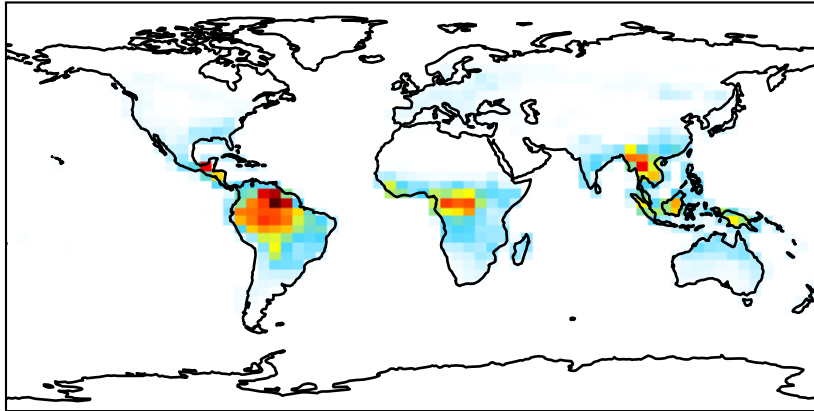


Ratio (1x1.25)  
Dev/Ref, Fixed Range

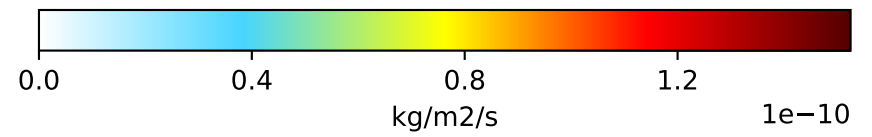
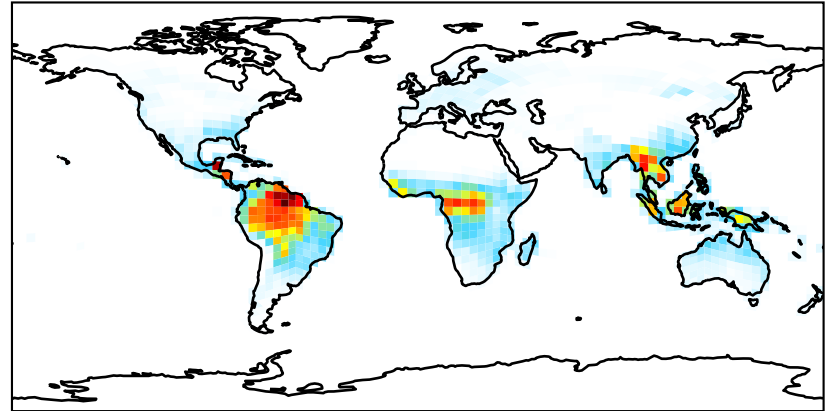


# EmisMTPA\_Total (Apr2019)

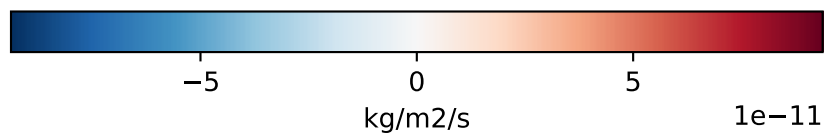
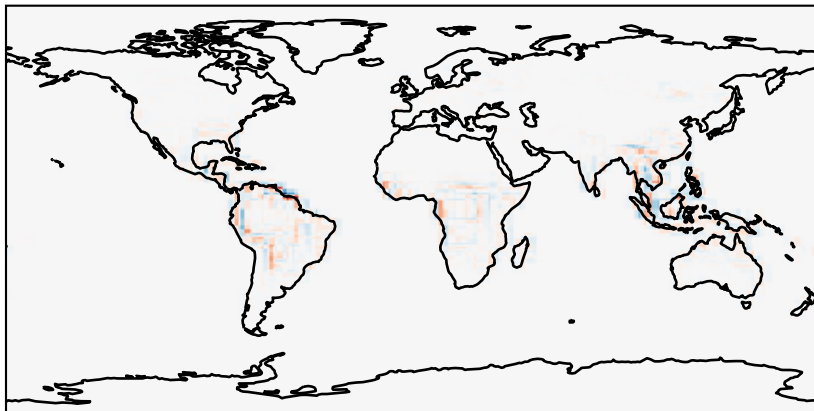
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



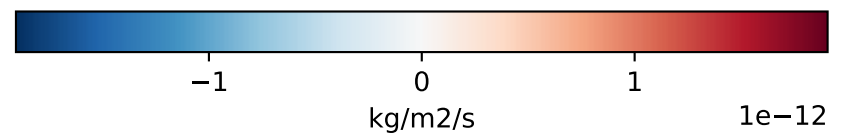
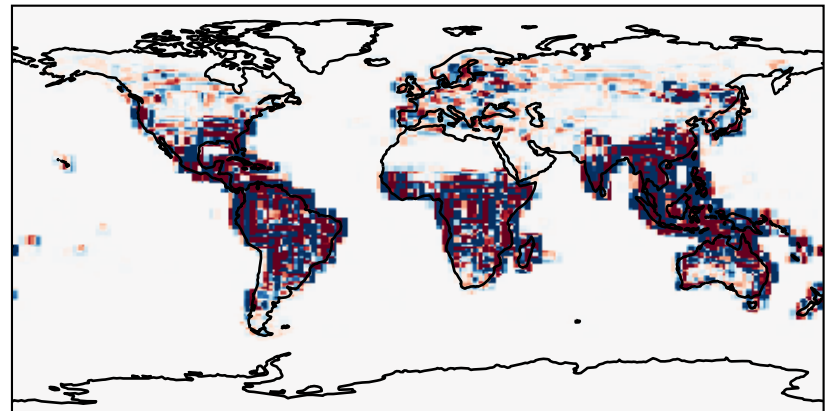
GCHP\_14.0.0-rc.3 (Dev)  
c24



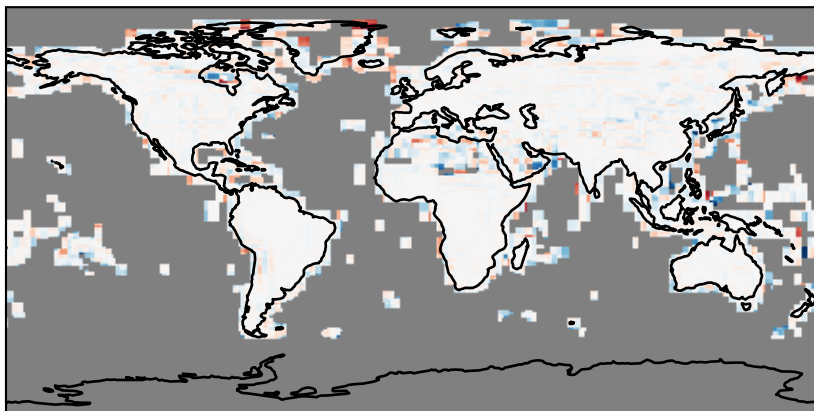
Difference (1x1.25)  
Dev - Ref, Dynamic Range



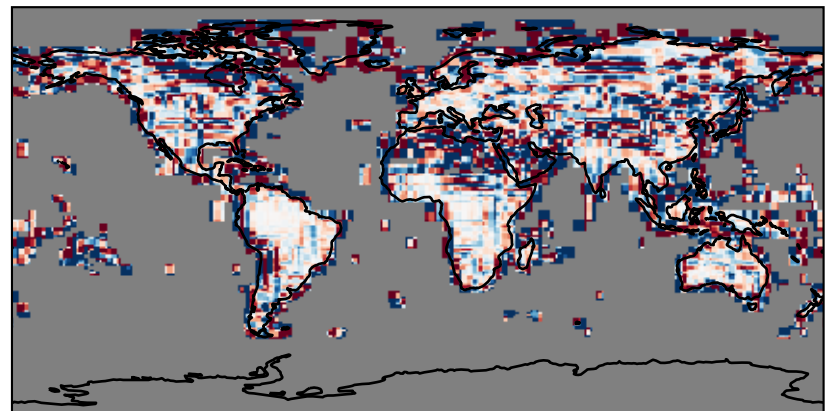
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Ratio (1x1.25)  
Dev/Ref, Fixed Range



# EmisNAP\_Total (Apr2019)

GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



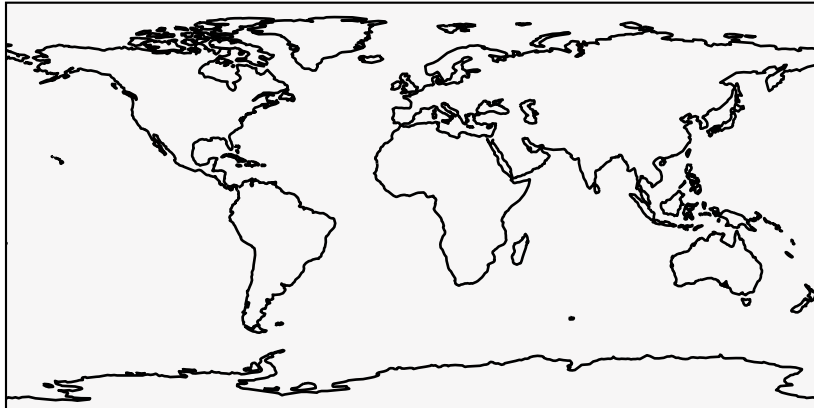
Zero throughout domain  
kg/m2/s

GCHP\_14.0.0-rc.3 (Dev)  
c24



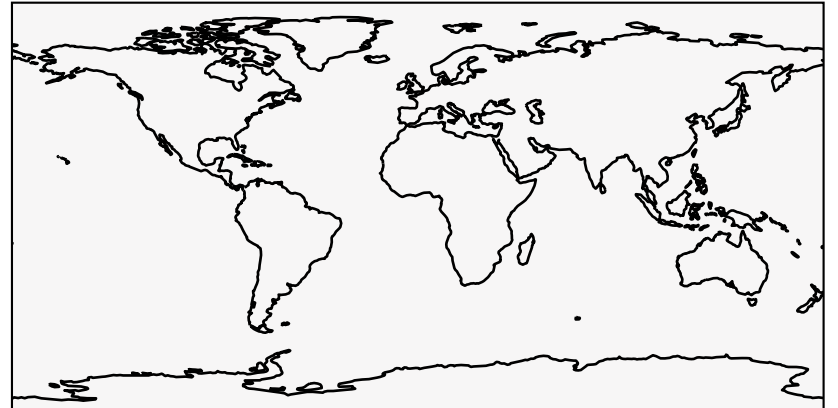
Zero throughout domain  
kg/m2/s

Difference (1x1.25)  
Dev - Ref, Dynamic Range



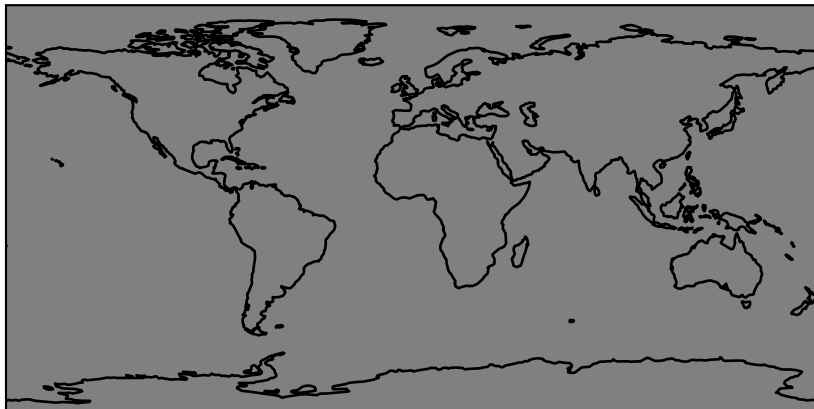
Zero throughout domain  
kg/m2/s

Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



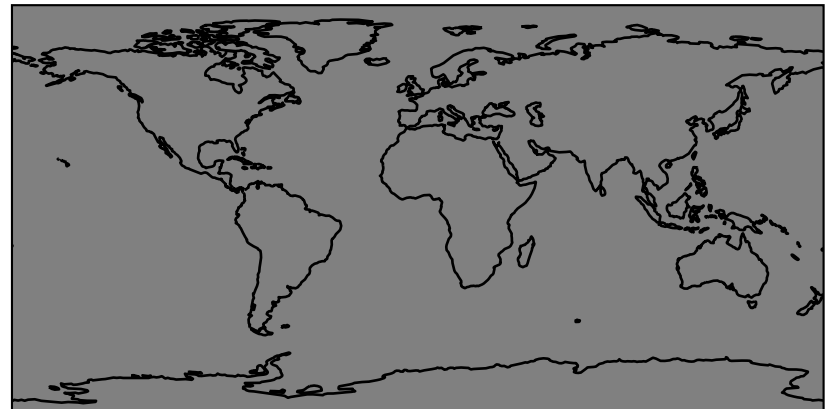
Zero throughout domain  
kg/m2/s

Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

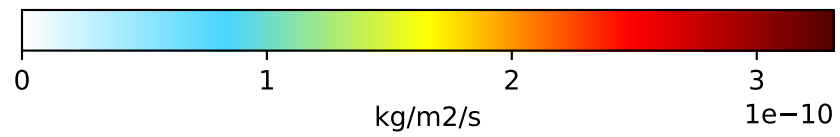
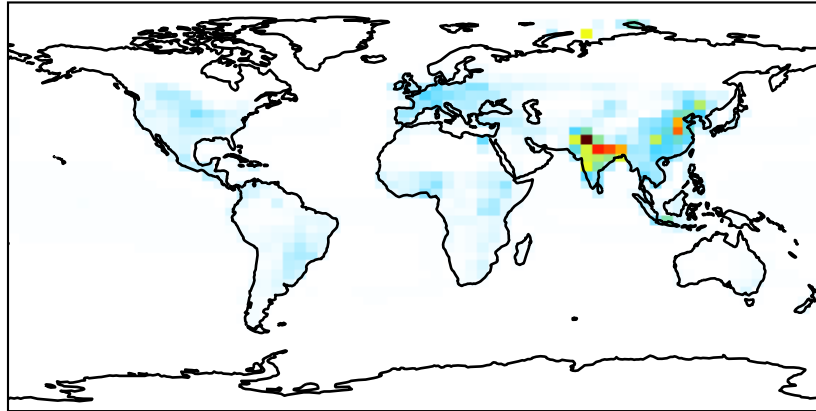
Ratio (1x1.25)  
Dev/Ref, Fixed Range



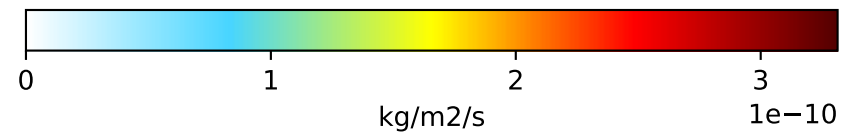
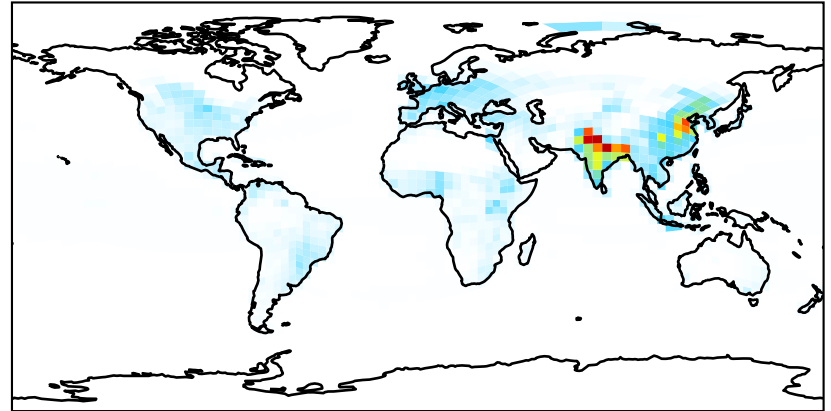
Undefined throughout domain  
unitless

# EmisNH3\_Total (Apr2019)

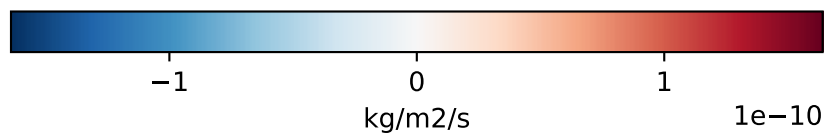
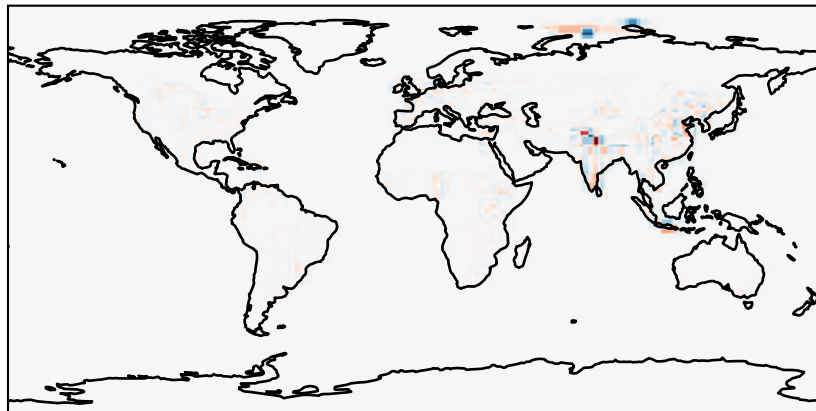
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



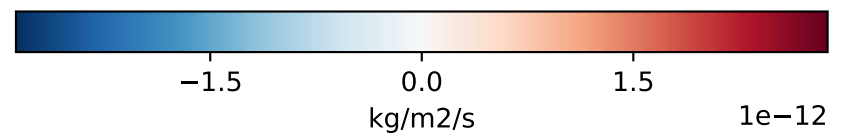
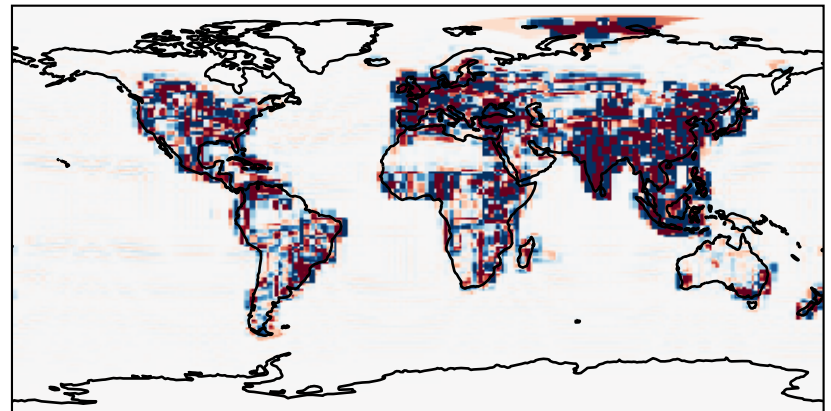
GCHP\_14.0.0-rc.3 (Dev)  
c24



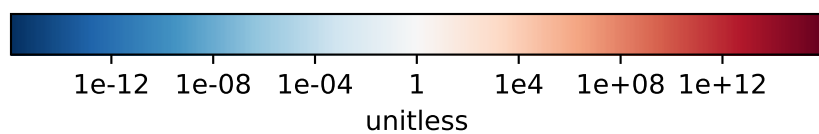
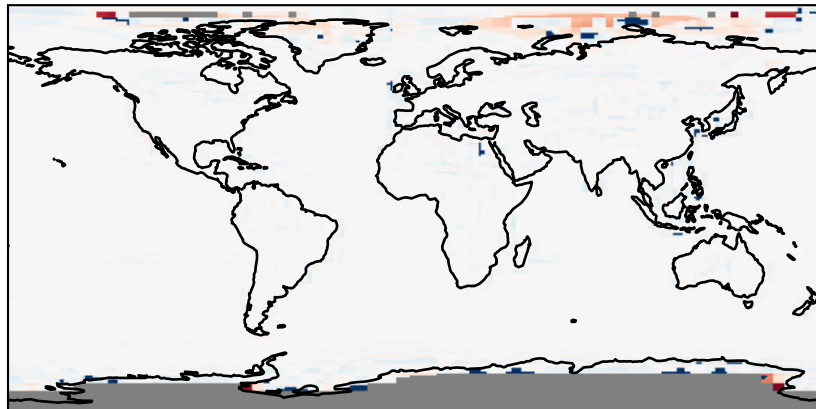
Difference (1x1.25)  
Dev - Ref, Dynamic Range



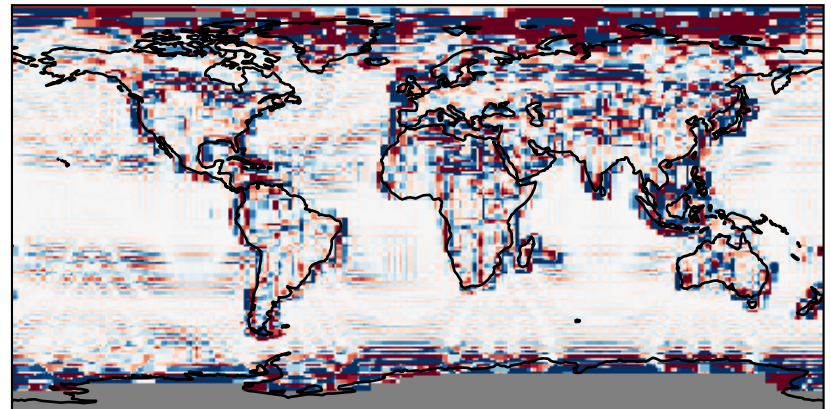
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

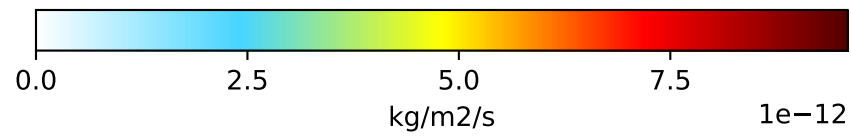
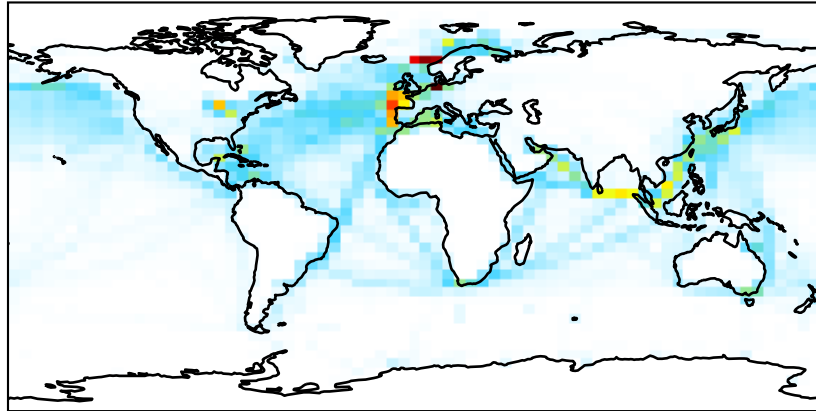


Ratio (1x1.25)  
Dev/Ref, Fixed Range

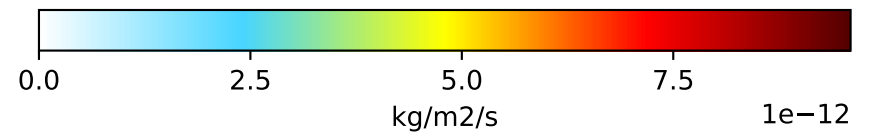
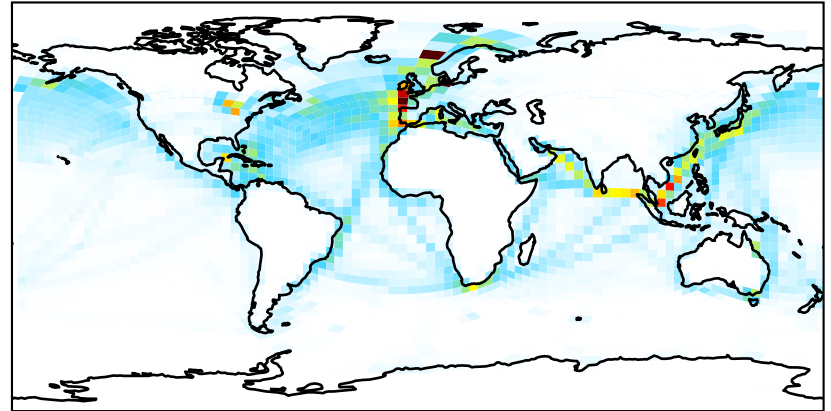


# EmisNO2\_Total (Apr2019)

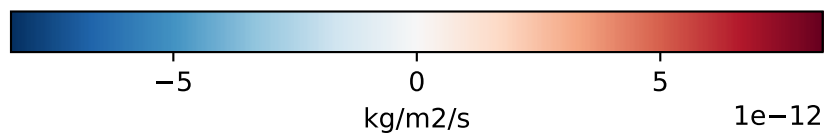
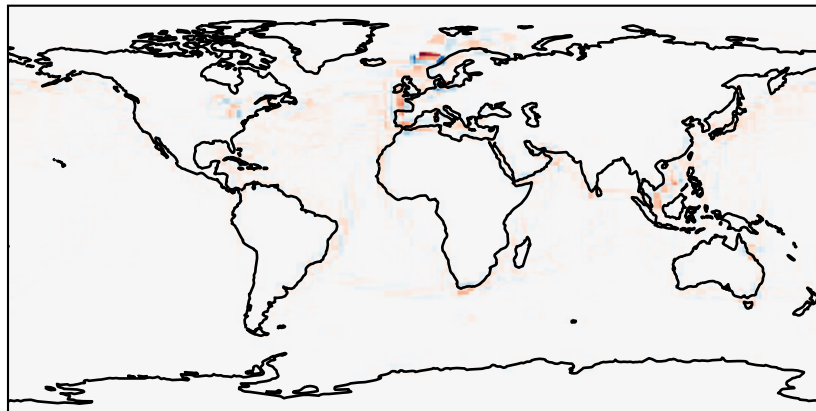
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



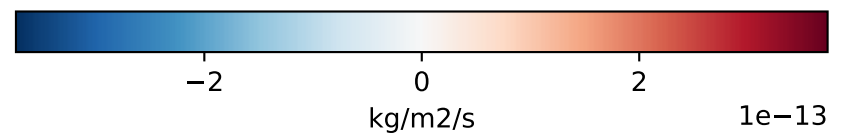
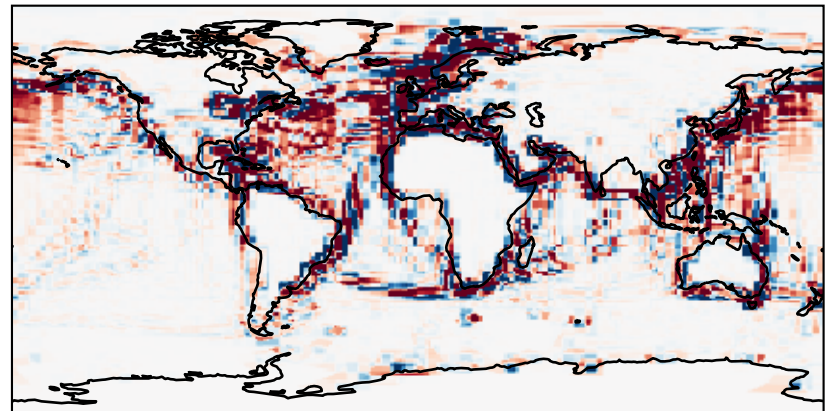
GCHP\_14.0.0-rc.3 (Dev)  
c24



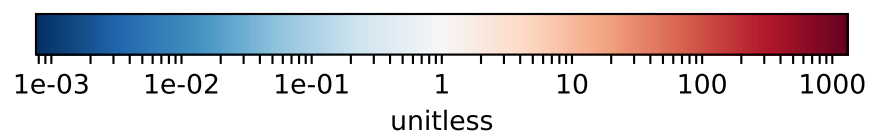
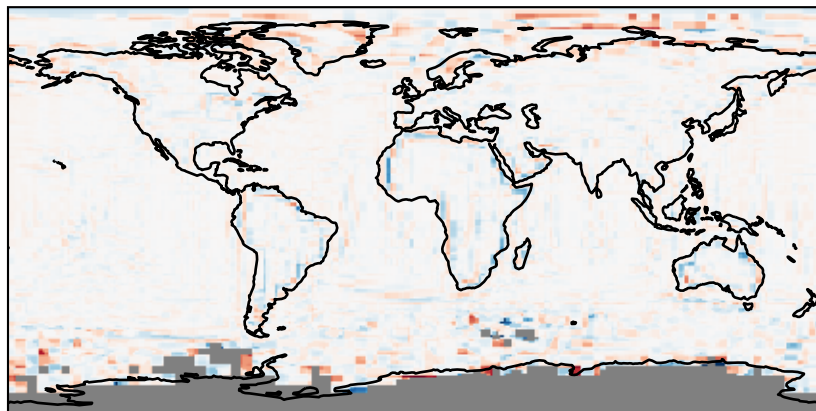
Difference (1x1.25)  
Dev - Ref, Dynamic Range



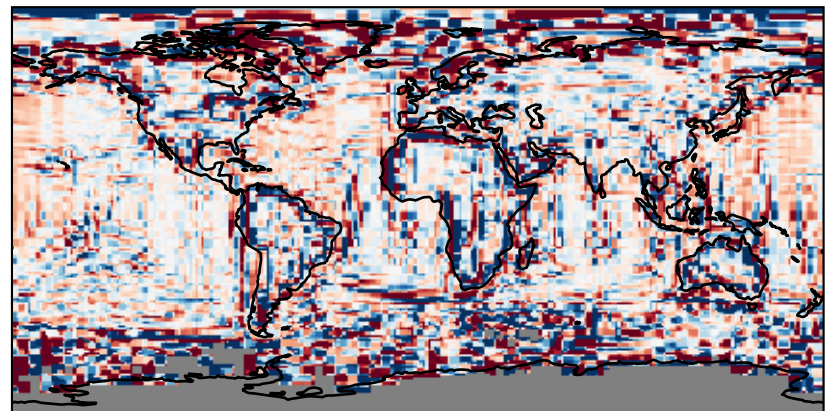
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



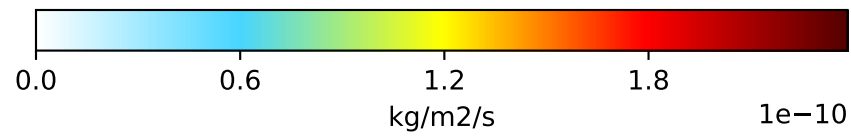
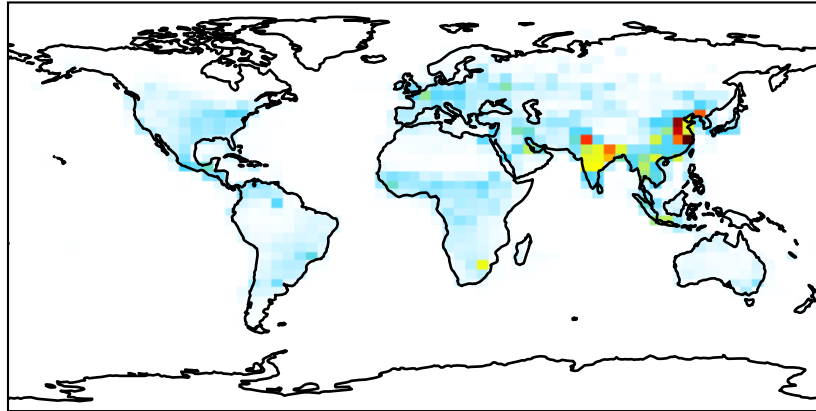
Ratio (1x1.25)  
Dev/Ref, Fixed Range



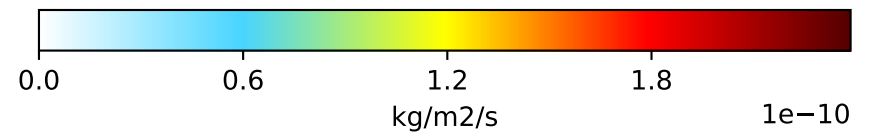
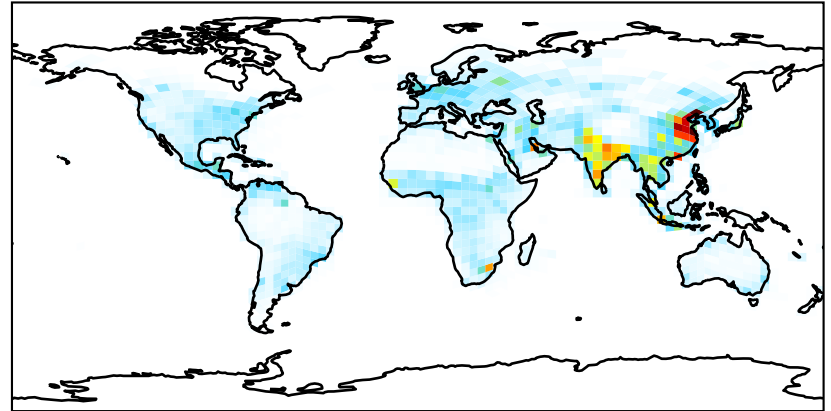


# EmisNO\_Total (Apr2019)

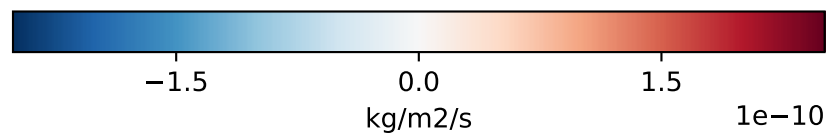
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



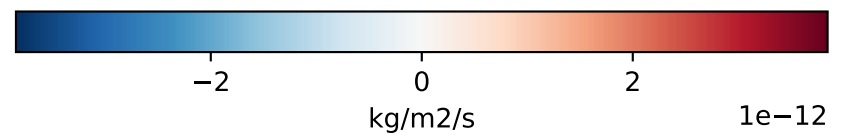
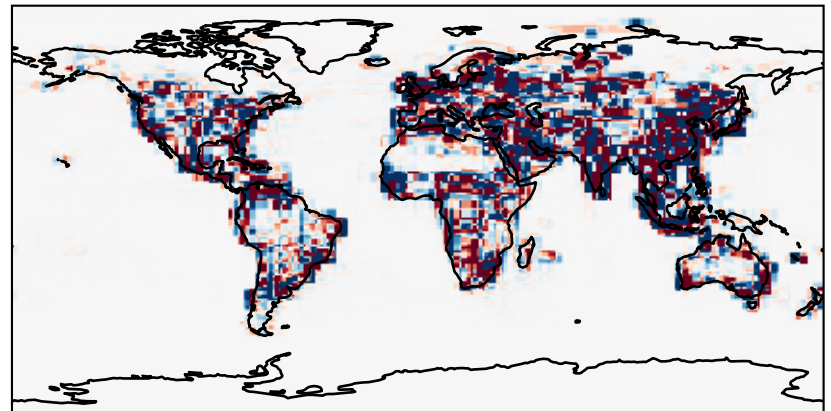
GCHP\_14.0.0-rc.3 (Dev)  
c24



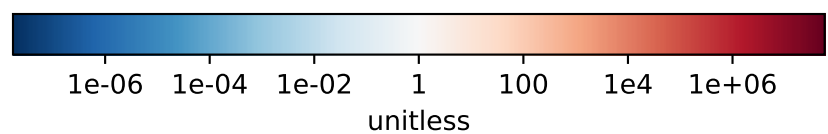
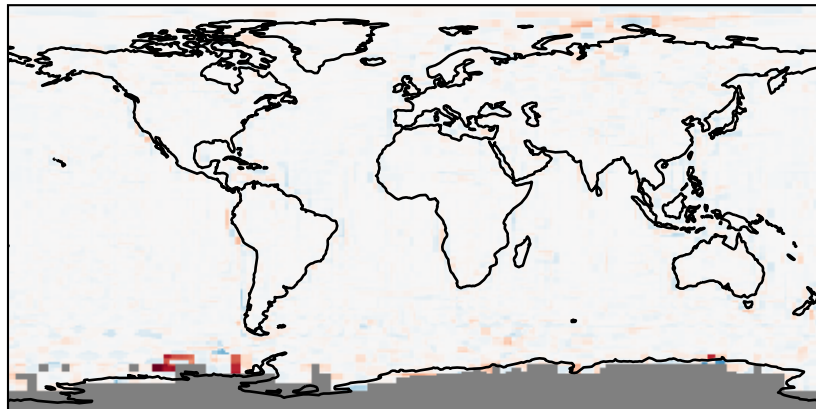
Difference (1x1.25)  
Dev - Ref, Dynamic Range



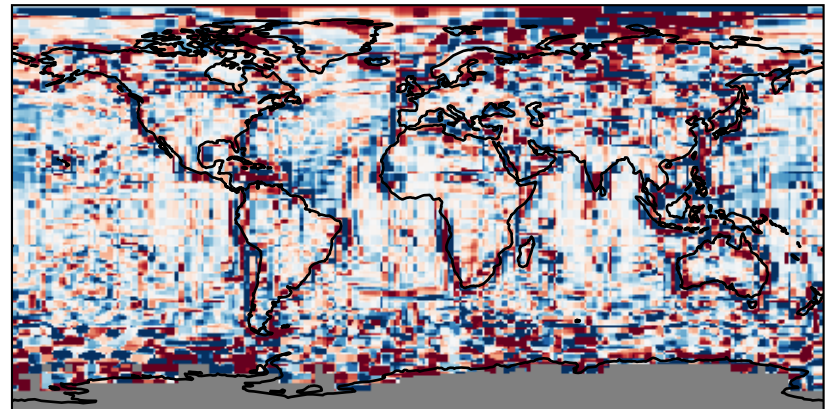
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

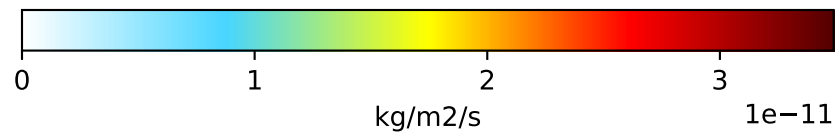
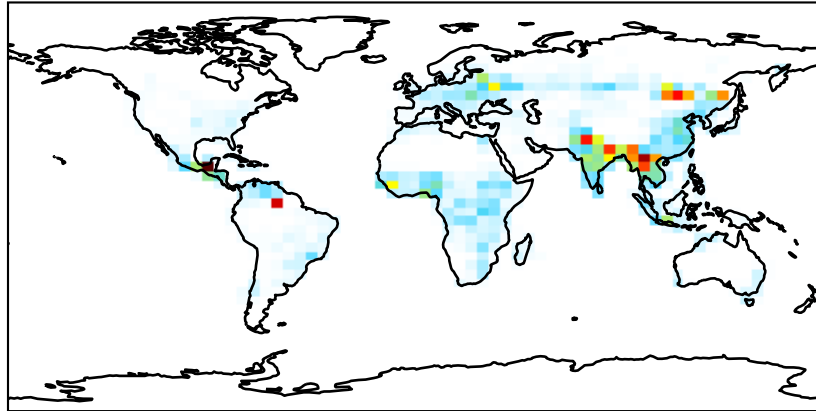


Ratio (1x1.25)  
Dev/Ref, Fixed Range

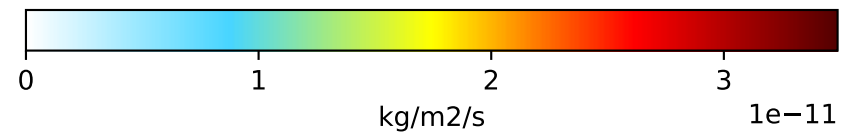
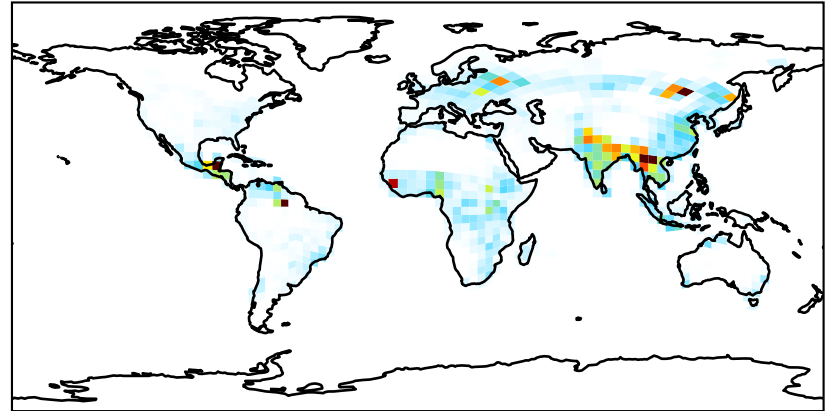


# EmisOCPI\_Total (Apr2019)

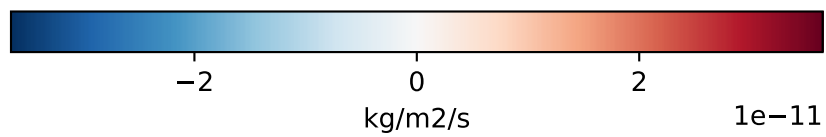
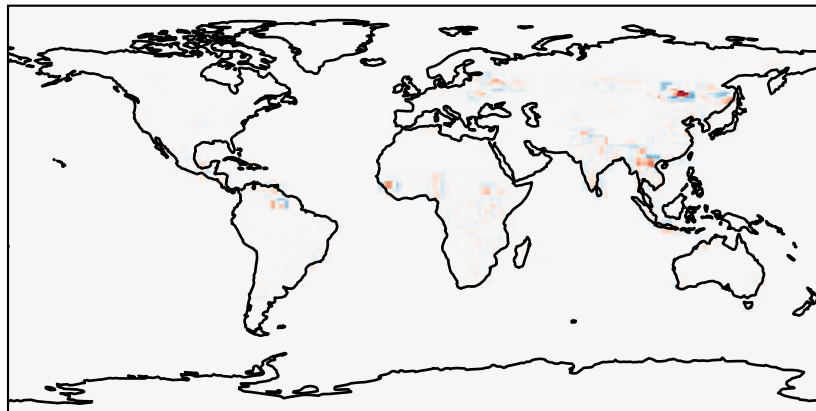
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



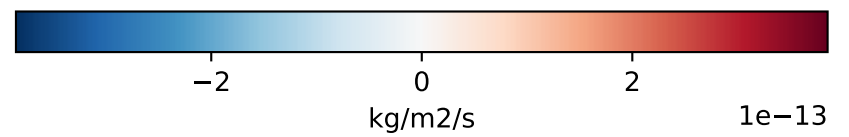
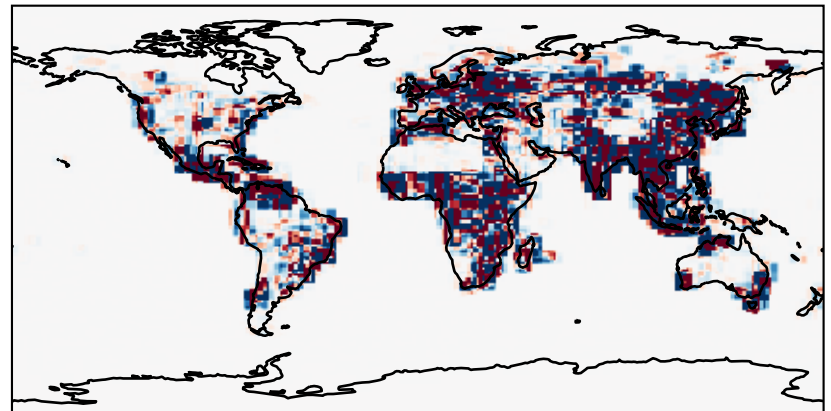
GCHP\_14.0.0-rc.3 (Dev)  
c24



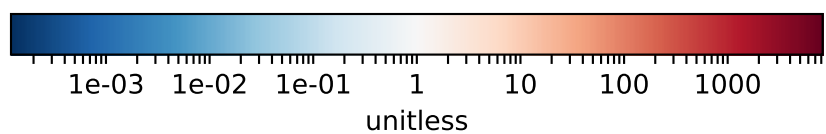
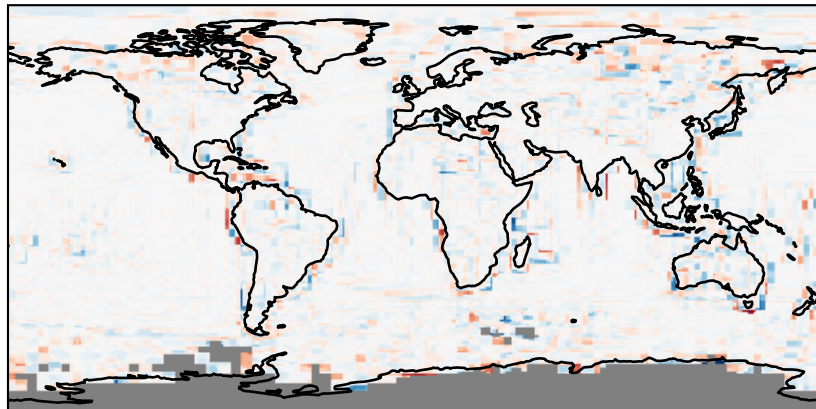
Difference (1x1.25)  
Dev - Ref, Dynamic Range



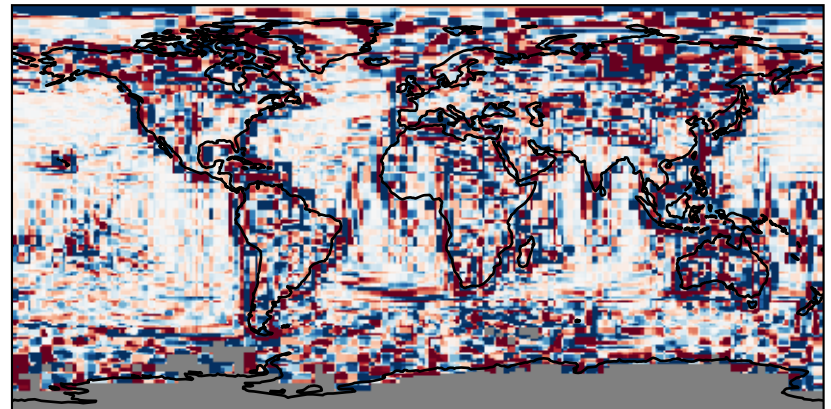
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

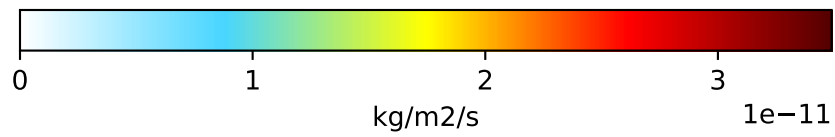
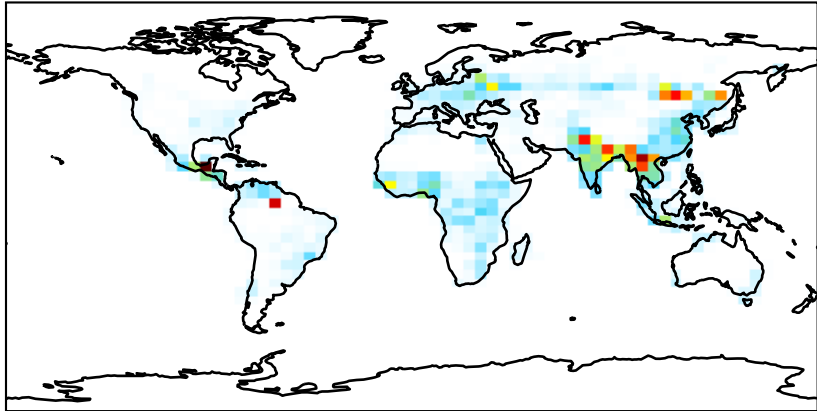


Ratio (1x1.25)  
Dev/Ref, Fixed Range

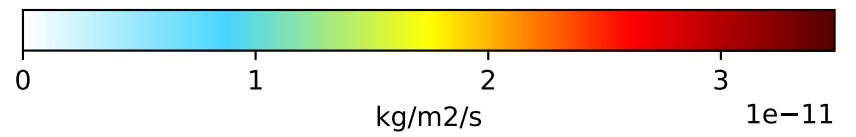
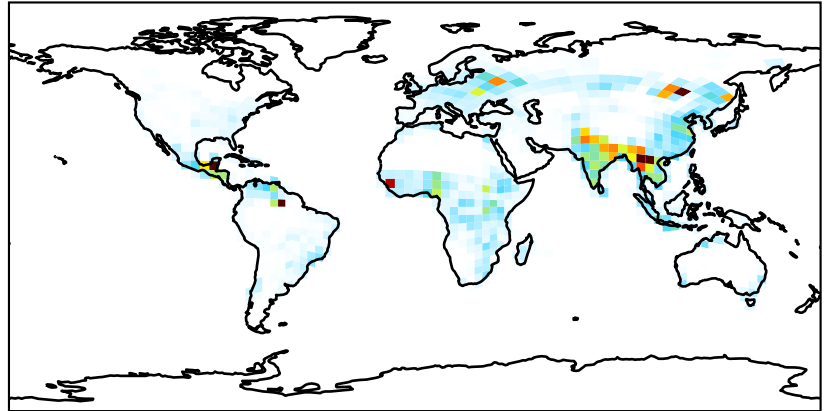


# EmisOCPO\_Total (Apr2019)

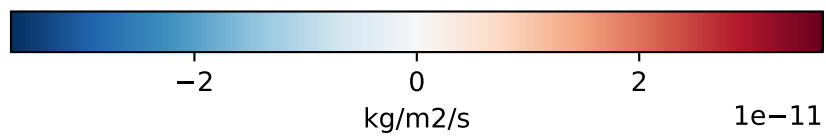
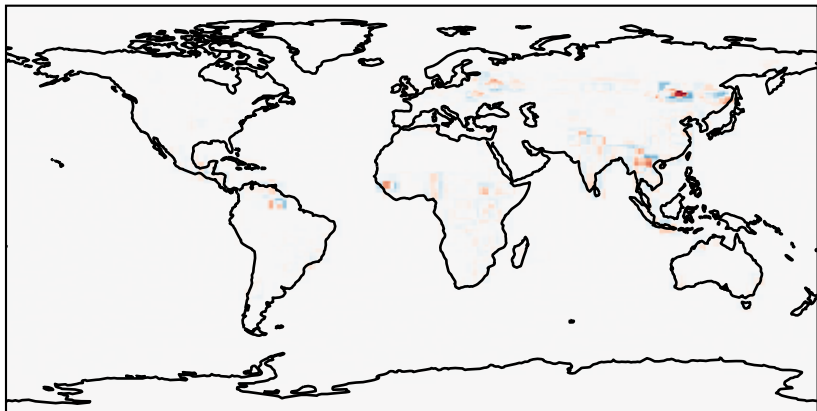
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



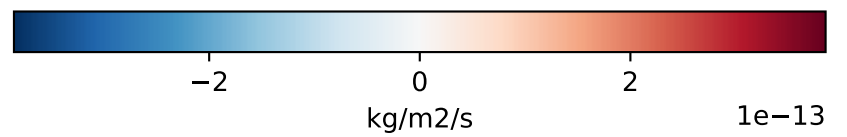
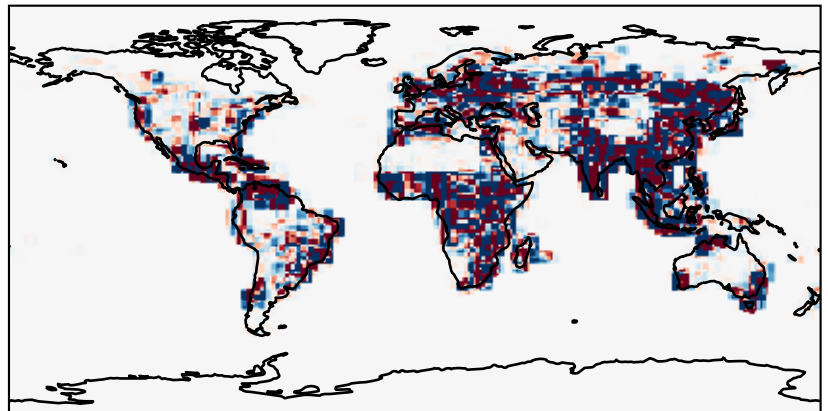
GCHP\_14.0.0-rc.3 (Dev)  
c24



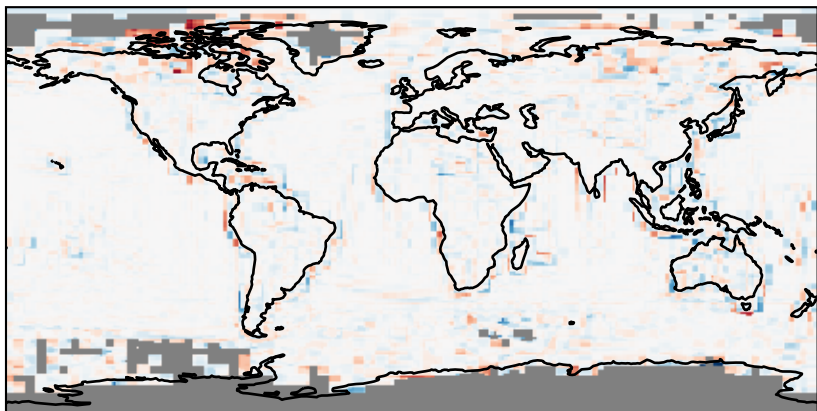
Difference (1x1.25)  
Dev - Ref, Dynamic Range



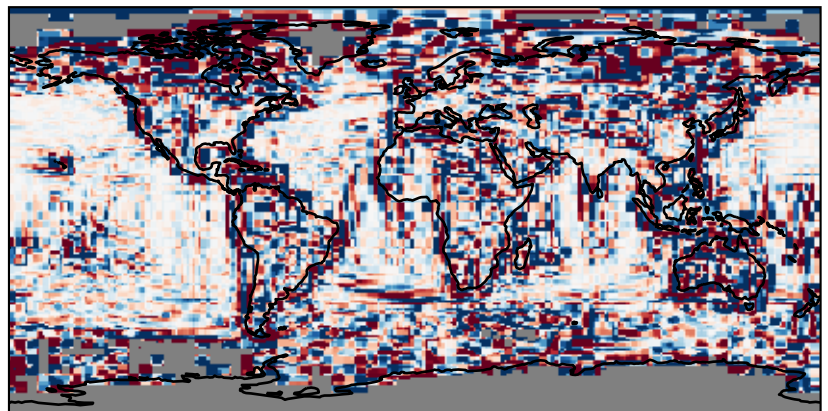
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

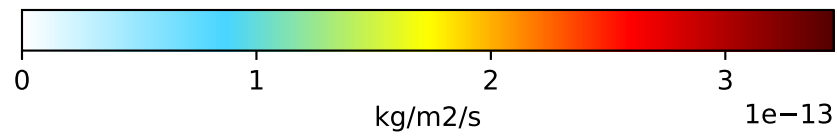
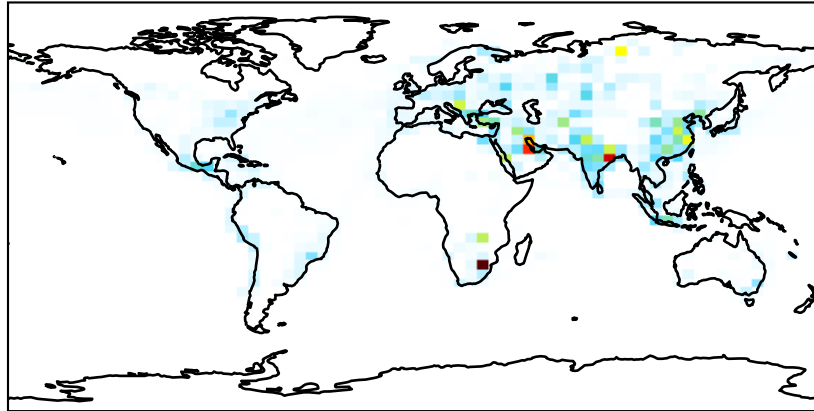


Ratio (1x1.25)  
Dev/Ref, Fixed Range

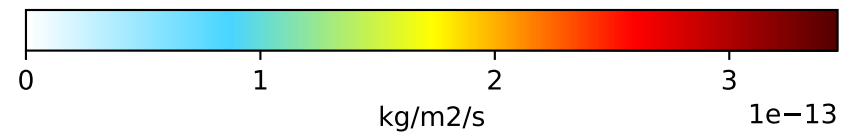
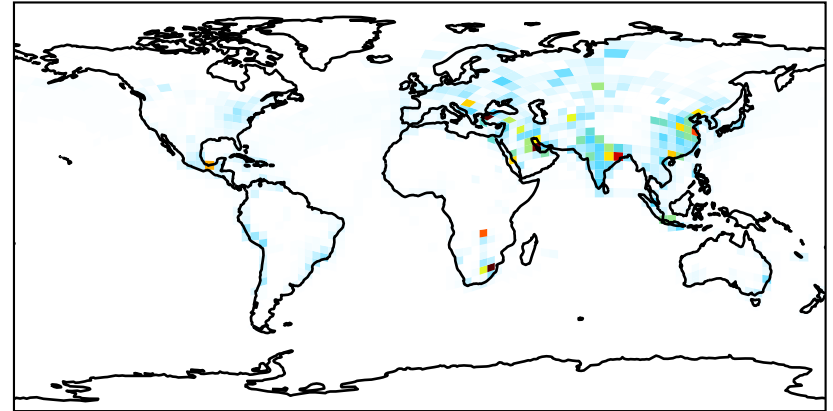


# EmispFe\_Total (Apr2019)

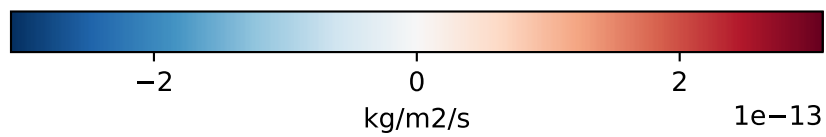
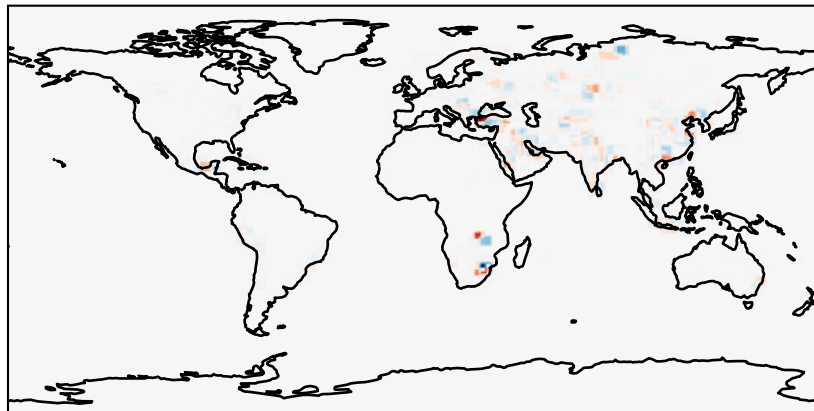
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



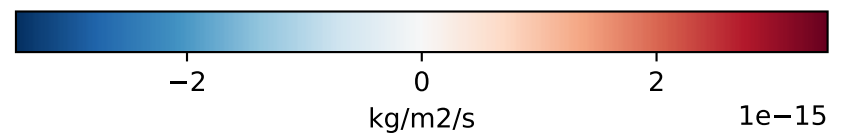
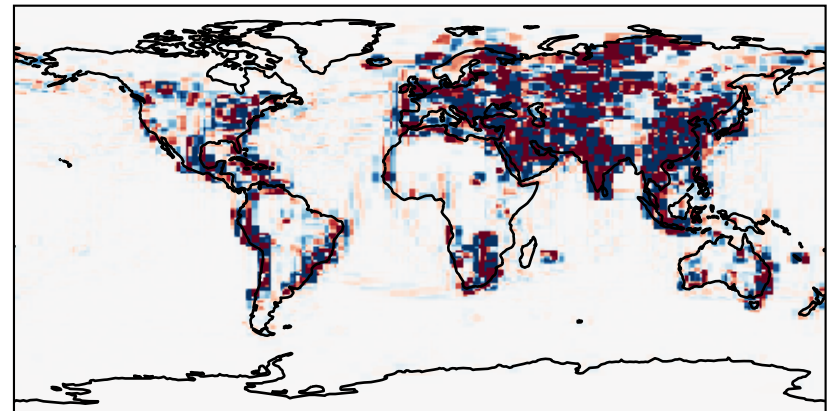
GCHP\_14.0.0-rc.3 (Dev)  
c24



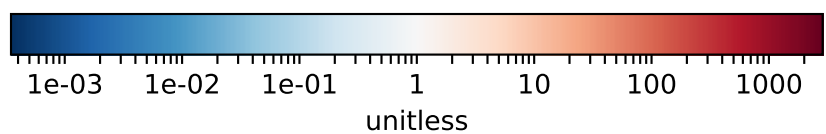
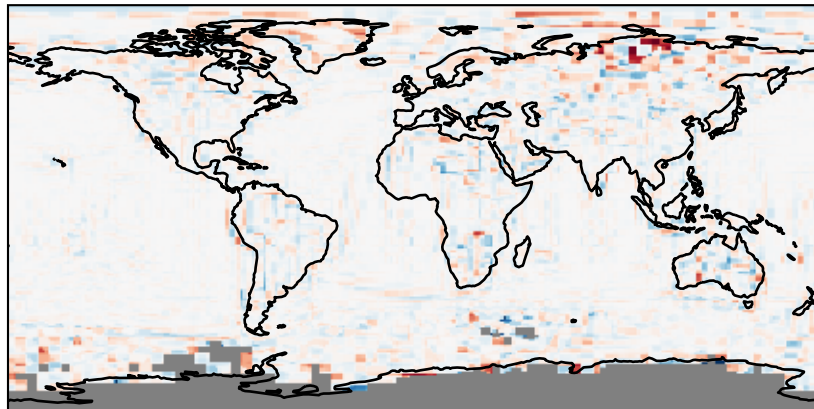
Difference (1x1.25)  
Dev - Ref, Dynamic Range



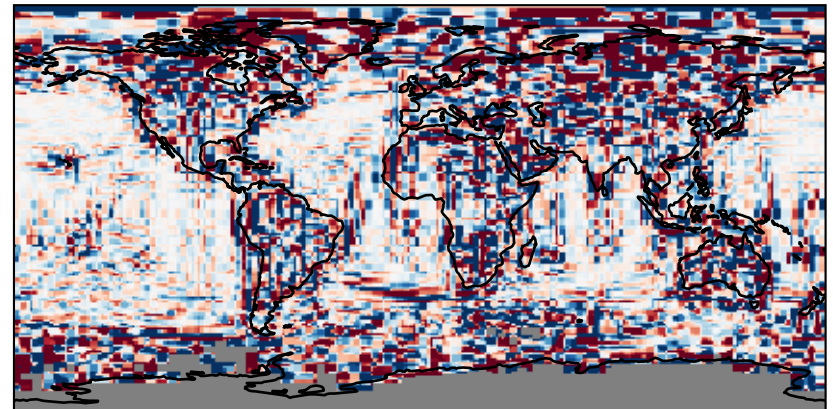
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Ratio (1x1.25)  
Dev/Ref, Fixed Range



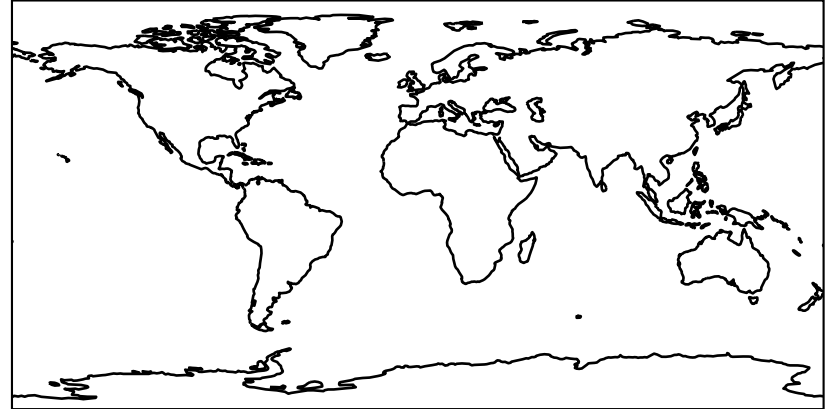
# EmisPOG1\_Total (Apr2019)

GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



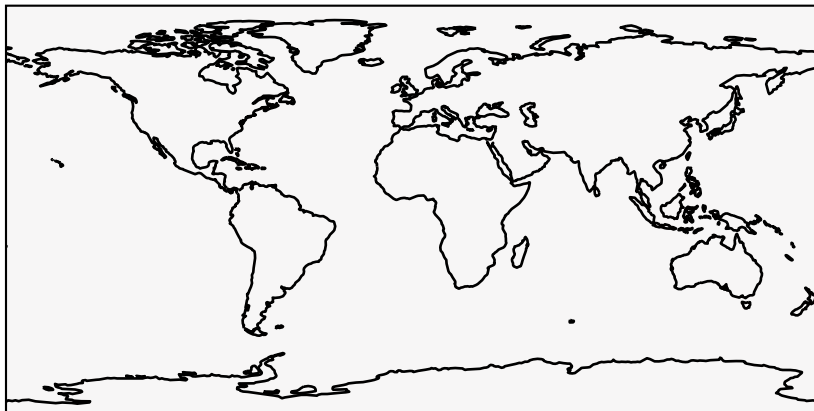
Zero throughout domain  
kg/m2/s

GCHP\_14.0.0-rc.3 (Dev)  
c24



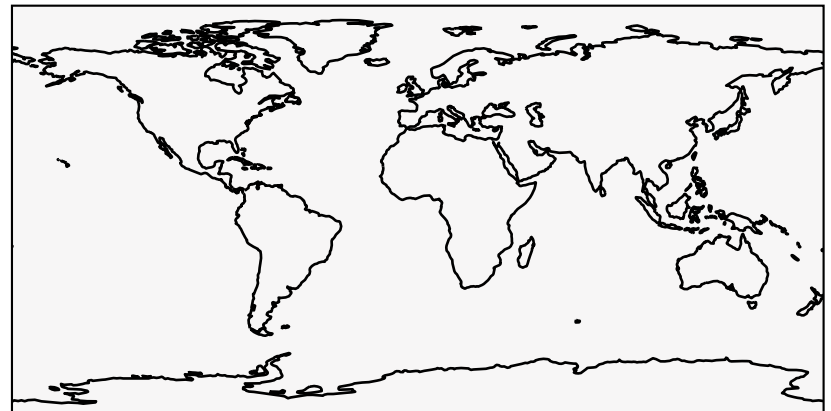
Zero throughout domain  
kg/m2/s

Difference (1x1.25)  
Dev - Ref, Dynamic Range



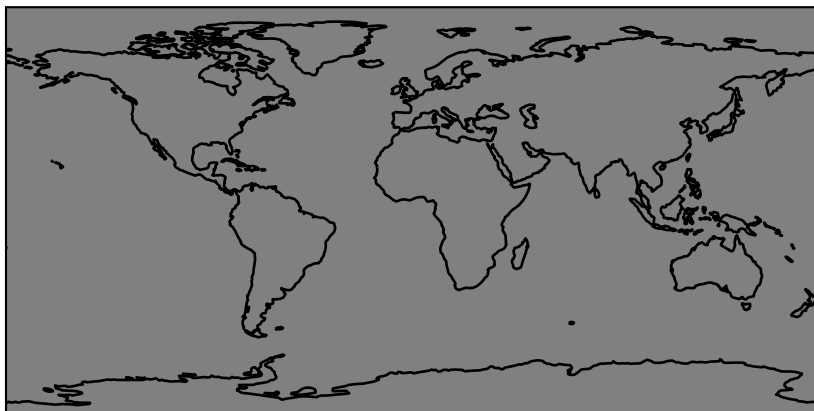
Zero throughout domain  
kg/m2/s

Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



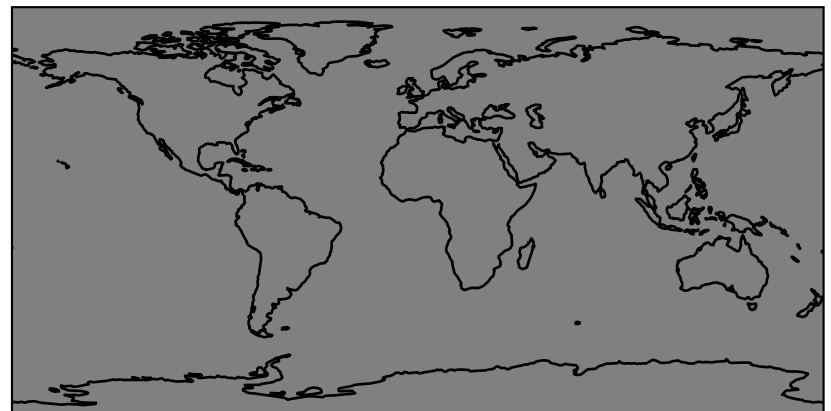
Zero throughout domain  
kg/m2/s

Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

Ratio (1x1.25)  
Dev/Ref, Fixed Range



Undefined throughout domain  
unitless

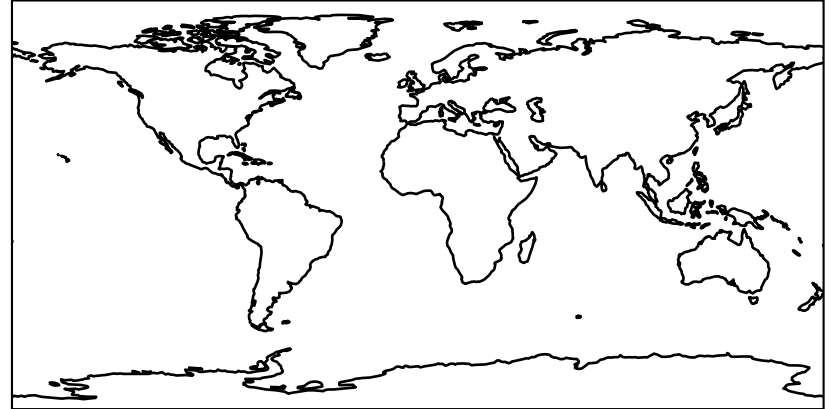
# EmisPOG2\_Total (Apr2019)

GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



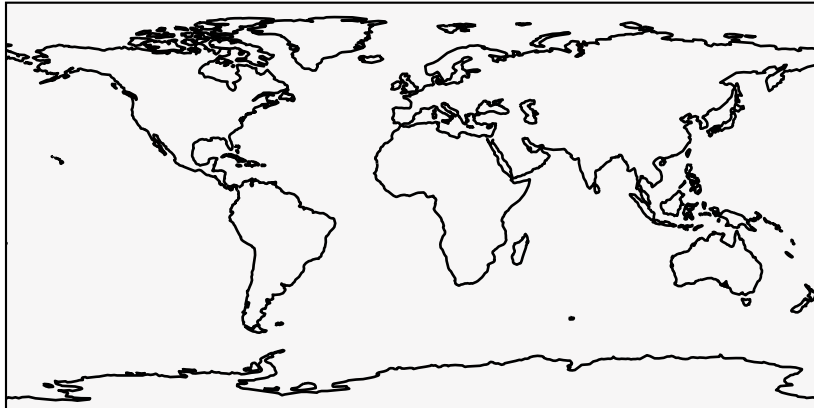
Zero throughout domain  
kg/m2/s

GCHP\_14.0.0-rc.3 (Dev)  
c24



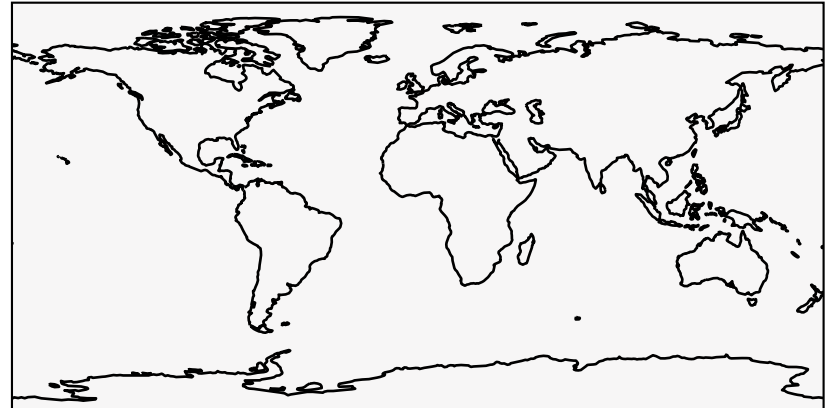
Zero throughout domain  
kg/m2/s

Difference (1x1.25)  
Dev - Ref, Dynamic Range



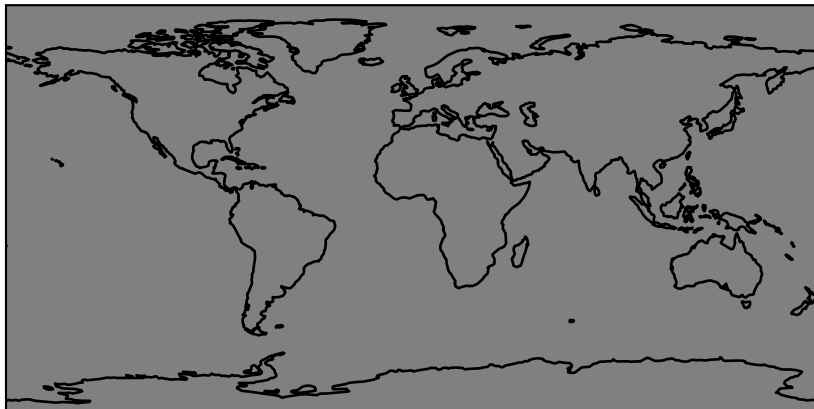
Zero throughout domain  
kg/m2/s

Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



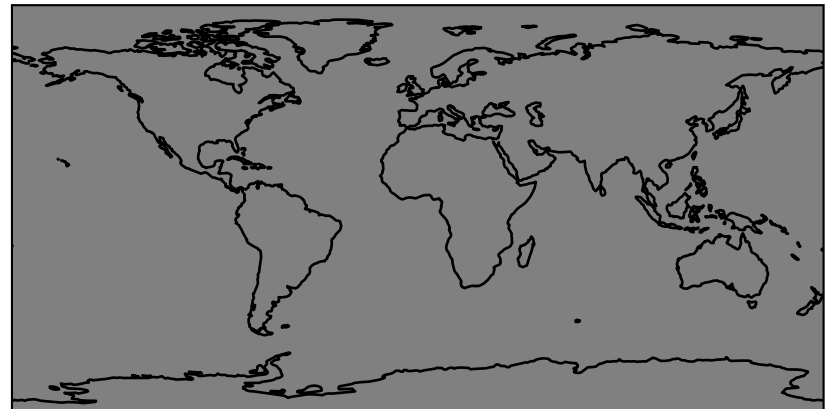
Zero throughout domain  
kg/m2/s

Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Undefined throughout domain  
unitless

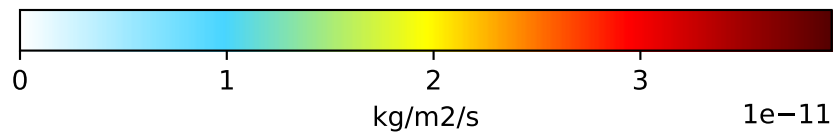
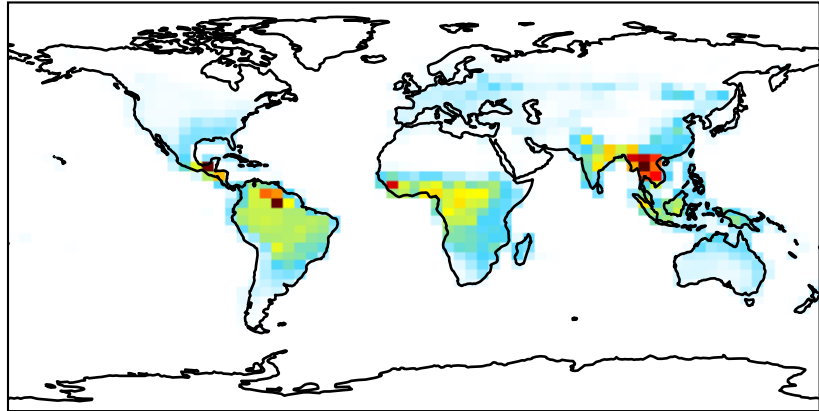
Ratio (1x1.25)  
Dev/Ref, Fixed Range



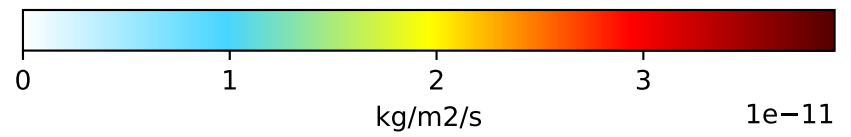
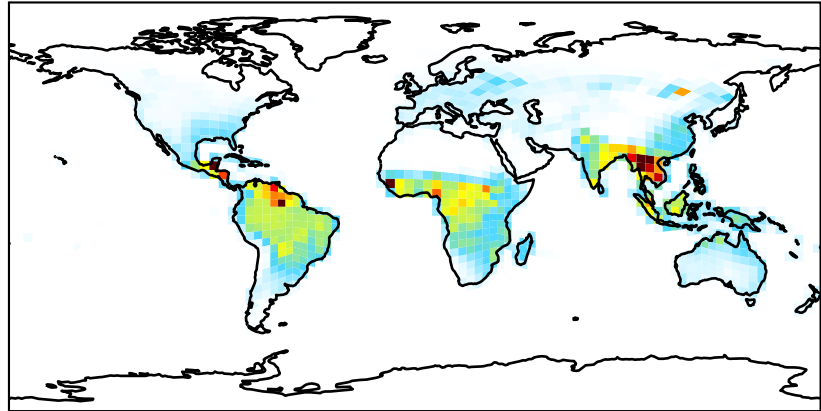
Undefined throughout domain  
unitless

# EmisPRPE\_Total (Apr2019)

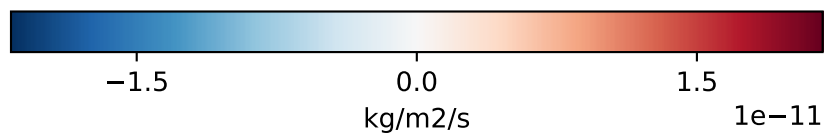
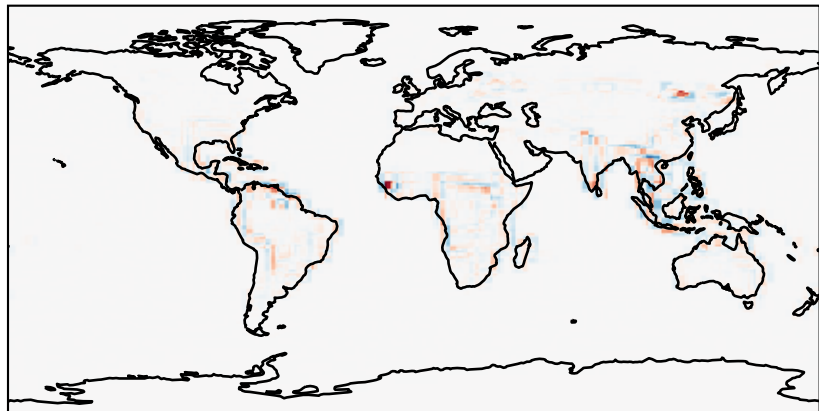
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



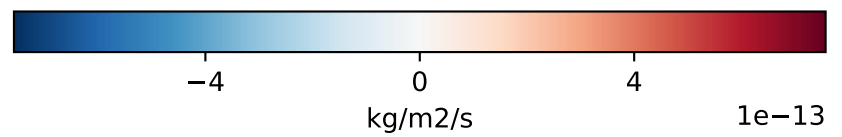
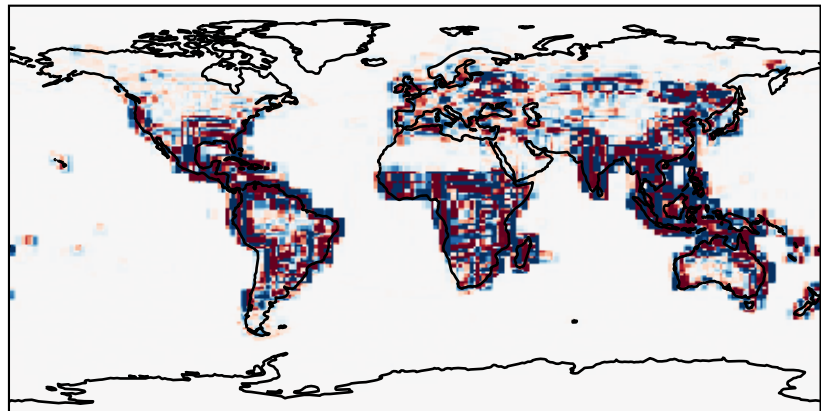
GCHP\_14.0.0-rc.3 (Dev)  
c24



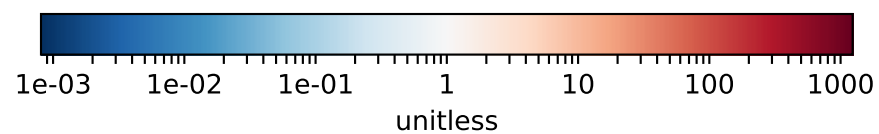
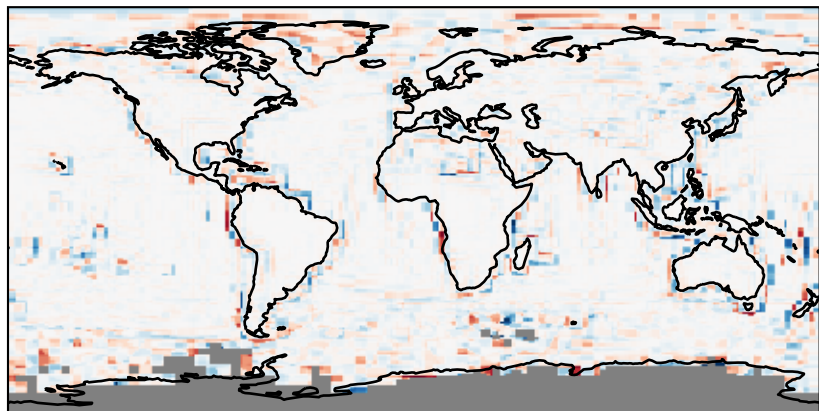
Difference (1x1.25)  
Dev - Ref, Dynamic Range



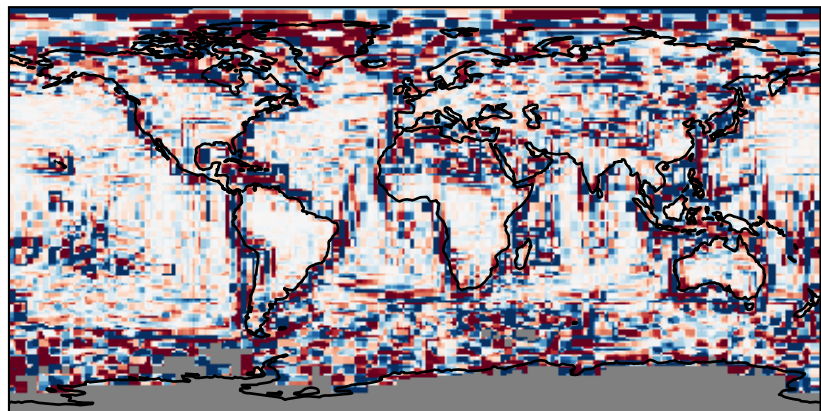
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

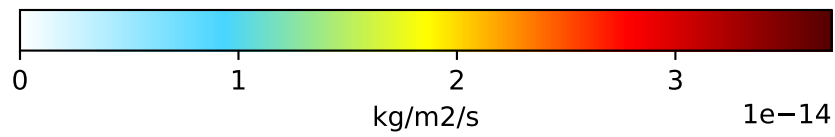
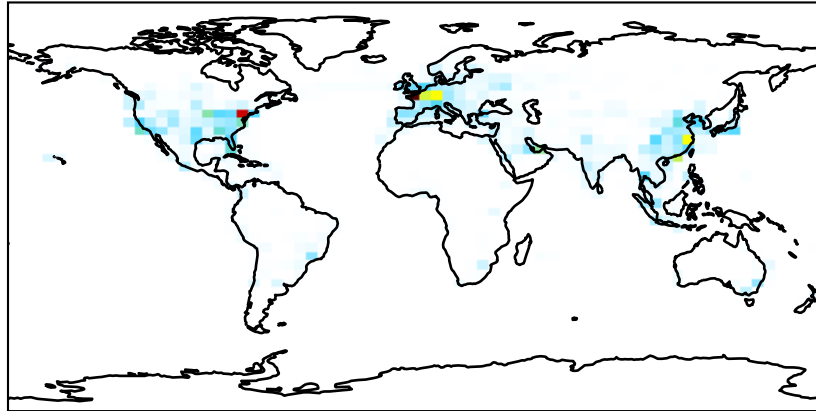


Ratio (1x1.25)  
Dev/Ref, Fixed Range

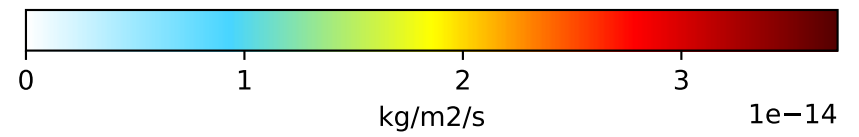
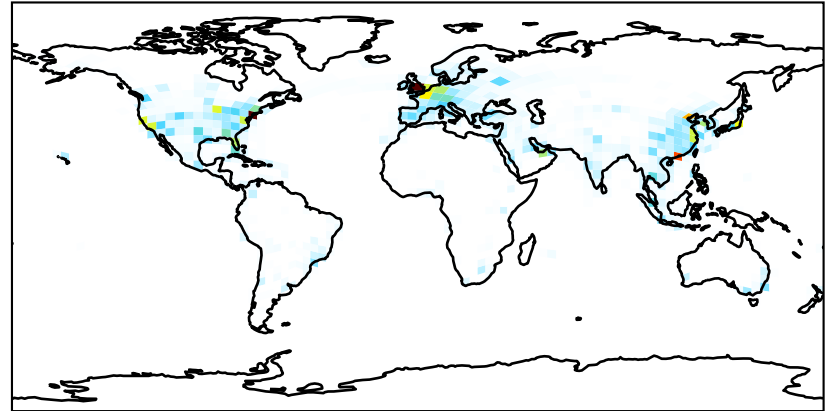


# EmisRCHO\_Total (Apr2019)

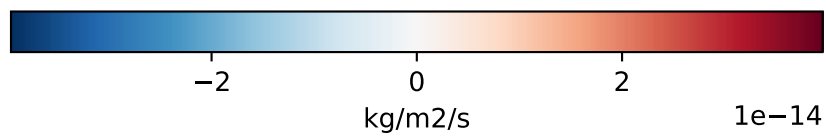
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



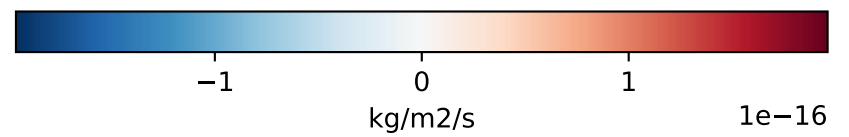
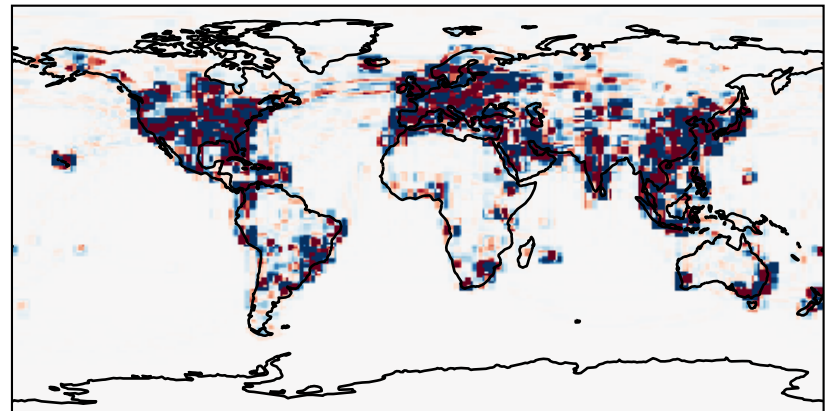
GCHP\_14.0.0-rc.3 (Dev)  
c24



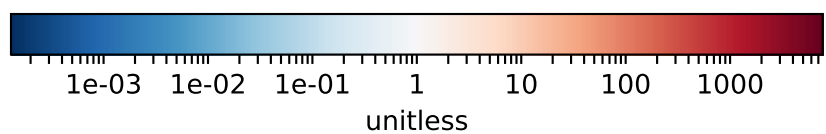
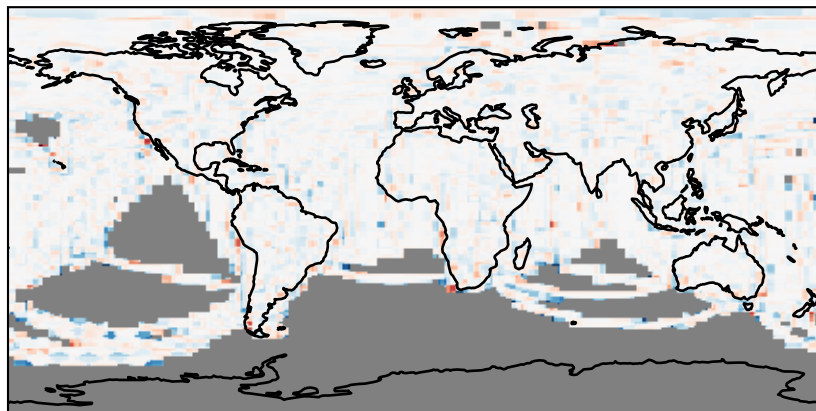
Difference (1x1.25)  
Dev - Ref, Dynamic Range



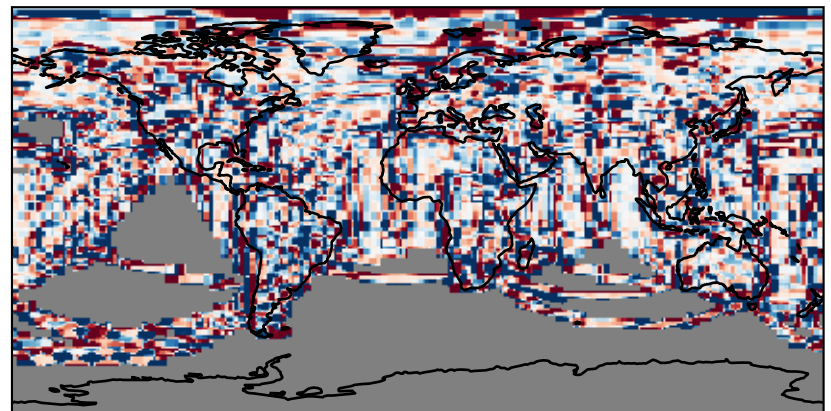
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



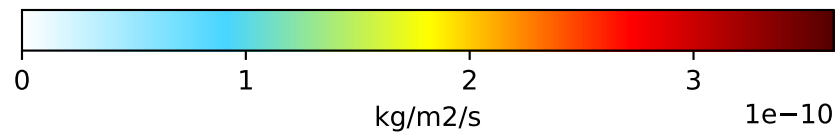
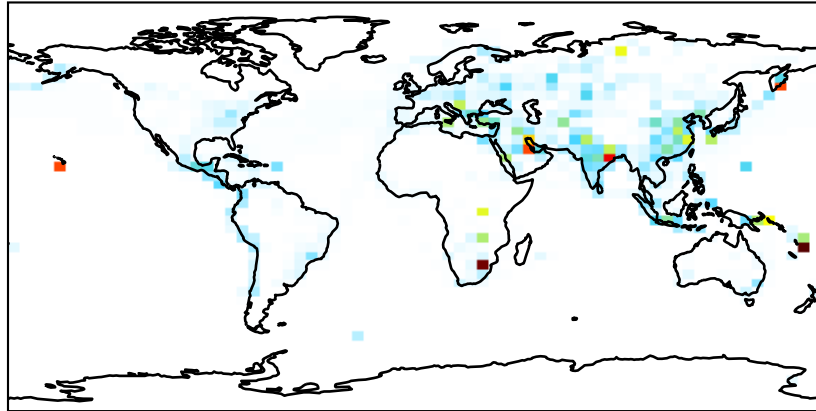
Ratio (1x1.25)  
Dev/Ref, Fixed Range



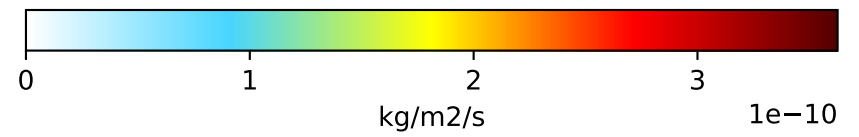
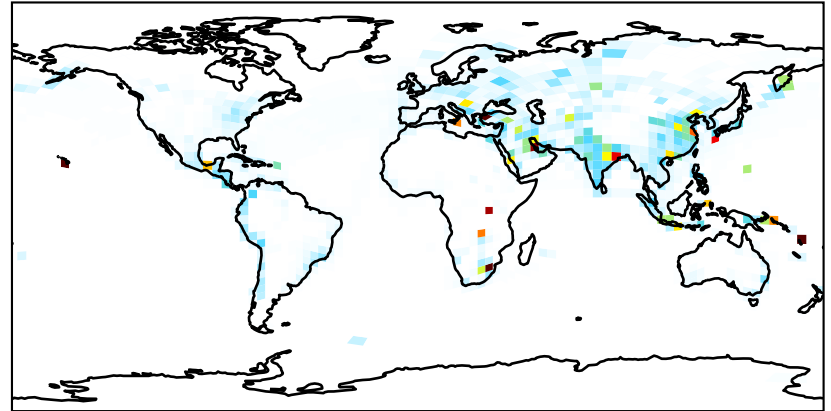


# EmisSO2\_Total (Apr2019)

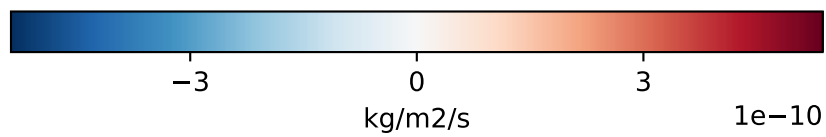
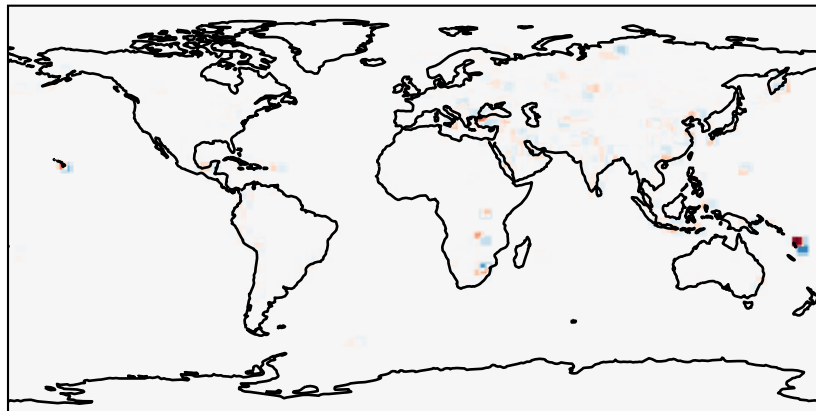
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



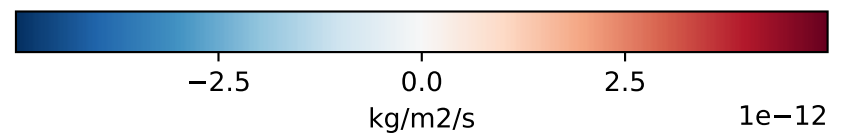
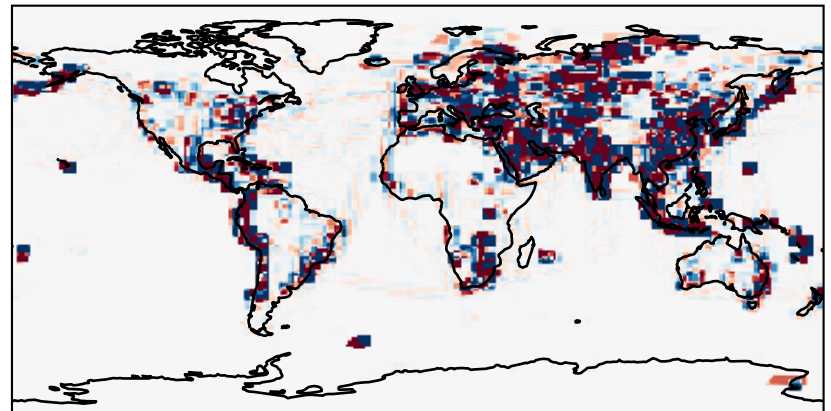
GCHP\_14.0.0-rc.3 (Dev)  
c24



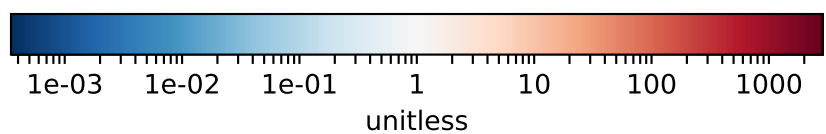
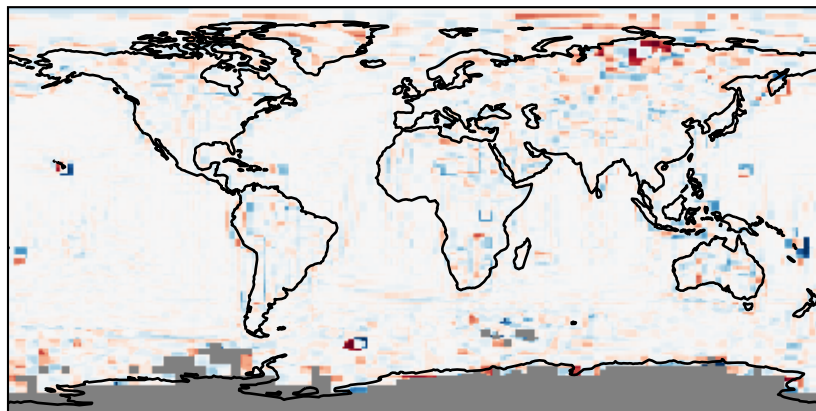
Difference (1x1.25)  
Dev - Ref, Dynamic Range



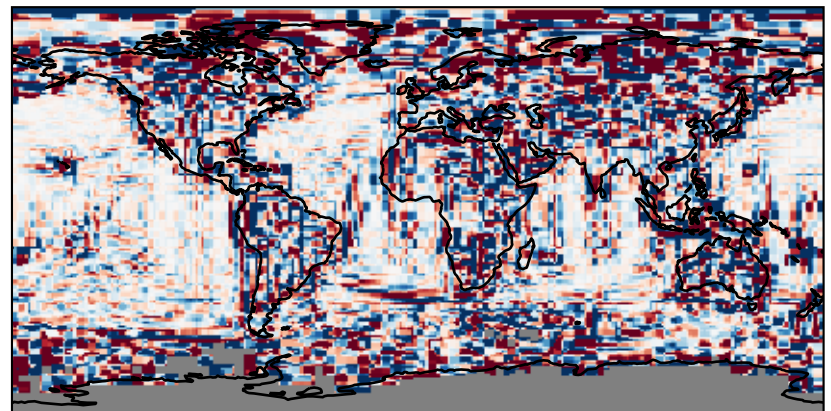
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

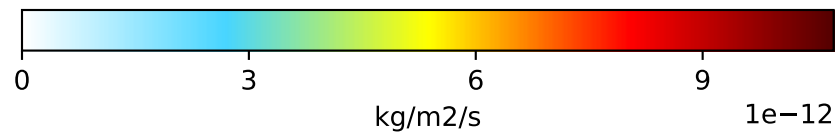
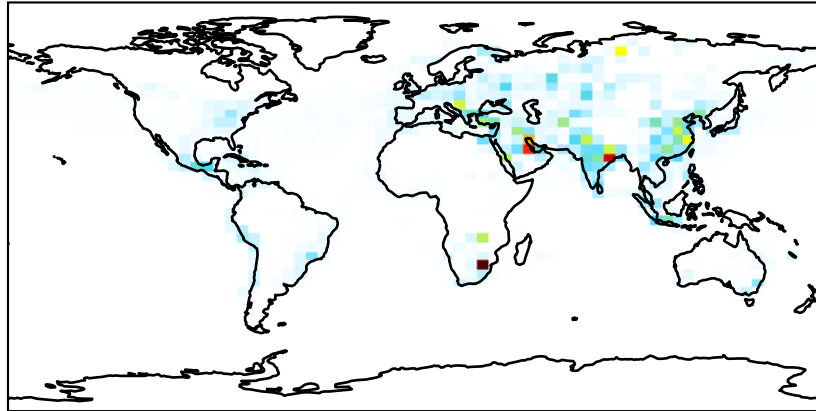


Ratio (1x1.25)  
Dev/Ref, Fixed Range

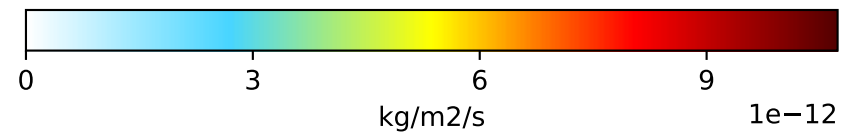
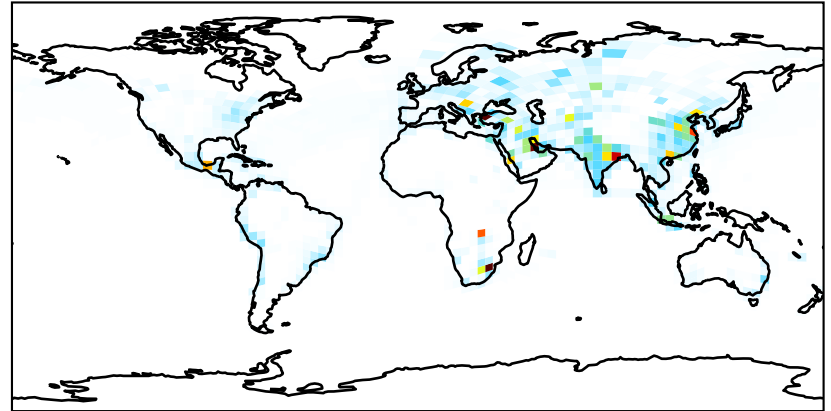


# EmisSO4\_Total (Apr2019)

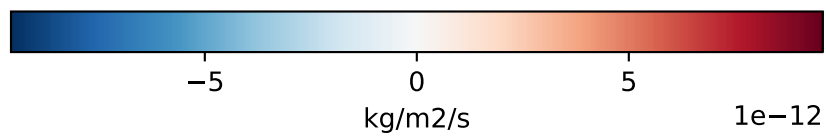
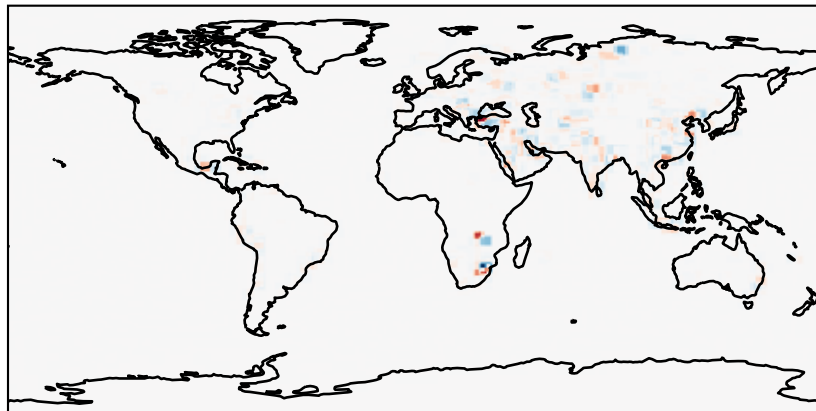
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



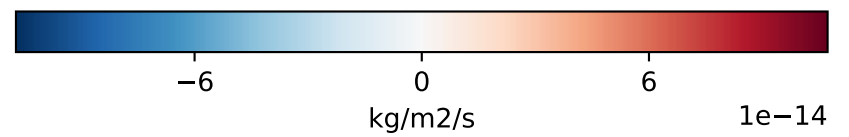
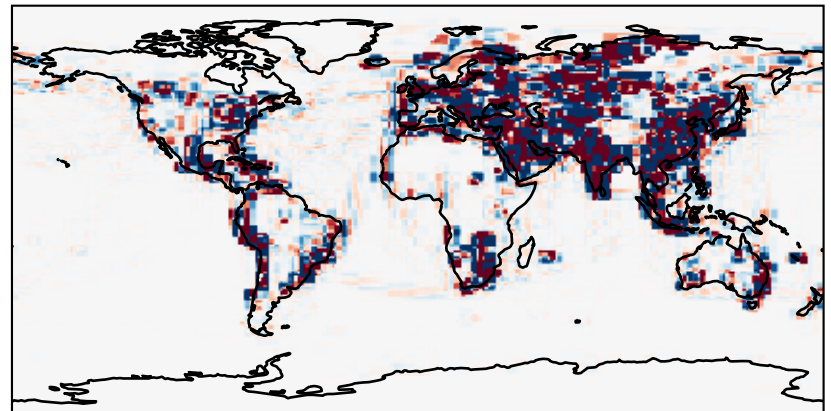
GCHP\_14.0.0-rc.3 (Dev)  
c24



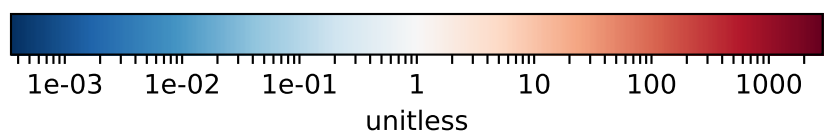
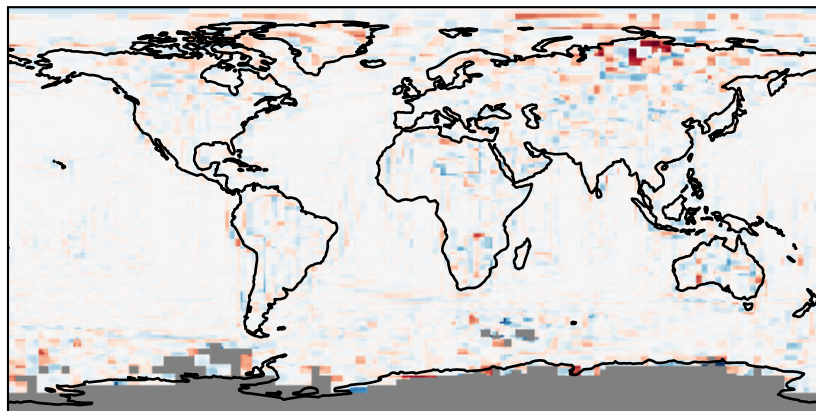
Difference (1x1.25)  
Dev - Ref, Dynamic Range



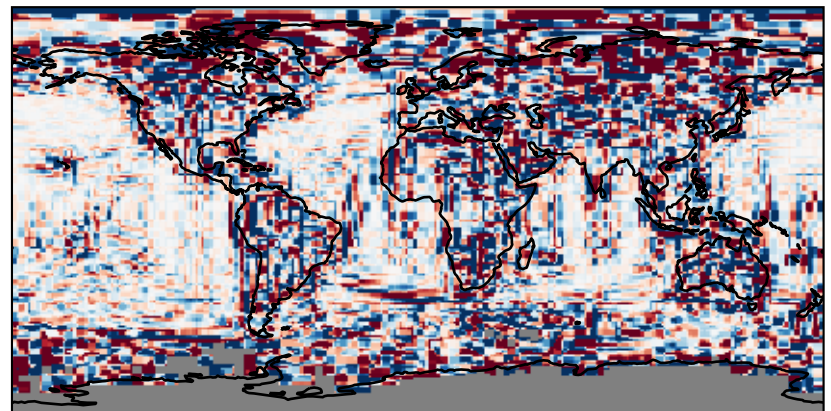
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

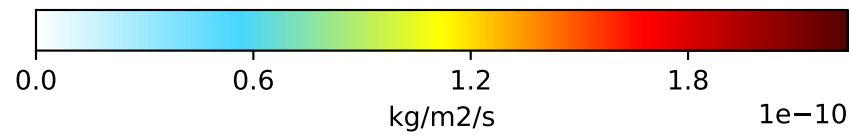
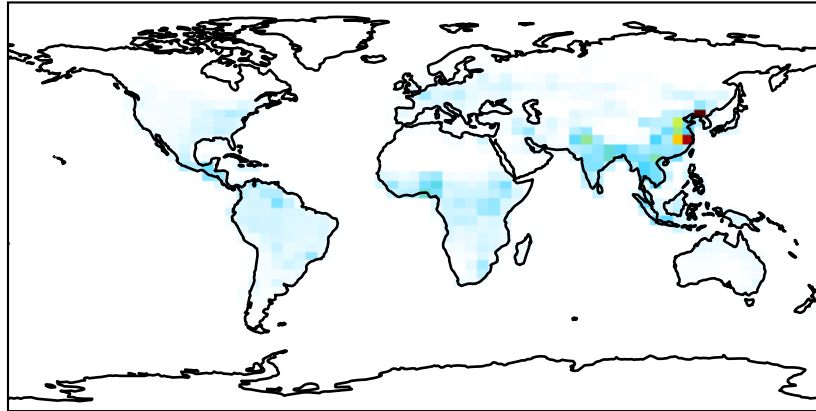


Ratio (1x1.25)  
Dev/Ref, Fixed Range

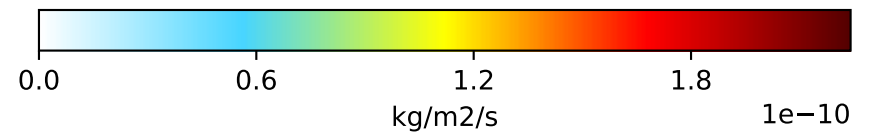
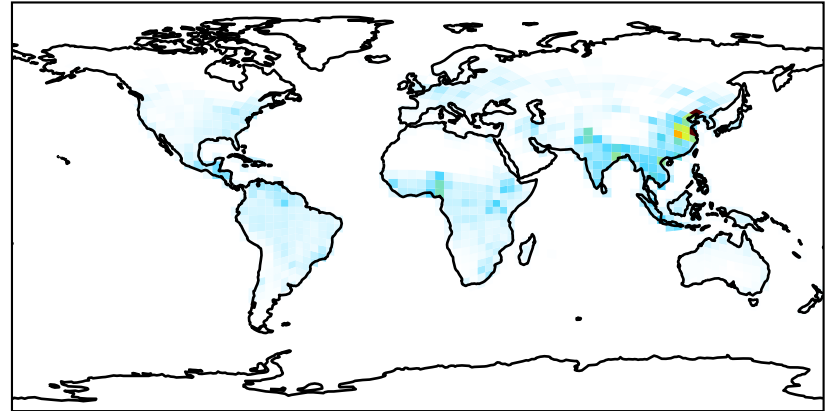


# EmisSOAP\_Total (Apr2019)

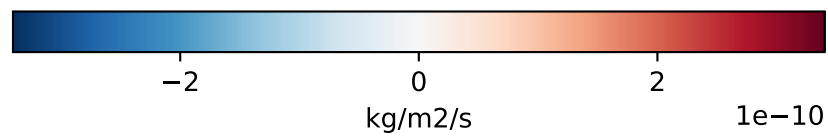
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



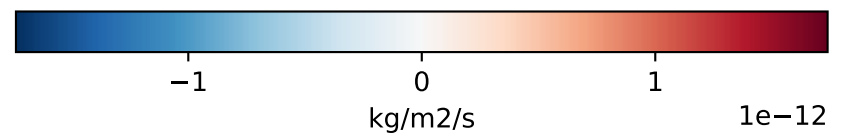
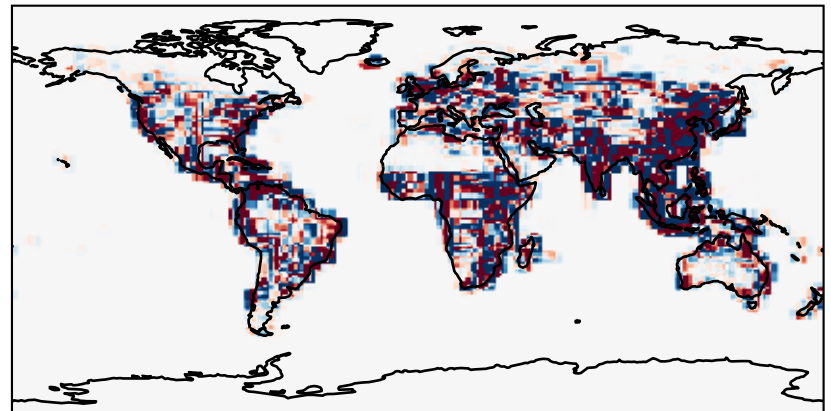
GCHP\_14.0.0-rc.3 (Dev)  
c24



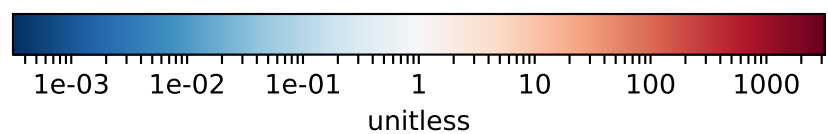
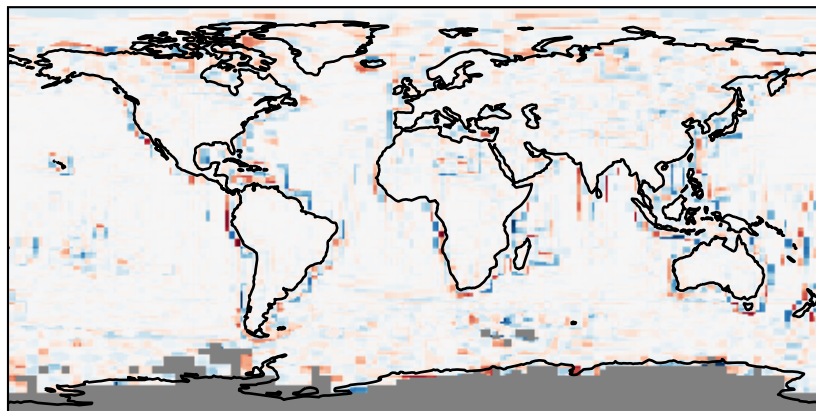
Difference (1x1.25)  
Dev - Ref, Dynamic Range



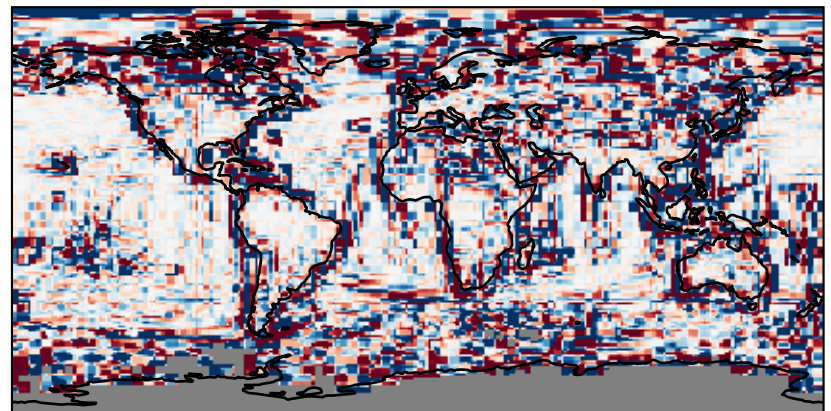
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

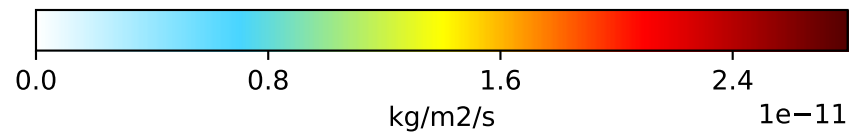
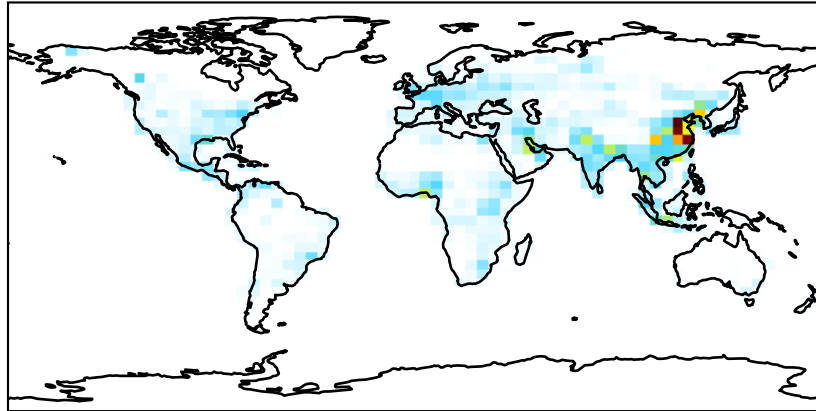


Ratio (1x1.25)  
Dev/Ref, Fixed Range

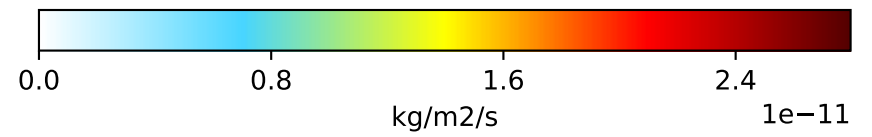
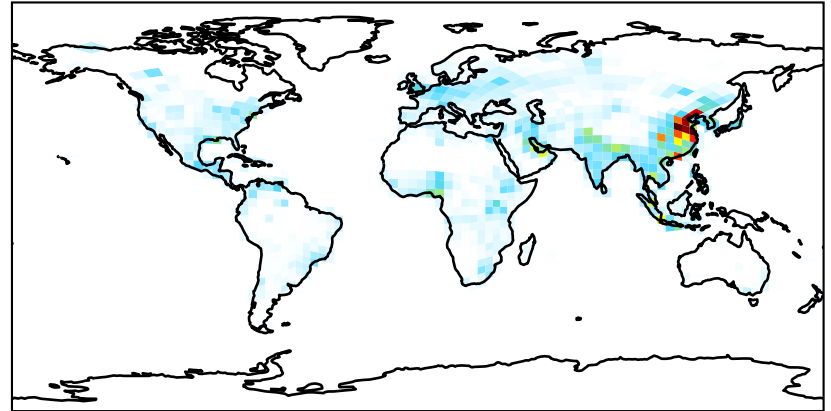


# EmisTOLU\_Total (Apr2019)

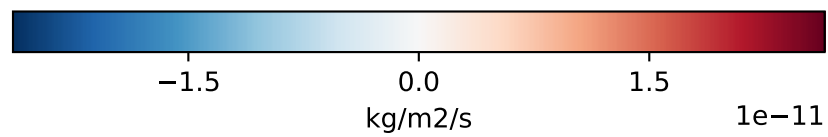
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



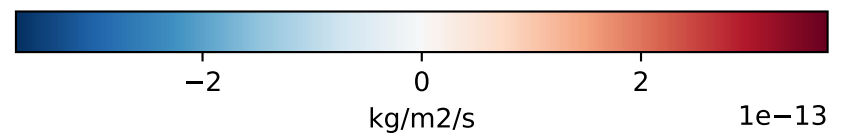
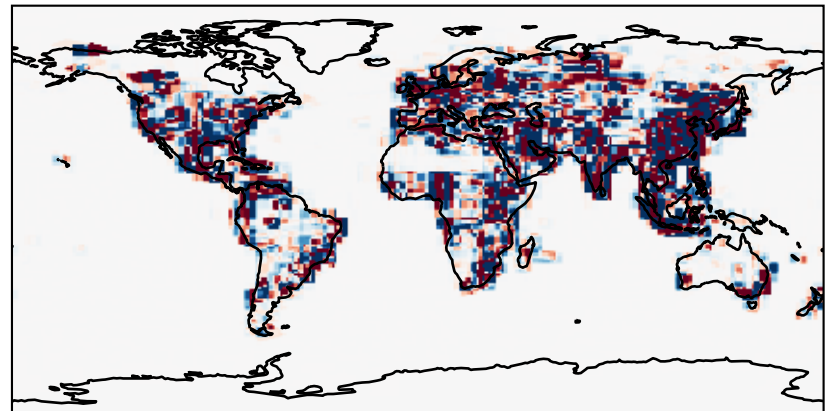
GCHP\_14.0.0-rc.3 (Dev)  
c24



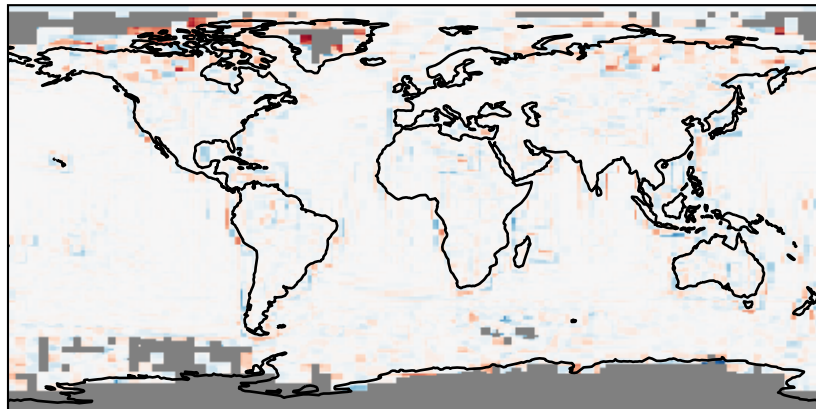
Difference (1x1.25)  
Dev - Ref, Dynamic Range



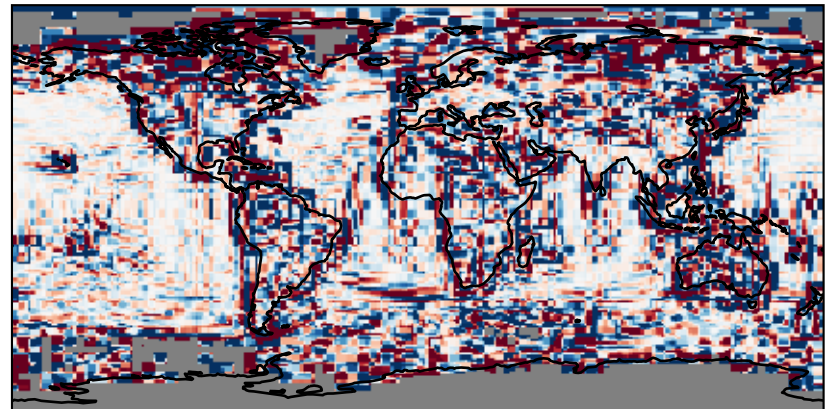
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

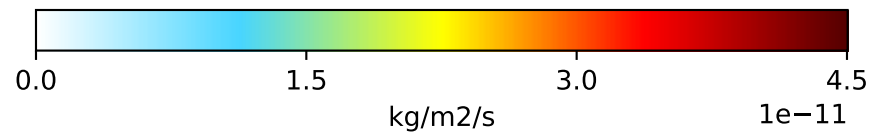
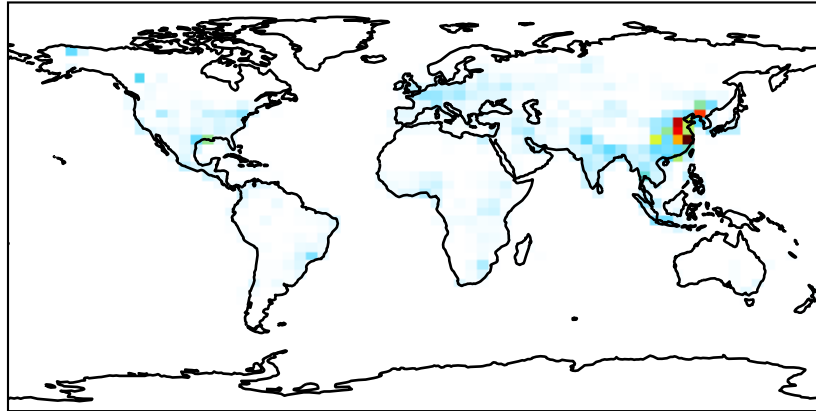


Ratio (1x1.25)  
Dev/Ref, Fixed Range

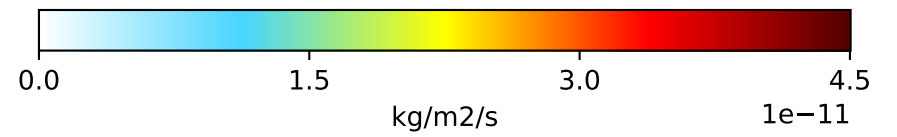
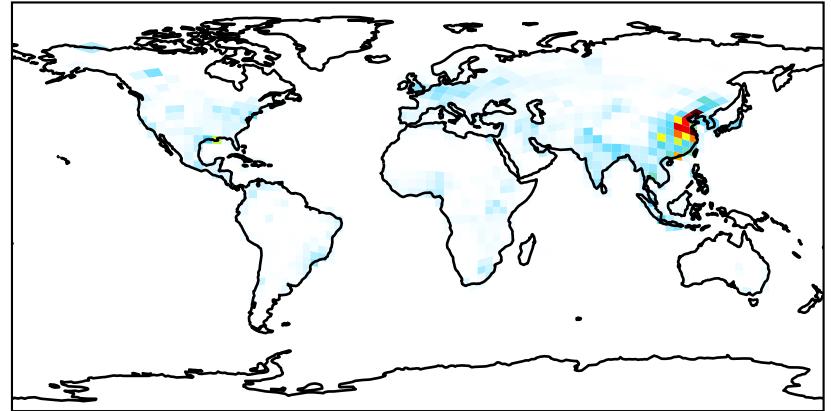


# EmisXYLE\_Total (Apr2019)

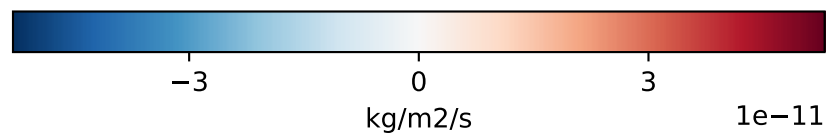
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



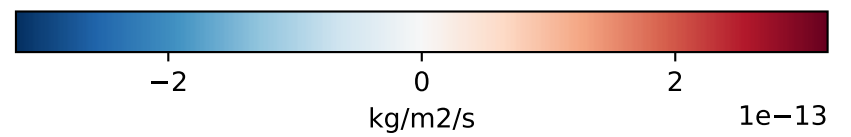
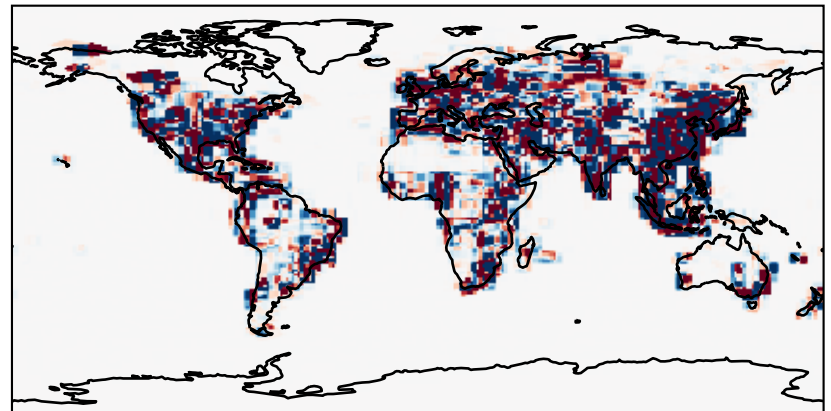
GCHP\_14.0.0-rc.3 (Dev)  
c24



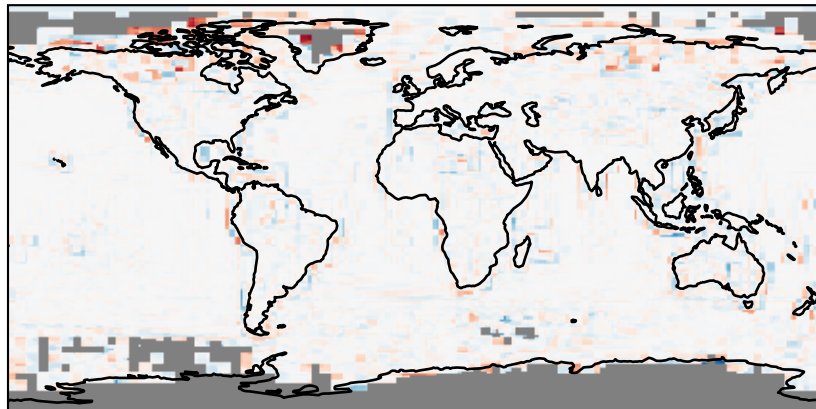
Difference (1x1.25)  
Dev - Ref, Dynamic Range



Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Ratio (1x1.25)  
Dev/Ref, Fixed Range

