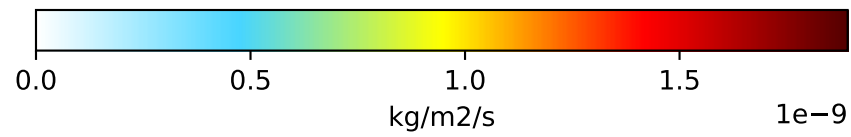
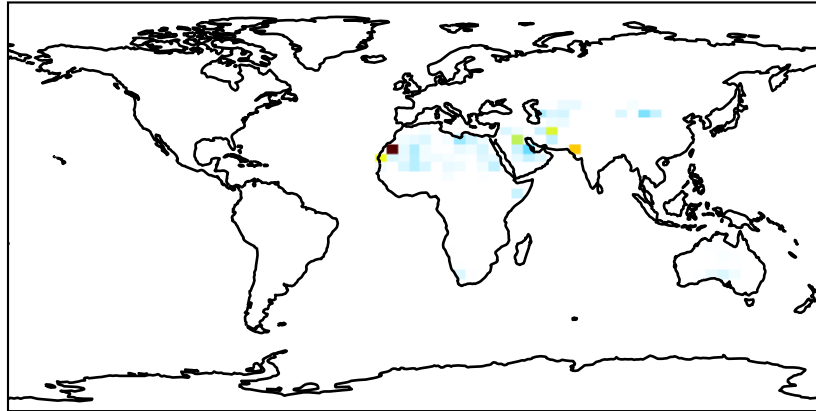
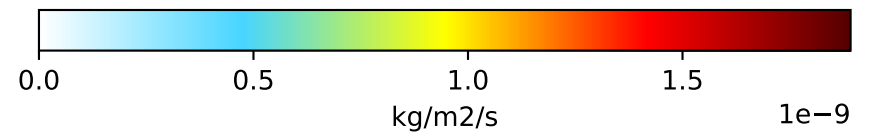
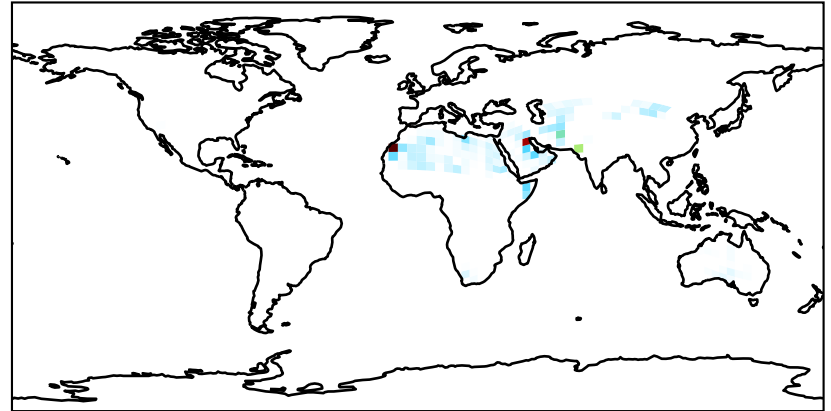


# EmisDST1\_Natural (Jul2019)

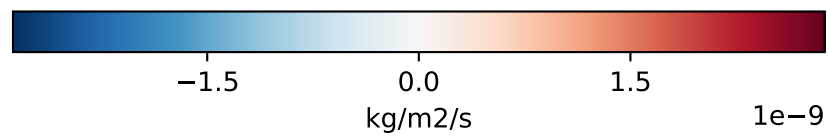
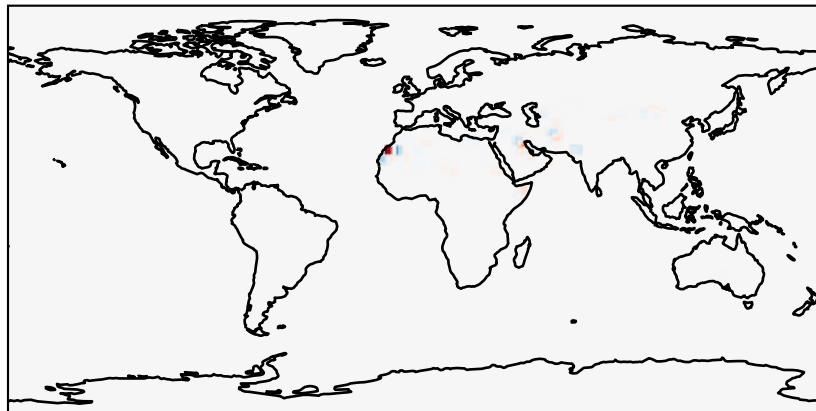
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



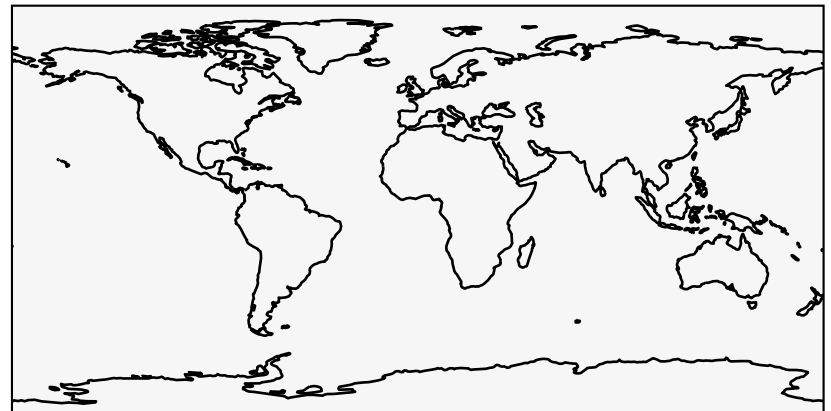
GCHP\_14.0.0-rc.3 (Dev)  
c24



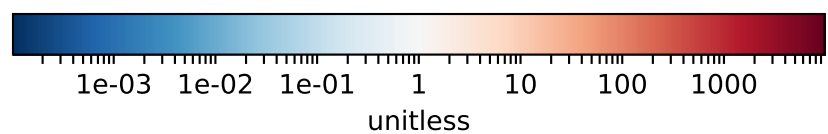
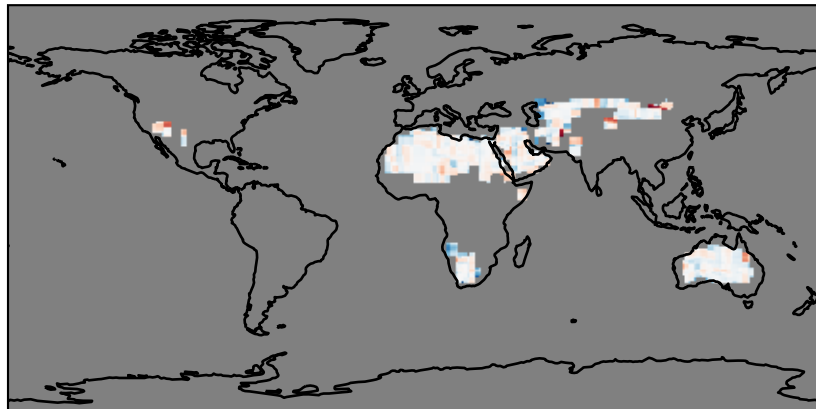
Difference (1x1.25)  
Dev - Ref, Dynamic Range



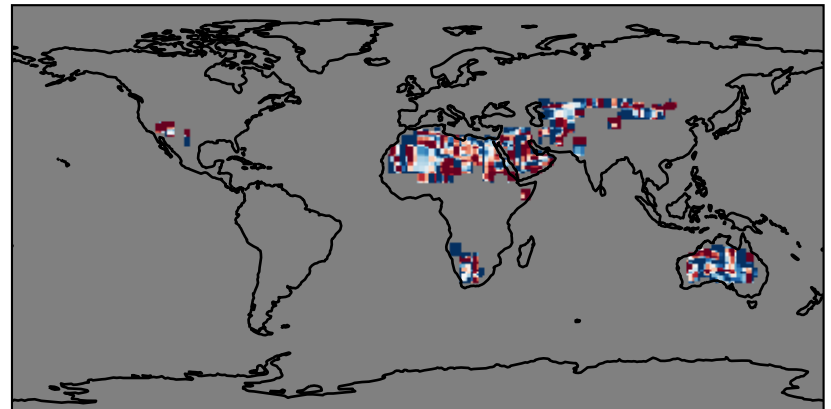
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

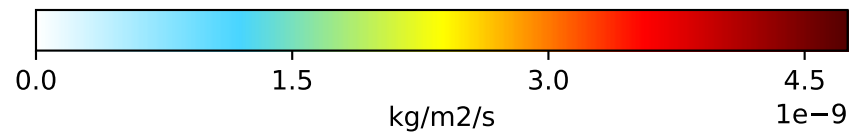
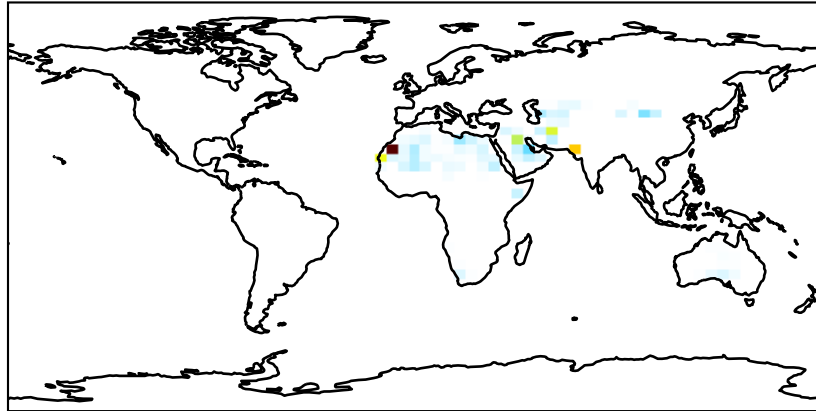


Ratio (1x1.25)  
Dev/Ref, Fixed Range

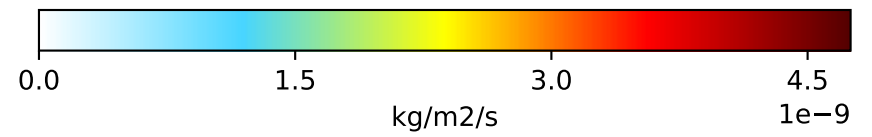
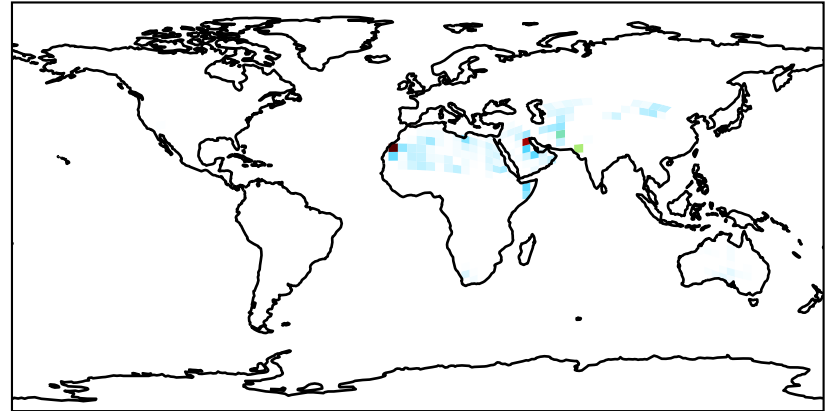


# EmisDST2\_Natural (Jul2019)

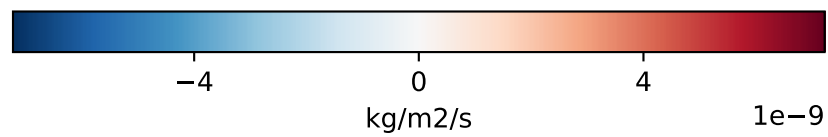
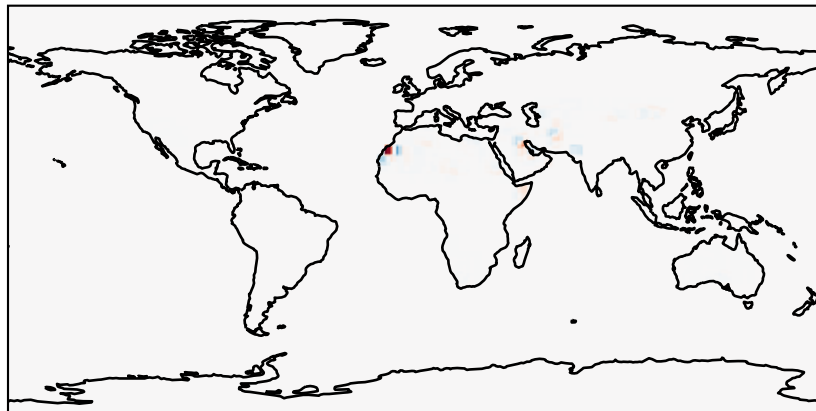
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



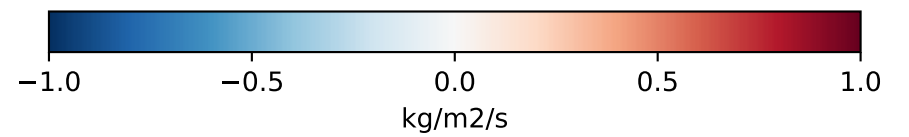
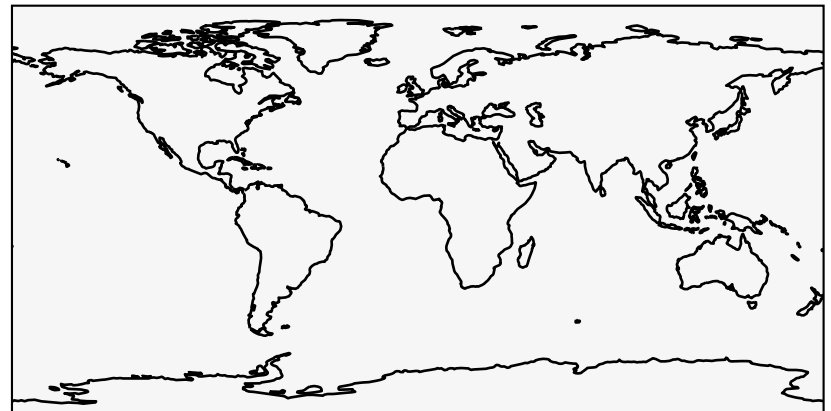
GCHP\_14.0.0-rc.3 (Dev)  
c24



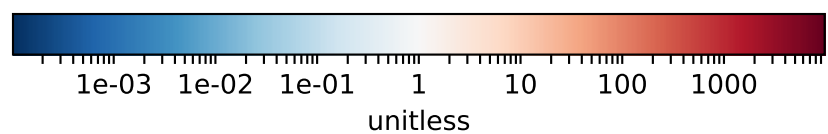
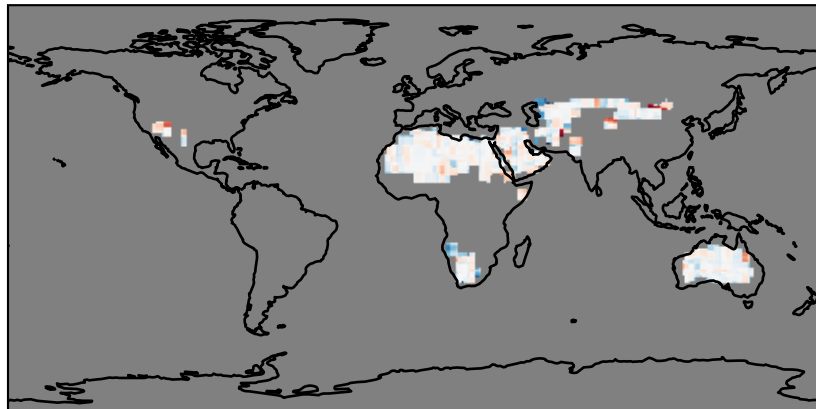
Difference (1x1.25)  
Dev - Ref, Dynamic Range



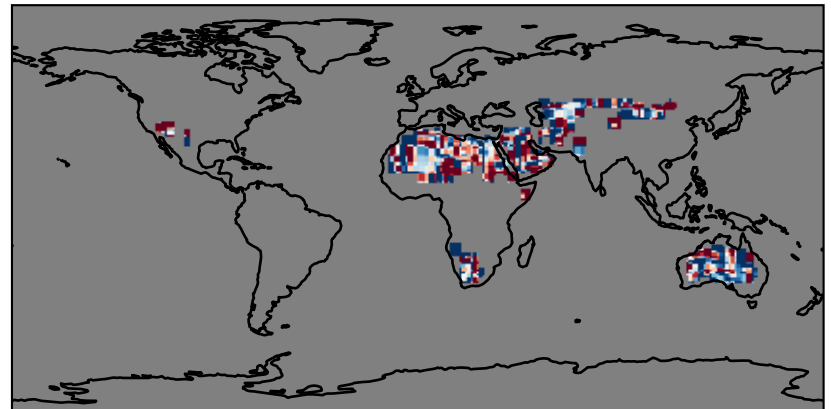
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

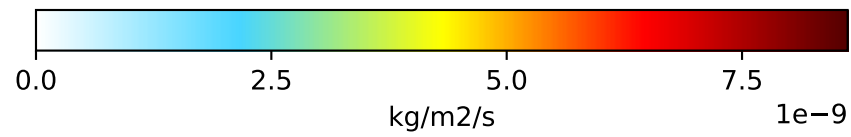
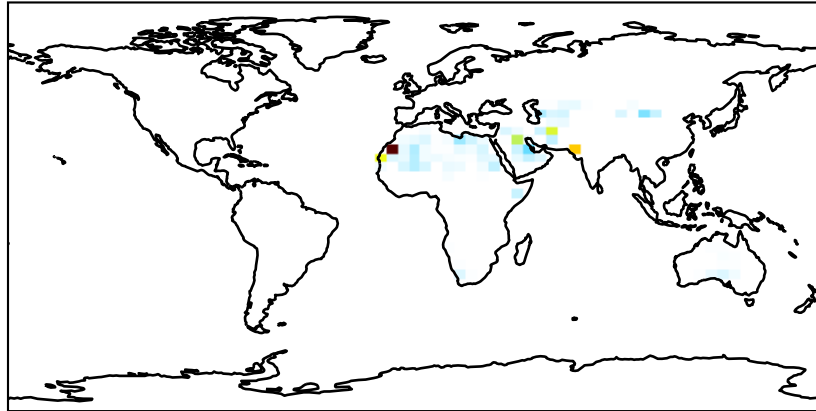


Ratio (1x1.25)  
Dev/Ref, Fixed Range

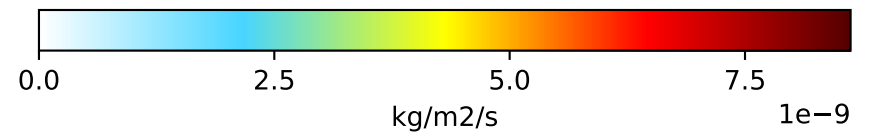
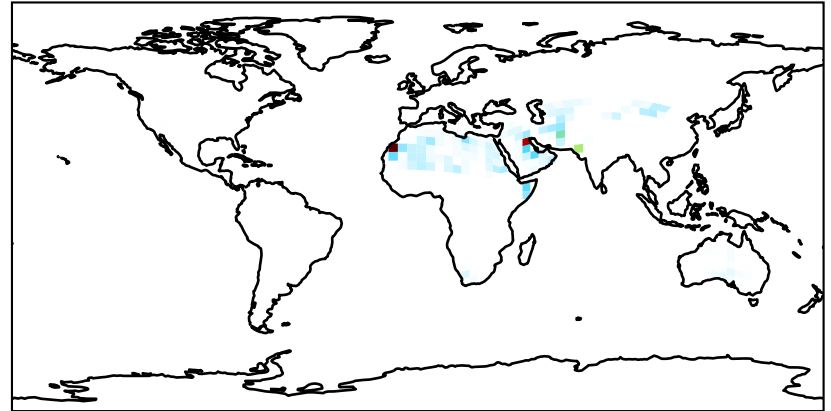


# EmisDST3\_Natural (Jul2019)

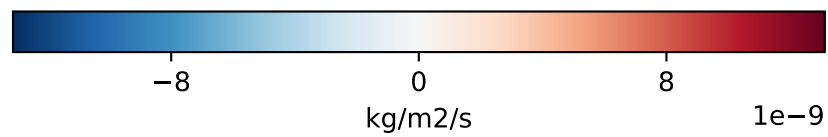
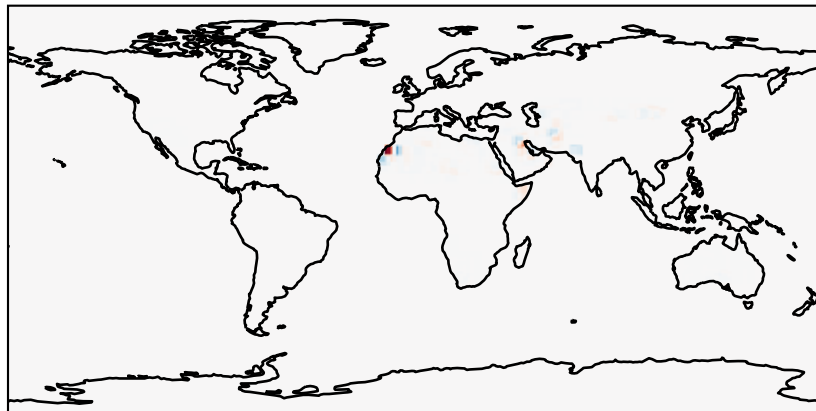
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



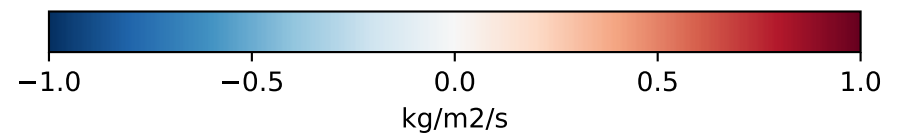
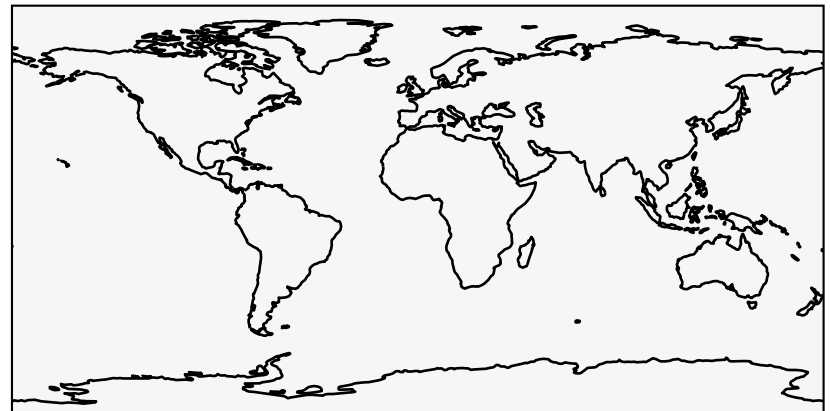
GCHP\_14.0.0-rc.3 (Dev)  
c24



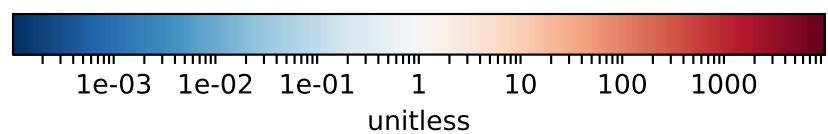
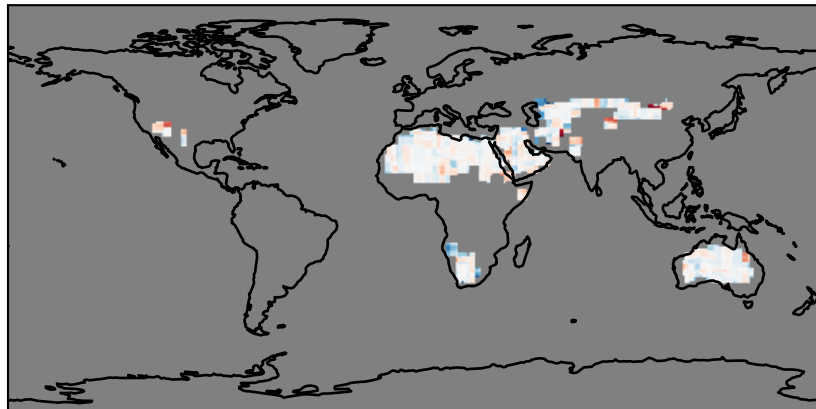
Difference (1x1.25)  
Dev - Ref, Dynamic Range



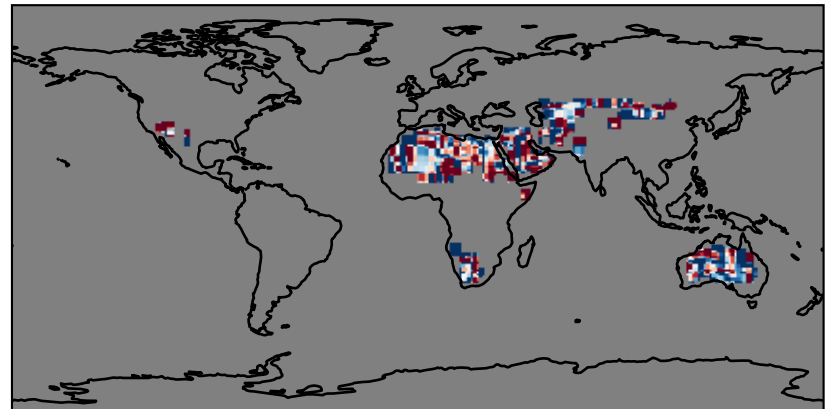
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

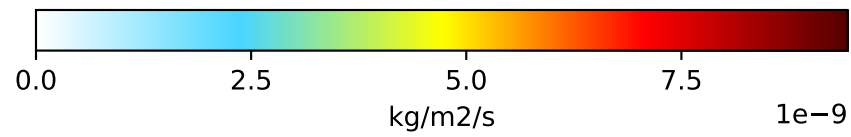
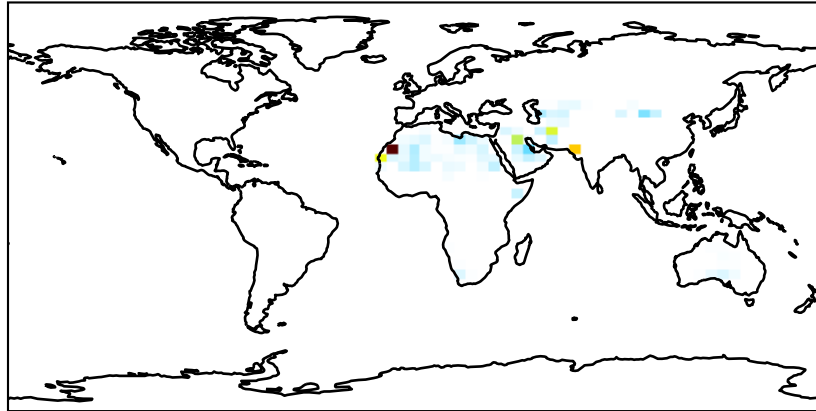


Ratio (1x1.25)  
Dev/Ref, Fixed Range

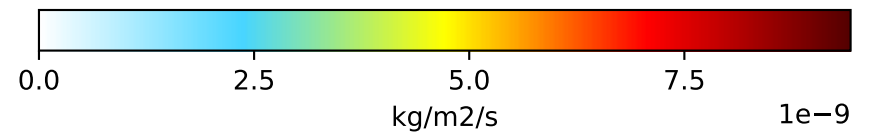
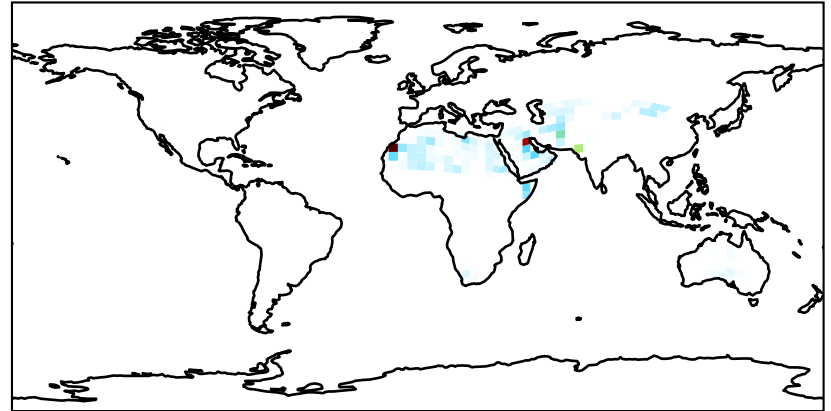


# EmisDST4\_Natural (Jul2019)

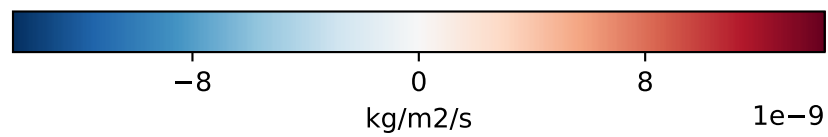
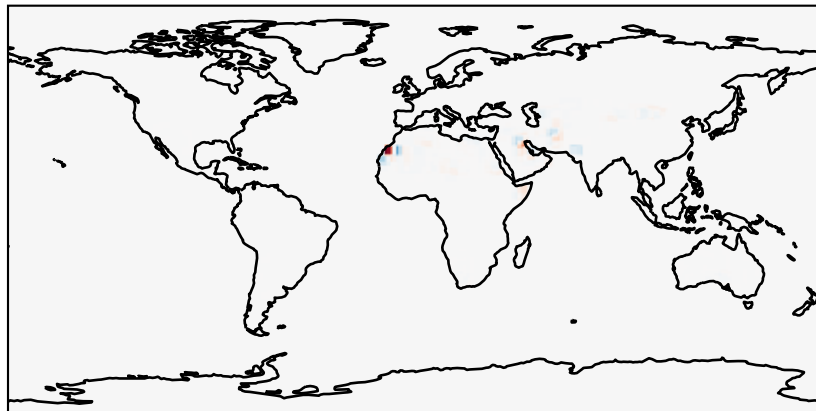
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



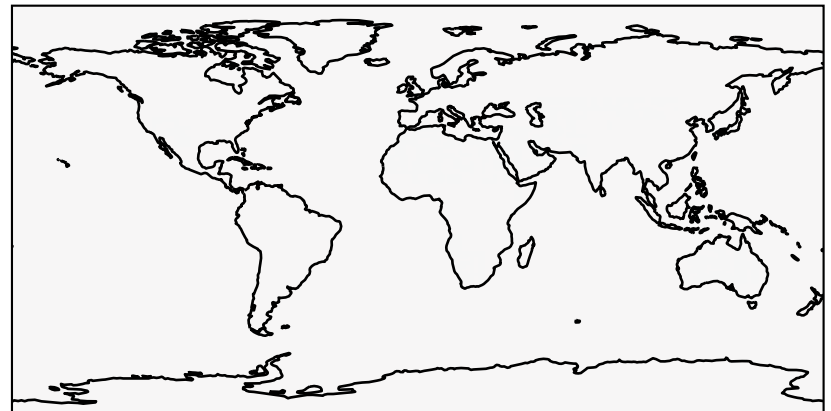
GCHP\_14.0.0-rc.3 (Dev)  
c24



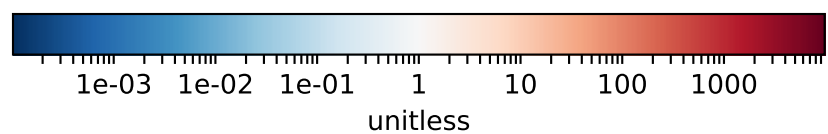
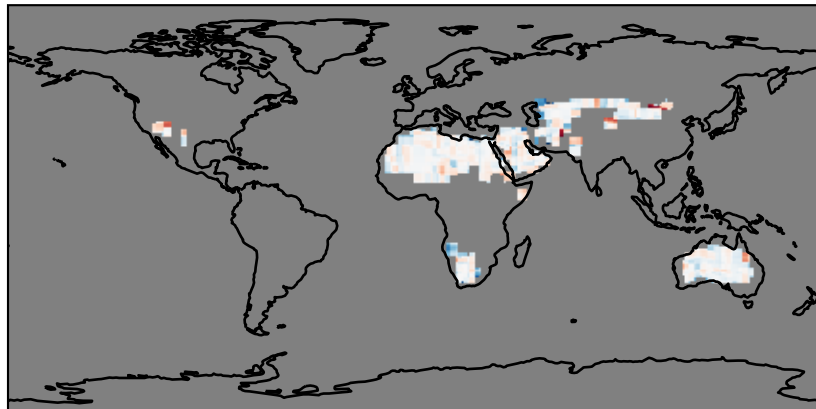
Difference (1x1.25)  
Dev - Ref, Dynamic Range



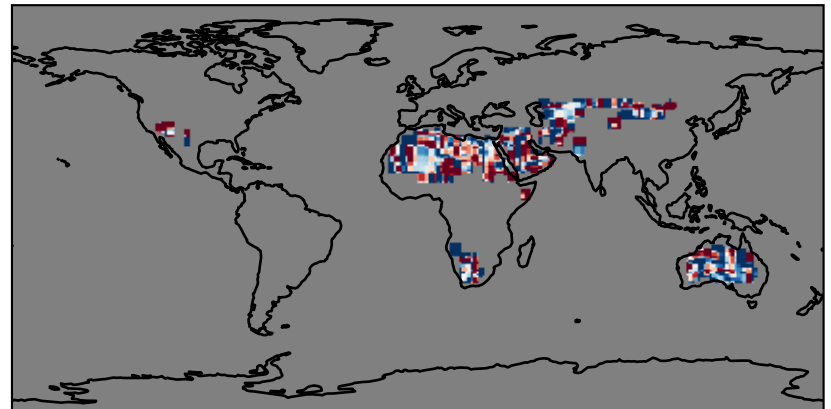
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

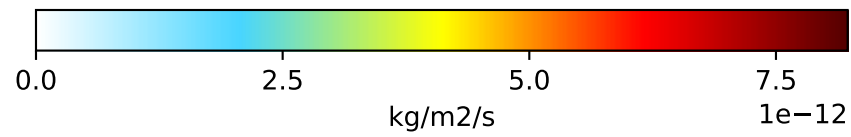
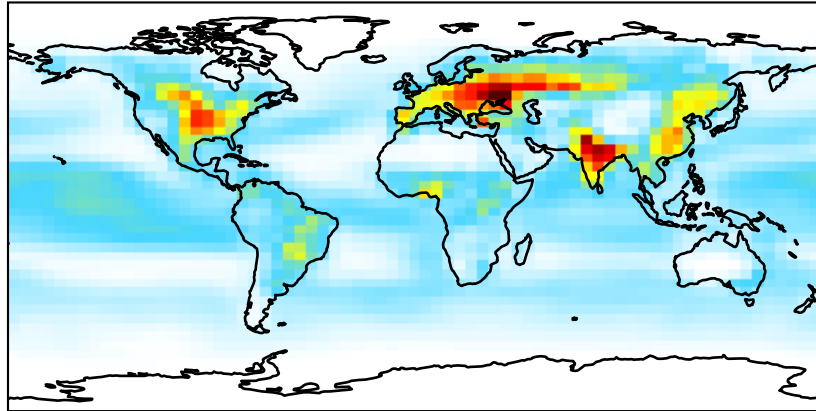


Ratio (1x1.25)  
Dev/Ref, Fixed Range

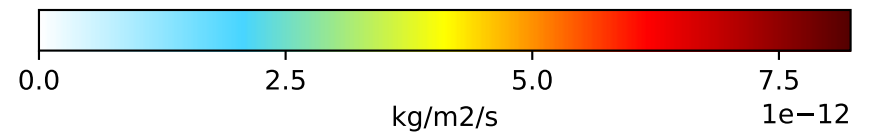
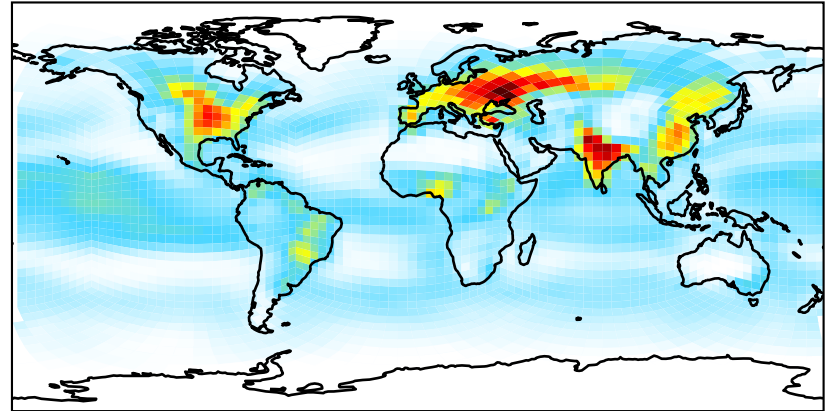


# EmisNH3\_Natural (Jul2019)

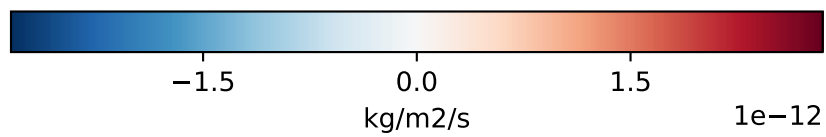
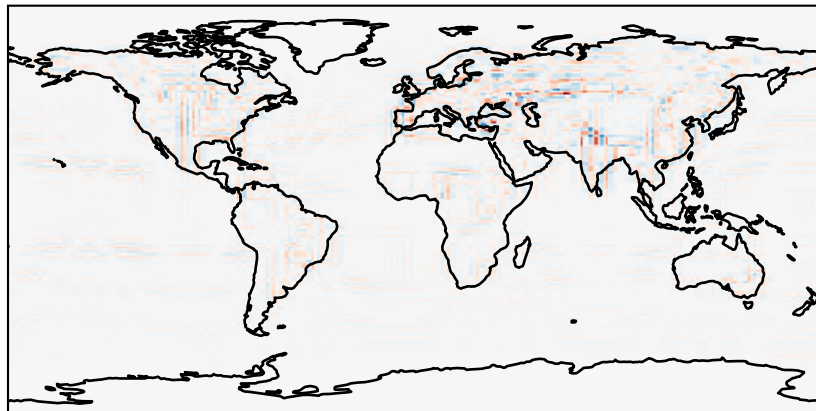
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



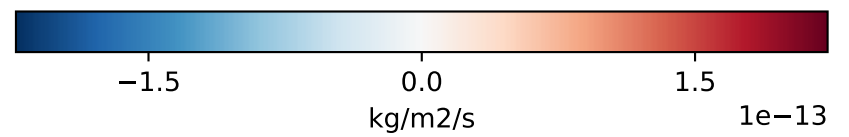
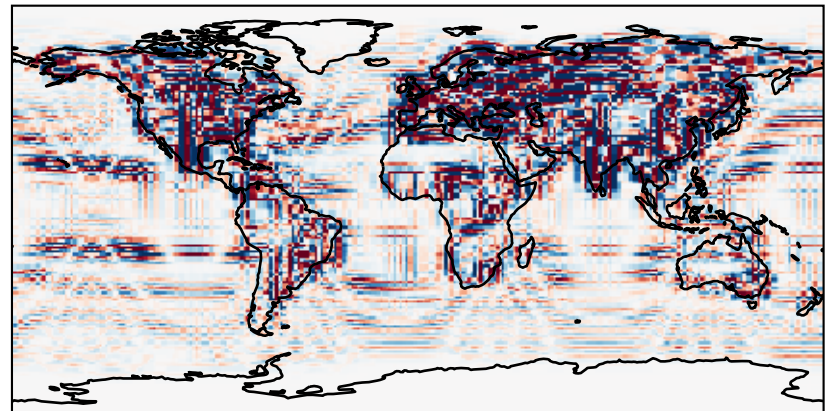
GCHP\_14.0.0-rc.3 (Dev)  
c24



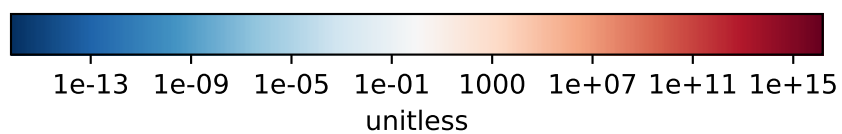
Difference (1x1.25)  
Dev - Ref, Dynamic Range



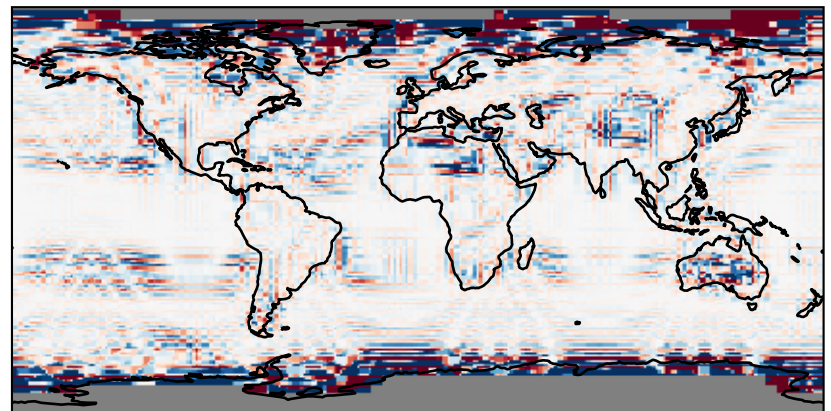
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

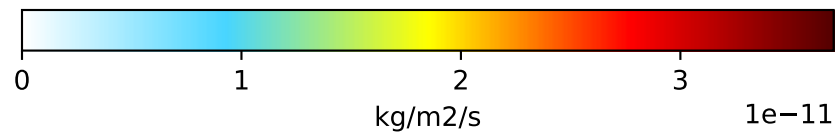
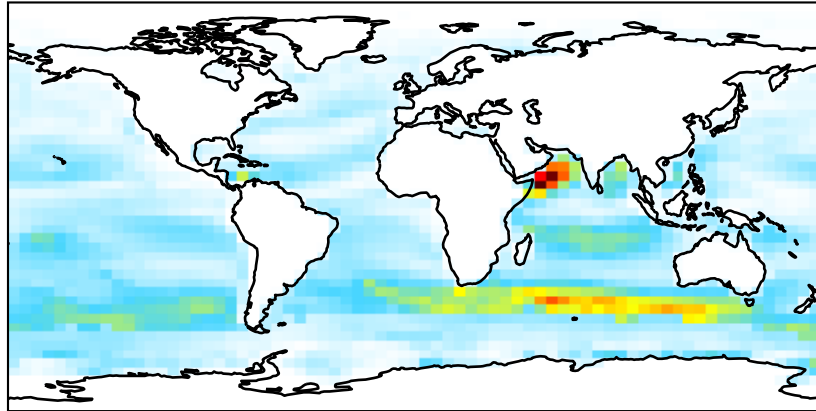


Ratio (1x1.25)  
Dev/Ref, Fixed Range

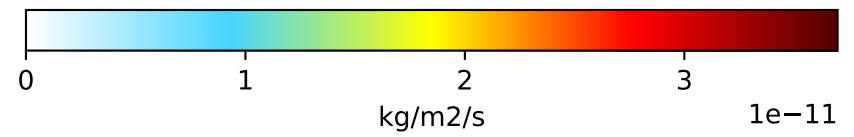
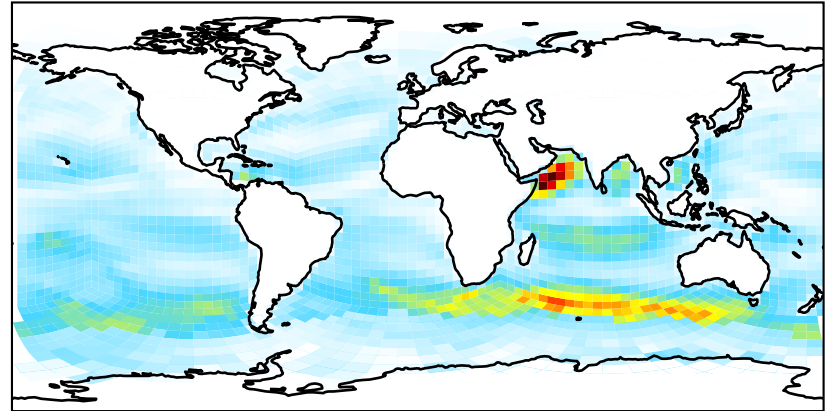


# EmisSALA\_Natural (Jul2019)

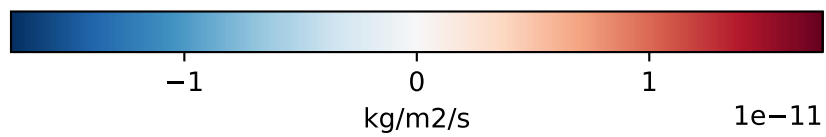
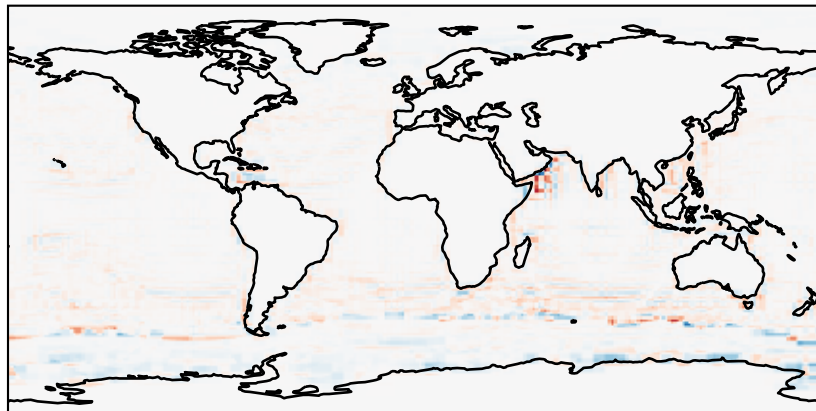
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



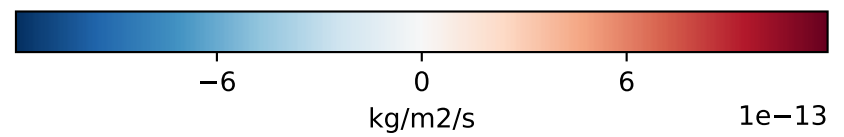
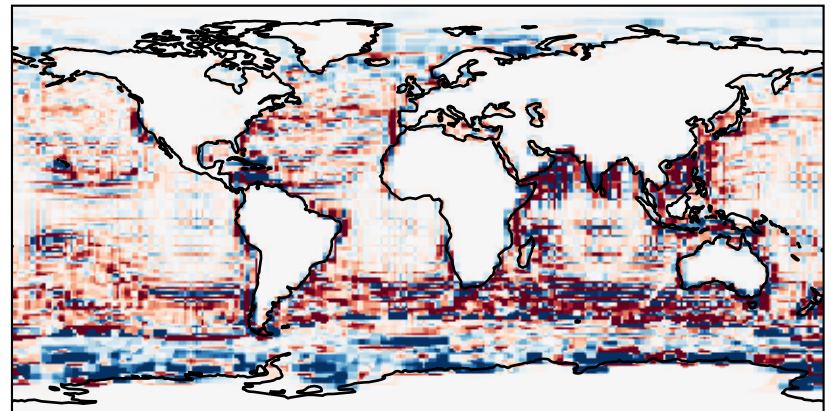
GCHP\_14.0.0-rc.3 (Dev)  
c24



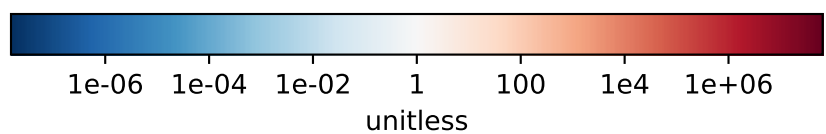
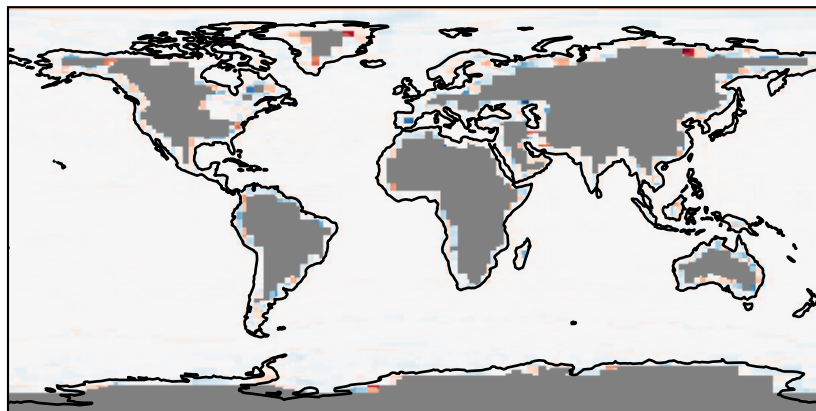
Difference (1x1.25)  
Dev - Ref, Dynamic Range



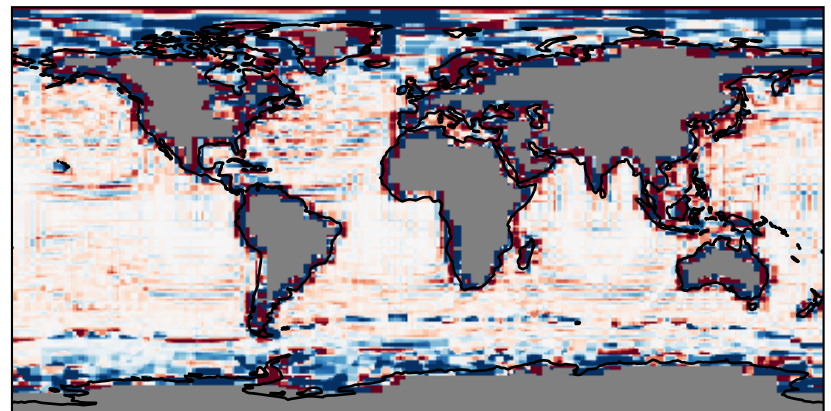
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

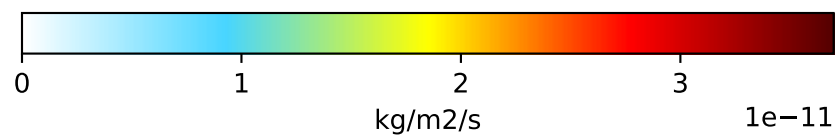
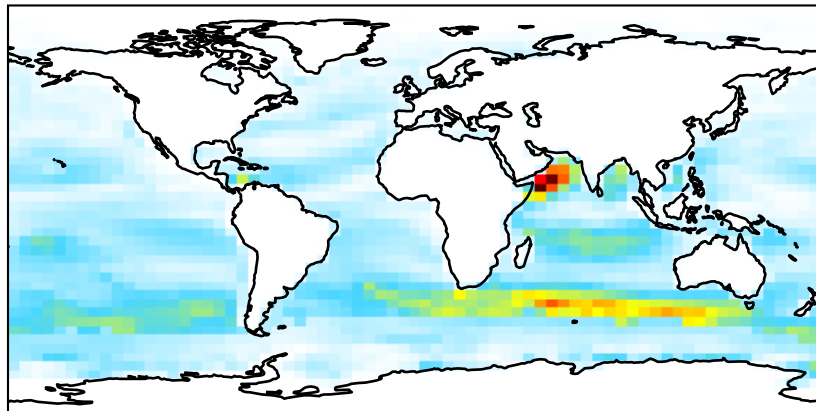


Ratio (1x1.25)  
Dev/Ref, Fixed Range

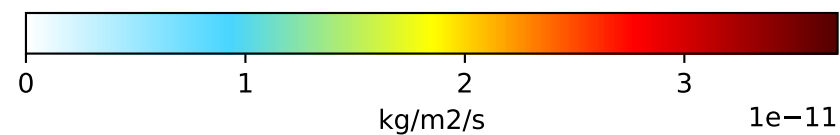
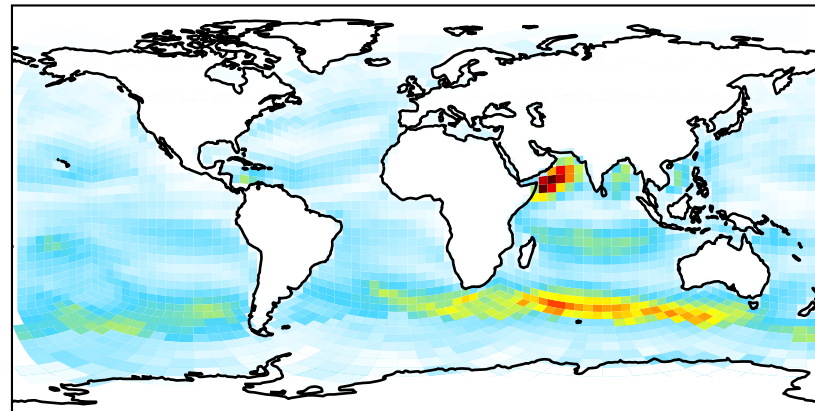


# EmisSALAAL\_Natural (Jul2019)

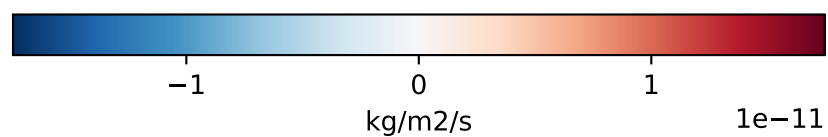
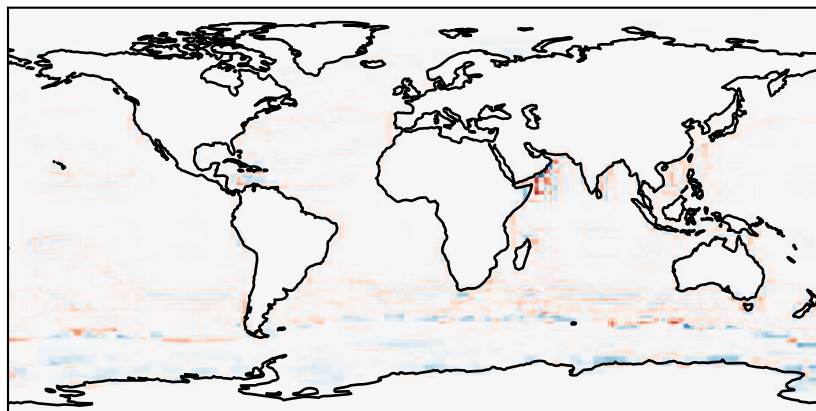
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



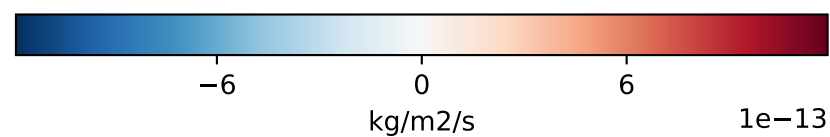
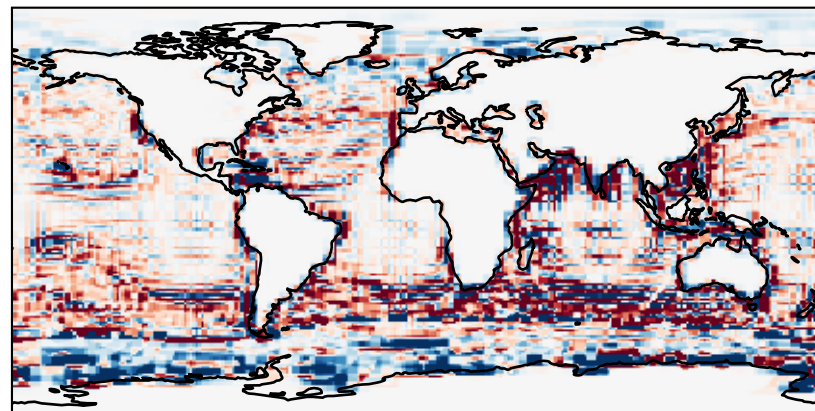
GCHP\_14.0.0-rc.3 (Dev)  
c24



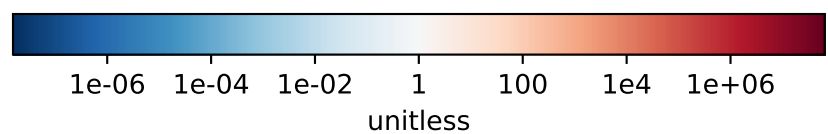
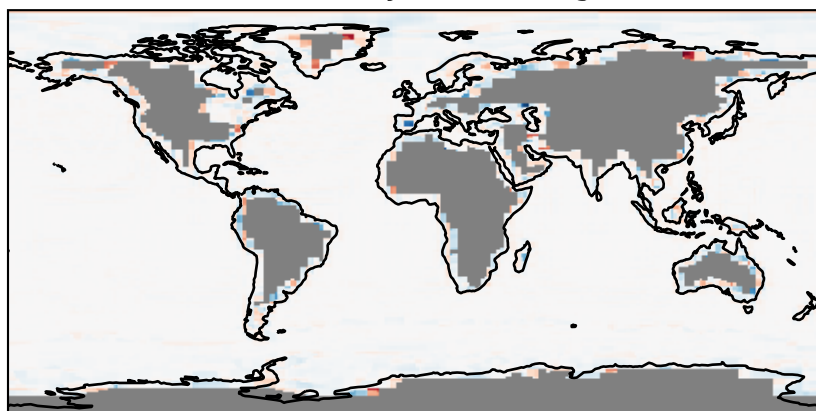
Difference (1x1.25)  
Dev - Ref, Dynamic Range



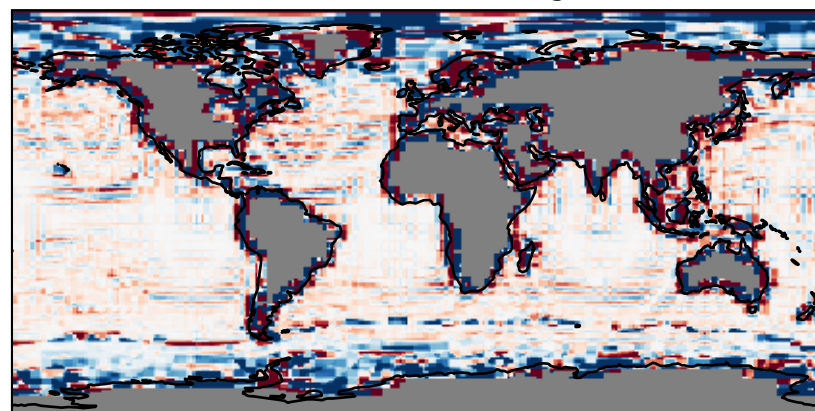
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

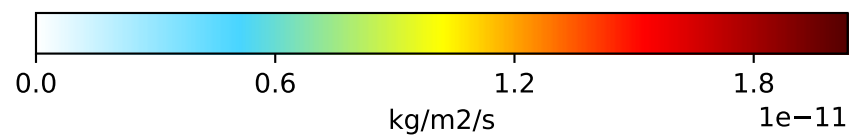
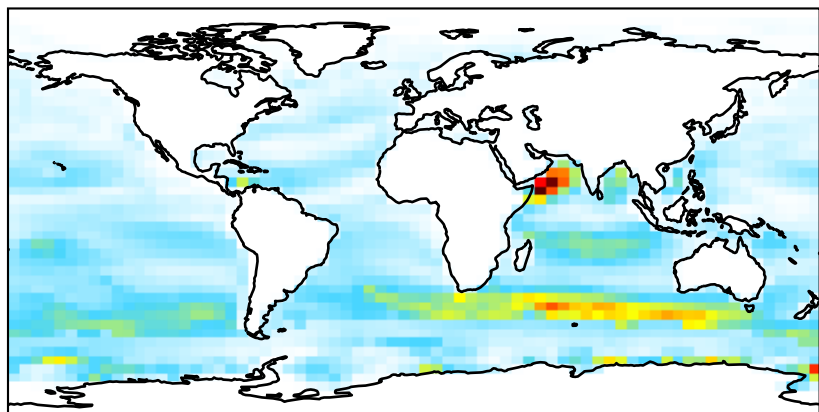


Ratio (1x1.25)  
Dev/Ref, Fixed Range

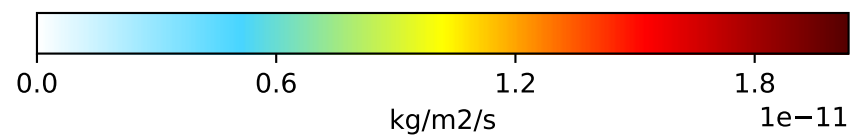
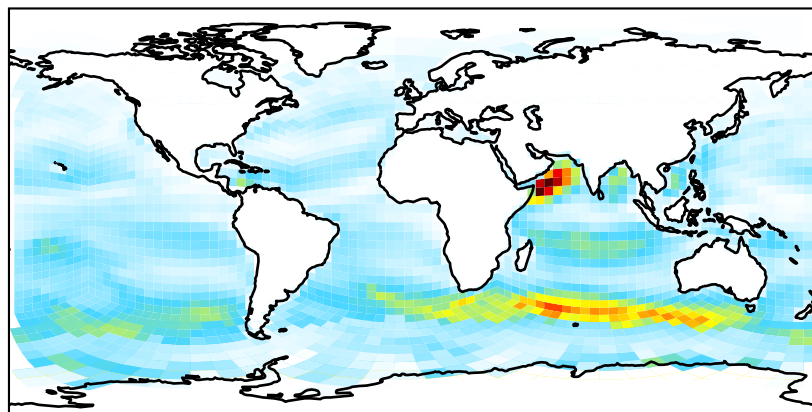


# EmisSALACL\_Natural (Jul2019)

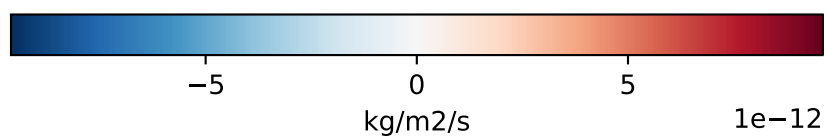
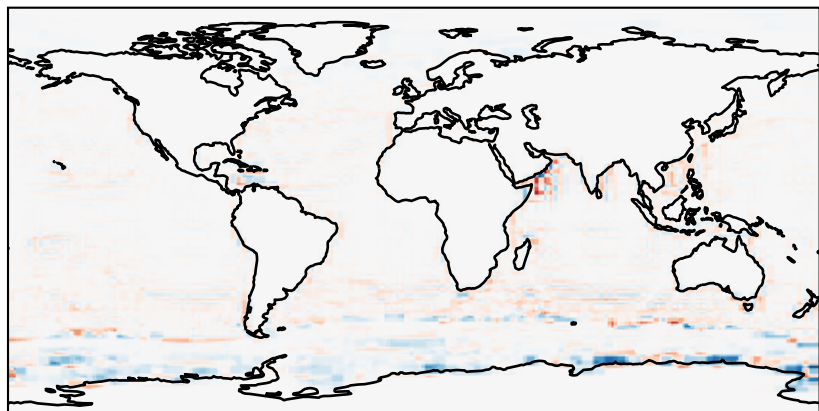
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



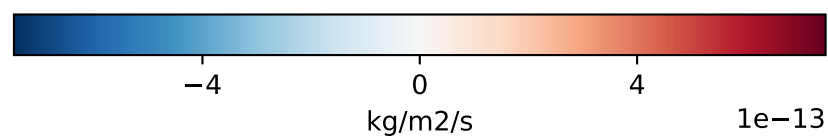
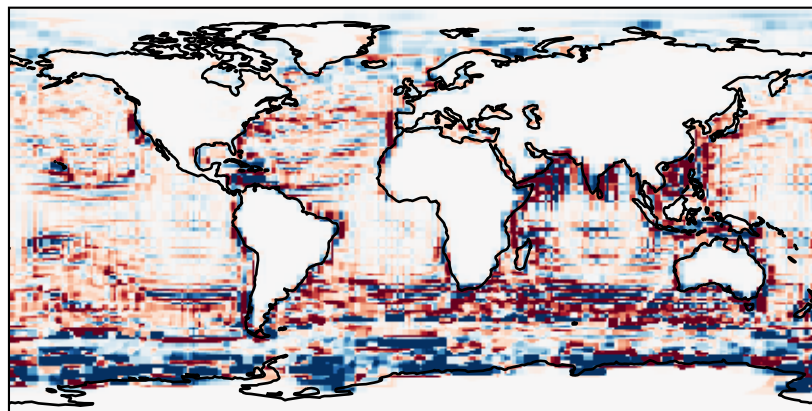
GCHP\_14.0.0-rc.3 (Dev)  
c24



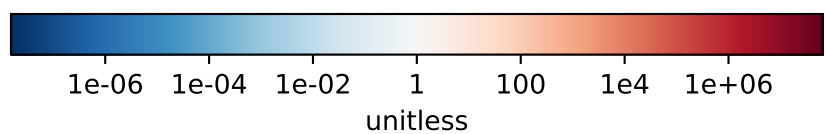
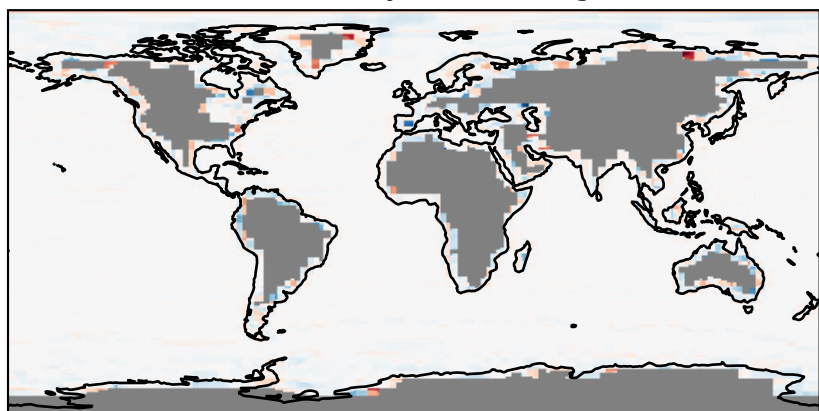
Difference (1x1.25)  
Dev - Ref, Dynamic Range



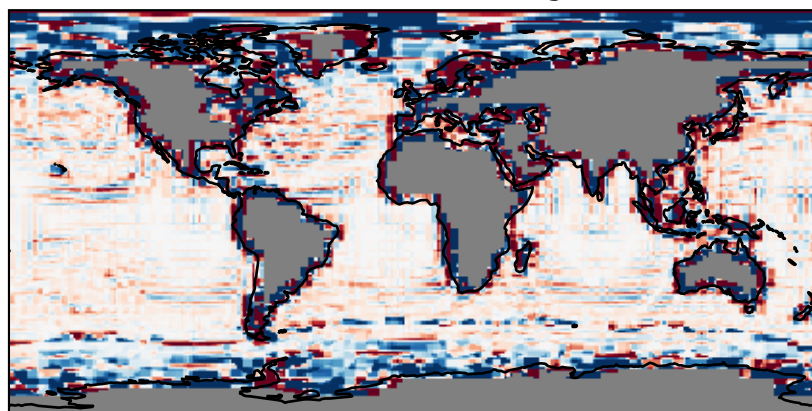
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



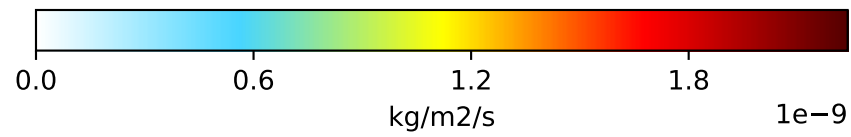
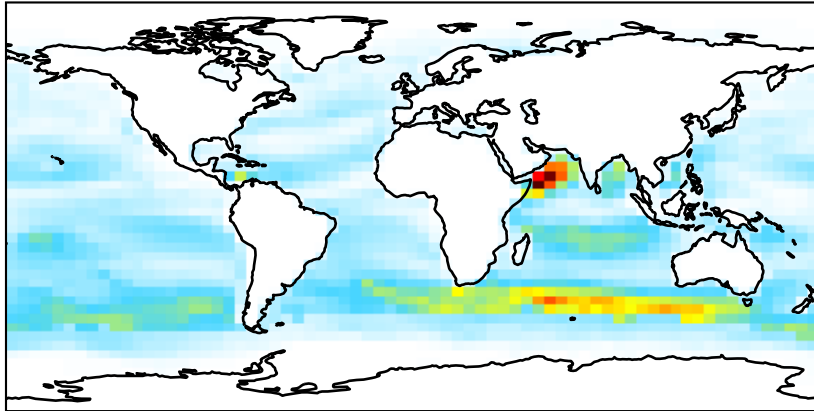
Ratio (1x1.25)  
Dev/Ref, Fixed Range



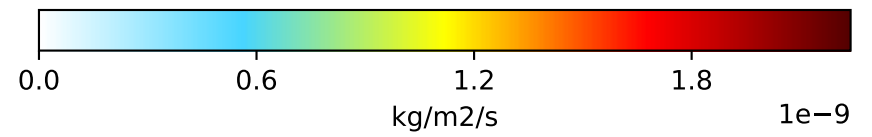
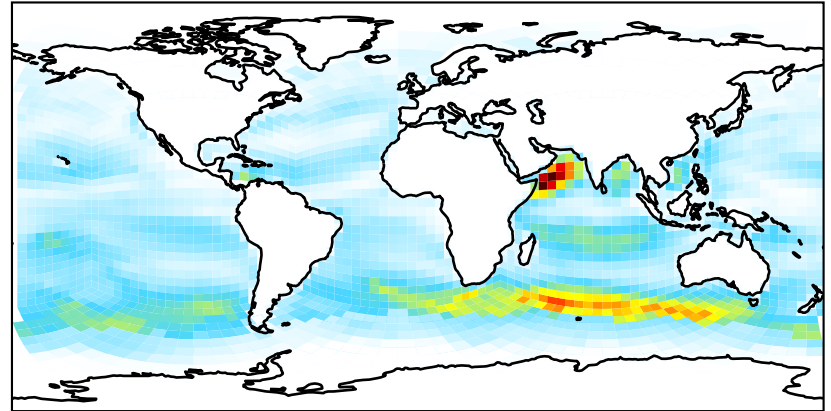


# EmisSALC\_Natural (Jul2019)

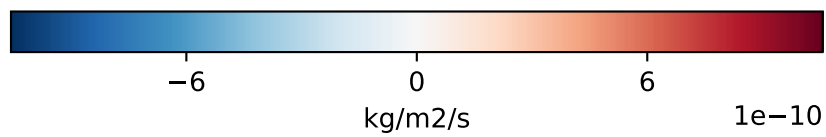
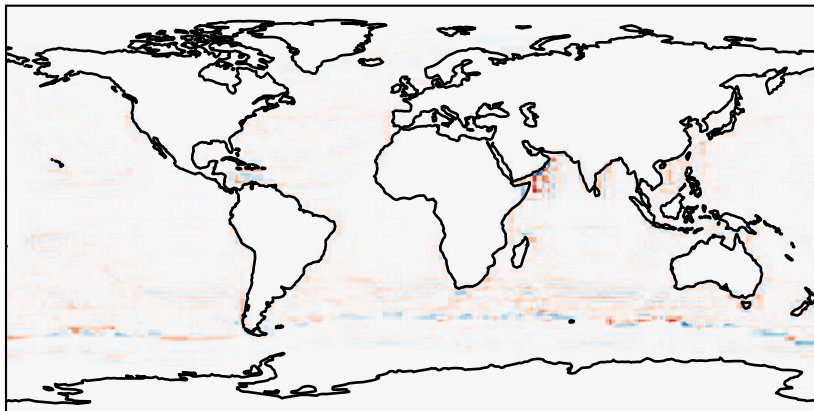
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



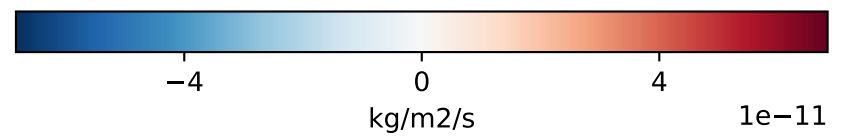
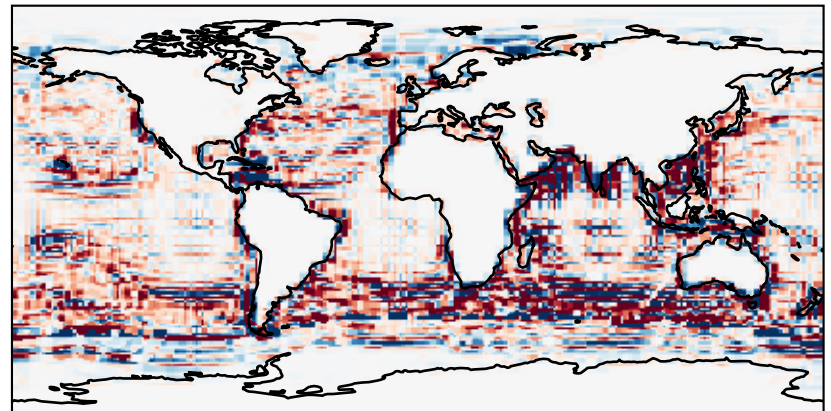
GCHP\_14.0.0-rc.3 (Dev)  
c24



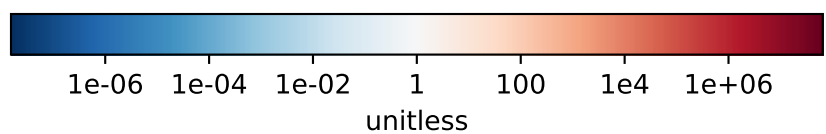
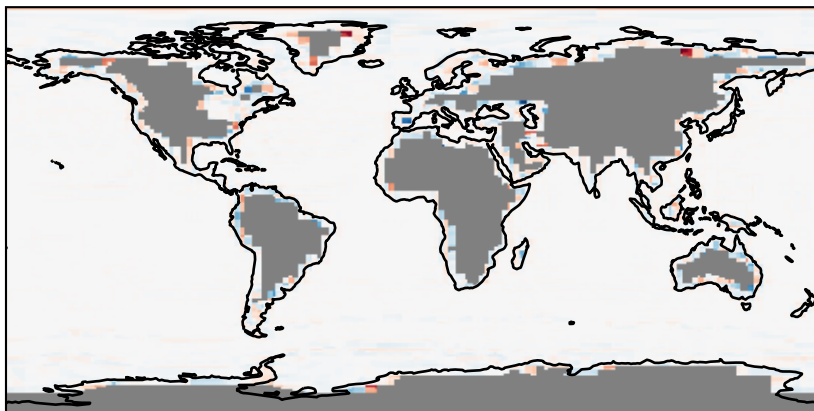
Difference (1x1.25)  
Dev - Ref, Dynamic Range



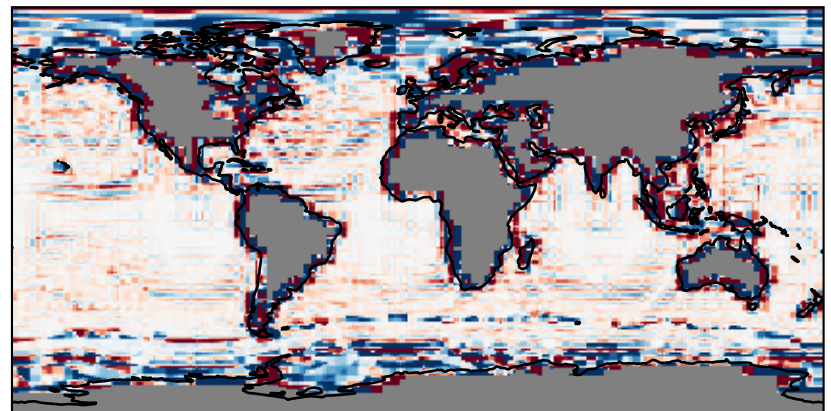
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

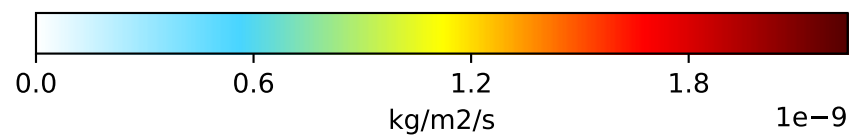
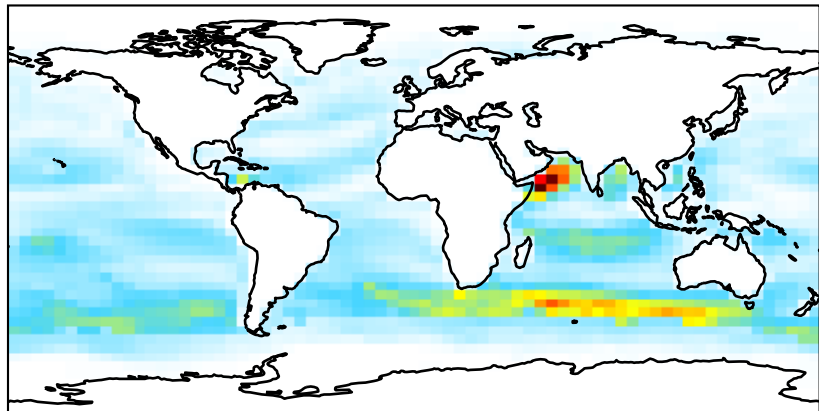


Ratio (1x1.25)  
Dev/Ref, Fixed Range

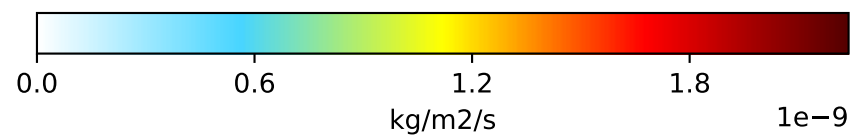
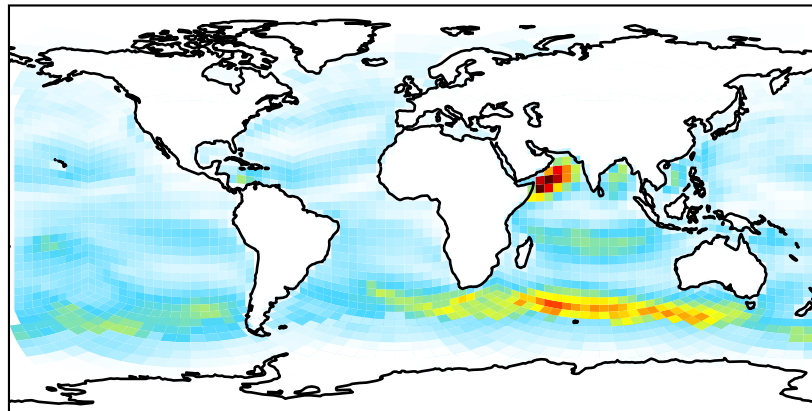


# EmisSALCAL\_Natural (Jul2019)

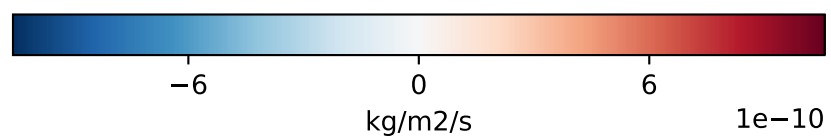
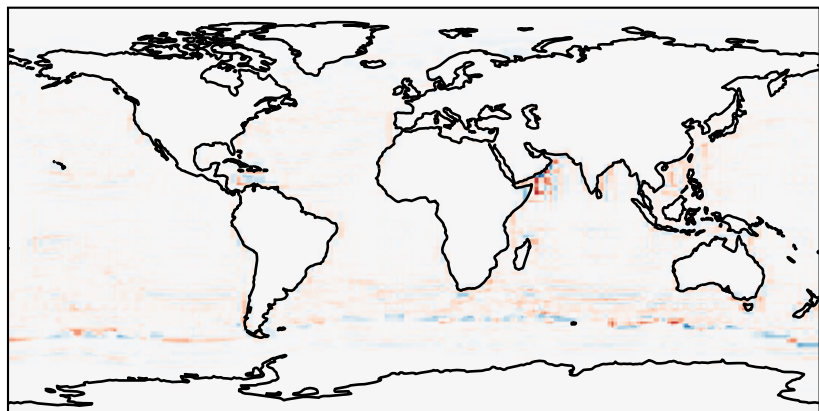
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



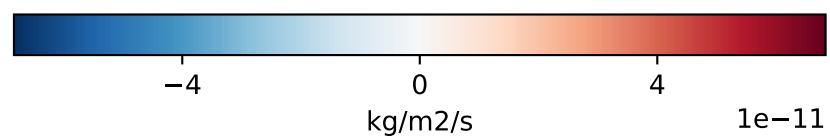
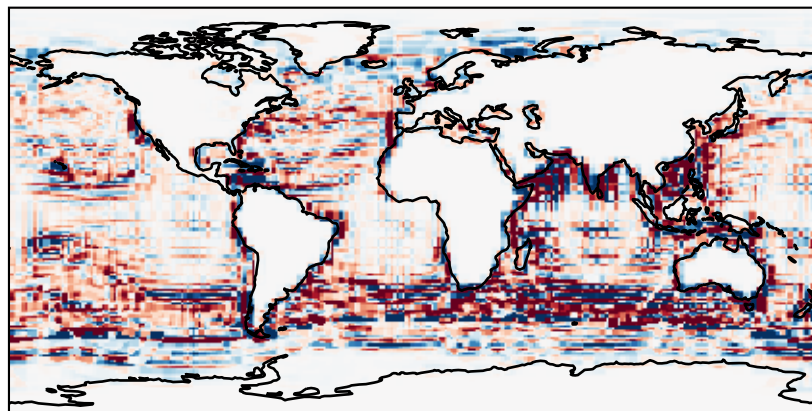
GCHP\_14.0.0-rc.3 (Dev)  
c24



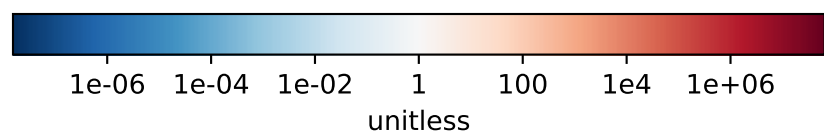
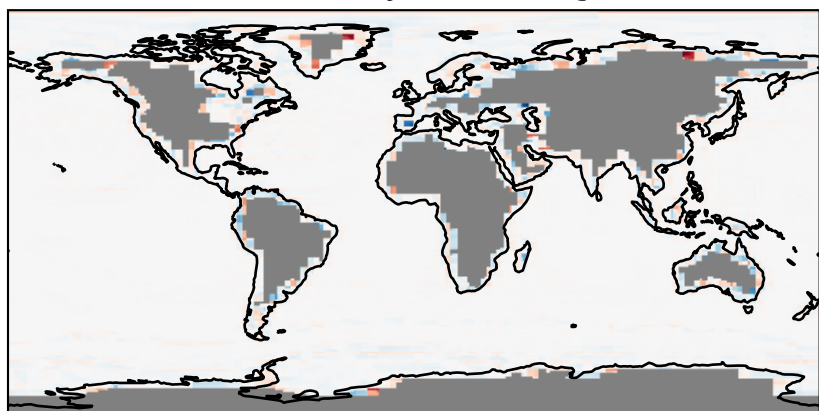
Difference (1x1.25)  
Dev - Ref, Dynamic Range



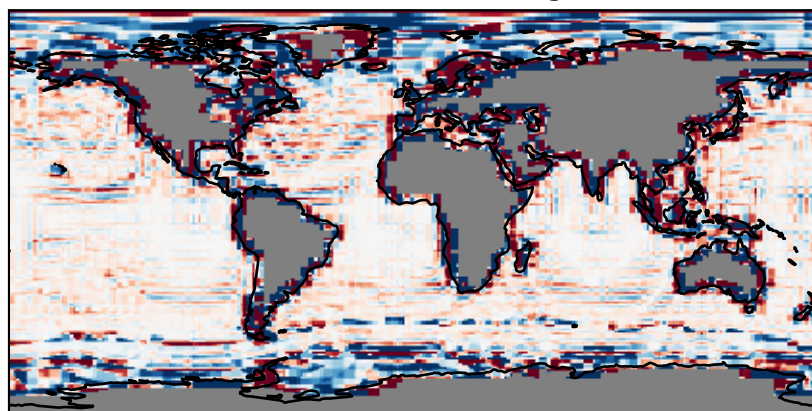
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

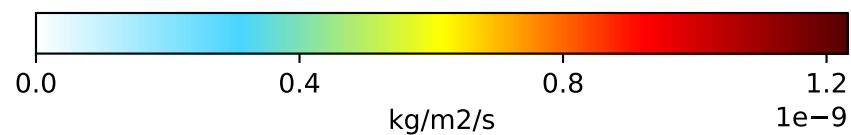
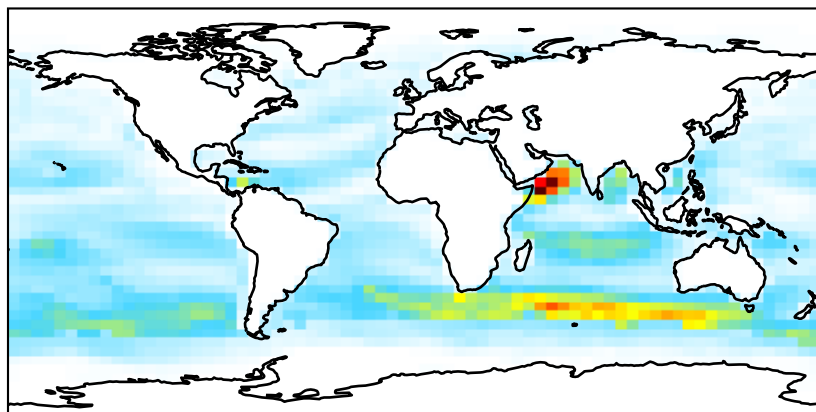


Ratio (1x1.25)  
Dev/Ref, Fixed Range

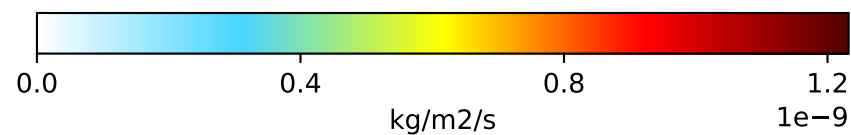
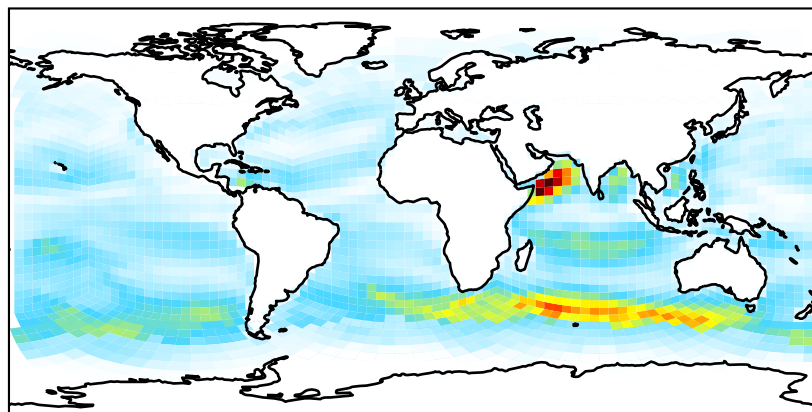


# EmisSALCCL\_Natural (Jul2019)

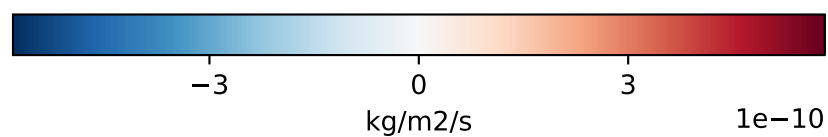
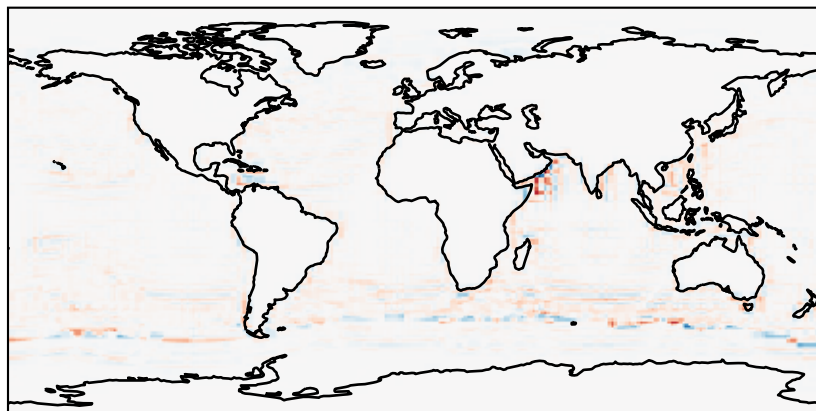
GCC\_14.0.0-rc.3 (Ref)  
4.0x5.0



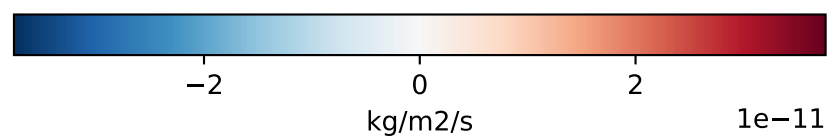
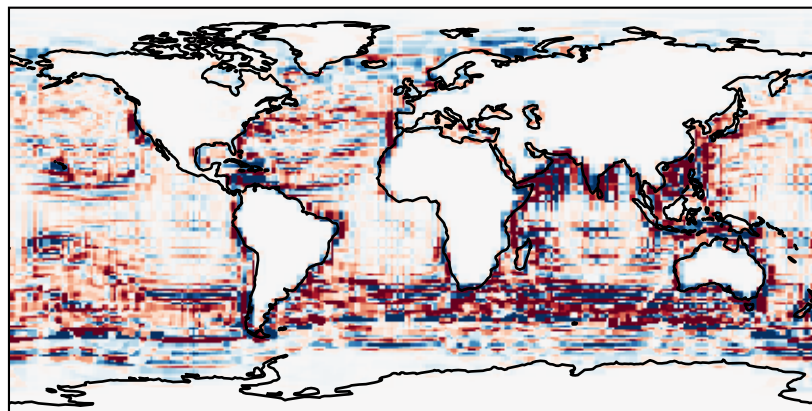
GCHP\_14.0.0-rc.3 (Dev)  
c24



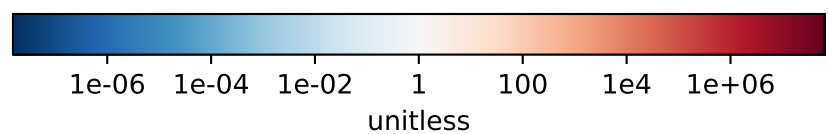
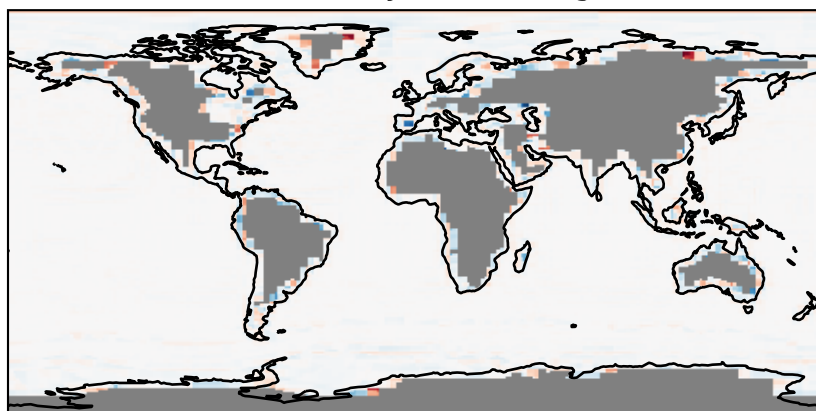
Difference (1x1.25)  
Dev - Ref, Dynamic Range



Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Ratio (1x1.25)  
Dev/Ref, Fixed Range

