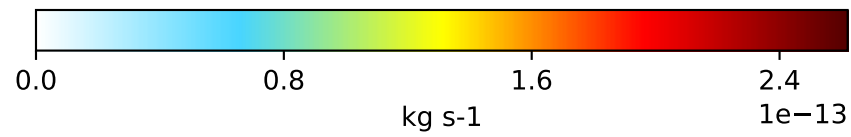
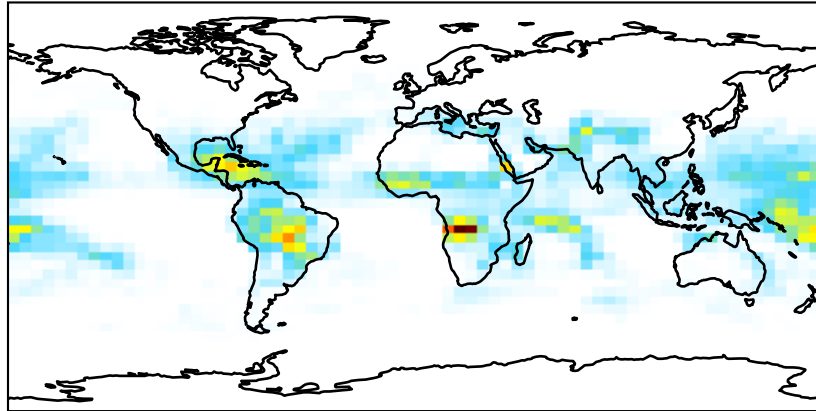
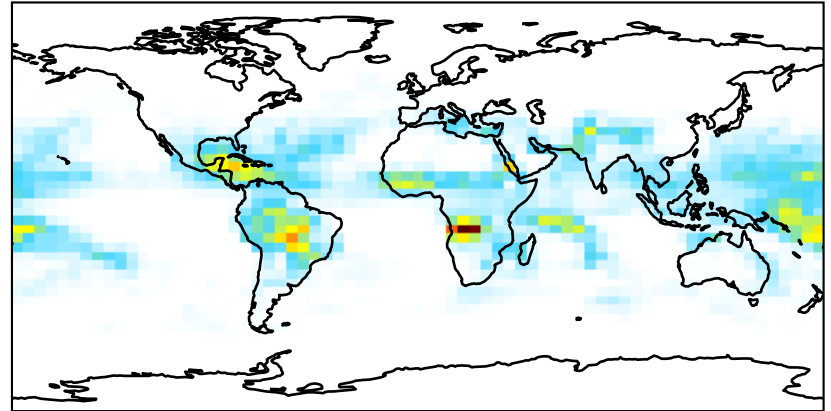


# WetLossConv\_Be10 (Oct2019)

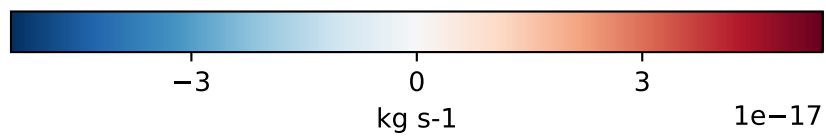
GCC\_14.0.0 (Ref)  
4.0x5.0



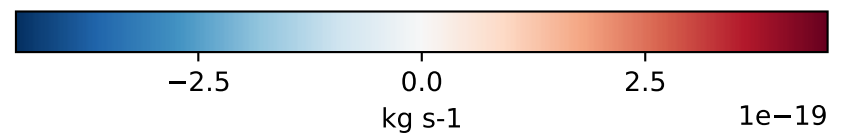
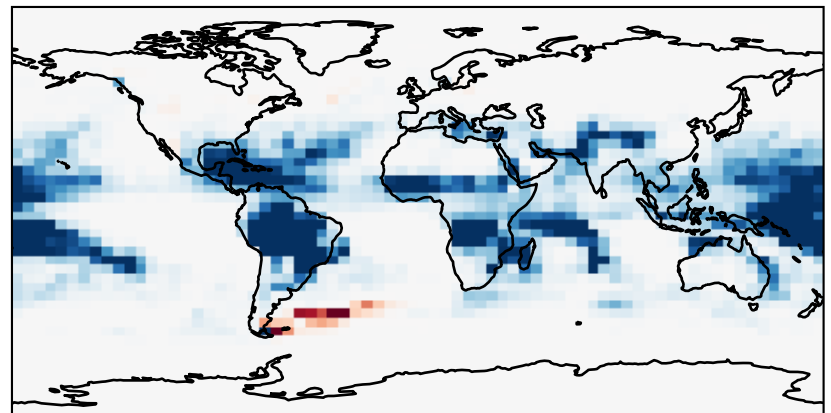
GCC\_14.2.0 (Dev)  
4.0x5.0



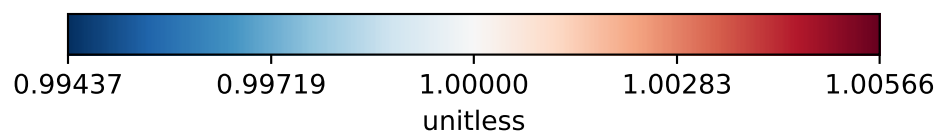
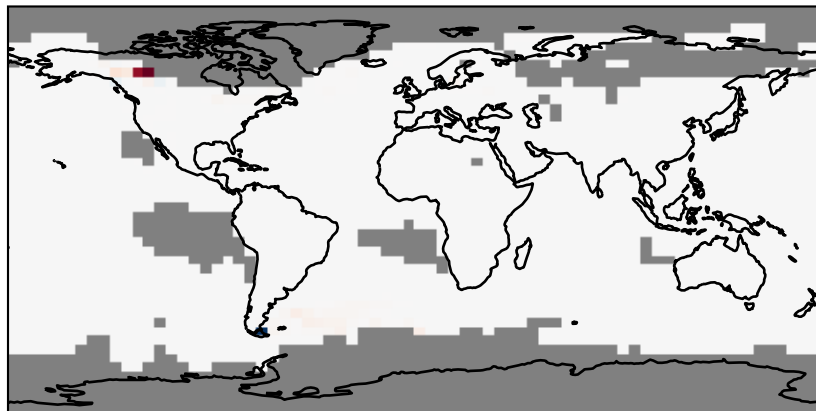
Difference  
Dev - Ref, Dynamic Range



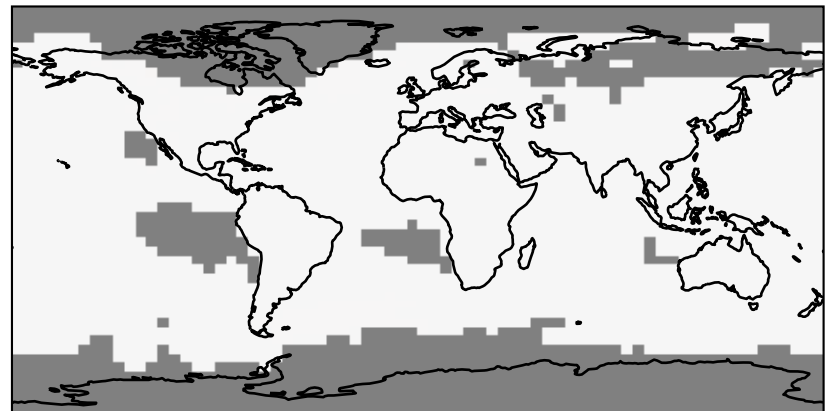
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

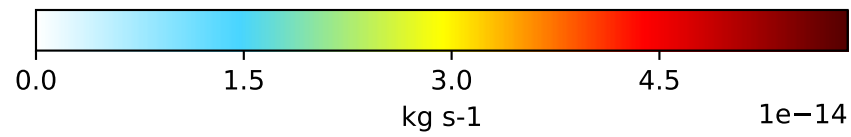
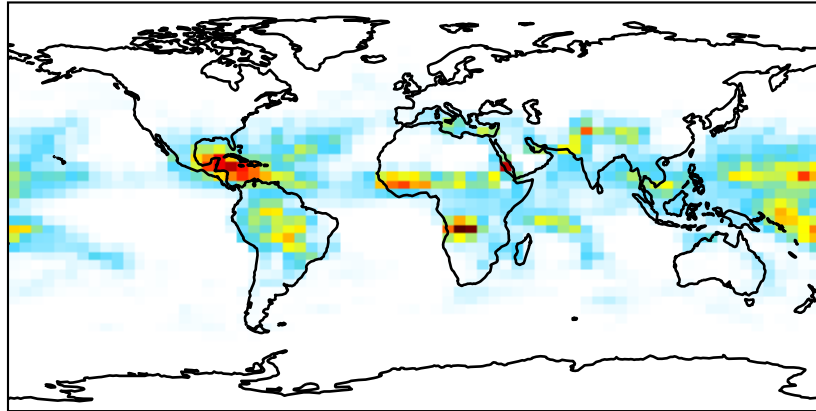


Ratio  
Dev/Ref, Fixed Range

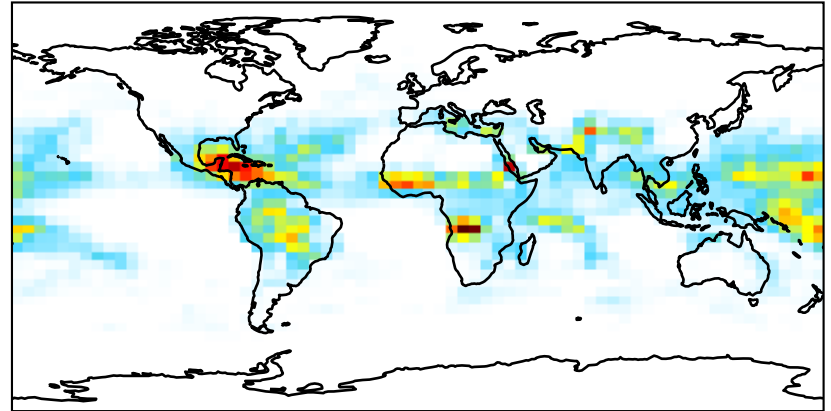


# WetLossConv\_Be7 (Oct2019)

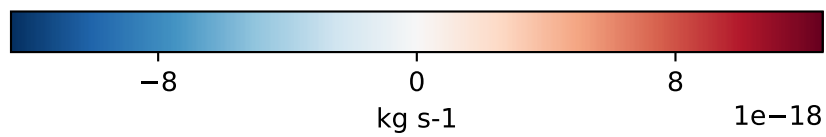
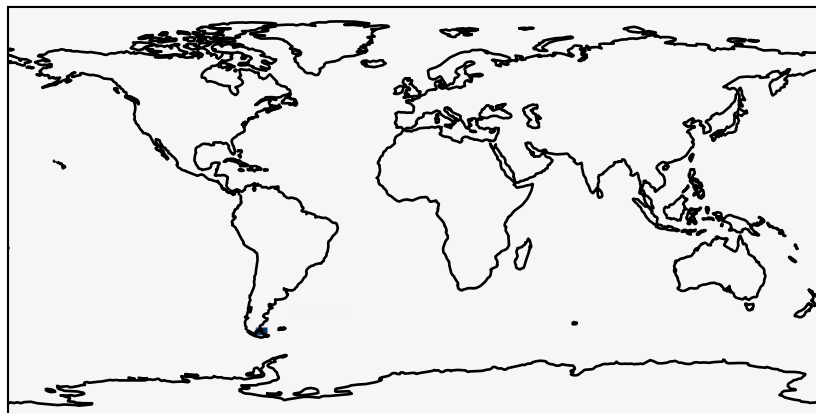
GCC\_14.0.0 (Ref)  
4.0x5.0



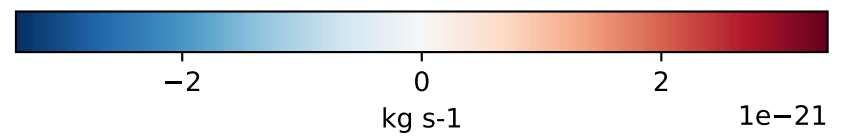
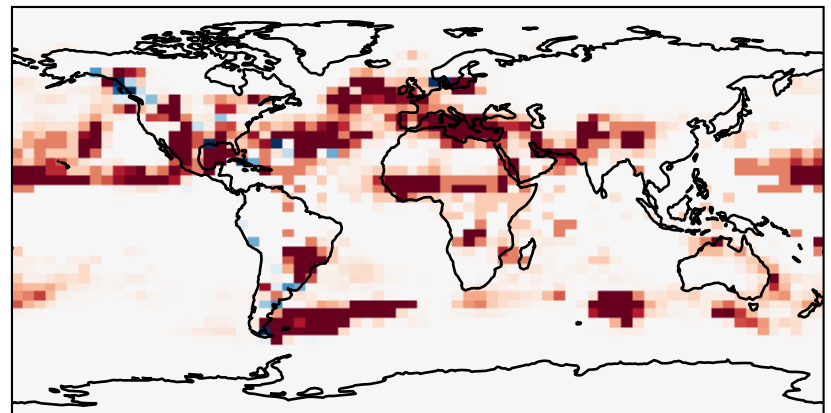
GCC\_14.2.0 (Dev)  
4.0x5.0



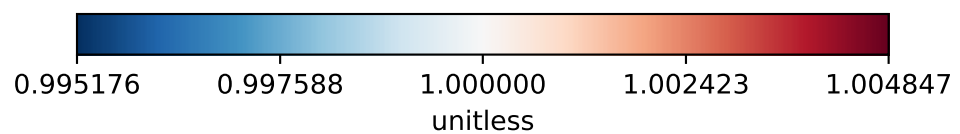
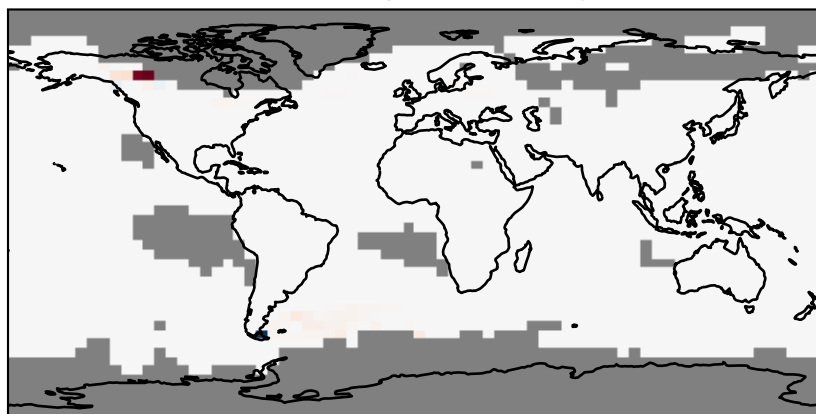
Difference  
Dev - Ref, Dynamic Range



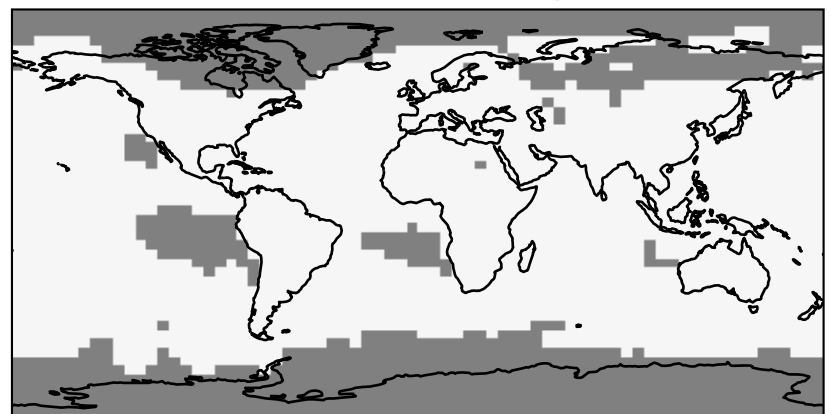
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

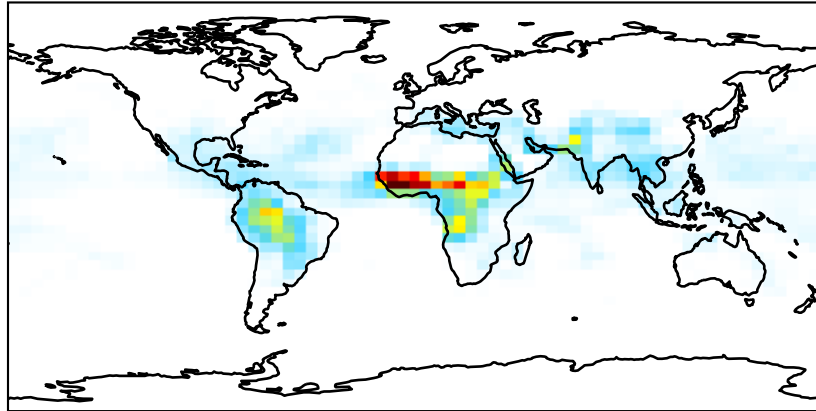


Ratio  
Dev/Ref, Fixed Range

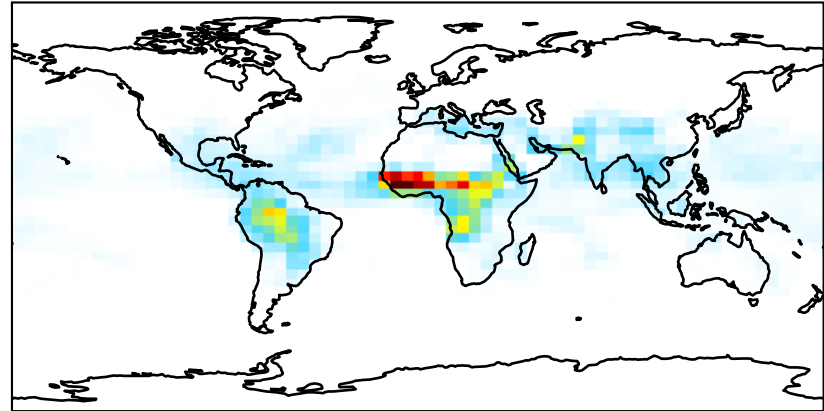


# WetLossConv\_Pb210 (Oct2019)

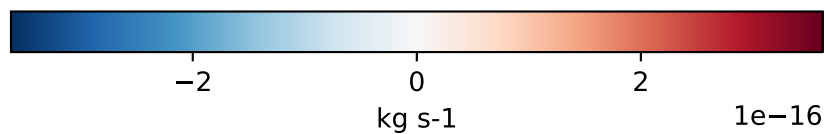
GCC\_14.0.0 (Ref)  
4.0x5.0



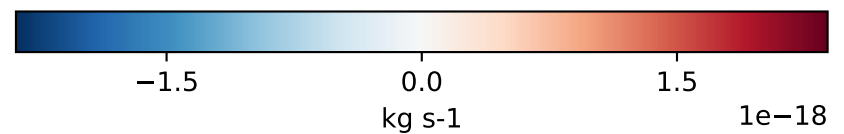
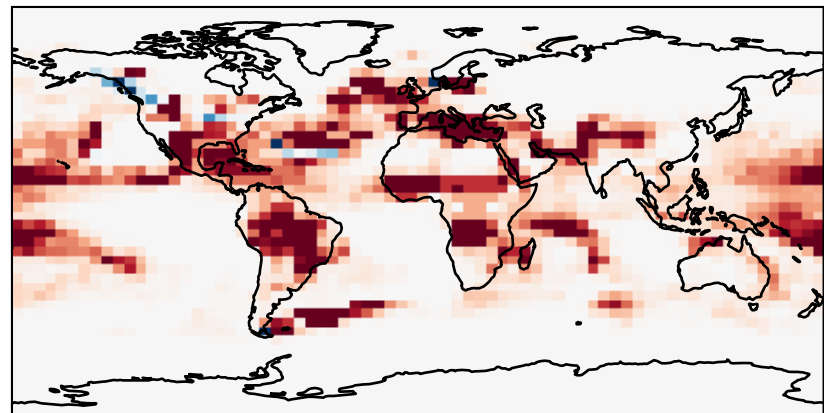
GCC\_14.2.0 (Dev)  
4.0x5.0



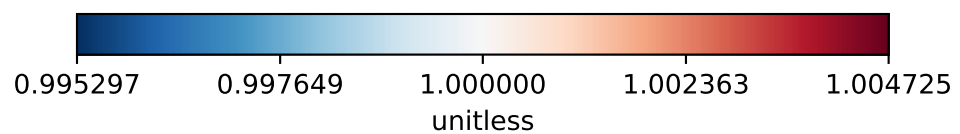
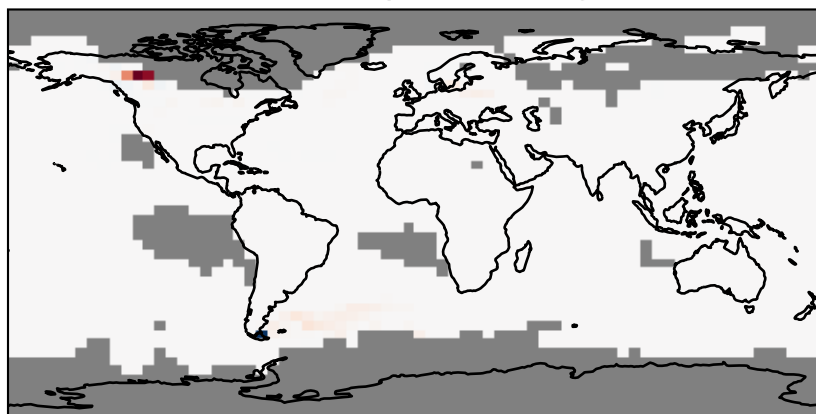
Difference  
Dev - Ref, Dynamic Range



Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

